Miniature-switch specifications sweep details under the carpet. Size, life and load capability are intricately interwoven, not simple to unravel. And installation data
are seldom laid bare in catalogs. However, miniatures can be tidily tacked into place, and if handled properly, wear well a long time. Get unpadded basics on p. 66.


## swage-Bond"'... a revolution in frimmer rellability!

## . . . here today at no extra cost in every Trimpot ${ }^{\text {P }}$ Potentiometer

Historically, pin-to-element termination problems have been one of the primary causes of trimmer failure . . especially during handling and PC board process operations. Bourns exclusive Swage-Bond ${ }^{\text {TM }}$ process virtually eliminates pin termination failure . . . truly a revolution in trimmer reliability. Furthermore, SwageBonding results in a marked improvement in temperature coefficient consistency
Other trimmer manufacturers utilize a simple clip-on termination. Some solder this connection, some rely on tension pressure alone. In the Swage-Bond process, the P.C. pins are secured through the substrate, with a high-pressure compression swage on both top and bottom sides. The pressure of the swage locks the pin solidly into the element, and thoroughly bonds it to the thick-film termination material.


The seal that seals . . . without springhack
Bourns trimmers stay sealed when others fail. We know. We've tested them all. Bourns uses a chevron-type sealing technique, that seals without 0 -rings . . . eliminating the windup and springback that frequently occurs with such seals. The result is faster and more precise adjustability . . . with a seal that really works.


Wrap-around wiper for better setting stability
Bourns multi-fingered, wrap-around wiper
 delivers more consistent, more reliable performance. The unique design significantly reduces CRV fluctuations and open circuit problems due to thermal and mechanical shock . . . by maintaining a constant wiper pressure on the element. Compare the ruggedness of Bourns design with the common "heat-staked" wiper designs. Compare performance. Specify Bourns.

## HERE'S PROOF:

Send for a copy of our new engineering report on TRIMMER PERFORMANCE. Tell us about your application, and we'll provide qualification samples that best suit your needs.

Bourns reliability is available at ordinary prices . . . off-the-shelf from nearly 100 local distributor inventories . . . plus our largest-ever factory stock. TRIMMER PRODUCTS, TRIMPOT PRODUCTS DIVISION, BOURNS, INC., 1200 Columbia Avenue, Riverside, California 92507. Telephone 714 781-5320 - TWX 910 332-1252.

Swage-Bond ${ }^{\text {TM }}$ eliminates pin termination failure, provides more reliable tempco. Microphotograph shows trimmer element magnified 20X.

The sweep signal generator world has a new leader to look up to. Model 2002 sweeps from 1 to 2500 MHz in four bands. Or it can sweep the entire range using the band stacking option. It has more flexibility than any broadband sweeper
we've made, along with +13 dBm output, $\pm 0.5 \mathrm{~dB}$ flatness, $0.005 \%$ marker accuracy, and $\pm 1 \%$ display linearity. Look at the Model 2002 from any angle and you'll become a follower. Send us $\$ 2700$ and you'll become an
owner. Circle our reader service number for details. WAVETEK Indiana Incorporated, P.O. Box 190, Beech Grove, Indiana 46107. Telephone (317) 783-3221. TWX 810-341-3226.


CIRCLE NUMBER 2

## Thencw 2002 wincepyou off youricet

model

# SURPRISE! Wide bandwidths,.015-600MHz SURPRISE! Low insertion loss, $\mathbf{0 . 5 \mathrm { dB }}$ SURPRISE! Microminiature,.230"x.270"x. 300 SURPRISE! One week delivery 

## - 铝 <br> DESIGNERS KIT AVAILABLE: <br> 2 TRANSFORMERS OF EACH TYPE <br> T1-1, T2-1, T4-1, T9-1,T16-1 <br> KIT \# TK-1 ... $\$ 32.00$



NO "MM XKE OR BUY" DECISION HERE ... it costs less to buy Mini-Circuits wideband transformers and there's no delivery delay. Impedance levels from 12.5 to 800 ohms with insertion loss typi cally less than 0.5 dB

| MODEL | T1-1 | T2-1 | T4-1 | T9-1 | T16-1 |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Impedance <br> Ratio $(50 \Omega$ pri, imp.) |  |  |  |  |  | I



For complete specs, performance curves and drawings, see pgs 192-193 of the 1976-77 MicroWaves Product Data Directory.
World's largest supplier of double balanced mixers -Mini-Circuits Laboratory

[^0]
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Circuit provides a Gaussian response with a multifunction converter and op amp. Circuit that latches/unlatches magnetic devices uses almost zero standby current. Diode-feedback comparator circuit regulates a LED's drive current. Time-interval meter reads digitally to 99.9 ms on a DVM IC.

## PRODUCTS

## 113 Modules \& Subassemblies: Industry's first 18-bit modular DAC fits in palm-sized case. <br> 114 Modules \& Subassemblies: Low-noise vhf synthesizer is small and inexpensive. <br> 120 Data Processing: Versatile PROM programmer takes on all memories. <br> 123 Integrated Circuits <br> 126 Power Sources <br> 129 Discrete Semiconductors <br> 130 Packaging \& Materials <br> 132 Instrumentation <br> 134 Components

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Cover: Photo by Art Director, Bill Kelly. Switches courtesy of Alco Electronic Products, AMF/IUD Electronics, AMP, Cherry Electrical Products, Chicago Switch, C \& K Components, Cutler Hammer, C-W Industries, Dialight, Digitron, EECO, Globe Union/Centralab, Grayhill, Janco, Licon, Micro Switch, RCL, Stackpole, Switchcraft, TEC.


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The best way to make a microprocessor system is the 8080 way. It's the most popular architecture; it has the best support. The flexibility, the upgrade capabilities are better than any other microprocessor.

## Two.

The best 8080A is a 9080A. Look:

|  | 8080A \& 90 | 30A) |
| :---: | :---: | :---: |
| Specification | Intel | AMD |
| Minimum Instruction Cycle Time | 1.3 microseconds | 1 microsecond |
| Maximum Power Dissipation (at 1.3 microsec. $\left.0-70^{\circ}\right)$ | 1307 milliwatts | 829 milliwatts |
| Output Drive | 1.9 mA @ 45 V | 3.2 mA @ 4 V |
| Minimum Input High Voltage | 3.3 V | $3.0 \mathrm{~V}$ |
| MIL-STD-883 | Special | Standard |

## Three.

Save shoe leather and heartache. We've already done the shopping. Advanced Micro Devices builds all the really important 8080 peripherals that you-know-who invented, a whole bunch that we invented, plus all the memories you'll ever need. And in our traditional heart-warming way, we add a little reliability and performance. (Everything we make is MIL-STD-883 for free.)

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# Ours and Ours. 

## (Am9080A System Circuits)

| AMD Part Number | Description | Availability |
| :--- | ---: | :--- |
| CPU |  |  |
| Am9080A $/-2 /-1 /-4$ | 0 to $+70^{\circ} \mathrm{C}$ | Now |
| Am9080A $/-2 /-1$ | -25 to $+85^{\circ} \mathrm{C}$ | Now |
| Am9080A -2 | -55 to $+125^{\circ} \mathrm{C}$ | Now |

STATIC READ/WRITE RANDOM ACCESS MEMORIES

| Am9101A/B/C/D | $256 \times 4,22$ Pin | Now |
| :--- | :--- | :--- |
| Am91L01A/B/C | $256 \times 4,22$ Pin | Now |
| Am9102A/B/C/D | $1 \mathrm{~K} \times 1,16$ Pin | Now |
| Am91L02A/B/C | $1 \mathrm{~K} \times 1,16$ Pin | Now |
| Am9111A/B/C/D | $256 \times 4,18$ Pin | Now |
| Am91L11A/B/C | $256 \times 4,18$ Pin | Now |
| Am9112A/B/C/D | $256 \times 4,16$ Pin | Now |
| Am91L12A/B/C | $256 \times 4,16$ Pin | Now |
| Am9131A/B/C/D/E | $1 \mathrm{~K} \times 4,22$ Pin | Now |
| Am91L31A/B/C/D | $1 \mathrm{~K} \times 4,22$ Pin | Now |
| Am9141A/B/C/D/E | $4 \mathrm{~K} \times 1,22$ Pin | Now |
| Am91L41A/B/C/D | $4 \mathrm{~K} \times 1,22$ Pin | Now |

DYNAMIC READ/WRITE RANDOM ACCESS MEMORIES

| Am9050C/D/E | $4 K \times 1,18$ Pin | Now |
| :--- | :--- | :--- |
| Am9060C/D/E | $4 K \times 1,22 ~ P i n$ | Now |


| MASK PROGRAMMABLE READ-ONLY MEMORIES |  |  |
| :--- | :---: | :--- |
| Am9208B/C/D | $1 K \times 8,250$ nsec. max. | Now |
| Am9216B/C | $2 K \times 8,300$ nsec. max. | Now |
| Am8316A | $2 K \times 8,850$ nsec. max. | Now |
| Am8316E | $2 K \times 8,550$ nsec. max. | Now |
| ERASABLE READ-ONLY MEMORIES |  |  |
| Am1702A | $256 \times 8,1.0 \mu$ sec. | Now |
| Am2708 | $1 K \times 8,450$ nsec. | 1st Q. 1977 |

CPU: 9080A $=480$ nsec. $-2=380$ nsec. $-1=320$ nsec. $-4=250 \mathrm{nsec}$. MEM: $A=500$ nsec. $B=400$ nsec. $C=300$ nsec. $D=250$ nsec. $E=200$ nsec
SECOND SOURCE SUPPORT

## Am8212

Am8216
Am8224
Am8226
Am8228
Am8238
Am8251
Am8255
Am8257

## IMPROVED SUPPORT

Am8224-4
Am8238-4
Am9511
Am9517
Am9519
Am9551/-4
Am9555/-4
Am25LS138
Am25LS139
*Am25LS273
*Am25LS373 *Am25LS374 *Am25LS377 *Am25LS2513 *Am25LS2537
*Am25LS2538
*Am25LS2539

| REPLACES |  |
| :--- | :--- |
| N/A | Now |
| N/A | Now |
| N/A | 3rd Q. 1977 |
| 8257 | 3rd Q. 1977 |
| 8259 | 3rd Q. 1977 |
| 8251 | Now |
| 8255 | Now |
| 8205 | Now |
| 8205 | Now |
| N/A | 2nd Q. 1977 |
| 8212 | 4th Q. 1977 |
| 8212 | Now |
| 8212 | 2nd Q. 1977 |
| $8214 \& 8212$ | Now |
| $8205(2)$ | Now |
| N/A | Now |
| N/A | Now |


| High-Speed Generator | N/A | Now |
| :--- | :--- | :--- |
| High-Speed System Controller | N/A | Now |
| Arithmetic Processing Unit | N/A | 3rd Q |
| Multi-mode DMA Controller | 8257 | 3rd Q |
| Universal Interrupt Controller | 8259 | 3rd Q |
| Prog. Communications Interface 8251 | Now |  |
| Prog. Peripheral Interface | 8255 | Now |
| 1-of-8 Decoder | 8205 | Now |
| Dual 1-of-4 Decoder | 8205 | Now |
| 8-bit Common Clear Register | N/A | 2nd Q |
| 8-bit Transparent Latch | 8212 | 4th Q. |
| 8-bit 3-State Register | 8212 | Now |
| 8-bit Common Enable Register | 8212 | 2nd Q |
| Priority Encoder | $8214 \& 8212$ | Now |
| 1-of-10 3-State Decoder | $8205(2)$ | Now |
| 1-of-8 3-State Decoder | N/A | Now |
| Dual 1-of-4 3-State Decoder | N/A | Now |

*All combine high performance and low power in space saving 20-pin package.

# A <br> Advanced Micro Devices 

Bipolar LSI. N-channel, silicon gate MOS. Low-power Schottky. Multiple technologies. One product: excellence.


Tucked in the corner of this Pulsar Watch is a miniature capacitor which is used to trim the crystal. This Thin-Trim capacitor is one of our 9410 series, has an adjustable range of 7 to 45 pf , and is $.200^{\prime \prime}$ x $.200^{\prime \prime} \times .050^{\prime \prime}$ thick.

The Thin-Trim concept provides a variable device to replace fixed tuning techniques and cut-and-try methods of adjustment. Thin-Trim capacitors are available in a variety of lead configurations making them easy to mount.

A smaller version of the 9410 is the 9402 series with a maximum capacitance value of 25 pf . These are perfect for applications in sub-miniature circuits such as ladies' electronic wrist watches and phased array MIC's.


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Boonton, New Jersey 07005
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## Reprints

Maxine Correal

## Across the Desk

## Two left out are now included

The Focus on Keyboards (ED No. 22, Oct. 25, 1976, p. 122) was very interesting, but left out two manufacturers:

Data Interfaces, Inc.
12 Cambridge Ave.
Burlington, MA 01803
CIRCLE NO. 318
Mechanical Enterprises, Inc.
8000 Forbes Place
Springfield, VA 22151
CIRCLE NO. 319
Data Interfaces offers adapters that go between other brands of keytops and their double crosspoint switches, as well as standard keyboards.

One subject the Focus didn't consider is separate switches and encoders for those of us that "roll our own" keyboards. More information in this area would be desirable. For instance, I have not been able to locate an encoder for use with electronic keyswitches.

William B. Rossman
Cascade Research Associates
P.O. Box 534

274 28th Ave.
Longview, WA 98632

## Computer hierarchy doesn't do it all

Your recent article on microcomputer and microprocessor applications (ED No. 22, Oct. 25, 1976, p. 66) gives an excellent overview of their use in new equipment. However, the section on the Airtrans system at the Dallas-Fort Worth Airport is misleading in that it implies that the system is totally controlled by a hierarchy of computers. In fact, the system is structured like the Washington,

DC, Metro subway system mentioned earlier in the article. Airtrans also has three interfacing major subsystems:

- Automatic Vehicle Supervision (AVS) supplied by Vought, the prime contractor. This subsystem consists of the central processor and eight satellite computers. This subsystem can reduce vehicle velocities to reduce bunching, modify station dwell times, change the routes of the vehicles, and provide central control with vehicle-tracking and support-system information.
- Automatic Vehicle Operation (AVO) supplied by subcontractor General Railway Signal. The AVO subsystem controls vehicular speed regulation, station stopping, door operation and dwell, and route selection.
- Automatic Vehicle Protection (AVP), also by General Railway. The AVP subsystem sets and enforces safe speed limits for each vehicle as well as prevents conflicting route movements.

The AVO and AVP subsystems will continue to function and provide complete automatic operation even if the central and satellite computers are disabled.

Samuel J. Macano Senior Design Engineer General Railway Signal Co.
801 West Ave.
Rochester, NY 14602

## You missed itwe've got it

Jim McDermott's otherwise excellent article, "Focus on Adhesives and Coatings" (ED No. 23, Nov. 8, 1976, p. 40), missed an important type of EMI-combative
(continued on page 10)

[^1]
## The finest



## switches and indicators are



## also the easiest toinstall



## Snap!

## Snap-in instant panel mounting <br> Choice of sizes, colors, and lens styles <br> Flush or barrier configurations Re-lampable from front of panel.

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COMPONENTS GROUP
CIRCLE NUMBER 5

## Here's howDataGeneral's microNOVA system stacks up against the competition.




Data General, Westboro, MA 01581 Sounds like smart business. Send me more information.

## NAME

TITLE
COMPANY
ADDRESS
CITY
NOVA is a registered trademark of Data General Corporation. DASHER is a trademark of Data General Corporation.

The facts speak for themselves. For \$10,970, Data General's new microNOVA gives you more system, software and support than any other comparable computer. And we deliver in 60 days.

Any way you look at it, it all stacks up in your favor. For more information and our brochure, call our toll free number, 800-225-9497, or, fill out and return the coupon.
*Quantity and OEM discounts available.

1. DataGeneral

It's smart business.
Data General, Westboro, MA 01581, (617) 485-9100. Data General (Canada) Ltd., Ontario. Data General Europe, 15 Rue Le Sueur, Paris 75116 France. Data General Australia, Melbourne (03) 82-1361.

## Here's howData General's NOVA 3/D system stacks up against the competition.



The facts speak for themselves. For \$37,610, Data General's new NOVA 3/D gives you more system, software and support than any comparable computer. And we deliver in 60 days.

Any way you look at it, it all stacks up in your favor. For more information and our brochure, call or fill out and return the coupon.
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It's the new GOULD/Brush 110 with a thermal pen unmatched in the quality of its easy-to-read blue traces.
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When it comes to reliability, we back up our promise with a lifetime pen guarantee. One reason we can make such a strong guarantee is that the special ceramic pen tip is virtually wearfree. No frequent, costly pen replacements. And although other pens are sometimes damaged by excessive off-scale input signals, ours is not because we use hardelectronic limiters and soft mechanical stops.
Then take versatility. The 110 has features that let you tailor it to your exact application. For example, you can choose from ten
chart speeds. A selection of plugin signal conditioners accommodate a wide range of input signals. Charts may be pulse-driven by an external device. And an optional solid state electronic chart integrator follows positive and negative signals up to 4 times full scale on the analog channel.

We don't believe there's another strip chart recorder in the market that is as fast, dependable and versatile. But don't take our word for it. We'll be happy to give you a demonstration anytime, anywhere. Once you see it, we think you'll believe it too.


Call your nearest Gould Sales Engineer for a demonstration. Or write Gould, Inc., Instrument Systems Division, 3631 Perkins Avenue, Cleveland, Ohio 44114. Or Gould Allco S.A., 57 rue
St. Sauveur, 91160 Ballainvilliers, France.

CIRCLE NUMBER 11

# Licon non-lighted PB switches offer you the greatest possible value with good-as-gold quality and reliability at terrific prices! 

Know something?
Our Licon lighted PB's have been glowing so brightly that they've cast our non-lighted PB's into undeserved shadow.
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Most non-lighted PB models feature Licon's exclusive, ultra-reliable Butterfly ${ }^{\text {o }}$ switching mechanism.
Best of all, though, is the pleasant surprise you get when you check Licon pricing on 1,000piece and larger orders. Look into it. You'll be pleased.

Contact your local Licon distributor or call or write for our Switch Catalog: Licon, 6615 West Irving Park Road, Chicago, Illinois 60634. Phone (312) 282-4040. TWX 910-221-0275.

## fis LICON <br> A DIVISION OF ILLINOIS TOOL WORKS INC. CIRCLE NUMBER 12

# Golden opportunity Pushbutton Switches! 



## They're good... no matier how bad

TV MEETS NIL-s.8ETAI. oFFERS SWITCHING VERSATILIUY of larger Toceles WITH abvantace OF SMALLSIZE, PANEL SEALINC. IOEAL FOR APPLICATIONS LK ON OFF SWITCH INTHIS MANPACK EOUIPMENT.


PX KEYBOARDS. TOTALIY SEALED WHEN MOUNTED, WITH zaro DEPTH BEHIND RANEL, PXISA NATURALCHOICEROR MANDACK EOUIPMENT SWITCH MATREDE.

SERIES 1 ROUND LIGHTED
PUSHEUTTONSOFFER FLUSH MOUNT INO PANEI SEAINE AND VaRSATLE SWITCH CIRCUITRY: INCLVOINE SOLID SIGU NIL SPEC QUALTY (M1L-S.22:33) AT commERCIAL PRICESFOR GONTROL CONEOLES.
things get.
The five switches you see here have all been designed to operate reliably under extremely rugged environmental conditions. Exactly the kinds of environments where Command, Communications \& Control Systems are required to work.

But if these switches aren't exactly what you need, you're not out of luck.

Because they're only a sampling of literally thousands of MICRO SWITCH listings available to fill your needs. Including toggle switches. Lighted pushbuttons. Unlighted pushbuttons. Key switches. Sealed keyboards. Plus hermetically and environmentally sealed limit, proximity and basic switches.

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If you'd like more information on the devices you see here, or more information on how we can help, write or call your nearest MICRO SWITCH Branch Office or Authorized Distributor.

## MICRO SWITCH

FREEPORT. ILLINOIS 61032
A DIVISION OF HONEYWELL

## ACROSS THE DESK

(continued from page 10)

interruptions in the selector-magnet current. At the first interruption, the phototransistor in the optical coupler turns off, and pin 2 of the 555 falls to ground to start the timing cycle. Each interruption also turns on $Q_{1}$, which discharges the timing capacitor and prevents the circuit from timing out until 110 seconds after the last data character. Diode $D_{1}$ protects the emitter junction of $Q_{1}$ from excessive reverse voltage, and $D_{2}$ prevents the capacitor from being
charged through $\mathrm{D}_{1}$.
Supply voltage should be between 5 and 15 V . Up to 200 mA , the circuit will deliver a volt or two less than the supply voltage to the relay coil. The relay can either switch the teleprinter motor directly if its contact ratings are adequate, or control a larger relay.

Craig R. Allen
Naval Electronics Laboratory
Center
Code 3400
San Diego, CA 92152

Misplaced Caption Dept.


Congratulations. You won the medal for the best circuit design of the year.

Sorry. That's Hans Holbein the Younger's "Sir Thomas More," which hangs in the Frick Collection, New York City.
4 CIRCLE NUMBER 180 for DATA
4 CIRCLE NUMBER 236 for SALESMAN CALL

## Skip interference may be considerable by '78

In reference to your "Washington Report" concerning the sunspot effect on the Citizen's Band (ED No. 17, Aug. 16, 1976, p. 45) : You are in error in indicating that the interference due to skip is not as bad as feared; the report is based on the assumption that we are at the peak of the sunspot cycle.

On the contrary, we are at the bottom of sunspot cycle 20 with the beginning of sunspot cycle 21 expected to take place some time next year. Its peak is expected to occur around 1982.

Therefore, in the next two to three years, considerable skip interference will be noticed, with complete worldwide interference two to three years before and after the peak-at which time communication in the $27-\mathrm{MHz}$ CB band will be next to useless.

Joseph F. Mibelli, WA4JLX
Chief Electronics Engineer Coulter Electronics, Inc.
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# MEMORY AT WORK 

# Process Control 

Loss of power on


## MEMORY AT WORK

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## NMOS memories-they're faster and run cooler

Power reductions as large as $80 \%$, density boosts of 2 to 1 and speed improvements by factors of three signalled advances in NMOS memory design at last month's Solid-State Circuits Conference.

The size of a new static memory cell developed by designers at Mostek, Carrollton, TX, has been reduced so much-to just $2.75 \mathrm{mil}^{2}$ that a $4-\mathrm{k}$ RAM chip, the MK4104, can measure $136 \times 184$ mil. But what's even better, the power dissipation has dropped drastically to a mere 80 mW in the active stateabout $20 \%$ of the power normally required by an NMOS 4-k RAM.

In Santa Clara, CA, Intel engineers have pushed NMOS to bipolar speeds in their new 4-k static RAM. Access times have been cut to 45 ns (about one-third that of other NMOS RAMs), typical, and power dissipations held to nominal


Requiring only $\mathbf{8 0} \mathbf{m W}$, this $4-\mathrm{k}$ RAM from Mostek accesses in 150 ns .

MOS levels of 500 mW .
Just down the road from Intel, designers at American Microsystems are the first to apply VMOS devices to memory arrays, claims Thurman Rogers, AMI's program director of VMOS and developer of the process. A 1-k static RAM family introduced at the conference will have access times of less than 50 ns and chip sizes as small as $80 \times 55 \mathrm{mil}$.

Read and write speeds in Mostek's RAM haven't been compromised to get the low power. Typical access time is 150 ns , and a full read-write cycle requires 260 ns . The small-area cell uses high-impedance load devices that are made from polysilicon by means of a simple modification of the standard silicon-gate process.

The high-impedance poly regions permit a compact layout-about twice as dense as previous NMOS static-memory cells. Also, the highvalue load resistors permit data retention at low values of $\mathrm{V}_{\mathrm{dd}}$-as low as 2 V . And when the chip is not enabled, the RAM has a standby mode that cuts power to a scant 8 mW .

The performance of Intel's RAM is achieved by combining device scaling and substrate bias, and reducing gate-oxide thicknesses. Polysilicon gate lengths are kept under 4 microns and oxide thick-


The VMOS transistor, when used in RAMs, keeps cell size to $3 \mathrm{mil}^{2}$.
nesses to well under 1000 A. Rather than sacrifice a pin, the substrate bias is generated on the chip. Cell size for the RAM is 3.75 milㄴ.

American Microsystems' VMOS process is used in the company's 1401, a 1-k RAM. Cell sizes have been kept to 3 mil- so that larger static arrays can soon be made available.

The VMOS arrays' performance also approaches that of bipolar ar-rays-access times of 28 to 45 ns are expected for $1-\mathrm{k}$ chips.

By applying the same VMOS technique to ultra-violet, erasable ROMs or PROMs, arrays of up to 64 k can be easily attained, AMI claims, since cell sizes are less than $0.36 \mathrm{mil}^{2}$.

UV PROMs made with the VMOS technique can be programmed with a $15-\mathrm{V}, 100-\mathrm{ms}$ pulse instead of the $26-\mathrm{V}$ pulses normally required for the NMOS versions. Moreover, the VMOS cell requires only $36 \%$ of the area needed by currently available NMOS PROM cells.

Computer-scaling the VMOS cell can reduce its area even further for both the ROM and RAM designs. For instance, the 1401 RAM's chip area has been experimentally reduced from $81 \times 125$ mil to $69 \times 100 \mathrm{mil}$. As a result, the speed improves by $20 \%$ to an access time of 34 ns .

A second scaling application on an experimental RAM reduces the size to $80 \times 55 \mathrm{mil}$ and improves the speed to 28 ns . Thus, a larger array such as a 4-k RAM can easily be fabricated on a chip whose area is less than 25,000 sq. mils.

## Simulated operations help oil tanker officers

In light of the recent spate of oil tanker mishaps around the world, officers of Texaco's 160 -vessel tanker fleet are receiving additional, specialized training at La Guardia Airport's Marine Air Terminal in Flushing, NY. Their "trainer" is a computerized shiphandling system owned and operated by Marine Safety International.

Designed and built by Sperry Systems Management, a division of Sperry Rand Corp., the simula-
tor features a full-scale replica of a ship's bridge, including wheelhouse and chart room, and is equipped with all navigational, pro-pulsion-control and communications equipment normally found on board.

The ship's master or conning officer operates the simulator from the wheelhouse and responds to images projected on a 12 -by- $60-\mathrm{ft}$ curved screen. So the view from the bridge is precisely what it would be if the ship actually were being maneuvered in the geographical area being simulated.

Imagery is provided by a wideangle optical probe, with three television cameras and a singlelens system, which "tracks" a 15-by- $30-\mathrm{ft}$ model board of the simulated geographical area.

As the probe tracks the board in response to helm and engine orders executed on the bridge, it transmits video signals to three closed-circuit TV projectors below the wheelhouse, which project lifesize, dynamic video images onto the panoramic screen.

The actual movements of the probe are correlated by a Varian 620-100 minicomputer to correspond precisely to the hydrodynamic characteristics of the ship type being imitated.

A dynamic presentation of a real harbor-also under computer con-trol-includes all the navigational aids, and topographical and physical features of the area, including shore building, lighthouses, piers, jetties, rocks, cliffs and islands.

## Engineers: prepare for a taxing time

Did you know that you can deduct $\$ 2500$ on your federal tax return for premoving expenses, such as house-hunting trips and tempo-rary-living expenditures?

And did you know that if you are called in for an audit and there are two IRS representatives instead of one, that one or both are Special Agents, which means that the IRS suspects something "criminal" in your return?

Detailed explanations of these and other issues, which are designed to "demystify" the inner workings of the IRS for the engineer, are set forth in "The Engineer \&

Federal Taxes"-a 44-page booklet sponsored by the United States Activities Board of the Institute of Electrical and Electronics Engineers.

Authored by Paul Opalack, a CPA, and Paul S. Richter, a tax and patent attorney, the booklet is primarily for engineers who wish to prepare their own returns. Priced at $\$ 3$ for IEEE members and $\$ 6$ for nonmembers, it can be ordered postpaid from: IEEE Service Center, 445 Hoes Lane, Piscataway, NJ 08854.

## Now pilots will know where lightning strikes

Severe storm turbulence, the bane of aircraft pilots everywhere, can now be avoided with a passive weather-mapping system that displays the location and range of electrical activity in thunderstorms. The new kind of mapping system, called the Ryan WX-7 stormscope, is especially helpful to single-engine aircraft-no weather radar is available for these planes.

Each stroke of lightning is displayed as a bright green dot on the face of a high-intensity CRT. During a thunderstorm, the lightning strokes gather in groups in the central area of the storm. So storm intensity can be gauged on the scope by the size of the dot clusters.

The location of the dot clusters on the scope gives the position of the storm relative to the plane. The display has ranges of 20,50 and 100 nautical miles.

Named after its inventor, Paul A. Ryan, president of Stormscope in Columbus, OH , the WX-7 picks up energy from the lightning at various frequencies below 200 kHz . Although frequencies are present up into the MHz range, Ryan
found that the lower frequencies provide better data for processing into range and bearing data.

The lightning strokes are pinpointed by a special automatic di-rection-finding system that can scan a full $360^{\circ}$ very quickly.

To obtain the storm's distance from the plane-or "pseudo-distance," as Ryan calls it-the system sensitivity is calibrated so that the intensity of received signals is a prime measure of how far away lightning has struck.

The received signals are processed in a hybrid analog-digital com-puter-first by the analog system with proprietary techniques to enhance range information that is inherent in the signals. Then the signals are fed to the digital portion, which enhances the image and temporarily stores the dot pattern presented on the scope. Where there is a lot of storm activity, the display becomes animated because the dots automatically update themselves as new data come in.

## Zinc-chlorine batteries being readied for cars

Two experimental zinc-chlorine batteries are being developed for vehicular use by Energy Development Associates (EDA), a joint venture of Gulf \& Western Industries and Occidental Petroleum, under a contract with Energy Research and Development Administration. The $50-\mathrm{kWh}$ batteries are capable of cycling either to a complete discharge or in a partial-depth-of-discharge mode.

Eventually, EDA predicts a $40-$ kWh battery will be able to propel a four-passenger car 150 to 200 miles on a single charge, with an average speed of 50 mph and peak speeds of 70 mph .

## News Briefs

Quadrupling the component density used in the 8080 , Intel has developed several dual-processor chips, the 8271,73 and 75 , which will be released later this year. These circuits, a serial data link controller, a floppy disc controller and a CRT terminal controller, are only $218 \times 244 \mathrm{mil}$, but contain over 22,000 transistors.

Combining CMOS and SOS technology, Hewlett-Packard has developed a 16 -bit parallel microprocessor capable of operating at a $6-\mathrm{MHz}$ clock rate and consuming less than 400 mW . Although the $\mu \mathrm{P}$ will not be available as a stand-alone product, it will be used as a controller in future HP equipment.

Chances are, we already have the packaging you want. That's the beauty of our single-chip design and packaging concept: freedom of choice. Because our ultra-low $\mathrm{V}_{\text {CE (SAT) }}$ modules are pre-rated, pre-tested and inventoried, we can put them in any package to meet any need... help you optimize size, weight and performance without long lead times for custom packaging. Whether you need 5 or 500 pieces, we'll welcome the opportunity to discuss your special needs. For application notes and further information, call Sales Engineering, PowerTech, Inc., 0-02 Fair Lawn Ave., Fair Lawn, N.J. 07410; (201) 791-5050.

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## More on-chip I/O eliminates extra interface devices.

All of Rockwell's one-chip computers offer powerful, user-oriented I/O ports that eliminate costly interface circuitry in overall systems.

I/O features, including bidirectional ports, flexibly designed drivers and receivers, and serial input/output ports, provide you with powerful system options.

Many types of displays can be driven directly. Analog-digital conversion is easy. And serial I/O ports offer a new dimension of capability by giving you simple, "no-cost" interfacing for multi-computer systems.

## Rockwell flexibility assures costeffective design.

Rockwell's one-chip computers give you design options you couldn't afford with other logic approaches.

During the design stage you can add or reduce functions, allocate I/O differently and make dozens of other changes by simple reprogramming or by moving to another software-compatible chip within the family.

## Powerful instruction sets increase efficiency.

Rockwell's instruction sets provide ROM efficiencies of typically 2 to 1 over other microcomputers. For example, some one-byte multi-function Rockwell instructions perform operations requiring five instructions in other systems.

More than $80 \%$ of Rockwell's instruction
types can be executed in one byte and in a single cycle. Special ROM instructions allow many subroutine calls to be handled in one byte. Table look-up instructions for MM77 and MM78 chips provide easy look up of stored data and easy keyboard decoding with minimal programming.

## The PPS 4/1 family of one-chip computers.

| Model | MM76 | MM77 | MM78 | MM75 | MM76C | MM76D | MM76E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Description | $\begin{gathered} \text { Basic } \\ 76 \end{gathered}$ | Basic 77 | Jumbo 77 | Economy 76 | High speed counter* | $\begin{gathered} 12 \text {-bit } \\ \text { A/D } \\ \text { converter } \end{gathered}$ | Expand ed 76 |
| ROM ( x 8 ) | 640 | 1344 | 2048 | 640 | 640 | 640 | 1024 |
| RAM ( x 4 ) | 48 | 96 | 128 | 48 | 48 | 48 | 48 |
| Total I/O lines | 31 | 31 | 31 | 22 | 39 | 37 | 31 |
| Cond. Interrupt | 2 | 2 | 2 | 1 | 2 | 2 | 2 |
| Parallel Input | 8 | 8 | 8 | 4 | 8 | 8 | 8 |
| Bidirectional Parallel | 8 | 8 | 8 | 8 | 8 | 8 | 8 |
| Discrete | 10 | 10 | 10 | 9 | 10 | 10 | 10 |
| Serial | 3 | 3 | 3 | - | 3 | 3 | 3 |
| In-line package | 42 pin quad | 42 pin quad | 42 pin quad | 28 pin dual | 52 pin quad | 52 pin quad | 42 pin quad |
| Availability | Now | Now | Now | 2Q 77 | 2Q/77 | 3Q 77 | $\begin{aligned} & 16 \mathrm{wk} \\ & \text { ARO } \end{aligned}$ |

Power supply is 15 v except low voltage version of Basic 76 available $3 Q 77$. Typical power dissipation is 70 mw .
*Two 8 -bit or one 16 -bit presetable up/down counter with 8 control lines.

## Rockwell design aids also help lower your system cost.

To help control development costs, Rockwell makes available a universal Assemulator that lets you assemble, edit, develop and debug programs, as well as load PROMs. Special development circuits enable prototyping.

Your Assemulator can also handle incoming inspection and factory testing. And the same Assemulator can be used to develop systems based on all Rockwell one-chip and multi-chip microprocessors.

For the full story on Rockwell one-chip computers, and how quickly they can be a part of your new product, write on your company letterhead to: Marketing Services, D/727-B, Microelectronic Device Division, Rockwell International, P.O. Box 3669, Anaheim, CA 92803, U.S.A. or phone (714) 632-3729.

# To cut a building's energy use, call on 'Judy' the $\mu \mathrm{P}$ system 

How do you cut a building's use of electricity by over a third and its kilowatt-hour consumption by nearly a half? Simple. You sprinkle four microprocessors around the facility to control the building's energy use, monitor its security and manage equipment operation.

Called "Judy," a system of four microprocessors distributed around a 50,000 -sq-ft underground federal building in Denton, TX, has cut demand for electricity $38 \%$ and its actual kilowatt-hour consumption $49 \%$. What's more Judy has managed to save an extra $\$ 50,000$ a year by filling the void left by the elimination of the security force. The system is expected to pay back its $\$ 15,000$ cost in less than a year.

Although the four National Semiconductor SC/MP processors form the nucleus that is named Judy each is actually independent of the other, with its own RAM, ROM and duties.

SC/MP 1 is responsible for managing the building's equipment. SC/ MP 2 is responsible for controlling the security of the building. SC/ MP 3 accepts, formats and stores all the messages that Judy sends out. And SC/MP 4 is the communication control-it dials the phone, sends out messages, answers the telephone and corrects transmission errors.

## War games conserve energy

Judy's $\mu \mathrm{P}$ No. 1 monitors a variety of alarm points for smoke, intrusion, sump level, pneumatic pressure and fresh air. These points need only open or close switches. The microprocessor also controls the heating and cooling

[^3]

Judy's Question-and-Answer Control Panel. This array of LEDs and pushbutton switches is an inexpensive terminal to implement the system's command line mnemonic interpreter in firmware. Questions put to the operator via LEDs are answered by selecting mnemonic pushbuttons, thus setting SC/MP 1's strategy in a "war game" for energy conservation.


This is not a telephone, but is an input keypad to a distributed-intelligence network that watches over a government building in Denton, Texas.
machinery and lighting as well. To do all this efficiently, Judy's designers, Radix II, Inc., Oxon Hill, MD, chose to implement $\mu \mathrm{P} 1$ as a "war-gaming instrument."

Inside and outside temperature readings are applied as input to firmware "predict-or-correct" algorithms. A particular game strategy
is commanded by the operator, who, in programming her, considers such parameters as the season and recent temperature trends.

Every weekday morning, all the equipment under SC/MP 1's control is cycled so as to keep the building's electrical load as low and as constant as possible. Employees begin arriving at 8:00 a.m., but SC/MP 1 starts to "bring the building up" two hours beforeand very slowly so as to cause only the smallest demand increment on the power company. Just turning on the lights takes half an hour.
"In an uncontrolled building," explains Dr. G. Lamers, President of Radix II, "demand peaks constitute at least $20 \%$ to $30 \%$ of the total electric bill. The power utility must provide this level of service at all times, whether it's used or not." Over a three-month period, Judy reduced the peak-energy demand of her building by $38 \%$.

To determine the building's current rate of electrical consumption, Judy monitors the power company's rotating-demand meter wheel. Based on that rate, the SC/ MP 1 makes strategic decisions to switch equipment on or off; only the minimum pieces of equipment are activated at any given time. As a result, the building's actual kilowatt-hour consumption has been cut nearly in half.

All told, Judy's energy management has reduced the Denton Federal Building's electric bill to about one third. "After three months of operation, Judy has saved the taxpayers $\$ 5500$ over the same period last year," says building manager Robert Kuykendall. "She's saving $\$ 1800$ a month in energy costs alone; at this rate, she will pay for herself within a year."

Judy's SC/MP No. 2 secures the building by controlling access

# What this country needs is a good $\$ 39$ DPM. 

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Please send me $\qquad$ AD2026 for testing and evaluation at the low (1-9) price of \$62. (Enclose check or money order.)
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$\square$ I'm not ready to order my AD2026 DPM for testing and evaluation, but I would like to receive all available technical information.
$\Gamma$ - - - - -




The AD2026 conserves on space, too. Its small front panel size of $3.4^{\prime \prime} \times 2.0^{\prime \prime}$ and only $0.64^{\prime \prime}$ needed behind the panel makes it smaller than $31 / 2^{\prime \prime}$ scale APMs. But its performance outclasses 41/2" APMs.

When it comes to reliability, the AD2026 is unsurpassed. Its $I^{2} L$ technology combines most of the active analog and digital circuitry on one chip. The AD2026 has only 14 components and a MTBF of 250,000 hours at $25^{\circ} \mathrm{C}$. In a 24 -hour-a-day application, you shouldn't expect a failure for 28 years.

A new commercial tester automatically tests all AD2026's for defects such as bad components and solder shorts. It also fully tests both the LSI chip and the complete DPM. Following 168 hours of failure free burn-in, the units are again $100 \%$ tested.

The AD2026. Its low price ( $\$ 39 / 100 \mathrm{~s}$ ), small size, superior performance, and remarkable reliability make it the only sensible alternative to APMs. Which is just what this country needed.

Check it out. Return the coupon with your check or money order today to order an evaluation sample at the low 1-9 quantity price of $\$ 62$. And when you receive your evaluation samples you will also receive a Credit Certificate for $\$ 23$ redeemable when you place your order for the first hundred or more AD2026's.

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through as many as 256 doors, including the door to its own room. Each door is equipped with its own $\mu \mathrm{P}$ and a hexadecimal keypad.

To get into the building or any of the secured rooms within it, an individual must enter the correct prefix code, respond to a pattern of unidentified lights, enter a valid identification code and be in the correct time window. A certain number of honest mistakes is permitted, but if the door $\mu \mathrm{P}$ suspects the person outside doesn't know what he or she is doing, it notifies the SC/MP 2, which activates an alarm.


Distributed $\mu \mathbf{P}$ system can grow to $250 \mu$ Ps. Each section has its own RAM, PROM and job to do.

If the individual meets all entrance criteria, he or she is admitted, and this information is printed at a security station 50 miles away in Dallas. The inside of the main door also has a keypad that will log a person out when he leaves again.

To save cost, Judy has no teleprinter or ASCII keyboard. She is equipped with a panel of pushbuttons and LEDs (see photo)-this panel is the operator interface. With this panel, the operator can alter Judy's programmed strategies in both energy-saving war games and building security.

All programs reside in system firmware. Judy's language is a process-control language invented around Basic. It is a Command Line Mnemonic Interpreter-a high-level language for the operator who is unfamiliar with computer programming. LEDs repre-
sent canned queries to the operator, and pushbuttons represent input mnemonic responses.

Judy's third microprocessor is her print formatter. It receives and buffers messages from any part of the microprocessor network in Denton and formats the text for a 40 -column printer. A large font is provided for emergency messages, and a smaller font for routine messages. The formatter queues up the prioritized messages for transmission over telephone lines. The messages can be sent to any of 256 locations equipped with a telephone, modem and 40 -column printer.

Any event taking place at Judy's installation in Denton is reported to "George," a minicomputer in Dallas 50 miles away. They dial each other automatically, "talk" over regular phone lines and leave hard-copy memos to each other.

Microprocessor No. 4 is responsible for sending Judy's messages out to George or other remote locations. Because the telephone lines used might be low-grade, Radix II combined error-reduction and correction techniques-parity bits, cyclical-redundancy-check characters and Fourier transformation of the transmitted data.

## Judy, call George

Once SC/MP 4 has received a message for transmission, it goes about its assigned task relentlessly. Using a Bell 801 Automat Call unit, it repeatedly dials the receiving station until it gets connected.

The data to be transmitted are then provided with odd parity bits. A cyclical-redundancy-check character is generated and appended; finally, the message undergoes a firmware-resident Fourier transformation to desensitize it to transmission errors. This transformation converts the message from the time domain into the frequency domain, so that, for a 1024-bit message, each bit represents only $1 / 1024$ of its actual value. Thus, Judy's communications link is capable of tolerating a significant error rate.

If the message cannot be reconstructed by the receiving station or it is garbled, Judy requests subsequent transmissions until the message is received correctly. - -

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# Storing energy for peak power is a job for 'super' batteries 

Today, when more power is needed than a utility company's basic system can provide, intermediateload facilities are switched on. This equipment-usually fossilfueled, for making steam-is generally older, less efficient, and thus more costly to operate. For real peak-load demands-when both the basic and intermediate systems are close to being swamped-equipment that is older still and even move expensive to operate must be used. As a rule, this equipment is also fossil-fueled.

The solution to the peak-load problem, say members of the Electric Power Research Institute (EPRI) in Palo Alto, CA, is to store energy in large arrays of "super" batteries.

Energy can be generated at night during hours of low demand by the more economical basic pow-er-generating equipment, then stored in the giant batteries to augment peak power demands the next day.

Eventually, super batteries may handle up to $5 \%$ of the nation's

John F. Mason
Associate Editor
total electric energy needs and up to $17 \%$ of peak-time electricity, according to a study supported by EPRI and the Energy Research and Development Administration (ERDA). (EPRI was formed by the nation's power utilities in 1972 to carry out programs to improve the production of electric power.)

## Get them together

To supply a large amount of power, super-battery modules might be clustered like cars in a parking lot. With an individual storage capacity of, say $3 \mathrm{kWh} / \mathrm{ft}^{3}$, it would take $3000 \mathrm{ft}^{3}$ of these batteries to produce $100,000 \mathrm{kWh}$ of power.

A unit about the size of a file drawer, which is now being developed by General Electric, will have about 10 kWh . A standard lead-acid battery of the same dimensions would be hard pressed to provide a storage capacity of 5 kWh. Several of GE's units will be put into a desk-sized module capable of storing more than 100 kWh of electricity.
"We can't say how much these batteries will save the utilities in
every case," says EPRI's Dr. Fritz Kalhammer. "That depends on the cost of fuel and the efficiency of the peak-load equipment. We do know, though, that the savings would be considerable."

By the year 2000, according to utilities spokesmen throughout the country, storing energy won't be just a shrewd money saver. It will be required. The use of electricity in the United States will probably shoot up to 3.8 times the quantity used now, while the use of oil for generating this power will drop by a third. As a result, an estimated 1000 -billion kWh of peak power will have to be fueled annually by alternative means such as wind and sun, both of which will have to be stored. And whatever the source, some of the energy will have to be stored to handle peak loads.

According to the Institute, facilities will be needed for storing up to 100,000 MW of energy that can be discharged, on the average, for 2000 hours a year.

## The arguments for batteries

Batteries are uniquely versatile and rapid in their response to elec-


When the zinc-chlorine battery is charged, zinc is released and deposited on graphite electrodes. Released chlorine is moved to a chamber where it is cooled and stored in hydrate form. While the battery is discharging, chlorine released from a mix of chlorine hydrate and

water is pumped back into the graphite-electrode compartment. The zinc is then dissolved by the chlorine to form zinc chloride in an ion exchange that releases electrical energy. A 300 -kilowatt-hour battery will be tested this year with a 100-MWh battery to be built in 1980.


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tric systems' needs, says James R. Birk, EPRI's project manager for advanced-battery development. They can respond to demands within minutes and match the incremental changes in load with no penalty in efficiency. By contrast, a standby generator's efficiency is best only at one output.

The use of batteries to store power is attractive to environmentalists. Regardless of their size, batteries make little, if any, noise. They create almost no emission, and don't need extensive excavation. Moreover, neither fuel nor waste would have to be carried to and from battery sites. Even the land requirement would be relatively modest: about a half-acre for a 20 MWh battery storage installation.

The principal candidates for playing a role in tomorrow's utilities are four battery types now being developed, and one-the leadacid battery-that's nearly a hundred years old, says EPRI's Birk. The four advanced batteries are sodium-sulfur, sodium-antimony trichloride, lithium-metal sulfide, and zinc-chlorine. While development proceeds on these, the old lead-acid battery is being redesigned to see how well it fits into a load-leveling role.

Work on all five batteries is funded in varying amounts by both EPRI and ERDA.

## No battery is perfect

The lead-acid super battery is too expensive for widespread appeal to the electric utility industry, EPRI says. And by utility standards its life is limited-about 10 years. Nevertheless, the battery is rugged and reliable. By 1979, a lead-acid system will be ready for testing in the Battery Energy Storage Test (BEST) facility, which will be built by EPRI and ERDA in Hillsboro, NJ, to test all the advanced batteries now being developed for electric utilities. The lead-acid battery will be ready for commercial introduction between 1981 and 1983. At least five companies are working independently -and competitively-toward these goals.

The advanced batteries differ from the lead-acid battery in several ways:

- They operate at higher tem-

peratures-lead-acid operates at 20 to 30 C while the advanced batteries operate at from 50 to 450 C .
- Their energy and depth of discharge are both greater.
- Their cell size is much smaller.
- Their active materials are cheaper.

High operating temperatures are good and bad. They're beneficial because they permit the liquidelectrolyte materials to be used instead of solid materials, which are subject to morphology changes (sometimes called sluffing of materials). In time, the active material in solid electrodes deteriorates.

Liquids, on the other hand, have no hysteresis effect; a liquid doesn't remember from one cycle to the next what happened, thus making it possible, theoretically at least, for the electrolyte material to have a very long life.

High temperatures are bad because they accelerate corrosion. The materials used become more reactive.

## The four other candidates

The sodium-sulfur banner is being carried by three groups: General Electric, sponsored by EPRI, and Dow Chemical and Ford

Motor, both sponsored by ERDA.
A sodium-sulfur battery, whose materials, according to EPRI, may very well cost the least of the four advanced types, operates between 300 C and 350 C . It uses a solid (ceramic) electrolyte of beta alumina (made of sodium, aluminum and oxygen), which separates the molten sodium and sulfur electrodes.
"Coming up with this highquality electrolyte material has been a major step forward for the sodium-sulfur battery," Birk says. A year ago it wasn't possible to make use of more than $50 \%$ of the sodium-sulfur cell's storage capaci-ty-"what people sometimes call 'depth of discharge'," says Birk. "Now the figure is consistently 85\%."

Also, the cell life has been boosted to more than 8000 hours of continuous charge and discharge.

Currently, GE's lab cells-each about $1 \mathrm{in} . \times 8 \mathrm{in}$.-can store 32 Wh. But considerable engineering design will be needed to scale the battery up to $100-\mathrm{kWh}$ modules, Birk notes-each expected to employ 350 individual cells. "By 1981," he continues, "we hope to have GE install and test 50 modules in a 5 -MWh system at the BEST facility."


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Meanwhile, two problems remain to be solved: Deterioration of the glass seals, which react with sodium, and the corrosiveness of sulfur, which reacts with the metal container housing the electrode. If the problems can be cleared up, a full-scale battery module will be fabricated and tested in 1978.
Instead of beta alumina, Dow Chemical uses a very thin glass capillary as a solid electrolyte, 70 microns in diameter, with a wall thickness of 10 to 15 microns. "Both beta-alumina electrolyte and the glass capillary look very promising," Birk says.

Sodium-antimony trichloride, which is being developed by ESB, Inc., is a kind of offshoot of the sodium-sulfur battery. It's got the same sodium electrodes and the same beta-alumina electrolyte. But instead of sulfur in the positive electrodes, it uses a mixture of sodium chloraluminate and antimonic trichloride. The idea behind choosing the materials, Birk says, is to use something that melts at a lower temperature than sulfur and thereby avoid some of the problems, like heightened corrosiveness, that accompany high temperature. The sodium-antimony trichloride battery operates at 200 C , not 300 to 350 C .

The seal is also different. Instead of glass, silicone rubber is used, which, according to Birk, is easier to work with. Furthermore, the battery type's cell-operating potential, 2.6 V , is the highest of the four advanced types.

But there are negative tradeoffs as well. Lower temperature means the electrolytes have lower conductivity, therefore must operate
at lower current densities-less milliamps per square centimeter. So to get more current, the electrolyte area must be enlarged, which, in turn, jacks up the cost. As a result, according to Birk, the sodiumantimony trichloride battery costs more than the sodium-sulfur. The availability of antimony itself is even in doubt.

The lithium-metal (iron) sulfide battery has a long life ( 1000 cycles, to sodium-sulfur's 400 ), but operates at a high temperature ( 400 to 450 C ). Another minuslithium is not only expensive but scarce.

## Lithium systems on the way

Nevertheless, four giant organizations are developing lithium-metal systems and hope to have them tested at the BEST facility by 1981: Atomies International, Rockwell International, Argonne Na tional Laboratory and General Motors.

Since 1972, these manufacturers have been shifting gradually from liquid-lithium and sulfur electrodes to solid lithium-silicon and iron-sulfide electrodes. This change is necessary to eliminate capacity degradation caused by electrode materials migrating or dissolving into the electrolyte.

Atomic International reached a milestone last year by operating a $150-\mathrm{Wh}$ cell for nearly 1000 cycles and 10,000 hours. AI has now tested a $1-\mathrm{kWh}$ cell, and is building a $2.5-\mathrm{kWh}$ full-scale, load-leveling cell.

Zinc-chlorine, which operates at near ambient temperature ( 50 C ) and uses a water-based electrolyte,


Rows of battery modules will store electric energy generated at night during hours of low demand, and discharge it during periods of peak load.
will probably be the first of the big four to make it to the BEST test facility, EPR!'s Birk believes.

The battery is complex, he adds, because of its flowing, water-based electrolyte, and its external storage of chlorine-aspects which may lead to a cost penalty. The system is, however, farther along in development than the others and has performed well in sizes larger than any of the advanced batteries.

Under development since the late 1960 s, the zinc-chlorine battery got a shot in the arm last month when EPRI awarded a contract for more work to Energy Development Associates (EDA), a joint venture of Gulf \& Western Industries Inc., and Occidental Petroleum Corp. The two companies already had spent $\$ 10$-million of their own funds on the project. Under the terms of the new contract, they will invest $\$ 3.8$-million more, which EPRI will match.

The goal is to design, develop and fabricate a $10-\mathrm{MWh}$ zincchlorine system. An efficient 1 kWh battery has been tested successfully by EDA and cycled 100 times. "On a four-hour charge/ four-hour discharge regime, the zinc-chlorine prototype battery demonstrated a $78 \%$ energy efficiency with no degradation in performance over the first 100 cycles," says A. A. Guffey, executive vice president of Hooker Chemical, a subsidiary of Occidental Petroleum.
"During 1976, we scaled up in battery size and are now testing a 20 -volt, 20 -kilowatt-hour system. This year, we plan to build a $100-$ volt per 100 kilowatt-hour battery module for evaluation as the basic building block of megawatt-hourlevel systems. We also will assemble three of these 100 -kilowatthour modules in 1977 into a 300 -kilowatt-hour battery to demonstrate the interfacing of the modules for test next year," Guffey notes.
"Our 1978-79 commitments call for assembling a 10 -megawatt-hour system for testing during 1979 and 1980 at the BEST facility. The evaluation will be conducted at the Public Service Electric and Gas substation in Hillsboro, NJ. With success at BEST during 1979, the first 100 -MWh battery array will be built in 1980," the Gulf \& Western executive predicts. ■■


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Taking advantage of a microprocessor's flexibility, speed, and low cost, ECD Corp. of Cambridge, MA, has built a general-purpose computer with a base price of $\$ 987.54$. Built around MOS Technology's 6512A microprocessor, the MicroMind is designed not only for personal computing, but also for such OEM applications as environ-mental-chamber controls, text editing and animated sign controls.

The MicroMind package includes a keyboard, CPU, display processor and video interface, along with the necessary power supplies, system documentation, and software package. An interconnect bus system is designed for expandability and multiprocessing; up to 16 microprocessors can be connected to the bus for parallel processing.

The CPU board contains 8 kbytes of dynamic RAM and sockets for an additional 8 kbytes. Its memory capacity can be expanded by adding 32 -kbyte memory-expansion boards. And even though a 16 -bit-address microprocessor like the 6512 A can only address 64 kbytes of memory directly, a memory-mapping option allows the MicroMind to have a total addressable memory area of 64 Mbytes.

It's in the keyboard and display processors that the MicroMind
really benefits from its added flexibility. The display processor provides two point-plotting graphics formats on an external CRT, such as a video monitor or television set. The map format displays a bitmap pattern with a 128 by 160 point format. Each picture element corresponds to a bit in memory. The character-display format displays each character in a dot matrix. In this mode, a text memory holds the data to be displayed, and a font memory contains the definition of each character.

By using a RAM font memory instead of a ROM character generator, the MicroMind system can display any alphabet, character set or type face. Each character is defined by the appropriate set of dots within a 8 -wide and 12 -high dot matrix.

The MicroMind keyboard has 80 keys, each of which has a function defined by software. To match the flexibility of displayed characters, the key caps can be relabeled.

Additions to an improved MicroMind now being developed at ECD include an extension of BASIC high-level programming language -called notso Basic. A line printer to provide hard-copy printout from the system is also in the works. - $=$


The MicroMind package is a general-purpose computer system that includes a 6512A microprocessor, a character and graphics display processor, an input-output interface board, power supply and keyboard.


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## 

## Weapons costs approach $\mathbf{\$ 2 0 0}$-billion

Major increases in weapons procurement in the 1980s-such as two more Trident submarines and a doubling of F-16 fighters to 1368 -have been revealed in the latest Selected Acquisition Reports (SAR) submitted to the Congress by the Pentagon. The additions, which also include more FFG-7 frigates, F-14A fighters, P-3C patrol aircraft and E-2C airborne early-warning aircraft, drive up the projected cost of the 45 largest military systems to nearly $\$ 200$-billion. That's the biggest cost growth revealed in the quarterly SARs since 1970.

Scheduled to be procured in fiscal year 1984, the two additional subs will raise the number in the Trident program to 13 . Each will be equipped with 24 MIRVed missiles. Four Tridents are currently under contract with General Dynamics Electric Boat Div. The missile, produced by Lockheed Missiles \& Space Co., made its first two flight tests in January and February.

The Trident subs are expected to assume increasing importance as a U.S. strategic deterrent if President Carter carries out his plan to delay the B-1 bomber and M-X missile programs by a year or more.

## NASA eyes electronic advances

A synthetic aperture radar (SAR) image processor, a multispectralscanner data processor, and a family of microprocessors for future spacecraft are three of NASA's new electronic-development programs planned for fiscal 1978, the space agency has told Congress.

The SAR image processor would use digital charge-coupled devices to convert $120-\mathrm{Mbit} / \mathrm{s}$ satellite data into images in real time and, according to NASA, would reduce the cost per image from $\$ 12,000$ to $\$ 280$. (An analog CCD processor is being considered for the Landsat earth resources satellite.)

## Space station to cost up to \$3-billion

A minimum-capability space station based on present technology could go into operation by 1985 for $\$ 1$-billion to $\$ 3$-billion, reports NASA's two study contractors on the project, Grumman Aerospace Corp., and McDonnell Douglas Astronautics Co. Each company has a $\$ 700,000$ conceptformulation contract.

A basic station capable of accommodating a crew of four or six will cost $\$ 1$-billion, according to preliminary findings. Another $\$ 2$-billion will be required for conducting specific missions in low earth orbit, such as
processing materials, erecting large structures in space, generating power, conducting life-sciences experiments, and advanced communications. The station will be put into orbit in modules by the reusable Space Shuttle.

## NBS finds VSWR variations in connectors

Subminiature, Type A (SMA) coaxial connectors-the most commonly used types in military and communications microwave equipment-have excellent repeatability up to 18 GHz , but the voltage-to-standing-wave ratio (VSWR) varies drastically among connector types, according to tests conducted by the National Bureau of Standards.

Test values of VSWR, which measures impedance mismatch between cable and connector, ranged from 1.1 to 1.03 at 4 GHz and from 1.2 to 1.5 at 18 GHz . Typical insertion loss varies with frequency, and is equivalent to 1 and 2 cm of Type 141 coaxial line length at frequencies up to 12 GHz , and increases to the equivalent of 10 cm of line length at 18 GHz .

## Flying command post due in 1979

The Air Force's first full-capability Advanced Airborne Command Post, a Boeing 747 outfitted with uhf and shf communications-satellite terminals as well as its standard communications equipment, is now expected by the summer of 1979. The first of these aircraft, dubbed the E-4B, has undergone flight tests by prime contractor Boeing and is being outfitted with the satellite terminals by E-Systems of Greenville, TX.

The tested E-4B will join the current fleet of three E-4A flying command posts, which lack the E-4B's satellite communications capability. The aircraft are intended to be used by the President and Secretary of Defense to direct military operations from the air in case of nuclear attack. Two more E-4Bs will be delivered before the companies upgrade the current $\mathrm{E}-4 \mathrm{As}$ to the full-capability configuration. The whole program is due to be completed in 1983 at a cost of nearly $\$ 150$-million per aircraft, which makes the flying command post the most expensive aircraft in history.

President Carter last month became the first President to fly in an E-4A. President Nixon had once flown in the earlier model EC-135 flying command post.

Capital Capsules: Even though it isn't operational yet, the Air Force's Cobra Dane phased-array radar was used to track the recent Soviet missile tests in the Pacific. The system is currently being installed by Raytheon on Shemya Island in Alaska, at the western tip of the Aleutian islands and just 450 miles away from the Soviet Union. The system reportedly can also detect and track 200 objects in space simultaneously. . . . One of the Carter administration's first opportunities to spell out its future procurement plans for industry will be at the Electronic Industries Association's annual "Doing Business with the Government" seminar in Washington March 15 to 17. Also on the agenda is how the new A-109 procurement circular standardizing federal procedure will impact industry. . . . Despite opposition from local environmentalists, Michigan's Upper Peninsula remains the Navy's first choice for its Seafarer ELF communications system. The draft environmental impact statement has been completed and public hearings are planned in late March and April.

# New from Centralab... 

# IMPS PUSHBUTTON switches 

## A new miniature modular building block system that offers microprocessor control designers more of what they need.



To meet the special digital and analog needs of today's $\mu \mathrm{P}$-based controls, Centralab offers design engineers a whole new system of modular pushbutton switch building blocks. We call it IMPS - Integrated Modular Panel System. IMPS saves PC board and panel area and simplifies front panel design, cuts assembly costs, reduces back-panel space requirements, and meets the digital-analog needs of $\mu \mathrm{P}$-based controls. Check these space saving, cost-cutting features.

## Simplify front panel interface.

All IMPS switches regardless of function, are uniform in size, simplifying design and selection of front panel hardware. They have high volumetric efficiency, occupying .505" $\times$ .388" PC board area and require only $.608^{\prime \prime}$ of space between PC board and
 front panel.


IMPS switches may be mounted on the front panel, and are designed for automatic wave soldering installation and PC board cleaning. Insert molded terminals prevent flux and solder wicking and contact contamination. Integral PC board stand-offs provide for efficient board cleaning.

## Meet analog and digital needs.

IMPS switches are available with momentary, push-push and interlocking actions, with a long-life contact system that switches both digital and analog signals. To accommodate critical signal requirements, housings are highinsulation molded plastic with UL 94 V -0 rating.

## Available options.

Optional installations include ganged assemblies, front-panel mounting and wire-wrapping.


All IMPS pushbutton switches are built to Centralab's highest quality standards (see specifications at right). They're priced as low as 41 cents in 1,000 quantity. For full technical details, samples and quotation, call (515) 955-3770, or write to the address below.


Electronics Division GLOBE-UNION INC. P.O. BOX 858 FORT DODGE, IOWA 50501

## Built To

 Centralab Quality Specs.IMPS Pushbutton Switches combine compact size, low cost and highest quality throughout.

- Silver or gold inlay wiping contacts for long-life and lowcontact resistance.
- Less than 2 milliseconds contact bounce.
- SPST, SPDT, DPST, and DPDT switch contacts.
- Printed circuit, DIL socket or wire-wrap terminations available.
- 2.5 to 3.5 oz . actuation force (momentary).
- Choice of button interface square or blade shaft (shown) - permits use of a variety of Centralab and industry standard buttons and keycaps.
- $10,15,20$ or 25 mm center-tocenter spacing.


## New snap-in rockers with Cutler-Hammer reliability.

Here's a completely new line of snap-ins, each engineered with the kind of solid dependability you expect in Cutler-Hammer Rockette ${ }^{\circ}$ switches. Bright metal bezels, illuminated and non-illuminated, A-c and D-c capabilities up to 20 amps .

Switches snap in and stay in permanently. Speed up assembly time, cut costs.

Flush-mounted rockers in the same wide range of designeroriented colors and styles. One- and two-pole models.

Illuminated single-pole rockers. Choice of red, green, amber, white, or clear. Hot-stamped legends indicate switch functions.

For more information, call your Cutler-Hammer Sales Office or Switch Distributor.

Sub-panel rockers in a variety of colors, rocker or paddle designs in standard, special, or proprietary models.

How to get the benefits of CMOS in your static RAM sockets:


It's truly simple. Our 1 Kxl SY5102 static RAM is a pin-compatible CMOS replacen for the popular but power hungry 2102. With our new 5102 you can just replace parts in your existing designs and immediately cut power - both operating and standby. And the standby requirement is only $l$ (one!) mW at 5 Volts. No power-down circuitry needed.
Whenever our 5102 is not enabled, it's in standby. You can use power-down circuitry if you want, but it's not required. Terrific for systems that use battery backup. It runs at 5 V , keeps memory alive at 2 V !


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CIRCLE NUMBER 35
durability far superior to evaporated coatings. It is applied at relatively low temperatures, so the process has no effect on the flatness of the glass substrate. It can be readily etched to create diagrams, letters, numbers, or any other design that suits your purposes.

The other PPG electronic glass is $\mathrm{Nesa}^{\circledR}$ glass. Because of its durability, it is ideally suited for use in electronic controls, and offers you the opportunity to abolish buttons, knobs, dials, and switches.

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PPG: a Concern for the Future

# Last week Helen Kratzer had visitors from HamiltonJunior Corn had a new calfand Wabash made one million coils. 

It was a typical week in Wabash, Edna Fitch and Gladys Sands made 4 pots of their famous chicken noodle soup for the church supper, Tommy Butcher had to stay after school again and things were humming over at the Wabash plant.
Coils, ranging in size so small that $\mathbf{1 5 0}$ can be placed in a teaspoon to some weighing over 10 lbs . were moving off high speed production lines.
No wonder. Wabash is the country's largest maker of molded coils including epoxy, nylon,
and engineered thermoplastic and thermoset material with over 20 standard epoxy formulations and hundreds more that can be adapted for specific application such as heat, cold, rain, salt and sun.
Things change quickly nowadays. But two things you can count on. Whether it's next week or next year, Edna and Gladys' chicken soup will still be the best around-and Wabash will still be the nation's leader in coil manufacturing.

# THE OEM CONNECTION 

When you do business with SAE, you're well connected. We make edgeboard connectors, flat flex cable interconnection systems, switches, logic panels, backplanes and IC sockets. And that's just the beginning.

We also supply complete wiring and subassembly services, printed circuits, card files, MIL-C-5015 and other extreme environment connectors, transformers, chokes, delay lines and RF filters.

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tronic OEM hardware, ready to quote and deliver anything from a wide selection of individual components to a completely assembled interconnection system.

Component specs are in our catalog, and guidance on ways to save time and money is as close as an SAE sales rep. If you haven't yet made The OEM Connection, do it now. Write Stanford Applied Engineering, 340 Martin Avenue, Santa Clara, California 95050. Phone (408) 243-9200. TWX 910-338-0132.


Plants at Santa Clara and Costa Mesa, California, West Babylon, New York and Boulder, Colorado.

## Should you use static or dynamic RAMs? Only the system can say for sure

When designing a microprocessor memory system, the choice of a RAM is more than a simple question of dynamic versus static. "It's no use comparing dynamic and static data sheets," says Daryl Koker, memory applications manager at Mostek. "It isn't a device comparison at all-it's a system consideration."

While dynamic RAMs offer greater bit density than static RAMs- 1 sq. mil per bit versus 4 to 5 sq. mils-and are cheaper, there are other more important parameters to consider before committing to one family or the other.

The cost of power now runs about $\$ 1.25 / \mathrm{W}$, and dynamic memory systems use only one-third the power of static memory systems. But, as always, to avoid that cost, some complex peripheral circuitry must be added to the dynamic system.

Peripheral circuitry cost is essentially a step function-design and build it into a system just once, and it will accommodate a wide range
of memory system sizes. But the cost of power increases linearly with memory size.

In a small system, designing and providing peripheral logic to support dynamic chips may be very expensive. Interfacing dynamic memories to the rest of the system can also be complex: Three separate power supplies are usually needed ( $+12 \mathrm{~V}_{\mathrm{dd}},+5 \mathrm{~V}_{\mathrm{cc}},-5 \mathrm{~V}_{\mathrm{bb}}$ ) and designing the address-scanning and clocking logic can be time-consuming.

Static memories, however, are ideal for small systems. Directly TTL-compatible, they often require just a single $+5-V$ power supply and need only to be addressed by the central processing unit (CPU). And the design effort is quick and easy.

On the other hand, says Koker, large memory systems simply can't justify the power required by an array of static devices. Hardware is expensive, dissipated heat is adverse to reliability,
(continued on page 52)

## Computer-in-a-book easily expands to complete system



Originally designed to serve as an instructional course for engineers and/or hobbyists, the Iasis ia7301 computer-in-a-book will soon be expanded into a complete microcomputer family. Additional memory and interface cards, capable of increasing capacity from 1 to 65 kwords of memory and from 2 to 256 I/O ports, are now available. Soon to be announced are cards for CRT-display driving, full keyboard interfacing and floppy-disc interfaces.
The ia7301 consists of a 250 -page programming course, an 8080 -based microcomputer and a hex keyboard-all in a standard three-ring looseleaf. The $\mu \mathrm{C}$ board holds a cassette-tape interface, 1 kbyte of RAM, 1 kbyte of PROM, 8 seven-segment LED displays, 3 indicators and a 25 -key keypad.

Available completely assembled and tested, the ia7301 also includes a hex-conversion card and a machine-language coding pad. The computer requires +5 and +12 V , and all interfaces are made via a 28 -pin edge connector. The computer-in-a-book costs $\$ 450$, and delivery is 4 weeks.
Iasis, 815 W. Maude Ave., Sunnyvale, CA 94086. Charles Hornisher (408) 732-5700.
CIRCLE NO. 417

## MICROPROCESSOR DESIGN

(continued from page 51)
and end users are increasingly conscious of the cost of power.

Dynamic memories run cooler than statics and, since they can be packed more tightly together, save on costly board area. The largesystems manufacturer strives to get the highest possible memory size-to-package ratio and, once the peripheral circuits have been designed and built into a memory, can add address multiplexing logic to accommodate packages of ever-increasing density.

Because of lower over-all hardware cost, dynamic memories gravitate toward bulk-storage applications. And since static memory systems cost less to design, they are more efficiently used in applications requiring a minimal hardware outlay.

Traditionally, RAM manufacturers have addressed their biggest markets first-the large-system houses that buy high volumes of dynamic RAMs. Low-volume static-RAM users must wait for the higher-density memory products.

Today, 16-kbit dynamic memories, like Mostek's MK 4116, are available in production quantities. But for someone working with static parts, the maximum density currently available is only 4 kbits. However, since dynamic RAMs usually precede static-RAM development, many

static and dynamic 4-kbit RAMs are currently available.

In addition to the present repertoire of clocked static RAMs and self-refreshing dynamic devices, a number of schemes will be introduced to combine the ease of static RAM system design with the power-saving aspects of dynamic RAMs, predicts a spokesman for Texas Instruments.

For example, Texas Instruments will bring out 4 -k static RAMs, the TMS 4046 and 4047. These products have split internal power requirements-the actual memory array is powered independently of the decode, read, write and chip-enable circuitry. Most of the overhead power is eliminated, and the device's memory array can be kept "alive" with only a few milliwatts per package, versus hundreds of milliwatts for currently available static RAMs.

## Program 6502 systems with resident assembler/editor

A resident-software-assembler program, the DATA1K, provides 6502 -based systems with full assembly-language capability. The DATA1K assembler/text editor requires about 4500 words of RAM and comes on either papar tape or magnetic cassette. Available at a cost of $\$ 250$, which includes a one-year update service, the DATA1K is shipped from stock. Johnson Computer, P.O. Box 523, Medina, OH 44256. Kevin Johnson (216) 725-4560.

## 16-channel multiplexer handles synchronous/async data



Handling up to 16 asynchronous or synchronous digital data channels-or any mixture of both types-the $\mu \mathrm{P}$-based M1318 Multitran multiplexer lets remote job entry terminals, synchronous video-terminal controllers and interactive asynchronous terminals communicate over a single voice-grade telephone line. The M1318 may connect terminals via dial-up or dedicated modems, short-haul data sets, or direct EIA cabling.

Aided by an 8080 microprocessor, the M1318 also provides optimum multiplexing capabili-
(continued on page 54)


In a world where first impressions mean so much, why settle for esthetically (and functionally) inferior pushbutton switches, when you can choose EAO pushbutton controls at competitive prices? EAO holds the line on price AND quality. With EAO on your panel you don't have to accept second-class appearance or performance ... hot spots, fuzzy legends, tinny-looking bezels, uncoordinated configuration and size, and faulty human engineering. EAO controls enhance your product.

And EAO gives you complete design freedom. You can select mechanical or electronic switches . . . combine up to 9 lens colors with bezels in rectangular, round, square or dual
rectangular shapes ... choose switch and contact combinations from 1 to 4 poles momentary or alternating action... even harmonize the full range of lighted and nonlighted pushbutton controls with compatible key lock designs.
Available contact arrangements and contact materials have ratings from low-level to 10A @ 250 VAC. No matter how complex or wide-range your needs, EAO Status Symbols allow you to singlesource all requirements.

Write or call today for samples, technical data, and prices on these and other Unimax PB switches. Unimax Switch Corp., Ives Road, Wallingford, Connecticut, 06492; Tel.(203) 269-8701.

## MICROPROCESSOR DESIGN

## (continued from page 52)

ty for small-to-medium sized networks. The byte-interleaved M1318 can handle asynchronous low-speed inputs at data rates of $75,110,134.5,150,300,600$ and 1200 bps , on either dial-up or dedicated lines. It can also accommodate synchronous high-speed data rates of $1200,2400,4800$ and 9600 bps .

The basic unit consists of an interface module, chassis, cabinet and power supply and measures $9.5 \times 18 \times 13.8 \mathrm{in}$. A complete 16 -channel, point-to-point multiplexing network between a terminal cluster and a computer center with mixed synchronous and asynchronous inputs, costs approximately $\$ 10,000$.
Computer Transmission Corp., 2352 Utah Ave., El Segundo, CA 90245. J. Robert McConlogne (213) 973-2222.

CIRCLE NO. 419

## Minimize programming with $\mu \mathrm{P}$-controlled laser-trim system

For the engineer who wants to maintain a dialogue with a laser trimmer without resorting to time-consuming computer programming, a $\mu \mathrm{P}$-controlled system permits working programs to be stored and retrieved easily with a dual floppy-disc memory. The Model 1080 from Quantrad, El Segundo, CA, comes with software that is compatible with interactive and adaptive programming languages and is fully operational with IEEE/ASCII instrumentation interface.

Considered a low-cost approach to the high production of thick and thin-film resistor trimming, the system combines a krypton-pumped Q-switch YAG laser with closed-loop galvanometer beam-positioning at a basic price of $\$ 85,000$.

The $\mu \mathrm{P}$ in control is the 8080 -chosen, Quantrad's chief scientist says, because it was the only multiple-source $\mu \mathrm{P}$ around when the system was being designed, and because of the $\mu \mathrm{P}$ 's wealth of available software.

For active or functional trimming, the Model 1080 is capable of full computer control and instrumentation interface. Critical operational parameters of the laser are displayed on the monitor in real time, such as laser-pump current, laser power and, as safety measures, the flow rate of the cooling water and the temperature of the water.

CIRCLE NO. 420

## Reduce 8080 program development with flexible microcomputer



Designed to shorten the time needed to design an 8080 -based system, the QMS development system is a flexible design with seven basic boards: an 8080-based processor board, a system-extender board, four different memory boards and an EPROM adapter board. Two motherboards are also available.

The processor board, the QMS 80-1180, contains an 8080 $\mu \mathrm{P}, 2$ kbytes of RAM, 1500 bytes of PROM, an RS-232/20 mA interface, a $1-\mathrm{MHz}$ clock and baud-rate selector, and a comprehensive monitor program. The extender board, QMS 80-1401, provides a fully mnemonic direct assembler that does instruction decode, tables, program printout, program trace, ASCII manipulation and priority interrupts. RAM boards are available in capacities ranging from $4 \mathrm{k} \times 8$ to $16 \mathrm{k} \times 8$ in Models QMS $00-1250,1230,1201 \mathrm{~A}, 1201 \mathrm{~B}$. The EPROM adapter board, which plugs into the PROM


SPRRAGUE TYPE 194D solidelectrolyte tantalum chip capacitors are available in 8 different sizes to ensure the most efficient use of substrate space in hybrid circuit layout. The smallest size, a tiny $.100^{\prime \prime} \times .050^{\prime \prime}$ x $.050^{\prime \prime}$, has a capacitance range up to $2.2 \mu \mathrm{~F}$ @ 4 WVDC, while larger chips have capacitance values to $100 \mu \mathrm{~F}$.

Completely compatible with mechanized hybrid circuit assembly equipment, without the problems normally associated with flexible terminal lead wires or unprotected anode contacts, MIDGET capacitors can be attached to substrates by dip-soldering, reflow soldering, epoxy bonding, or other conventional methods.

The capacitor elements of Type 194D capacitors are completely covered with a silicone encapsulant to minimize undesirable parametric changes or failures due to mechanical degradation. Operating temperature range is $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$.

For complete technical data, write for Engineering Bulletin 3534 to: Technical Literature Service, Sprague Electric Co., 347 Marshall Street, North Adams, Mass. 01247.

4SE-6103


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- Threaded-neck construction... effectively isolates input and output terminals.
> selected units NOW available OFF-THE-SHELF from your Sprague Industrial Distributor

[^5]SPRAGUE
the mark of reliability.

[^6]
## MICROPROCESSOR DESIGN

(continued from page 54)
sockets on the processor board, permits UV PROMs to be used instead of fusible-link PROMs.

Both three-slot and six-slot motherboards are available for system development. However, the six-slot board, the QMS 00-1501, contains data and address displays as well as many control switches. The three-slot board, the QMS 00-1510, is a bare-bones system with no display and just three control switches. All boards except the EPROM board use 100-pin edge connectors.

Due to international currency fluctuations, prices cannot be printed. But inquiries will be answered. The company is also inviting U.S. distributors to inquire into handling the equipment line.
Quaindon Electronics, Slack Lane, Derby DE3 3ED, England. Telephone 32651. Telex 37163.
CIRCLE NO. 421

## Support circuits added to $\mathbf{8 0 8 0}$ repertoire simplify system design


available from the company.
Three I/O buffer drivers will soon be ready for both 8080 A and general $\mu \mathrm{P}$ applications. The DP8216 and DP8226 are 4-bit parallel transceivers and the DP8304 is an 8-bit bidirectional bus transceiver. The DP8304 will provide high, active outputs to both ports, as well as sink 50 mA on the bus port and 5 mA into low-power Schottky loads. Prices for the circuits start at $\$ 2.90$ (in 100 -unit lots) for the 8212 , and delivery is from stock to 60 days.
National Semiconductor Corp., 2900 Semiconductor Dr., Santa Clara, CA 95051. Chuck Troiani (408) 737-5873.

CIRCLE NO. 422

## Micro Capsules

Cutting corners for high-volume users of its 1802, RCA in Somerville, NJ, has trimmed down the 40 -pin $\mu \mathrm{P}$ so it can fit into a 28 -pin DIP. Tentatively called the 1803 , the new unit will lose some flag lines, an N line, the DMA interface and some other features, but otherwise be compatible with the 1802 . . . A complete microcomputer on a chip is being readied for introduction in late April by Signetics, Sunnyvale, CA. The chip will contain 2 kbytes of mask-programmable memory, have $31 \mathrm{I} / \mathrm{O}$ lines and be program-compatible with the company's $2650 \mu \mathrm{P}$. . . An 8085-based microcomputer kit, under development by Intel, Santa Clara, CA, will be patterned after the company's successful SDK-80 and should be available by mid-year. . . . Look for a complete Basic interpreter to come from Motorola, Phoenix, AZ, in the next month. The interpreter is expected to require 8 kwords and cost under $\$ 300$.

## Need power-switching inductors for switching regulators? TRW/UTC has a stock answer.

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Available from stock in three sizes. Type SRA measures $7 / 8-\mathrm{in}$. OD by $7 / 16-\mathrm{in}$. height; SRB measures $1-3 / 16-\mathrm{in}$. OD by $9 / 16-\mathrm{in}$. height; and SRC measures $1-3 / 8-\mathrm{in}$. OD by 3/4-in. height.

Check your authorized TRW/UTC local distributor for immediate off-the-shelf delivery or contact TRW/UTC Transformers, an Operation of TRW Electronic Components, 150 Varick Street, New York, N.Y. 10013. Area Code: 212 255-3500.

. . . this is a lot of buzzer in a little package! Designed with two pins . . . for PC board mounting and wave soldering. Easy to use for computer terminals, medical equipment, point-of-sale terminals. Rated to 90 dbA at $2.9 \mathrm{kHz} ; 5$ to 30 vdc ; draws just 10 mA maximum current. Sturdy yellow plastic case; 1.625" ( 41.3 mm ) overall diameter. Ask for free catalog and a demonstration.

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for Designers by Edward A. Torrero, \#5777-6, paper, 1975, 144 pp., $81 / 2 \times 11$, illus., $\$ 10.95$.
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Rev. 2nd Ed., by Donald D. Spencer, \#5103-4. cloth, 1976, $320 \mathrm{pp},. 6 \times 9$, illus. $\$ 16.95$.

## 3. FUNDAMENTALS AND APPLICATIONS OF DIGITAL LOGIC CIRCUITS by Sol Libes, \#5505-6, paper, (\$6.95), \#5506-4, cloth, (\$9.95), 1975, 192 pp., $6 \times 9$, illus.

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Computers Work by Donald D. Spencer, \#5861-6, paper, 1974, 160 pp., $6 \times 9$, illus., $\$ 5.50$.
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8. COBOL WITH STYLE: Programming Proverbs by Louis J. Chmura, Jr, and Henry F. Ledgard, \#5781-4, paper, 1976, 144 pp., $6 \times 9$, illus. $\$ 5.45$.
9. MINICOMPUTERS: Structure and Programming, by T.G. Lewis and J.W. Doer, \#5642-7, cloth, 1976, 288 pp., $6 \times 9$, illus., $\$ 12.95$.

## 10. PATTERN RECOGNITION by

M. Bongard, \#9165, cloth, 1970, 256 pp., $6 \times 9$ illus., $\$ 14.90$.

## 11. DIGITAL SIGNAL ANALYSIS by

 Samuel D. Stears, \#5828-4, cloth, 1975, 288 pp., $6 \times 9$, illus., \$19.95.12. BASIC BASIC: An Introduction to Computer Programming in BASIC LANGUAGE by James S. Coan, \#5872-1, paper, (\$7.95), \#5873-X, cloth, (\$9.95), 1970, 256 pp., $6 \times 9$, illus.
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## My friend

Jack is a true friend. Whenever I demonstrate one of my usual signs of brilliance, he's right in there to tell me how smart I am. And if I ever make a mistake, rare as that may be, he lets me know that it was a unique departure from my norm.

Always smiling, always ready with a kind word or enthusiastic praise, Jack is the kind of guy anybody would treasure as a friend.

Charlie is different. Sometimes I can't stand him. If I make a mistake, especially one that might be important, Charlie's always ready to tell me where I went wrong-and why. Yes, I
 know he does it gently and with understanding, but he frequently makes me feel I'm not as smart as I really am. Sometimes, in fact, he blows away the clever reasons I use to justify some of my actions that he thinks are stupid. When, for example, I explained away a nutty action on the grounds that nobody had informed me of company policy on the matter, he gently informed me that ignorance of company policy is not a particularly fine excuse for stupidity. As if I didn't know.

You can well imagine what Charlie said when I explained one of my actions on the grounds of tradition (it was what was usually done in such cases). And he just laughed in my face when I told him I did something because that's what our competitors did. After all, we're living in a hard business world, and we're forced to be a bit clever because our competitors are a bit dishonest-or they would be if we gave them half a chance.

While Jack always lets me know how brilliantly I do things, Charlie frequently lets me know how I could do things better. And that's often painful. I admit that, in some ways, I am a better human being, and I'm certainly better in my profession, as a result of Charlie's chiding. But heck, friends are supposed to make you feel good. Aren't they?


George Rostiy
Editor-in-Chief


## SPECIAL REPORT: MICROPROCESSOR PERIPHERAL CHIPS

With all the recent editorial attention given to microprocessor chips, on memories and central processing units, readers have asked that "equal time" be devoted to devices that make a system work ... to interfacing. Our May 10 report will cover the new breed of complex LSI circuits that are designed to support microprocessors (some of which are becoming more complex than the microprocessor itself). Among the circuits covered will be floppy disc controllers, UARTs, specialized interface circuits, programmable timers and mixed ROM and RAM inputoutput circuits.

## also in the may 10 ISSUE - ANOTHER INSTALLMENT IN OUR MICROPROCESSOR BASICS SERIES: SOFTWARE FOR MICROPROCESSORS

One of the most important subjects to appear in Electronic Design - and one of the most popular - will continue in the May 10 issue. "Software for Microprocessors" is gutsy, important, how-to information. The series assumes no prior knowledge, works systematically with building blocks to give hardware designers fluency in low level machine or assembly language. Software is a vital part of any computer. For a specific application, instructions and hardware/software tradeoffs may determine the microprocessor selection. Don't miss this installment. (The series began in Electronic Design's January 4 issue.)

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Along with the prob-
lems encountered when
specifying and applying
standard-sized switches, miniatures present extra difficulties, particularly during installation. ${ }^{1-5}$ Moreover, the smaller they become, the more they cost. Clearly then, you should specify the largest "miniature" switch the available space can accommodate.

But what's a miniature switch? The definition changes continuously. As the average size of electronic components shrinks, the definition of the word "miniature" changes. Today, double-pole toggle switches with about $1 / 2$-in.-cube bodies, usually with $1 / 4$-in.-diameter bushings, are popularly called miniatures, say experts at Alco. These same switches were called subminiatures, or even ultraminiatures, five or ten years ago. For example, toggle switches like Alco's TT Series with approximately $3 / 8$-in.-cube bodies, although called subminiatures today, may well be the miniatures of tomorrow.

Speaking for European manufacturers, Carl Evington, Product Manager of A. B. Electronic Components Ltd. (Wales, U. K.) defines today's miniature as any switch that occupies a panel area of about $30-\mathrm{mm}$ (1-3/16-in.) diameter. In Evington's view, a subminiature switch occupies about $20-\mathrm{mm}$ diameter; a microminiature (ultraminiature?) 10 mm or less. All of these dimensions include projecting solder clips. There are no hard and fast rules, however, for these categories, and the labels are applied loosely.

## Biggest problem is installation

But whether the units are miniature, subminiature or microminiature, the biggest problem

[^7]stems from improper installation. Seldom are miniature switches hermetically sealed; consequently, immersing or spraying degreasing solvents often carries contaminants into the switch and onto the contact surfaces. Excessive heat applied to the switch terminals often causes the terminals to expand and crack the seals, so more paths are created for the entry of contaminants. In particular, soldering flux enters the switch via wicking action. Neither molded-in terminals nor epoxy seals can provide protection when terminals are overheated.

Flux and other contaminants that end up on the contacts can cause a permanently open circuit, or worse, an intermittent condition. Once contaminants enter the switch, the damage is generally irreversible. But catalogs seldom warn of these dangers or how to cope with them. Miniature switches designed to mount on panels or other places where they are hand-soldered into the circuit should be handled according to the following rules:

- Don't immerse or spray to preclean. Switches should be ready for soldering as received from the manufacturer.
- Use a small iron- 25 to 40 W .
- Use small-diameter solder and a minimum of flux.
- Apply the iron for no more than 2 s .
- Don't solder with the terminals upright.

Observe those rules, and no after-cleaning should be needed.

Ideally, when a miniature switch must mount onto a PC board, the designer should be able to handle the switch as he would any other component. But he can't with far too many switches -even when the switches are adapted with PCmounting terminals and tabs. Usually, the adaptation doesn't go far enough.

Many switches originally designed for hand soldering to the terminals, one at a time, can't take the heat when all the terminals are wavesoldered simultaneously. The switch can't take the heat even when the temperature at each terminal is correct. Not only can internal heat buildup break terminal seals, but it also can distort, relax spring tension, misalign contacts and cause many other problems.

Switches adapted but not specifically designed for PC mounting tend to retain contaminants that enter the switch's housing during normal PC cleaning processes. They can't be cleaned with all the other components. Although such switches can be temporarily sealed for wave-soldering and board cleaning, this extra preparation is costly. And hand-soldering the switch on a completed PC board is also expensive and time-consuming.

Such problems are neatly solved with a twopiece design in Chicago Switch's Mr. Clean miniatures, intended for wave-soldering applications. Only the lower-base half of the switch is

wave-soldered to the PC board; the unit's overcenter, snap-action mechanism is not attached. After a thorough cleaning process, with the contacts completely accessible, the upper half of the switch is snapped by hand to the base.

Other manufacturers, such as Cutler-Hammer's Control Switch Division, make it easy to clean contacts by designing their PC-mountable switches with an "open" construction that allows the cleaning fluid to flow easily into and out of the switch structure and to remove contaminants.

## Keep your options open

After you have considered the special difficulties in installing miniature switches and examined the available switch designs that may help with these problems, you still have three selection criteria to fulfill-size, electrical rating and life. Leave those to the last and you may find that you have locked yourself in.

If you need a 24 -position switch that can carry


Miniature versions of toggle switches abound. Some come with PC-type terminals-both straight and angled -and others come with conventional terminals for hand soldering. Bat handles, short and long handles, and many unconventional custom-handle styles can be obtained. Among the manufacturers who supply miniature toggles are (from top left, counterclockwise) Alco, American Switch, Micro-Switch and Chicago Switch.


Small rotary switches adapted for PC mounting (Standard Grigsby-top left) and enclosed and ganged rotaries (McGraw-Edison, S Series-top center), subminiature rotaries (Chicago Switch, TO-5-left center), tiny thumb or screwdriver-actuated rotaries with BCD coding (AMP 642-1 Series-top right) and miniaturized thumbwheel types (C\&K Components-bottom right) are only a fraction of the large variety of miniature rotary switches available. The sizes, shapes and styles of miniature rotaries are many and continually being added to. But the problems of specifying, selecting and installing them are generally the same no matter what the type is.


3 A reliably for 100,000 cycles and must fit into a small space (say, $1 / 2 \times 1 / 2 \times 1 / 4 \mathrm{in}$.) you have a tough problem to solve. At this late point you can't go to a manufacturer's catalog and pick a switch with optimum specifications. Trade-offs among size, electrical rating and life must be made-the earlier the switch is selected, the wicler the options.

To make effective trade-offs, however, you need more information than you can find in most catalogs. Right off, you should realize that space limitation and the inability to anticipate all possible combinations of uses force the manufacturer to list only the most obvious conditions, and then often in a nominal or "coded" form. This lack of details often can lead to misapplications.

For simplicity, a manufacturer may give a certain miniature switch a 2 -A rating. It's true that the switch can carry 2 A safely, but switch only 100 mA at 12 V ac-and then only if the temperature is no higher than 40 C . These important details may not be listed or may be buried in footnotes and tables. The life of the switch may be listed as 100,000 cycles, but this value may be
the mechanical life without an electrical loadanother fact sometimes left out or mentioned only in a footnote.

However, a more conscientious manufacturer may list this switch's life as 25,000 cycles when switching 100 mA , because thereafter the contact resistance exceeds, say, $0.15 \mathrm{~m} \Omega$. But what if your circuit can tolerate $1 \Omega$ ? A telephone call to the manufacturer might inform you that a $1-\Omega$ end-of-life will raise the effective switch life to 50,000 cycles-just what you need. But you don't know any of this without that telephone call.

On the other hand, the same switch can carry 250 mA at 12 V ac if the end-of-life criterion is $1 \Omega$ and the life is derated to 10,000 . Maybe you're willing to trade life for higher switching current. But to do so, you need information.

## Catalog ratings can be misleading

The load and life ratings shown in many catalogs usually don't apply to most applications. The interplay of environment, duty cycle, failure criteria, actual load and contact material is com-


Pushbutton, rocker and paddle switches-some lighted -and low-cost versions for home appliances, also have followed the path to "miniaturization." However, the size of the human finger provides a lower limit to size. Small pushbuttons are represented by (top down) Licon's Type-05 and Compu-Lite's "shorty" switches; both can be LED-illuminated. And Cutler Hammer's and Carling Switch's small paddles and rockers are adapted particularly to electrical appliances.
plicated with an almost infinite variety of possible combinations. The complete specs and tables for a single switch type could easily fill a sizable book.

Reliable manufacturers normally have enough test data to provide reasonable estimates of switch performance under almost any set of specified conditions-high temperature, low temperature, high humidity, corrosive atmosphere or high altitude. Or vendors will obtain such information for a sufficiently interesting order. Don't hesitate to ask for supporting data. And be ready to pay for unusual requirements. Over the long run, you will save quite a bit.

Be wary of vendors who waffle when asked clear questions. Some are merely running machine shops without competent electrical back-up or any real knowledge of switch technology. Be wary, too, of numbers that look too good to be true: Ask for supporting evidence.

## The old numbers game

The search for ways to make spec numbers appear bigger and better is an ongoing pastime. At one point, for example, dielectric strength for switches was commonly defined as the voltage at which 1-mA leakage occurred-still a standard in most MIL specs. To improve the numbers, some manufacturers have established their own highercurrent leakage. Others have come up with a parameter called "voltage breakdown"-the voltage required to strike an arc and maintain it between two conducting members. This spec is unnecessary if the more difficult $1-\mathrm{mA}$ criterion is observed.

Another example of creative spec writing revolves about the life of rotary switches. A complete cycle of operation for a rotary switch calls for rotating from position one through all active positions and returning to the starting point. However the search for "improved" numbers has created the term, "circuit operations," so a 12position, 25,000-cycle switch ends up with an impressive life of $12 \times 25,000 \times 2$ circuit operations. Fortunately, a knowledgeable designer isn't likely to be fooled by such a blatant ploy.

If a vendor makes a claim that seems far out of line compared with what other vendors offer, greet it with skepticism. Ask him to substantiate the claim. If his design improvement, new material or process truly and dramatically improves the switching function, he will be most happy to demonstrate his claim.

Contact-system design, switch materials, actuator mechanisms and other switch parts have been so thoroughly investigated by most reliable manufacturers over the years that real breakthroughs can be expected to be rare. So be from Missouri!


Generally, there is little room for overcautious design in small switches. You can't expect to use a safety factor of 100 to $1000 \%$ and still keep the switch small.

## Miniature switches require compromise

Often, when occasional surges exist for only a few milliseconds, a small, 1-A rated switch can handle as much as 10 A .

Even with inductive loads, miniature switches must work with small safty factors. Study the load and its Q carefully to determine the true needs. Use suppressors to keep the current surges down.

And, of course, if you must squeeze a lot of capability into a small space, you may have to compromise on life expectancy-which may be a smaller sacrifice than you think. The actuations per hour in your application may be low, so even with a derated life, the hours of operation can outstrip the useful life of the equipment. The switch need not outlast its circuit.

Moreover, a switch rated at, say, 10,000 actuations with a $50 \%$ duty cycle, may be able to operate for 50,000 actuations at a $20 \%$ duty cycle. Designing the 10,000 -actuation switch for a 50,000 actuation life, however, may not only increase its size substantially, but also raise the price 200 to $500 \%$.

What's most important, miniature switches are often used in relatively high-impedance applications such as logic circuits, while end-of-life criteria (when provided) are generally measured in milliohms of contact resistance. Between 0.1 and $1 \Omega$ is realistic for an end-of-life value in such circuits; in some cases even $10 \Omega$ can be tolerated. Such impedance values vastly increase the switch's life expectancy so that is nearly as long as the mechanical life.


Snap-action switches were one of the first types to be miniaturized. Micro Switch, a pioneer in miniaturized snap-action switches (right), shows off some of its tiny versions next to standard sizes, as does McGill Manufacturing (left).

A large proportion of miniature-switch applications control currents in the range of only tens of milliamperes with voltages under 28 V . Yet low-cost silver and silver-alloy contacts are most often supplied as standards, despite the tendency of silver to form oxide and sulfide films that require substantial pressure or voltages to punch through the surface contaminants.

## Silver-a primrose path?

A clean silver surface is an excellent conductor, has good mechanical qualities and is relatively cheap. However, silver performs best when the electrical energy in the switched current can produce some small amount of arcing (about 0.4 VA) and the available contact forces are 30 g or higher. But since such a combination isn't always available in miniature switches, the switches often perform erratically. Therefore, unless specifically stated, assume the miniature switch is unreliable for voltages below 28 V and 100 mA .

When some arcing is present but without 30 g of contact force, choose palladium and its alloys. Palladium alloyed with up to $40 \%$ silver to reduce cost performs reliably in the low-level circuits encountered in the telephone industry.

But where neither arcing nor 30 g of force is present (voltages under 12 V at milliampere currents), gold becomes the contact material of choice, especially for dry circuits.

## Gold is not a panacea

However, gold is by no means a solution to all the problems that can occur at low-level switching. Even the best gold contacts may be useless if lubricant in the switch assembly or a contaminant such as flux gets on the contact surfaces. The low-level energy may not be able to punch

through the oil film and the contact pressure may be too weak to push it aside, especially in miniature switches. And above a level of about 0.4 VA and 20 V , gold tends to erode very rapidly.

Some manufacturers bend the specs of their so-called "gold contacts." The high cost of gold is an inducement to cut corners, so a few switch makers try to whistle by with words such as "gold-plated contacts," or even "heavy gold-plated contacts." Actually, the gold contacts may be


A miniature matrix slide switch made by AMP contains several manually operated, 10-position slide switches in a matrix configuration for PC mounting. Gold-plated crosspoints for logic-level switching ( 12 V dc 100 mA ) offer a dry-circuit contact resistance of less than $1 \Omega$.


Miniature dual in-line switch assemblies, variously called (from top left counterclockwise) DIL (Spectra series by Erg Industrial), DIP (Series 206 by CTS-Keene) and BIT switches (Standard Applied Engineering) are all designed for PC mounting and need very small fingers, or better, a stylus to actuate them.
merely flashes-between 10 and 15-millionths of an inch thick-virtually useless for long-term reliable operation. Such a thin flashing may be useful, however, to extend the shelf line of silver contacts by keeping oxide and sulfide formation down. But the layer will burn or wear off rapidly in use.

Even "average thickness" is suspect. Variations in thickness can be over $50 \%$. Only a guaranteed minimum thickness fully documented and tested-preferably in-house-can assure that the plating is adequate.

Gold plating should usually be applied over a barrier layer of hard dense material, like nickel, to prevent the gold from migrating into the contact base-silver, copper or brass. However, plating directly onto silver, some experts claim, may sometimes be advantageous, ${ }^{3}$ while other experts report that silver/gold compounds with poor conductivity can form.

At low-level switching, gold thickness of about 50 -millionths of an inch is adequate for switch lives of about 10,000 actuations. Longer-lived units call for greater thickness or inlaid solidgold inserts. But only the mating parts of a contact set need be gold. Careful contact design puts the gold only where needed and can save a lot of money. But even though the contacts may be most important, the other parts of a miniature switch also must receive careful consideration.

Although all forms of miniature switchestoggle, rotary, pushbutton, slide, snap, and others


Tiny inertia switches, Models 1575 and 1576 (bottom), used for lighting wristwatch LEDs at the flick of the wrist; a rotary switch, type 1508, used in helicopters; and a delicate 0.007 -in.-diameter beryllium-copper unit (upper right) for consumer electronics are all part of the J. M. Ney Company's extensive line of specialized miniature switches.


Thumbwheel switches, such as these EECO 2500 series low-profile switches in a strip, feature PC-board mount ing by wave, flow or hand soldering. No mounting hardware is needed. Single modules can be snapped together to form a multistation switch assembly of any desired length. Tabs of the end switches snap off easily.
—are being used more and more, the slide and pushbutton types seem to be advancing in popularity at a faster rate. Slide switches are particularly desirable for low-level or dry circuits because of their inherent wiping action.

Butting-type contacts often allow deposits to build up rapidly and shorten life prematurely in low-level circuits. Generally, butting contacts should be applied to power circuits (above 0.4 VA) and slide units used for logic levels. One of the most reliable butting-contact arrangements for the pushbutton is the gold-alloy crossbar, which


Miniature switches in all shapes and styles, as in this family portrait from Raytheon, show the diversity of available terminals, actuators, body shapes and mounting methods to fit almost any need.


Miniature rotary and slide switches for easy PC mounting are part of Centralab's extensive line of both standard and miniature-sized switches.
concentrates the available contact pressure into a small area.

Actuating mechanisms and mechanical contact systems used in many switches are often chosen by manufacturers more to avoid infringing existing patents than to provide a unique function, so don't be taken in by exaggerated claims.

Up to a few years ago, few switches were made specifically for PC mounting. At first, standardsized switches were adopted for PC use, but they were too large and their terminals were spaced on odd-ball centers. Now, switches specifically de-
signed for PC use, such as the many versions of the so-called DIP switch, have terminals on 0.1in. centers. Some manufacturers have introduced designs that prevent or alleviate solder and flux wicking. And many slide and pushbutton switches have shrunk so small that they are below humanfinger size and must be actuated with a stylus.

## Imported switches-blessing or curse?

Ideally, you should be able to select a standard product and save substantially over a custom design. And with some further searching, you may be surprised to find that a significant proportion of the designs you need not only are manufactured overseas but offer further cost savings. Many come from Japan, particularly miniatures. And even some prominent U. S. brands are made
in offshore factories-unfortunately, with widely varying standards of performance.

If the domestically produced miniature switches must be scrutinized, the foreign products must be investigated even more. Start by buying a few. Examine them carefully, tear them apart and compare their structure and techniques with more familiar switch products. Then test them in your circuit. If all goes well, carefully verify delivery, lead time and reliability of the source.

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## Need more information?

For further information on miniature switches, readers may consult the manufacturers listed here by circling the appropriate numbers on the reader service card. More vendors and inforbation may be found in Electronic Design's GOLD BOOK.

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# Avoid relay-specification pitfalls. Don't depend solely on published information. The catalogs leave out too much data essential for proper design. 

You can't depend entirely on specification sheets when selecting relays. If you do, you just might give a relay credit for capabilities it doesn't have. So in addition to the manufacturer's published information, keep these considerations in mind:

- Over-all electrical limitations.
- Over and undervoltage characteristics.
- Protection against transients.
- Mechanical and electrical lifetimes.
- Temperature limitations.

Detailed data on these topics are often missing, sketchy or at best buried in footnotes and incomplete charts and tables.

## Watch those over-all ratings

Relay specification sheets almost always list the maximum permissible amperage per contact. But they don't always make clear that a multipole relay may be limited further by a maximum total current-carrying capacity. For example, consider a four-pole relay that incorporates contacts rated at 10 A each. Apparently, the total relay capacity is 40 A . But as a matter of fact, the maximum total load on all contacts of the relay might be only 30 A . Such information must either be dug out of footnotes or obtained via a phone call to the manufacturer.

Or say a relay is rated at 10 A at 120 V ac or 24 V dc . What is the current rating at 48 V dc? The catalog probably doesn't say, and you shouldn't guess. Ratings above 24 V dc are a function of contact gap, which can vary widely from one $10-\mathrm{A}$ relay to another. The only safe solution is to explain your needs to the relay manufacturer and get his opinion.

Similarly it's easy to inadvertently exceed the load ratings for reed relays. Usually, the maximum amperage, voltage and wattage (or voltamperes) are specified separately. However, the wattage is the limiting factor. Even if amperage and voltage are within limits, you should multiply

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You might give a relay credit for capabilities it doesn't have.
them together to determine if the wattage also is within allowable limits. For example, if a reed relay is rated at a maximum of 250 V and 1 A , then

$$
15 \mathrm{~V} \times 1 \mathrm{~A}=15 \mathrm{~W} \text { (maximum rating }
$$ at 1 A )

or
$250 \mathrm{~V} \times 0.06 \mathrm{~A}=15 \mathrm{~W}$ (maximum rating at 250 V )
or
$24 \mathrm{~V} \times 0.625 \mathrm{~A}=15 \mathrm{~W}$ (maximum
rating at 24 V ).

All the maximums can't be used together.

## Don't operate relays near voltage limits

Although specification sheets usually provide an allowable under-to-overvoltage tolerance range around a relay coil's nominal-rating range, the effect of continuous use outside the nominal range is rarely mentioned. Such a tolerance range is meant to allow for the occasional fluctuations that occur in nearly all power sources. It doesn't mean you may operate at a continuous over or undervoltage without paying some price. This is seldom explained.

Usually, the specification clearly indicates the allowable range of continuous operating voltage. For example, many relays are rated to operate continuously between 110 and 125 V dc. However,
the relays also have an extra tolerance-typically 80 to $110 \%$ of nominal at 25 C -for transient fluctuations. This means that the voltage normally available must be between 110 and 125 V dc, but the relay can still operate properly when the voltage temporarily drops to 88 V dc or surges to 137 V dc.

But if the operating voltage remains continuously at, say, 100 V , you can expect problems. The low voltage provides reduced contact pressures, which can cause premature contact failure. On the other hand, a continuous supply of, say, 130 V can overheat and also cut the relay's life. Nevertheless, both the 100 and 130 V are well within the 88 -to- $137-\mathrm{V}$ spec.
Especially with ac relays, when operating at low voltages (less than $85 \%$ of rated nominal), the armature may not close firmly, resulting in a noisy, chattering relay that will fail prematurely. The coil may overheat or the contacts weld.

Select a relay nominally rated as closely as possible to the actual long-term supply voltage. If the normal supply voltage is $100 \mathrm{~V}, 60 \mathrm{~Hz}$, don't try to use a relay rated at $120 \mathrm{~V}, 60 \mathrm{~Hz}$-even when the relay's under and overvoltage specs allow such operation. Get a special $100-\mathrm{V}$ coil. The small added cost of the special coil can save you quite a bit of money in the long run, as well as provide more reliable performance.

Dependable relay manufacturers usually have a wealth of information-far more than they can publish in their catalogs. Perhaps you need a relay coil that can pull in at a very low voltage but also can withstand high voltages. It generally won't be in the catalog. Neither will the information you must have about transients.

## Transients troublesome to SSRs

Electromechanical relays normally handle most transients in both the coil and contact circuits so well that manufacturers rarely specify or provide transient protection for them. But solid-state (SSR) or hybrid relays can be readily damaged by transients.

For example, a specification sheet may read: "Relay is unaffected by transients of 5-ms duration with an exponential slope from 0 V to a 20 $\mu \mathrm{s}$ peak of $\pm 2000 \mathrm{~V}$." But what if the period of peaking is longer? Does the same relay withstand, for instance, a $100-\mu$ s peak, even if the voltage is well below 2000 V ?

If you anticipate such long-duration transients, you must ask the relay manufacturer for complete transient-protection statistics; the longer, the more important the information.

And don't overlook the two kinds of transients. Besides the familiar transients that occur every time a circuit closes, there are random, unexpected ones that are difficult to predict and protect
against. There's no excuse for not selecting a relay that can handle the former. But the latter may require some measurements and statistics pertaining to your particular application.

An elementary-but often overlooked-point: Most relays carry two "lifetime" ratings. One is the anticipated electrical lifetime based on operation at the rated load; the other is the mechanical lifetime with no electrical load.

A given relay may have a life of 500,000 cycles at 10 A , but 10 -million cycles with no elec-


Electromechanical relays normally handle transients well.
trical load. Data for loads between zero, which is the mechanical life, and rated load are rarely published and usually not linearly proportioned between these points. So again you must ask the manufacturer, and hope he has the data. Some manufacturers may even have life data for load currents above the rated and other variationssuch as with transients. For a given relay, you may be willing to trade life for a high load.

A relay's life is also affected by temperature. Relays are usually rated for a "safe" ambient temperature range. But since temperature also affects operating parameters, relays don't necessarily operate equally well at all points within the safe temperature range.

A relay may have an operating range of -10 to 55 C . But the relay's pull-in and drop-out voltages and timing repeatability are almost certain to fall off as either temperature extreme is approached. Especially with time-delay relays, a timing repeatability rating of $\pm 3 \%$ in the range of 20 to 25 C can easily deteriorate to $\pm 10 \%$ at the extremes of the operating range. Such data are rarely published in the catalog.

And finally, even with all the engineering data in the world, some decisions must still be based upon value judgments. If a relay fails, will the failure cause only a minor inconvenience? Or, can the result be serious? And, of course, what's the manufacturer's reputation? - =

# Dialight Switches 

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Reason 1: Dialight offers three switch configurations to meet all your needs-snapaction switches with silver contacts for mod-erate-level applications, snap-action switches with gold contacts for intermediate-level applications, and wiping-action switches with gold contacts for low-level applications.
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Reason 2: Dialight's snap-action and wiping-action switches come in a new modular design concept a common switch body for either high or low current operation. All 554 series switches and matching indicators have the same rearpanel projection dimensions. The snap-action switching mechanism guarantees a fast closing and opening rate. This insures that contact force and contact resistance

| SWITCHING ACTIONS | Snap-Silver contacts |  | Snap-Gold contacts |  | Wiping-Gold contacts |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | SPDT D | DPDT | SPDT | DPDT | SPDT DPDT |
| MOMENTARY | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc 0$ |
| ALTERNATE | $\bigcirc$ | 0 | 0 | $\bigcirc$ | 00 |
| OPTIONS |  |  |  |  |  |
|  | PUSH BUTTON CAP SIZES |  |  |  |  |
|  | $1 / 2^{\prime \prime} \mathrm{Sq}$. | . $5 / 8^{\prime \prime}$ Sq. | $58^{\prime \prime} \times 3 / 4^{\prime \prime}$ | 3/4" Sq. | $34^{\prime \prime} \times 1^{\prime \prime}$ |
| BEZEL MOUNTING TO ACCOMMODATE | 0 | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| beZEL MOUNTING WITH BARRIERS TO ACCOMMODATE |  | $\bigcirc$ | 0 | 0 | 0 N |
| PANEL MOUNTING TO ACCOMMODATE | 0 | 0 | 0 | $\bigcirc$ | 0 - |
| MATCHING INDICATORS | 0 | 0 | 0 | 0 | 0 |

are independent of the switch's actuation speed. In the wiping-action switch, the contacts are under constant pressure (A unique Dialight design). This insures long life with a minimum build-up of contact resistance.

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Reason 4: Dialight's 554 series is designed as a low cost switch with computer-grade quality.

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# Revisiting the cross-field inductor: Ordinary ferrite pot cores improve this useful device, which offers high Qs in tuned circuits and other advantages. 

The cross-field inductor not only can do many jobs better than conventional parallel-field reactors, it's also versatile. It can provide remote control, detect picoamps, transform dc, multiply frequencies, trim antennas, couple amplifiers and tune oscillators. But the cross-field inductor is largely ignored because of the mistaken belief that special and costly magnetic cores are required. In fact, you can adapt ordinary ferrite pot cores for cross-field inductors to do otherwise difficult jobs.

Basically, the cross-field inductor is a saturable magnetic device that has two or more windings with orthogonal fields. Mutual inductance cou-pling-the familiar transformer action-is absent, but a current in one winding affects the inductance of the others. With currents in several windings, energy can be transferred by parametric pumping.

Figure 1 shows a cross-field device and a conventional parallel-field reactor. Both devices exhibit zero mutual inductance, yet in either device, current in one winding affects the inductance of the other winding. The major difference between the two devices is that you don't have to match cores in the cross-field inductor.

In the figure, the two sections of winding 2 are in series opposition to the flux produced by the current in winding 1 . Transformer coupling is cancelled if the cores are balanced. The mutual inductance between windings 3 and 4 is zero because of the orthogonal configuration. No balancing of core components is necessary.

Windings 1 and 2 have similar properties, so usually it makes little difference which is used for the control and which for the modulated circuits. The magnetic paths for windings 3 and 4 are quite different, so how the winding roles are assigned is important.

Winding 3 is a conventional bobbin coil whose flux path has a substantially uniform cross-section area over its entire length. If 3 is used as a

[^9]

1. Contrasting two kinds of saturable reactor: the paral-lel-field type (a), widely used in magnetic amplifiers (for clarity, less than the optimum number of turns is shown) and the cross-field counterpart made with a standard ferrite pot core (b). The annular winding (No. 4) consists of 16 turns through the mounting hole and usually acts as the control or modulator. A bobbin coil works best in circuits requiring the highest Q or highest impedance. Winding details are shown in (c).
control winding, all portions of the core will saturate at the same current level.

## The effects of core geometry

On the other hand, modulating current in winding 4 first saturates the inner region of the center post. As the current increases, the saturated region spreads to the entire center post. With very large currents, the entire pot core becomes saturated. Thus, the effect of $\mathrm{I}_{4}$ on $\mathrm{L}_{3}$ is strongly influenced by the core geometry, whereas the effect of $I_{3}$ on $L_{1}$ depends largely on the B-H curve of the material.

Standard international pot cores aren't ideal for cross-field use because the center holes are too small, and the slots on the outer diameter increase the MMF needed for saturation. Of the international cores, the 1408, 2213 and 2216 are most suitable for cross-field use. The adverse effect of the core slots can be reduced by rotating the two halves a quarter turn (see Fig. 1b).

The sharp edges of the center hole should be relieved by grinding to avoid damage to the annular-coil insulation. Some devices can be constructed with very large pot cores (the AllenBradley C2400, for example) that have been rebored by grinding.

Design considerations of conventional inductors apply to cross-field devices as well. The ferrite should have appropriate properties for the highest expected frequency. For high-frequency coils, use Litz wire; for high-frequency, highimpedance coils, use multisegmented bobbins.

Because of low permeability, high-frequency ferrites are not easily saturated. So, since wide swings in inductance cannot be achieved with modest control-signal power, consider ferrites of higher permeability than you normally would choose to get the maximum Q at the operating frequency.

## The cross-field as a chopper

Data for a working cross-field device are shown in Fig. 2. The open-air inductance of the bobbin

2. The inductance of a cross-field bobbin coil vs its toroidal dc ampere-turns. The bobbin has 68 turns, and its core is Magnetics, Inc., type 43622-UG, a type-D, moderately permeable ferrite that saturates at about 3 oersteds. The steep part of the curve, between 6 and 15 ampere-turns, corresponds to the saturation of the center post. An appreciably greater inductance swing can be obtained with a similar core made from Magnetics type G or Ferroxcube type 3B7 ferrite.

3. Almost three decades of linearity are obtained from a cross-field modulator made with a 1408-A100 miniature pot core. Winding $\mathrm{W}_{\mathrm{a}}$ is annular, with 30 turns, $\mathrm{W}_{\mathrm{b}}$ is a 750 -turn bobbin coil made with 39 -gauge wire.

4. Output of the cross-field inductor of Fig. 1b when driven from a $50-\Omega$ signal generator. The bobbin winding has 130 turns of Litz made with 16 strands of AWG 38. The core is a Ferroxcube 3622-A1000-3B7. The Q of the resonant circuit is approximately 600. The slightly greater attenuation on the high-frequency side of resonance is an anomaly caused by stray capacitance.

5. The results of tuning winding 3 of the device in Fig. 1 b . The inductor is tuned to 13 kHz with a $0.013-\mu \mathrm{F}$ capacitor. The vertical sensitivity is $0.1 \mathrm{~V} / \mathrm{div}$ for both traces, and the horizontal sweep speed is $50 \mu \mathrm{~s} / \mathrm{div}$. A dc bias of 120 mA in the 16 -turn toroidal winding (No. 4) is used to peak the harmonic response. Note the absence of $6.5 \cdot \mathrm{kHz}$ drive signal in the top trace.
coil is $84 \mu \mathrm{H}$, a low value that can be approached with a large number of ampere turns in the annular (center-hole) winding if the current is applied in bursts to avoid overheating.

Small de signals, such as those generated by a thermocouple, can be chopped by a cross-field inductor to produce relatively large ac voltages, which are easier to measure or amplify. The dc signal is applied to the bobbin winding of the pot core, and a strong modulating current appears in the annular winding. At some point following s zero crossing of the modulator current, the core suddenly saturates and the bobbin winding's field collapses.

The energy stored ( $1 / 2 \mathrm{LI}^{2}$ ) in the bobbin winding manifests itself as a voltage spike that occurs after both negative and positive-going zero crossings of the modulator current. Thus, the frequency of spike repetition is twice the modulation frequency, and the device is called, appropriately enough, a second-harmonic magnetic modulator.

The modulator is much cheaper than a conventional magnetic amplifier, has a much longer life than mechanical or photo choppers and, unlike FET choppers, can function in such hostile ambients as a high neutron flux. Moreover, the modulator readily provides galvanic isolation between its input and output circuits. It is ideal for conditioning and transmitting signals in processcontrol instrumentation.

## The cross-field as a modulator

Figure 3 illustrates the performance of a crossfield modulator made with an ordinary miniature pot core. The device operates open-loop and is linear over nearly three decades. Output falls off at the upper end because the signal current causes the core to be partially saturated. The inductance of the bobbin coil thus decreases, and so does the energy stored per $\mathrm{I}^{2}$.

At the lower end, a finite signal develops despite a zero-input signal. The reason, in this case, is residual magnetization, which cannot be distinguished from the input signal. This magnetic effect can be eliminated by an appropriate dc bias on either winding. But then other effects begin to be important.

A second harmonic of the modulating frequency is coupled by the small but finite mutual inductance between $\mathrm{W}_{\mathrm{a}}$ and $\mathrm{W}_{\mathrm{b}}$. The isolation between $W_{\mathrm{a}}$ and $\mathrm{W}_{\mathrm{b}}$ is on the order of 50 dB , and allows some of the modulating signal to feed through. Much of the fundamental component is rejected by the tuned circuit, $\mathrm{CW}_{\mathrm{b}}$, which is resonant to 10 kHz . A second harmonic of the modulator is also coupled but, of course, is not attenuated by the $10-\mathrm{kHz}$ tuned circuit.

Square-wave modulating current is chosen for
two reasons: It is easy to generate, and there's no second harmonic-theoretically, at least.

Galvanic isolation prevails if you provide an additional bobbin winding for the output. To preserve sensitivity, most of the available bobbin space should be allocated to the de signal circuit. Since this coil will thereby have the higher Q, the capacitor should remain.

Even with a low-source impedance, inductance $L$ and resistance $R$ allow a high-tank $Q$. If desired, a much smaller $L$ can be used. Temperature changes in the resistance of $\mathrm{W}_{\mathrm{b}}$ will introduce errors that can be compensated for by a nega-tive-tempco series resistance.

## Preventing saturation

With feedback from an absolute-value amplifier, you needn't use a gapped core to prevent saturation by the signal current. The sensitivity of the device is proportional to the inductance of the bobbin coil which is increased about thirtyfold by replacing the A-100 core with an ungapped $1408-3 \mathrm{C} 8$. Whereas the sensitivity of the device in Fig. 3 is approximately $1 / 4 \mu \mathrm{~W}$, the ungapped core should work down to $10^{-8} \mathrm{~W}$.

Most of this energy is dissipated in the resistance of the isolating inductor, L , which contributes $340 \Omega$ to the input circuit's resistance vs $60 \Omega$ for the bobbin winding. Further improvement in sensitivity can be obtained by lowering signalcircuit resistance or by raising the inductance with a larger pot core.

As the sensitivity is pushed above $10^{-10} \mathrm{~W}$, modulator feedthrough and ferrite anisotropy pose formidable problems. The theoretical limit imposed by ferrite noise is about $10^{-15} \mathrm{~W}$, a performance ceiling that shouldn't be too confining in process-control, among other applications.

The cross-field inductor in tuned circuits offers many advantages. In conventionally coupled circuits, the Q is limited by the terminating impedances. But cross-field inductors can be driven by low-impedance sources without Q impairment. Figure 4 shows the response of the device of Fig. 1 b when resonated with a $0.005-\mu \mathrm{F}$ capacitor. The achieved Q of 600 is hard to obtain in conventional circuits ; the resonant circuit is currenttunable and not affected by either the driver or the tuning-current source.

Cross-field devices are also very efficient at frequency doubling and tripling as shown in Figure 5, top and bottom. Harmonic selection is accomplished simply by tuning the circuit of Figure 4 to the desired harmonic. Best results are obtained when some de bias, such as provided by a Class-A driving stage, is allowed to flow in either winding. The inherent isolation between the two windings ensures an output that is free of any fundamental frequency component. - -

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# Design EMI shielding more accurately by taking aperture effects into account. They can be appreciable, even at low rf frequencies. 

What do microwave ovens, fast computers, wireless intercoms, and the recent flood of CB gear have in common? They all pose a threat to air waves that already are overcrowded. So shielding against unwanted electromagnetic radiation has become extremely important, and you must be able to predict it with higher accuracy than was common in the past.

Traditionally, the shielding effectiveness of metallic barriers has been determined by breaking it down into two components-one accourting for reflections at both surfaces of the shield, and the other describing the attenuation of the wave, as it proceeds through the interior of the metal. Based on transmission line theory, these two effects are well understood, and can be predicted with high accuracy. ${ }^{1}$

This method is very useful for the selection of materials and their required thickness for the construction of a particular shield. Material selection is most critical for shielding high power, low frequency sources from predominantly magnetic fields. Otherwise, the structural metal enclosures of electrical equipment usually provide adequate shielding.

## There's the hole problem

In practice, over-all shielding efficiency is nearly always determined by openings in the enclosure, often resulting from the construction methods used. What you need is a simple means for estimating their effects.

Since high-frequency leakage involves only the concepts of radiated and received power, you can apply antenna theory directly to solving the problem. Leakage is most serious at microwave frequencies, but extends down to the $100-\mathrm{MHz}$ range. Fig. 1 maps out the strategy for attacking leakage. Antenna 1 with gain $\mathrm{G}_{1}$ is radiating power $P_{1}$, directly at a slot in an infinite shield. Assume that the polarization of the incident electric field E is perpendicular to the long dimension

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1. To attack the slot-leakage problem, put a radiating and a receiving antenna on opposite sides of an infinitely large metal plate. The slot itself becomes an antenna, receiving power from antenna 1 , and reradiating it to the receiving antenna 2.
of the slot, which induces maximum currents perpendicular to the slot and gives minimum shielding effectiveness.

The slot reradiates part of the power to antenna 2 , which has a gain $\mathrm{G}_{2}$. The difference in dB between the powers received by antenna 2 with and without the shield in place gives an insertion loss called shielding effectiveness SE.

## A slot absorbs no power

You can consider the slot as an antenna that, by the principle of duality, has the general characteristics of a similarly shaped metallic antenna in free space. In fact, the field distributions are the same for both antennas, except that the magnetic and electric components are reversed. Since the energy contained in a radiated wave is equally distributed between the electric and magnetic components, the power received by similarly shaped slot and metallic antennas oriented for maximum pick-up is the same.

Equations generally used to calculate antenna power assume that the antenna is terminated in a conjugate-impedance load. The antenna then absorbs the maximum power from a passing
electromagnetic plane wave of given power density. In the case of the slot, however, the antenna is not terminated, and all of the intercepted power is immediately reradiated into the surrounding space on both sides of the shield. This reradiated power is four times the power received by a matched termination.

Assuming a slot gain $G_{s}$, the power received and reradiated by the slot is given by the following equation, in rationalized MKS units:

$$
\mathrm{P}_{2}=\frac{4 \mathrm{P}_{1} \mathrm{G}_{1} \mathrm{G}_{s} \lambda^{2}}{(4 \pi \mathrm{D})^{2}}
$$

where $\lambda$ is the wavelength. Power received by the receiving antenna is then:

$$
P_{3}=\frac{P_{.} G_{2} G_{s} \lambda^{2}}{(4 \pi d)^{2}}=\frac{4 P_{1} G_{1} G_{2} G_{s}{ }^{2} \lambda^{4}}{(4 \pi d)^{2}(4 \pi D)^{2}}
$$

If the shield is removed, the received power becomes:

$$
P_{4}=\frac{P_{1} G_{1} G_{2} \lambda^{2}}{[4 \pi(d+D)]^{2}}
$$

The shielding effectiveness is the ratio of $P_{*}$ to $P_{3}$, in decibels:

$$
\begin{equation*}
\mathrm{SE}=10 \log \frac{\mathrm{P}_{4}}{\mathrm{P}_{3}}=10 \log \left[\frac{2 \pi \mathrm{dD}}{(\mathrm{~d}+\mathrm{D}) \mathrm{G}_{\mathrm{s}} \lambda}\right]^{2} \tag{1}
\end{equation*}
$$

But Eq. 1 is only accurate at the slot's resonant frequency, which represents the worst case. You thus obtain a simple estimate of the minimum shielding efficiency due to a particular opening in a shield. Eq. 1 also shows :

- Shielding effectiveness is independent of the gain of the test antennas, for the completely aligned condition. If the test antennas are aligned with each other but not with the slot, the measured SE then becomes a function of the directivity of the test antennas, which results in higher SE. Lacking slot orientation is the primary cause for large variations in shielding measurements.
- SE depends on the distance of the receiver from the flaw in a shield. This fact becomes extremely important when safety hazards are involved. For instance, when looking into a microwave oven, you may put your eyes close to a flaw in the oven door. Distance is also important in the protection of equipments from an electromagnetic pulse caused by a nuclear burst (EMP).
- If distances d and D are equal (the usual condition for shielding-test specifications), shielding effectiveness is 6 dB lower than when D is very large compared to $d$.
- Eq. 1 is symmetrical with respect to d and D; the principle of recriprocity holds and the transmitting and receiving antennas may be reversed without changing shielding effectiveness.


## Don't forget harmonics

Eq. 1 also indicates that SE becomes greater with frequency (decreasing $\lambda$ ). However, you must remember that a linear antenna resonates at
multiples of a half-wave, and the radiation pattern breaks up into a number of high-gain lobes. The test antennas in Fig. 1 are therefore always positioned and directed so as to intercept the main lobes of the slot in the shield. Tests indicate that the shielding effectiveness at successive resonance points (higher frequencies) becomes progressively greater at half-wave resonance.

It has often been assumed that the shielding efficiency of an opening is inversely proportional to the cross sectional area of the opening. Antenna theory, however, shows that a terminated halfwave dipole in space has an effective area equal to $1.64 \lambda^{2} / 4 \pi$. This area yields the maximum power that a given plane-wave can deliver to the load. For the half-wave slot the intercepted power is four times as great, but because the power also passes on to the remote side of the barrier, the area (called the "acceptance area") is twice as great, namely $3.28 \lambda^{2} / 4 \pi$. In effect, the slot's acceptance area is approximately equal to the slot length squared, and not the slot's cross section.

Table 1 gives SE values for several half-wave slots if the two test antennas of Fig. 1 are each 1 meter away from the shield, and if $G_{s}$ equals 1.64. The results of Table I are accurate only if the test antennas are far enough from the shield to locate the slot within the far-field, or "Fraunhoffer," region of the transmitting antenna. If L is the antenna's largest dimension, this region begins at $2 \mathrm{~L}^{2} / \lambda$. When the test antenna's dimensions are similar to slot dimensions, and $d=\mathrm{D}=$ 1 m , the calculations shouldn't be used for slots much larger than 0.5 m .

## Short slots act differently

Slots shorter than half a wavelength have reactive components. Instead of using the effective area, you must consider the electric and magnetic fields, and the effective length of the antenna.

The voltage induced in an electric dipole in free space due to an incident electric field, $\mathrm{E}_{\mathrm{i}}$, is

## Table 1. SE of half-wave slots

| Frequency | Slot <br> length | Accept- <br> ance <br> area | Shielding <br> effective- <br> ness |
| :---: | :---: | :---: | :---: |
| $(\mathrm{MHz})$ | (meters) | $\left(\right.$ meter $\left.^{2}\right)$ | (dB) |
| 300 | 0.5 | 0.26 | 5.65 |
| 1000 | 0.15 | 0.02 | 16.12 |
| 1500 | 0.1 | 0.01 | 19.64 |

found as follows:

$$
\mathrm{V}=\mathrm{E}_{\mathrm{i}} \times \mathrm{L}_{\mathrm{e}}
$$

where $L_{e}$ is the effective length of the dipole.
For the magnetic dual antenna, an equivalent equation gives the current induced by a magnetic field:

$$
\mathrm{I}=2 \mathrm{H}_{\mathrm{i}} \times \mathrm{L}_{\mathrm{e}} .
$$

The current (and magnetic field) at the surface of the metal sheet is twice the incident field. The factor of 2 indicates that the dipole in free space is not the exact analog for the slot in a metal sheet. The equivalent would be an electric dipole imbedded in an infinite sheet of very high intrinsic impedance. The electric field would double and the factor 2 appear in the equation for the electric field as well.

The power reradiated by the slot is $\mathrm{I}^{2} \mathrm{R}_{\mathrm{s}}$, where $R_{*}$ is the slot's radiation resistance:

$$
\mathrm{P}_{2}=4\left(\mathrm{H}_{\mathrm{i}} \mathrm{~L}_{\mathrm{e}}\right)^{2} \mathrm{R}_{\mathrm{s}}
$$

If you use the standard equations for H at the slot due to antenna 1, and follow the same procedure as for the derivation of Eq. 1, you obtain the shiolding effectiveness off resonance:

$$
\mathrm{SE}=10 \log \left[\left(\frac{\mathrm{dD}}{(\mathrm{~d}+\mathrm{D}) \mathrm{L}_{\mathrm{e}}}\right)^{2} \frac{480 \pi^{2}}{\mathrm{R}_{\mathrm{s}} \mathrm{G}_{\mathrm{s}}}\right]
$$

The series impedance of the slot is related to the metallic dipole impedance $\mathrm{Z}_{\mathrm{d}}$ by :

$$
\mathrm{Z}_{\mathrm{s}} \mathrm{Z}_{\mathrm{d}}=(120 \pi)^{2}
$$

You can then express the radiation resistance of the slot in the more familiar terms of dipole resistance $R_{d}$ and reactance $X_{d}$ :

$$
\mathrm{R}_{\mathrm{s}}=\frac{(120 \pi)^{2}}{\mathrm{R}_{\mathrm{d}}{ }^{2}+\mathrm{X}_{\mathrm{d}^{2}}}
$$

You obtain the shielding effectiveness for the slot:

$$
\begin{equation*}
S E=10 \log \left[\left(\frac{d D}{\left.(d+D) L_{e}\right)}\right)^{2} \frac{R_{d}{ }^{2}+X_{d}{ }^{2}}{30 R_{d} G_{s}}\right] \tag{2}
\end{equation*}
$$

where $G_{s}$ and $L_{e}$ are the same for both antennas and where $R_{d}$ and $X_{d}$ are the characteristics of the equivalent metal dipole.

When the physical length of the antennas increases from very short to a half-wavelength, the length factor varies only from 0.5 to 0.637 , and the gain of the antennas varies from 1.5 to 1.64. So, neither factor has much effect on the final result. Indeed, if you assume the extremes in any given calculation, the result changes by a maximum of 5 dB . Thus, if you know only $\mathrm{R}_{1 \mathrm{i}}$ and $\mathrm{X}_{\mathrm{d}}$, you can obtain reasonable estimates of the shielding effectiveness below half-wave resonance.

Table 2 contains representative results assuming a slot length of 10 cm and a width of 0.61 cm . The calculations are based on values for $R_{d}$ and $\mathrm{X}_{\mathrm{d}}$ taken from the literature. ${ }^{2}$ At 1500 MHz , where the slot is half-wave resonant, the values for SE are the same as in Table 1, as you would expect.

## Magnetic fields can pose problems

In the vicinity of low-impedance antennas or other low-impedance radiators, magnetic fields prevail over electric field components, often by a large factor. Because testing for magnetic fields is usually done with loops, assume that the antennas of Fig. 1 are now small, coplanar loops with distances d and D measured from the loop centers. The equations for magnetic field emission of small loops then yield:

$$
\begin{equation*}
\mathrm{SE}=20 \log \left[\frac{2 \pi \omega \mu \mathrm{~d}^{3} \mathrm{D}^{3}}{(\mathrm{~d}+\mathrm{D})^{3} \mathrm{~L}_{\mathrm{e}}{ }^{2} \mathrm{Z}_{\mathrm{s}}}\right] \tag{3}
\end{equation*}
$$

where $\omega=2 \pi \mathrm{f}$ and $\mu$ is the permeability of free space $\left(4 \pi \times 10^{-7}\right)$.

Measurements in induction fields require that all distances and antenna sizes be small compared to a wavelength, and are therefore restricted in practice to frequencies of 10 MHz or less. The metal-dipole equivalent of the slot then has an almost purely capacitive impedance:

2. Shielding effectiveness in the presence of a $10-\mathrm{cm}$ slot is summarized over a frequency range from the kHz
to the GHz region. Different effects prevail in the different frequency regimes.

$$
\left|Z_{s}\right| \approx \frac{(120 \pi)^{2}}{1 / \omega C_{d}}=(120 \pi)^{2} \omega C_{d}
$$

After substitution of $Z_{s}$ in Eq. 3, the magnetic shielding effectiveness reads as follows:
$\mathrm{SE}=20 \log \left[\overline{18\left(\mathrm{~d}+\frac{\mathrm{d}^{3} \mathrm{D}^{3}}{\mathrm{D})^{3} \mathrm{~L}_{e}{ }^{2} \mathrm{C}_{\mathrm{d}}} \times 10^{-9}\right]}\right.$
where $\mathrm{C}_{\mathrm{d}}$ is the capacity of the dipole, approximately given by :

$$
\mathrm{C}_{\mathrm{d}} \approx \frac{\pi \epsilon \mathrm{~L}}{\ln (\mathrm{~L} / \mathrm{a})}
$$

where $\epsilon$ is the permittivity of free space $(8.85 \times$ $10^{-12}$ ) and L and a are the length and diameter, respectively, of the dipole. Assuming a length of 1 m and a diameter of $0.6 \mathrm{~cm}, \mathrm{C}_{\mathrm{d}}$ becomes 5.5 pF . The capacitance of a one-meter whip antenna above a ground plane is twice that, or 11 pF . The known value for the $41-\mathrm{in}$. whip used in military electric field tests is 10 pF , in very good agreement with the calculation.

To apply the capacity of the equivalent dipole to the slotted shield, set the diameter, a, to onehalf the width, w , of the slot. ${ }^{3}$ Then, the shielding effectiveness of the slot due to a magnetic field, expressed in terms of slot parameters alone, is:

$$
\mathrm{SE}=20 \log \left[\frac{2 \mathrm{~d}^{3} \mathrm{D}^{3} \ln (2 \mathrm{~L} / \mathrm{w})}{(\mathrm{d}+\mathrm{D})^{3} \mathrm{~L}^{2} \mathrm{~L}}\right]
$$

Because the effective length, $L_{e}$, for short antennas equals one-half the physical length, L, the equation can be simplified to:

$$
\begin{align*}
\mathrm{SE} & =20 \log \left[\frac{8 \mathrm{~d}^{3} \mathrm{D}^{3} \ln (2 \mathrm{~L} / \mathrm{w})}{(\mathrm{d}+\mathrm{D})^{3} \mathrm{~L}^{3}}\right]  \tag{5}\\
& =60 \log \left[\frac{2 \mathrm{dD}}{(\mathrm{~d}+\mathrm{D}) \mathrm{L}}\right]+20 \log [\ln (2 \mathrm{~L} / \mathrm{w})]
\end{align*}
$$

Under the conditions specified in MIL-STD-285 ( $\mathrm{d}=\mathrm{D}=0.47 \mathrm{~m}$, loop dia $=12 \mathrm{in}$.) the shielding calculation for a $10-\mathrm{cm}$ slot of 1 mm width results in a shielding effectiveness of 55 dB .

Eq. 5 indicates that SE for magnetic fields is independent of frequency, which is confirmed by many tests with small loops, over a wide frequency range. Nor is SE affected much by the slot width, which is also borne out by measurements.

## Shielding for electric fields

Fields generated by high-impedance antennas such as short whips or dipoles are predominantly electric, with a very small magnetic component. Assuming that the test dipoles are short, the shielding effectiveness for electric fields is given by :

$$
\mathrm{SE}=20 \log \left[\frac{\mathrm{~d}^{2} \mathrm{D}^{2}}{(\mathrm{~d}+\mathrm{D})^{3} \mathrm{~L}_{\mathrm{e}}{ }^{2} \mathrm{Z}_{\mathrm{s}} \in \mathrm{f}}\right]
$$

where $\epsilon$ is the permittivity of free space $\left(10^{-9} / 36\right.$ $\pi$ ) and $f$ is the frequency in Hz . In terms of the capacity of the equivalent short dipole, the equation becomes:

$$
\mathrm{SE}=20 \log \left[\frac{\mathrm{~d}^{2} \mathrm{D}^{2} 1.266 \times 10^{5}}{(\mathrm{~d}+\mathrm{D})^{3} \mathrm{~L}_{\mathrm{e}}{ }^{2} \mathrm{f}^{2} \mathrm{C}_{\mathrm{d}}}\right]
$$

Table 2. SE for a 10 cm meter slot at and below resonance

| Freq | Impedance |  | Length | Gain | SE |
| :---: | :---: | ---: | :---: | :---: | :---: |
| $(\mathrm{MHz})$ | $\mathrm{R}_{\mathrm{a}}$ | $\mathrm{X}_{\mathrm{d}}$ | $\mathrm{L}_{.}$ | $\mathrm{G}_{*}$ | $(\mathrm{~dB})$ |
| 500 | 6 | 650 | 0.051 | 1.5 | 51.78 |
| 1000 | 20 | 250 | 0.055 | 1.6 | 37.34 |
| 1500 | 73 | 0 | 0.0637 | 1.64 | 19.6 |

Using only the slot dimensions, the shielding effectiveness is:

$$
\begin{equation*}
\mathrm{SE}=20 \log \left[\frac{\mathrm{~d}^{2} \mathrm{D}^{2} \ln (2 \mathrm{~L} / \mathrm{w})}{(\mathrm{d}+\mathrm{D})^{3} \mathrm{~L}^{2} \mathrm{f}^{2} \mathrm{~L}} \times 4.65 \times 10^{15}\right] \tag{6}
\end{equation*}
$$

Under MIL-STD-285 test conditions ( $\mathrm{d}+\mathrm{D}=$ 25 in .), the electric-field SE for the $10-\mathrm{cm}$ slot with a $1-\mathrm{mm}$ width becomes 132 dB at 1 MHz , and 92 dB at 10 MHz . Both the large values and rapid change with frequency are again confirmed by experience.

## Getting it all together

The various calculations for the $10-\mathrm{cm}$ slot under the methods of MIL-STD-285 are plotted in Fig. 2, which provides an over-all view of how shielding varies throughout the usual frequency range.

The region between 10 MHz and a few hundred MHz , indicated by dotted lines, is the range where simple equations for low-frequency conditions are no longer accurate. The wave impedances approach that of free space, and with further increase in frequency the results become equal to those for plane waves.

For magnetic fields, at frequencies lower than about 100 kHz (depending on shield thickness), the metal is penetrated and the shielding effectiveness is no longer a function of the slot characteristic.

In the plane-wave or radiated-power region, antenna sizes become large in comparison with reasonable antenna spacing if the frequency falls below a few hundred MHz . The shield is then well within the Fresnel (or near-field) zone, and the simple equations assumed for radiated and received power no longer apply.

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# Predict printed-circuit dc performance with a circuit-analysis program and a measured voltage drop matrix. You can expect over-all accuracies on the order of $5 \%$. 

Multilayer printed-circuit (PC) cards often contain a power distribution plane that is tapped at several points, for instance by DIP power connections. Generally, the power planes contain cutouts to provide clearance for other circuit elements. Consequently, you can't readily calculate the resistance from the edge connector to the tap points. But you can perform a number of measurements and derive the equivalent network for the power-distribution plane.

If you have access to a computer, these equivalent networks can be used to determine such data as dc drops, voltage differentials and required power supply tolerances for any particular PC card design. Powerful computer circuit analysis programs are available that provide remarkable speed and accuracy, even for networks consisting of many hundreds of elements. ${ }^{1,2}$ The computer should be capable of inverting moderatesize matrices.

Consider the circuit card with eight large components (load locations 1 through 8) and power feed points along the socket edge ( 9 through 11 and Ref) in Fig. 1. (Numbers 1 through 11 and Ref are assigned arbitrarily.) The following vector matrix equation completely describes the nodal dc behavior of the power plane:

$$
\mathrm{v}=\mathrm{Mi},
$$

where
$i=\left[i_{1}, i_{2}, \ldots i_{11}\right]$ is a vector made up of elements $i_{k}$ defined as the current entering the $\mathrm{k}^{\text {th }}$ node
$\mathrm{v}=\left[\mathrm{v}_{1}, \mathrm{v}_{2}, \ldots \mathrm{v}_{11}\right]$ is a vector with elements $\mathbf{v}_{\mathrm{k}}$ defined as the potential measured from node k to node Ref
$\mathrm{M}=11 \times 11$ matrix that describes the resistive nature of the plane.
If M is known, your computer can calculate the solution to any dc problem by conventional methods for solving simultaneous linear equations. The matrix, M, can also be represented by an equivalent circuit such as the one shown in Fig. 2. You can find the values of the resistors in Fig. 2 with the following measurement method.

[^10]Inject a known current in node 1, return it through node Ref and measure $\mathrm{v}_{1}, \mathrm{v}_{2}, \ldots \mathrm{v}_{11}$. The measured values divided by the known current represent the first column of M. Now remove the current from node 1 and inject it at node 2, and again return it through node Ref, and measure $\mathrm{v}_{1}, \mathrm{v}_{2}, \ldots \mathrm{v}_{11}$. Those measured values divided by the known current represent the second column of M. Continue through node 11 for a total of 121 measurements to fill in M completely.


1. This power plane of a printed-circuit card has 8 load connections ( 1 through 8) and 4 power feed points (9, 10, 11, Ref).

2. The equivalent circuit of the power plane used in the example contains a resistor connecting every node (8 load points and 4 power inputs) to every other node. The resistor connecting nodes 3 and 11 is highlighted.

The Reciprocity Theorem ${ }^{3}$ provides a check on the measurements because it requires that $\mathrm{m}_{i \mathrm{j}}=$ $m_{\mathrm{i}}$, that is, M is a symmetric matrix. Use a computer to subtract $\mathbf{M}$ from its transpose, element by element. Any large elements in the difference matrix are suspect, and the corresponding measurements should be repeated.

Note that this procedure does not check the diagonal elements. The accuracy of off-diagonal elements can be improved by calculating a new M equal to half the matrix sum of the measured M and its matrix transpose, thereby effectively using the average of two measurements.

Once you know the M matrix, you can calculate the individual elements in the equivalent circuit of Fig. 2 from conductance matrix $G=M^{-1}$ as follows: ${ }^{+}$

The value of the resistor elements, $\mathrm{R}_{\mathrm{i}}$ connecting the reference node to node i is

$$
R_{i i}=\frac{1}{\sum_{j=1}^{i=n}} g_{i i}
$$

$$
(\mathrm{i}=1,2,3 \ldots \mathrm{n})
$$

The value of resistors $R_{i j}$ connecting nodes $i$ and $j$ is

$$
R_{i j}=\frac{1}{-g_{i j}} \quad(i, j=1,2,3, \ldots, n \text { but } i \neq j)
$$

## How well does it work?

An application example will clarify the measurement technique. Fig. 1 shows a typical PC card. Power enters the card at four tab locations and is connected to an internal power plane by means of a short surface line and platedthrough holes. The power plane itself is made from $1 \mathrm{oz} / \mathrm{ft}^{2}$ copper with 0.075 -in.-diameter clearance holes located on a $0.100-\mathrm{in}$. grid. The clearance holes provide a path for the signal to pass through the card without contacting the power plane. There are no clearance holes where a component picks up power, as at locations 1 through 8 in Fig. 1.

Resistance measurements on such highly conductive materials as these copper planes require proper 4-terminal connection procedures to eliminate errors due to contact resistance. Fig. 3 shows the typical connecting points used in this example. The contact posts, one of which is shown in Table 1 were measured, and the resistplane, which had no clearance holes at these points.

With the numbering scheme of Fig. 1, the data shown in Table 1 were measured, and the resistance values calculated as described. They range from $4.26 \mathrm{~m} \Omega$ between nodes 2 and 4 to $506 \mathrm{~m} \Omega$ between nodes Ref and 11.

To establish the over-all accuracy of the calculated equivalent circuit, a particular load pattern

## Table 1. Measured-resistance matrix M (milliohms)

$$
\left[\begin{array}{llllllllllll}
3.86 & 3.25 & 3.11 & 3.19 & 3.11 & 3.17 & 3.13 & 3.15 & 3.01 & 3.09 & 3.12 \\
3.25 & 4.71 & 3.38 & 3.74 & 3.48 & 3.63 & 3.53 & 3.60 & 3.21 & 3.43 & 3.49 \\
3.10 & 3.39 & 4.30 & 3.54 & 3.60 & 3.59 & 3.59 & 3.59 & 3.42 & 3.56 & 3.58 \\
3.18 & 3.75 & 3.53 & 4.75 & 3.71 & 3.93 & 3.77 & 3.86 & 3.34 & 3.65 & 3.73 \\
3.09 & 3.49 & 3.60 & 3.71 & 4.89 & 4.01 & 4.18 & 4.09 & 3.51 & 4.13 & 4.16 \\
3.13 & 3.63 & 3.57 & 3.91 & 4.01 & 5.10 & 4.14 & 4.31 & 3.41 & 3.92 & 4.08 \\
3.10 & 3.54 & 3.58 & 3.76 & 4.18 & 4.14 & 5.42 & 4.44 & 3.49 & 4.29 & 4.68 \\
3.11 & 3.60 & 3.57 & 3.85 & 4.09 & 4.31 & 4.44 & 5.50 & 3.45 & 4.08 & 4.35 \\
2.98 & 3.21 & 3.40 & 3.32 & 3.52 & 3.43 & 3.51 & 3.46 & 6.06 & 3.56 & 3.52 \\
3.07 & 3.46 & 3.56 & 3.65 & 4.13 & 3.94 & 4.29 & 4.09 & 3.56 & 7.18 & 4.40 \\
3.09 & 3.52 & 3.57 & 3.73 & 4.17 & 4.09 & 4.69 & 4.36 & 3.51 & 4.40 & 7.58
\end{array}\right]\left[\begin{array}{c}
\text { Except for the diagonal elements (deep color), this sym- } \\
\text { metrical matrix contains all resistance values twice - } \\
\text { e.g. R6.3 }
\end{array}\right] \text { R3.6 (white). }
$$

like that shown in Fig. 4 was applied to the actual card. The voltage differentials between all nodes and point Ref were measured. The same voltage differentials were also calculated with the circuit-analysis program ASTAP using the equivalent circuit of Fig. 2. The resistors of Fig. 4 were represented as current loads. The calculated and measured voltage differentials were then compared for three different load patterns to establish over-all accuracy of the equivalent circuit.

The three separate load patterns were:
Case 1: Identical $1-\Omega \pm 1 \%$ loads at all 8 component locations simulated a uniformly loaded card; four nearly identical power feeds were used at the four indicated tab locations, and one ampere was applied to the card through the four wires attached to the power input tabs. The free ends of the eight resistors are connected together

3. Low-resistance measurements require separate feed points for the current and voltage connections of a milliohmmeter.

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Table 2. Result of verification tests

| Node | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Case 1 | 0.958 | 1.136 | 0.958 | 1.178 | 0.93 | 1.111 | 0.985 | . 07 |
|  | 0.927 | 1.109 | 0.926 | 1.111 | 0.911 | 1.1 | 0.885 | 1.066 |
| Case 2 | 0.953 |  | 1.061 |  |  |  |  | 1.032 |
|  | 0.936 | 1.093 | 1.039 |  | 0.898 | 1.197 |  | 1.038 |
| Case 3 | 1.204 | 1.37 | 1.35 |  |  | 1.504 |  | 1.353 |
|  | 1.159 | 1.351 | 1.303 |  | 1.239 | 1.504 |  | 1.355 |

Comparison between measured (white) and calculated (color) voltage differential (mV) from REF to nodes 1 through 8.

4. For the verification test, eight $1-\Omega$ resistors and four power feed points are used.
and then to the other power-supply terminal.
Case 2: Identical $1-\Omega$ loads were applied at six of the eight module locations; with no loads at the remaining two, four power feeds were used at four tab locations.

Case 3: Identical 1- $\Omega$ loads were applied to six of the eight module locations; three power feeds were used at three of the four tab locations.

The results are summarized in Table 2. It compares the calculated with the measured voltage differentials between the reference point (REF) and the load points. Similar tables can be compiled for the power entry points 9,10 and 11 .

The conclusion from the three test cases is that this method of generating resistive equivalent networds for PC card power distribution planes yields a 5\% over-all accuracy, with a few $10 \%$ worst-case values. Thus, equivalent networks and a circuit-analysis program for the circuit models permit you to predict the over-all dc operation of any PC card. $=$

## References

1. Advanced Statistical Analysis Program (ASTAP), IBM Program Reference Manual (SH20-1118-0).
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## Jim Wolfe of Centralab Speaks On Decentralizing Eingineering



Most companies with many plant facilities do their manufacturing in the various plants, but do their engineering back home. The theory is that this makes for highest efficiency. The manufacturing is done where it's close to raw material, transportation facilities, labor supply or markets. The engineering is done where enginears can talk to each other and learn from each other. And there's economy in the engineeringsupport services.
Well, we don't think that works well for an electronics business with a diverse product offering in a broad spectrum of markets. And we speak from experience because that's just the way we used to do things up to about seven years ago. We had the typical functional organization, with the main functions here at headquarters in Milwaukee. Here's where our VP Egineering, our VP Marketing and our VP Manufacturing ran things. We used to have almost all manufacturing located here, but now we're spread around the world.

When we first started moving manufacturing facilities to different parts of the country and then, the world, we did it mainly for reasons like labor supply. But what started as an effort to find better labor supply emerged, 10 years later, as a situation where we are now in the markets we want to be in. The labor-base question is still important, but less significant.

The point is that, when we now move out geographically, whatever the impetus for the move, we move our engineers, too-as many as we can justify.

As soon as we can, we try to get our engineers right next to manufacturing. We'd like to get them out there from Day 1. We want to get as much locally designed tooling as possible and as
much local engineering. We want our engineering there because that's how we get the most effective manufacturing and the most effective problem solving. We want our engineering close to the market that's influenced by it.

That's a significant departure from the practice followed by most companies. For example, if they have an operation in Seoul, the only senior person there might be a manufacturing manager. Engineers would commute from the home office whenever there's a problem and, in most cases, the problems would be frequent. Or they would try to fix things by telephone, cable or letter. That doesn't work too smoothly either.

My observation is that, in most places, when somebody at a remote manufacturing organization has a problem and asks for help, he gets a pedantic letter from the home office telling him things he already knew. That wastes time and accomplishes nothing.

We don't think it's practical to manage an operation on a day-to-day basis from a distance. The way to avoid glitches, communications blocks and plain lost time is to have the engineering where the manufacturing action is.

Now the obvious question: Is this efficient? You need separate engineering staffs, separate labs, duplicate equipment, and redundant support services. Every time you have an engineer, you need a secretary, a technician and a draftsmanperhaps not on a one-to-one basis, but in some ratio.

Well, we've found that it's not a problem if you have the courage to make an offsetting change at the home office. Using local engineering, we solve at least 90 percent of the operational problems. For the remainder-and only for the remainder-we can send in talent from the home office.

What led to our reorganization was the understanding that the problems in manufacturing and marketing that were faced by our men in the Far East were completely different from the problems faced by the people in South America or, for that matter, Europe or North America.

We found that we needed intimate communications among the people abroad who were involved in marketing and manufacturing and the people back home who were involved in engineering.

When you have a centralized engineering force, the communications link with far-flung markets becomes too strained. It's just too easy to get things wrong. And it becomes impossible to respond quickly to a change in the marketplace.

So we now support these organizations locally with the highest level of engineering that those businesses will sustain. Of course, if they have a special problem they may have to reach back here for help. Our engineering staffs in Milwaukee and Los Angeles are available for major trouble-shooting jobs anyplace in the world. But the day-to-day manufacturing-support engineering is done at the local plants. As these businesses grow, we push more and more engineering talent into them.

Now there's a risk in making that kind of transition. When you begin to transfer functions, unless you're alert, you can find that you're doing the same job in two locations. And that you don't need.

If you're going to move work to someplace else, you must remove it from the first location. That's traumatic and it takes a certain amount of discipline to make the transition.

But there's a factor that makes the transition easier and less traumatic, and it doesn't show up
on any organization chart. Managers and engineers at all our locations are in close contact with each other. A problem faced by one engineering group could easily involve a solution developed by another.

For example, the people working on monolithic chip capacitors in Los Angeles might well be able to use circuit knowledge from our people in Lafayette, Indiana. And the people in Lafayette, who make thick-film hybrid circuits, are one of the largest customers for the chip capacitors from Los Angeles.

Further, ceramic capacitors for the U.S. market are assembled in Juarez, Mexico. But Juarez gets much of its raw ceramic material from Mexico City. And Mexico City also manufactures complete capacitors and sells them in South America as well as Mexico.

Of course, in a large organization, it may not be possible or wise to decentralize the entire engineering staff.

## Who is Jim Wolfe?

After he got his Masters in Business Administration from Harvard in 1948, 25-year-old James W. Wolfe went to work as a financial man in a nut-and-bolt works. "Well," he says, "they weren't nuts and bolts for long. When we went from $\$ 13$ million a year to $\$ 25$ million, we realized that they were industrial fasteners."

After seven years, Wolfe moved to the consulting firm of Booz-Allen-Hamilton, where he remained for another seven years, then on to A.O. Smith for a seven-year hitch. In 1969, eight years ago, Wolfe joined Centralab as general manager and vice president of the parent company, Globe-Union Inc., which enjoys annual sales of about $\$ 290$ million.

He finds the components part of the business particularly fascinating because there is a high degree of price elasticity and extreme sensitivity to capacity and demand. "You can have wild price fluctuations over a very short period of time," he points out, "and that's a problem."

Wolfe and the former Marion Hayner were married in 1945-as soon as he got off the boat, he says. Well, he admits, maybe it was a few weeks later. They have three sons, David, Paul and Stewart, and a daughter, Anne. When he wants to relax, Wolfe often races his 23 -foot, full-keel sailboat-sometimes with his wife.

If you decentralized all your engineering, you might find the local engineers devoting their efforts to local and immediate problems. Design of future products and new types of products might be left to chance. The engineers working on trimmers, fixed capacitors or switching arrays would probably spend most of their time designing enhancements of trimmers, fixed capacitors or switch arrays. They wouldn't be working towards a completely new electrode system for capacitors.

That kind of development normally emanates from central product development and research groups whose work is normally aimed at a more distant future. Those central engineers and scientists should not normally be involved in the day-to-day engineering required by on-going businesses. They must be experienced, versatile and have the technical breadth and depth to solve any problem.

We have our central scientists in a group we call Corporate Applied Research. The head of this group is responsive to corporate management, that is, Globe-Union Inc., Centralab's parent company. The activities of this group can stem from requests for help on difficult problems or from projects generated by the division mar-ket-research staff.

Let's say, for example, that we were planning a product development and we identified a basic materials problem. Perhaps it was something in the chemistry or physics of a substrate-something needing a breakthrough that we're not equipped to work on at the normal engineering level. Here's where the Corporate Applied Research people come in. They don't develop products, but they can define the material composition that we need, say, for a ceramic capacitor or a resistor.

An example of their effort lies in the Cerbon ${ }^{\mathrm{TM}}$ system for our resistors. Our new system gives us trimmer resistors with almost the qualities of cermets at a price that's almost as low as conventional carbon compositions.

As another example, the original technology for our Base-Metal-Electrode chip capacitors came from this group. One of the researchers came out of engineering and studied for his doctorate in a rather esoteric branch of ceramics engineering at Marquette University. When he came back, he had some ideas and went into the Corporate Applied Research Group. When BME ${ }^{\text {TM }}$ became much more than a laboratory concept and moved into production in Los Angeles, we asked him to transfer so he could contribute broadly in our entire line of monolithics.

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# Circuit provides a Gaussian response with a multifunction converter and op amp 

A simple circuit that provides a Gaussian response accurate to within $2.2 \%$ of full scale can be built with a multifunction converter, an op amp and a few discrete components (see figure).

The Gaussian, or normal-distribution, function is very important in electronic computations and statistical analysis. A circuit having a Gaussian transfer response is therefore often needed for analog computation systems and in special applications requiring statistical weighting in accordance with the function ${ }^{1}$

$$
\begin{equation*}
f(x)=\frac{1}{\sqrt{2 \pi}} e^{-x^{2 / 2}} \tag{1}
\end{equation*}
$$

Such a nonlinear function can be approximated with straight-line segments produced by diodes. Or, a power series with multipliers to generate the power terms could be used. However, using a multifunction converter with op-amp feedback is much simpler. A multifunction converter is a multiplier/divider that has the capability of generating powers and roots.

Analysis of the circuit reveals that its transfer function is

$$
\begin{equation*}
\mathrm{e}_{\mathrm{o}}=-0.7 \frac{\mathrm{R}^{3}}{\mathrm{R}_{1}} \frac{\mathrm{E}_{\mathrm{R} 2}}{1+0.7\left(\frac{\mathrm{e}_{\mathrm{i}}}{\mathrm{E}_{\mathrm{R} 1}}\right)^{2.62}} \tag{2}
\end{equation*}
$$

This expression approximates the positive half of the Gaussian distribution as follows:
$\mathrm{e}_{0} \approx-1.75 \mathrm{E}_{\mathrm{R} 2} \frac{\mathrm{R}_{-}}{\mathrm{R}_{1}} \cdot \frac{1}{\sqrt{2 \pi}} \mathrm{e}^{-\left(\mathrm{e} i / \mathrm{E}_{\mathrm{R} 1}\right)^{2 / 2}}, \mathrm{e}_{\mathrm{i}} \geq 0$.
The choice of $R_{1}, R_{2}$ and $E_{R 2}$ determines the circuit's scaling; voltage $\mathrm{E}_{\mathrm{R} 1}$ sets the input range. With the components and voltage levels shown, a 0 -to-10-V input swing produces the same outputsignal range. An $e_{i} / E_{R 1}$ span of 0 to 1.64 represents the coverage of two standard deviations.

The mathematical approximation between Eqs. 2 and 3 is the primary source of error. For a range of two standard deviations, the approximation error is $2.1 \%$ of full scale, which is only $0.8 \%$ of the content of the total distribution. When designed for a span of three standard deviations, the approximation deviates by $3.9 \%$ of full scale. The multifunction converter con-
tributes an error of only $0.1 \%$ of full scale, and the op-amp dc errors are negligible.

To limit circuit errors, the exponent, $m$, of the multifunction converter and the $R_{3} / R_{2}$ ratio must be set within $1 \%$. At high frequencies, feedback current of the phase-compensation capacitor, C , and bandwidth limitations of the multifunction converter and the op amp introduce more errors. Phase-compensation capacitor $C$ and resistor $R_{4}$ are needed to ensure circuit stability. ${ }^{2}$

## References

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Jerald Graeme, Manager Monolithic Engineering, Burr-Brown Research Corp., International Airport Industrial Park, Tucson, AZ 85734.

Circle No. 311


A transfer response approximating the positive half of the Gaussian distribution is produced by a multifunction converter when it is introduced into the feedback circuit of an op amp.

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For complete information and specifications, contact your P\&B Sales Representative, or write or call Potter \& Brumfield Division AMF Incorporated, Princeton, Indiana 47671. 8123861000.

## Circuit that latches/unlatches magnetic devices uses almost zero standby current

Magnetic-latching devices, such as reed relays and miniature electromechanical indicators, are advantageous in low power applications. They don't need any continuous electrical holding power. But only if their drive circuit requires little power, can this benefit be realized fully.

The circuit of Fig. 1 consumes virtually zero standby power (only nanowatts of leakage). It is used in a small portable micropower electrometer (radiometer) where a latching-reed relay switches the unit's high-impedance ( $5 \times 10^{10} \Omega$ ) ranges within a sealed module (Fig. 2). The circuit can also be used for many other purposes, such as for driving a Minelco-type Bite scale-range or alarm indicator.

Switch S (Fig. 1) is part of the instrument's multiposition range switch ( $\mathrm{S}_{1 \mathrm{~B}}$ in Fig. 2). When $S$ is switched, toggle action from position 2 to position 1 produces a positive trigger pulse with peak value $2 \mathrm{~V}_{\mathrm{S}}$ in the gate terminal of $\mathrm{FET} \mathrm{Q}_{3}$. This pulse causes $Q_{\text {: }}$ to turn on rapidly and $Q_{4}$ to saturate. Point P of the device's coil connects to $-\mathrm{V}_{\mathrm{s}}$ momentarily but long enough to ensure latching.

The ON period of $Q_{ \pm}$can be calculated from the following relationships:

$$
\begin{equation*}
\mathrm{t}_{\mathrm{on}}=\mathrm{R}_{3} \mathrm{C}_{2} \ln \left[\frac{2 \mathrm{~V}_{\mathrm{S}}}{\mathrm{~V}_{\mathrm{BE}(\mathrm{OX})}+\mathrm{V}_{\mathrm{GS}(\mathrm{th})}+\mathrm{V}_{(Q)}}\right] \tag{1}
\end{equation*}
$$

and


$$
\begin{equation*}
\mathrm{V}_{(Q)}=\left[\frac{2\left(\mathrm{~V}_{\mathrm{s}}-\mathrm{V}_{\mathrm{CE}(\mathrm{san})}\right)}{\mathrm{h}_{\mathrm{FE}} \beta \mathrm{R}_{\mathrm{L}}}\right]^{1 / 2}, \tag{2}
\end{equation*}
$$

where
$\mathrm{V}_{\mathrm{s}} \quad=$ Supply voltage.
$\mathrm{V}_{\mathrm{BE}\left(\mathrm{ON}_{\mathrm{S}}\right)}=$ Voltage drop of forward-biased baseemitter junction of transistor $Q_{2}$ or Q.
$\mathrm{V}_{\mathrm{CE}(\text { sat })}=$ Voltage drop (collector-emitter) of saturated transistor $Q_{2}$ or $Q_{i}$.
$\mathrm{h}_{\mathrm{FE}}=$ Forward-current transfer ratio (static-current gain) of transistor $\mathrm{Q}_{2}$ or $\mathrm{Q}_{\mathrm{t}}$.
$\mathrm{V}_{\mathrm{GS}(\mathrm{th})}=$ Gate-source threshold voltage of (enhancement type) MOSFET $\mathrm{Q}_{1}$ or $\mathrm{Q}_{3}$.
$\beta \quad=$ Gain factor (mhos $/ \mathrm{V}$ ) of MOSFET $Q_{1}$ or $Q_{3}$.
$\mathrm{R}_{\mathrm{L}} \quad=$ Resistance of magnetic device.
Energy demand during switching is approximately $\left(\mathrm{V}_{\mathrm{s}}{ }^{2} / \mathrm{R}_{\mathrm{t}}\right) \mathrm{t}_{\mathrm{on}}$ in joules per pulse.

The magnetic device becomes unlatched when switch S is thrown from position 1 to position 2. The resulting positive current pulse through the coil of the magnetic device restores the device to its original state.

The trigger signal for the circuit also can be a square-wave derived from a control system.
J. C. Nirschl, US Army ECOM, Fort Monmouth, NJ 07703.

Circle No. 312


# Guildline Introduces the only 14 Million Count DVM on the Market Today 



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# Diode-feedback comparator circuit regulates a LED's drive current 

Here's a comparator circuit that can drive a LED display with constant current independently of wide power-supply voltage changes. Add a diode to a standard LM3900 current-differencing amplifier and the circuit can operate with a pow-er-supply range of at least 4 to 30 V (see figure).

An external 1 N 914 diode, along with the for-ward-biased diode that is part of the LM3900's internal circuit at the inverting input, blocks current flow from output to input for low outputvoltage states. However, when the output voltage swings up and the voltage across $R_{1}$ rises to approximately 1 V , a small current (typically less than 30 nA ) flows into the inverting input and maintains a constant 1 V across R .

With $10-\mathrm{M} \Omega$ resistances for $\mathrm{R}_{\mathrm{in}}$, and the inverting input of the comparator grounded, the circuit becomes a LED driver with very high input impedance. Of course, the circuit can figure in many other applications where a controllable constantcurrent source is needed.

Tom Frederiksen and Martin Giles, Application Engineers, National Semiconductor Corp., 2900 Semiconductor Dr., Santa Clara, CA 95051.

Circle No. 313


> Without the added feedback diode, the comparator would drive the LED with a widely changing current as the power-supply voltage changed. However, the diode acts to keep the LED current nearly constant at about 10 mA .

# Time-interval meter reads digitally to 99.9 ms on a DVM IC 

Measuring time intervals is a frequent requirement. To meet this need conveniently, the circuits in Figs. 1 and 2 read time in decimal numbers to three places-from 00.0 to 99.9 ms .

The circuit uses a 555 timer in combination with a current source to form a sawtooth generator (Fig. 1). The sawtooth voltage across capacitor $\mathrm{C}_{1}$ is buffered with a FET input to an L144 op amp. A second op-amp circuit in the L144 scales the ramp and permits the offset to be adjusted. This op-amp's output is a negative-going ramp that periodically resets to zero.

A sample-and-hold circuit stores a voltage sampled from the ramp in capacitor C.. Feeding this voltage into the input of a three-digit DVM IC (LD130) produces a digital readout of the time between ramp initiation and sampling
(Fig. 2). Direct interface between the hold capacitor and the DVM IC is possible, because the LD130's input impedance is $1000 \mathrm{M} \Omega$.

A current source charges the $0.1-\mu \mathrm{F}$ capacitor, $\mathrm{C}_{1}$, to generate a linear ramp. The ramp slope, controlled by the charging current $(3.33 \mu \mathrm{~A})$, is obtained with two 2N4403s connected as a current source and adjusted by varying the $10-\mathrm{M} \Omega$ potentiometer, $R_{1}$. Resistor $R_{3}$ controls the discharge time of $\mathrm{C}_{1}$, and an output pulse at pin 3 of the 555 of about 1 ms in width is produced.

The ramp amplitude, scaled by $1 / 3$, results in an approximately $1-\mathrm{V}$ ramp height. The ratio $\mathrm{R}_{\mathrm{s}_{1}} / \mathrm{R}_{\mathrm{s}_{2}}$ controls the ramp scaling without varying the over-all frequency.

All of the system's offsets can be "tweaked" (continued on page 110)


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## IDEAS FOR DESIGN



1. A linear ramp voltage generated by a timer IC and constant-current source is sampled and then
measured by a DVM to determine time intervals. All circuit offsets can be zeroed out with $\mathrm{R}_{\text {os }}$.
out and the ramp dc level translated by adjusting the $\mathrm{R}_{\mathrm{os}}$ potentiometer. The ramp across $\mathrm{C}_{1}$ starts at $1 / 3 \mathrm{~V}_{\mathrm{CC}}$ and ends at $2 / 3 \mathrm{~V}_{\mathrm{CC}}$, but after scaling its magnitude becomes $(1 / 9) \mathrm{V}_{\mathrm{CC}}$ and its polarity, inverted.

The DVM IC measures 000 to 999 mV and consumes little power. Very few additional components are needed to create a complete three-digit DVM.

As an elapsed-time meter for sonar (water or air) and phase and rise or fall-time measurements, the system can be synchronized to external events by controling pin 4 , the 555 's reset input.

Thomas J. Mroz, Applications Engineer, Siliconix, Inc., 2201 Laurelwood Rd., Santa Clara, CA 95054.

2. A three-digit DVM constructed with an IC a/d converter needs few other components.

IFD Winner of November 8, 1976
Peter A. Ernst, Institut fur Regelungstechnik, Universitat Erlangen-Nurnberg, Cauerstrasse 7, 8520 Erlangen, Germany. His idea "Remotely Control a Pocket Calculator with a Simple CMOS Interface Circuit" has been voted the Most Valuable of Issue Award.
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- Dual truth table
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Retriggerable Pulse Stretcher
349 Dual Retriggerable Pulse Stretcher
355 HiNIL Timer
MSI Circuits
343 4-bit Digital Comparator
361 Dual HiNIL to 5 Volt Interface
362 Dual 5 Volt to HiNIL Interface
363 Quad 5 Volt to HiNIL Interface
367 Quad Schmitt Trigger/Line Receiver
368 Quad Schmitt Trigger/Line Receiver (Open Collector)

## Interface Buffers

390 Dual 4-Input AND
391 Dual AND
Dual NAND
Dual OR
Dual NOR
95 Dual 4-Input NAND
396 Dual Line Driver/Receiver

## HiNIL CIRCUITS

```
Gates
301 Dual 5-Input Power NAND
    Quad 2-Input Power NAND
    (Open Collector)
3 0 3 ~ Q u a d ~ 2 - I n p u t ~ P o w e r ~ N A N D ~
    (Passive Pullup)
304 Triple 4, 3, 4-Input Power NAND
    (Passive Pullup)
3 0 6 ~ Q u a d ~ 2 , ~ 2 , ~ 2 , ~ 3 - I n p u t ~ N O R ~
307 Quad 2, 2, 2, 3-Input NOR
    (Open Collector)
    Quad 2-Input NAND
    Dual 5-Input NAND
    Quad 2-Input NAND (Open Collector)
    Quad 2-Input NAND (Passive Pullup)
    2, 2, 3,3-Input NAND
    2, 2, 3, 3-Input NAND (Passive Pullup)
    Dual 5-Input Gate Expander
    Hex Inverter (Open Collector)
    Hex Inverter (Passive Pullup)
    Strobed Hex Inverter (Open) Collector)
    Strobed Hex Inverter (Passive Pullup)
    331 Sual 2-Input AND-OR-INVERT
344 Dual Expandable AND-NOR
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## Flip Flops

311 Master/Slave RST
312 Dual J-K Edge Triggered
313 Dual J-K Master/Slave
370 Quad D (Passive Pullup)
375 4-bit Shift Register

## Counters

371 Decade (Passive Pullup)
372 Hexadecimal (Passive Pullup)
373 Up/Down Decade
374 Up/Down Hexadecimal

## Decoders/Multiplexers

350 8-bit Multiplexer
351 Dual 4-bit Multiplexer
380 BCD to Decade Decoder/Lamp Driver (Open Collector)
BCD to Decade Decoder/Logic Driver (Open Collector)
382 BCD to Decade Decoder/Gas Discharge Driver
383 BCD to Seven Segment Decoder/Driver
384 BCD to Seven Segment Decoder/Gas Discharge Driver

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## New Products

## Industry's first 18-bit modular DAC fits in palm-sized case



Analog Devices, Rte. 1 Industrial Park, P.O. Box 280, Norwood, MA 02062. Barry Friedman (617) 3294700. P \& A: See text.

Claiming a major first in converter products, Analog Devices has squeezed an 18 -bit accurate digital-to-analog converter into a $2 \times 4 \times 0.4-\mathrm{in}$. module. Moreover, in addition to now offering the most accurate modular DACs anywhere-the DAC- 1138 seriesAnalog has also introduced what are reported to be the lowest-cost DACs with 16 -bit accuracy-the DAC-1136 series.

The DAC-1138 comes in two versions, one that delivers 18 -bit accuracy and resolution (the DAC1138 K ), and one that delivers 17 bit accuracy but 18 -bit resolution (the DAC-1138J). Neither unit will put much of a strain on your budget: the 1138 K costs only $\$ 950$ and the 1138 J a mere $\$ 750$-just slightly more than the cost of previously available top-quality 16 -bit converters.

And if you don't need 17 or 18 bits, there are the DAC1136K and DAC1136J, also $2 \times 4 \times 0.4 \mathrm{in}$. modular DACs, that offer 16 -bit accuracy and resolution or 15 -bit accuracy and 16 -bit resolution, respectively. Prices for the 1136 s are
$\$ 220$ for the J and $\$ 260$ for the K model.

Both the DAC1136 and 1138series units come in $2 \times 4 \times 0.4$ in. modules and are pin-compatible with the company's older QM series of 14 and 16 -bit converters. Offsettemperature coefficients for the converters are $0.5 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$; differential linearity tempcos are 0.4 $\mathrm{ppm} /{ }^{\circ} \mathrm{C}$. Except for the 1138 K , each d/a converter operates at a specified resolution over a range of 5 to 50 C . The 1138 K maintains its accuracy over a $\pm 10$-C window around any precalibrated point within the 5 -to- $50-\mathrm{C}$ range. Converter linearity for the 1138 K and 1136 K is specified at $\pm 0.5$ LSB and $\pm 1$ LSB for the J models.

All converters can be set for either current or voltage outputs, which can be either unipolar or bipolar. Available current outputs are 0 to -2 mA or $\pm 1 \mathrm{~mA}$, and possible voltage ranges include 0 to $+5,0$ to $+10, \pm 5$ and $\pm 10 \mathrm{~V}$.

Settling times to within 0.5 LSB for the current output mode are $10 \mu \mathrm{~s}$ for the 1138 series and 8 $\mu$ s for the 1136 units, both for a full-scale step. In the voltage-output mode, full-scale settling times for the 1138 increase to $250 \mu \mathrm{~s}$ (unipolar), 3 ms (bipolar), and for
the 1136 to $30 \mu \mathrm{~s}$ (unipolar) and $40 \mu \mathrm{~s}$ (bipolar), and in all cases the internal amplifiers are used. For a faster voltage settling, an ex-ternal-output amplifier can be used.

Requirements for the converter's power supply aren't hard to satisfy : only +5 V at 9 mA and $\pm 15$ V at 30 mA for any unit-all regulated to within $5 \%$. Power supply rejection in the voltage-output mode is 80 dB for gain error and 75 dB for offset error.

Since both converter series are also available in card-mounted assemblies, you can select options from a "library" of input codes and output amplifiers. Input options include binary, complementary binary, 2 's complement, complementary 2 's complement, sign-plus-magnitude binary and complementary sign-plus-magnitude binary. The $4.5 \times 6-\mathrm{in}$. circuit card also permits you to use three output options: the DAC1138 internal output amplifier, the Model 44 K high-speed modular op amp or the 234 L low-noise, low-drift op amp. A four-terminal output connector on the card permits contact-resistance compensation.

No other companies offer an 18bit d/a converter in modular form, but a few others besides Analog Devices sell 16-bit units: Analogic, Wakefield, MA, offers the MP1916A; Burr-Brown, Tucson, AZ, the DAC-70; Datel, Canton, MA, the DAC-HR; and Intech, Santa Clara, CA, the 416 BIN and A-86716.

All of Analog's DAC1136 and 1138 converters are available from stock to 30 days. And the DAC1138 s come with a certificate of performance, including a 1000 -hour zener-stability record and a test record of linearity.
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# Low-noise vhf synthesizer is small and inexpensive 

Rohde \& Schwarz Sales Co., 14 Gloria Lane, Fairfield, NJ 07006. Allen Freeland (201) 575-0750. $P \& A$ : see text.

If you're looking for a source of low-noise signals in the 60 -to- $120-$ MHz range, check the Model PT-2 from Rohde \& Schwarz Sales Co. This compact synthesizer doesn't have all the features of some of its competitors-for example, each PT-2 can only generate frequencies within a narrow part of the band. But, priced at only $\$ 2000$ in unit quantities, and $\$ 1400$ for 30 or more, the $1-3 / 8 \times 3 \times 5-1 / 4-\mathrm{in}$. PT-2 costs less than half of what you might otherwise have to pay.

Applications like frequency calibration and satellite communications usually require only a few discrete frequencies. But until now, engineers who needed a programmable low-noise source in the vhf range had to rely on general-purpose synthesizers such as HewlettPackard's 8660 A or Fluke's 6160B. These instruments can synthesize a wide range of frequencies and have all the controls and features expected in a rack-mount instru-
ment-along with prices upwards of $\$ 6000$.
The PT-2's price is much lower because it doesn't have all the frills. It doesn't even include a power supply-you'll have to provide 10 to 13 V at 300 mA . And while the PT-2's design is capable of generating any frequency from 60 to 120 MHz , individual units are tailored to a much narrower spectrum.
To specify a PT-2, start with a single frequency between 60 and 120 MHz . The module then provides up to 20 predetermined synthesized frequencies, starting at the specified frequency and stopping at a frequency $1.4 \%$ higher. For example, a single PT-2 might have 20 frequencies between 100 and 101.4 MHz . When it receives a BCD command, the PT- 2 delivers the right frequency at a level of zero $\mathrm{dBm} \pm 3 \mathrm{~dB}$ into $50 \Omega$.
The output signal is very quiet. In the PT-2, discrete spurious signals within 20 MHz of the output signal are suppressed by 100 dB or more, and all other discrete spurious signals are down 60 dB
or more. Harmonics are at least 60 dB below the output level.

Under worst-case conditions, phase noise in the PT-2 is down 70 dB at 20 Hz from the output. At 2 kHz from the chosen frecuency, phase noise is down more than 120 dB .

Two versions of the PT-2 are available. The PT-2A has an internal frequency standard that is voltage-tunable over a range of $\pm 0.01 \%$ of the basic frequency. The PT-2B has an output frequency that can be phase-locked to an external $1-\mathrm{MHz}$ standard.

For bench-top applications or manual-frequency selection, the PT-2 can be ordered in an instrument cabinet that measures $3 \times$ $4 \times 6 \mathrm{in}$. The output frequency is selected by front-panel thumbwheel switches instead of BCD signals. In a cabinet, the PT-2 is priced at $\$ 2250$ in unit quantities.

CIRCLE NO. 307
Unit measures gas flow from outside the pipe


Teledyne Hastings-Raydist, Hampton, VA 23661. (804) 723-6531. \$490; 3 wks.

All circuitry for the AFSC series of gas-mass flow meters is contained inside the unit's base. There are no sensing elements or projections into the flow stream. The unit operates on a thermal principle that measures true-mass flow. Available in eight ranges from 10 to 50,000 standard cubic $\mathrm{cm} /$ min , the devices operate from 24 V dc and produce linear outputs of from 0 to 5 V dc. Normal calibration is for air with conversion factors provided for most gases. Accessories include meters with di-rect-reading scales, single-and-twopoint alarm meters, digital panel meters, totalizers, controllers, and $115-\mathrm{V}$-ac to $24-\mathrm{V}$-dc converters.


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## Converter zips polar digits to analog X-Y

Interface Engineering, 386 Lindelof Ave., Stoughton, MA 02072. (617) 344-7383. From \$240; stock to 45 days.
The DR320 accepts a 10,12 , or 14-bit natural-binary angle ( $\Theta$ ) and a reference ( $R$ ) and delivers $R \operatorname{Sin} \theta$ and $R \operatorname{Cos} \theta$ outputs at up to $500-\mathrm{k}$ conversions/s. The trig functions are digitally controlled and the outputs conform individually to the theoretical sine and cosine functions as well as maintaining arctan $\theta$ ratio accuracy. The resultant vector, therefore, exhibits an accurate length as well as an accurate pointing angle. You can use the device as a fast multiplier with a trig-function translation and get a conversion lag of less than $1 \mu \mathrm{~s}$. The unit accepts a reference ( $R$ ) input in analog form, from dc to complex waveforms. The module provides $\mathrm{d} / \mathrm{r}$ and $\mathrm{d} / \mathrm{s}$ as well as polar-to-rectangular conversions. The converter's speed lets you dynamically control electron-beam deflection in CRT displays. The A model provides $\pm 0.3 \%$ vector-amplitude vs angle conformance and $\pm 0.2^{\circ}$ arc$\tan$ conformance. The B models provide $\pm 0.1 \%$ vector-amplitude conformance and $\pm .05^{\circ}$ arctan conformance.

CIRCLE NO. 320

## Multiplexer links 15 ports to a CPU

Digital Systems, Walkersville, MD 21793. J. R. Laughlin (301) 8454141. From $\$ 5250$; 90 days.

The Model 6116 connects to a high-speed bisynchronous communications adapter on a host processor and then fans out that highspeed port to 15 slower speed asynchronous ports. The unit functions either locally or remotely with respect to the host computer. The speed of each terminal device connected to the multiplexer can be preselected at from 110 to 9600 baud, and the speed for each device is independent of the others. The unit is built around a $\mu \mathrm{P}$ system with 15 separate input buffers. Data are transmitted a character at a time to the device, which then buffers the data until the end of a record is reached.

## Solid-state delay switches 4 A



Polytron Corp., P.O. Box 984, Elkhart, IN 46514. Alex Saharian (219) 294-3924. $\$ 6.50$ (1000 qty); stock to 2 wks.

The ND-2 line of solid-state time-delay relays is intended for heavy-industrial application. These normally-ON or normally-OFF devices are fully encapsulated and switch loads of up to 4 A at various line voltages. The unit measures $2.5 \times 2 \times 1 \mathrm{in}$.

CIRCLE NO. 322


Just those three features alone put Systron-Donner's new Model 6054B Microwave Counter in a class by itself! But there's lots more . . .

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- Information: Call Scientific Devices or contact S-D at 10 Systron Drive, Concord, CA 94518. Phone (415) 676-5000. Overseas, contact Systron-Donner in Munich; Leamington Spa, U.K.; Paris (Le Port Marly); Melbourne.

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CIRCLE NUMBER 76

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MODULES \& SUBASSEMBLIES
14-bit a/d offers four input ranges


CPS Inc., z22 E. Evelyn Ave., Sunnyvale, CA 94086. (408) 738-0530. \$195 (1-9): 8 whs.

A 14-bit successive-approximation a/d converter, Model CYAD-14 QM, offers pin-selectable analog input ranges of 0 to $+5-\mathrm{V}, 0$ to $+10-$ V unipolar, $\pm 5-\mathrm{V}$ and $+10-\mathrm{V}$ bipolar. The unit has a conversion time of $100 \mu \mathrm{~s}$, and guaranteed monotonicity over 0 to +50 C . The module includes input buffers. Power requirements are $+15 \mathrm{~V} \pm 5 \%$ at $60 \mathrm{~mA},-15 \mathrm{~V} \pm 5 \%$ at 40 mA or $+5 \mathrm{~V} \pm 5 \%$ at 180 mA . The device measures $2 \times 4 \times 0.4 \mathrm{in}$.

CIRCLE NO. 323

## D/f converter latches onto BCD inputs



Syntest, 169 Millham St., Marlboro, MA 01752. C. Hoffman (617) 4817827. $\$ 242$ (unit qty); stock.

Featuring direct-BCD programming of both frequency and amplitude range, the SM-106 also gives you on-board logic latches that simplify computer control. Output frequencies between 1 Hz and 160 kHz are available in five ranges. Three digits of BCD frequencycontrol plus one BCD-digit of range control is provided. The output is a sine wave capable of driving 10 V pk-pk into $600 \Omega$. Total harmonic distortion of less than $1 \%$ is provided over the entire frequency range. Power requirements are +15 V dc at 100 mA and -15 V dc at 50 mA and on-board monolithic voltage regulation is provided.

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These new Grayhill low-profile, 12-button keyboard pads feature a 2 out of 7 coded output, standard mounting dimensions, and are ready for top-side or sub-panel mounting. The contact system is life-rated for $3,000,000$ operations per button, and is readily interfaced with logic circuitry. The new Grayhill Series 87 modules offer excellent audio and tactile feedback characteristics with total button travel of only $.015^{\prime \prime}$. These durable keyboards are molded of tough ABS plastic; feature buttons with black on white molded-in legends as standard, and a variety of other legend options including clear snapon caps for user legending. Complete specifications and truth table are provided in Bulletin \#262, available free on request from Grayhill, Inc. 561 Hillgrove, La Grange, Illinois 60525 (312) 354-1040.


Data I/O, P.O. Box 308, 1297 N.W. Mall, Issaquah, WA 98027. (206) 455-3990. $P \& A$ : See text.

Able to program any available PROM, the Model 7 from Data I/O offers the user more than just another universal PROM programmer. To begin with, the Model 7 is a "portable" but line-operated unit, only $6 \times 11 \times 15 \mathrm{in}$., and has a wide selection of options and communication capabilities.

In its basic configuration, the Model 7 can program over 200 PROM types. Program-personality modules, which dedicate the programmer to a specific PROM family, plug into an access opening below the front panel. Socket adapters, which match the programmer circuitry to the PROM pinouts, plug directly into the front panel. Thus, generic PROM families from one manufacturer can be programmed with a single personality module and different socket adapters.

This generic programming capability can produce significant savings. For example, the 12 PROMs in the Monolithic Memories generic family can be programmed on the Model 7 at a cost of $\$ 400$ for one personality module and $\$ 600$ for 12 $\$ 50$ socket adapters. Without the Model 7's generic-programming ability, programming the Monolithic Memories family might cost well over $\$ 4800$. A separate person-
ality module may be required for each PROM in the family.

A modular design enables the Model 7 to communicate with serial or parallel peripheral devices. Serial I/O is ASR33 and RS232compatible, at 110 to 2400 baud. Parallel I/O operates on a busy/ ready basis. I/O interfaces, available to make the programmer compatible with any data terminal, can automatically check for faults in data transmission. And a remote control option lets the programmer work as a computer peripheral.

An expansion option upgrades the unit to a Model 9 by adding direct entry of keyboard data, address and data display of RAM contents, display of error codes, random-addressing capability, and insertion/deletion RAM editing. Also, a $1-\mathrm{k} \times 8$ bit RAM included in the basic Model 7 to hold programs can be optionally expanded to 4 k $\times 8$ in 1-k increments.

Power required for the programmer can be 100 to 240 V ac , 48 to 66 Hz , at 120 VA. Designed to operate over a 0 -to- 45 -C range, the programmer has an optional, $18.5 \times 14 \times 8$-in. carrying case.

Prices for the Model 7 start at $\$ 1095$ and I/O options up the price by $\$ 225$. Additional memory costs $\$ 50$ to $\$ 200$ more, while conversion to the Model 9 costs $\$ 995$. Delivery is from stock.

CIRCLE NO. 301

Super floppy claims only 3-ms access time


## Emulator board mimics Gould printer/plotter

Varian Graphics Div., 611 Hansen Way, Palo Alto, CA 94303. Robert Altieri (415) 494-3004. \$750.

You can replace a Gould series 5000 printer/plotter with the Varian Statos 4000 series, without having to change software handler, application programs, interface or cables. All you need is the Model

40-340 emulator option to support the full speed capability and high resolution of the Statos printer/ plotter in which it is installed. The emulator consists of a single PC board, which mounts in the card cage of the Varian printer/plotter. The cable is I/O plug compatible with the Gould interface. The emulator should be ordered at the same time as the Statos printer/plotter.

CIRCLE NO. 327

Wangco Inc., 5404 Jandy Pl., Los Angeles, CA 90066. (213) 390-8081. $\$ 500$ (small qty); 4 wks.

Said to be twice as fast as any other standard drive, the Model 78 floppy-dise drive boasts a $3-\mathrm{ms}$ track-to-track access time. Other features include optional autoload, double-density capability to 6.4 M bits, GCR encoding, separation of sector and index pulses, low-power drive and daisy chain capability up to eight drives. Two units fit side-by-side into a 19 -in. rack.

CIRCLE NO. 325
Portable terminals have fine resolution


Computer Transceiver Systems, E. 66 Midland Ave., Paramus, NJ 07652. Charles Kaplan (201) 2616800. From $\$ 3495$; 4 to 6 wks .

The Execuport 3000 series are not only the first portable terminals with a 136 -column width, but they also have plotting capabilities with a resolution of 240 points per square in. ( 24 vertical by 10 horizontal). The $1 / 4$-line stepping also permits subscripts, superscripts, and underscoring. The weight (22.5 lb ) is the same as for previous models, but the visibility of the print area has been improved. The 3000 series offers built-in couplers, a choice of keyboards, and an RS232 connection for interfacing with other peripherals.


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## Fast static memory expands to 512 kbytes

Cromemco, Inc., 2432 Charleston Rd., Mountain View, CA 94043. (415) 964-7400. See text; 1 wk.

The Model 4 KZ is a 4 -kbyte static RAM card, expandable to eight banks of 64 -kbytes each. The memory operates at 4 MHz to match the Cromemco Z-80 CPU card. The $4-\mathrm{MHz}$ speed of the Model 4 KZ is achieved by using an address anticipation strategy, in which on-board address counters are incremented at the end of each machine cycle in preparation for the subsequent cycle. The 4 KZ can be organized into as many as eight banks of 64 k each. An eight-position switch on the board is used to select the bank, or banks, in which the board resides. The board is available from computer stores or directly from the factory at $\$ 195$ for the kit, or $\$ 295$ assembled.

CIRCLE NO. 328

## Fast-access diskette edits, searches strings



Western Telematic Inc., 3001 Red Hill, Bldg. 5-107, Costa Mesa, CA 92626. (714) 979-0363. \$2750 (1 to 4) ; 8-10 wks.

DataMaster II is a flexible disc I/O recorder and editing system that plugs between an ASCII terminal and its RS232 modem. Effectively, it adds $311-\mathrm{k}$ characters to your "working storage." Random access to any of 2431 lines of 128 characters each averages 0.3 s , and a complete search for strings up to 128 characters takes 12 s per disc. Edit and file-jump commands are accepted either from the unit's own keyboard, the terminal, or remote processor.

CIRCLE NO. 329

## Number always busy? This phone dials itself



Communications Electronics Specialties, Inc., 2311 E. South St., Orlando, FL 32803. (305) 896-0215. \$100; 6-8 wks.

If you suffer from having to redial that same busy number over and over, here is an inexpensive prescription: A telephone that not only gives you pushbutton dialing, but also remembers the last number you called. Just lift the receiver, and there is your partybusy again, most likely. Except for the memory-enable switch, the unit looks like any pushbutton phone, and needs no external power. An optional memory module stores as many as 10 numbers.

CIRCLE NO. 330


## Redesigned op amp replaces LM108 units

Precision Monolithics, 1500 Space Park Dr., Santa Clara, CA 95050. Donn Soderquist (408) 246-9222. From \$4.50 (100-up); stock.

A totally redesigned 108A low-input-current operational amplifier series directly replaces the popular LM108A/308A series. Key maximum specifications include offset current of 200 pA , bias current of 2 nA , offset oltage of 0.5 mV and power consumption of 18 mW (with $\pm 15-\mathrm{V}$ power supplies). The offset-voltage drift is $5 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$, and offset current drift is 2.5 $\mathrm{pA} /{ }^{\circ} \mathrm{C}$. The PM108A is specified over -55 to +125 C and the PM308 A over 0 to +70 C .

CIRCLE NO. 331

## Dual op amp has little crossover distortion



Fairchild Camera and Instrument, Analog Products Div., 464 Ellis St., Mountain View, CA 94042. (415) 962-3816. From $\$ 1.15$ (100up); stock.

Able to operate from a single power supply, the $\mu \mathrm{A} 798$ dual op amp virtually eliminates the crossover distortion usually prevalent in single-supply op amps. The $\mu \mathrm{A} 798$ maintains the standard pin-out for dual op amps. It is a high-gain, internally compensated device with an input common-mode range, which includes ground during single single-supply operation and V during dual-supply operation. The device can sink a minimum of 0.35 mA at a $0.2-\mathrm{V}$ output and a $1-\mathrm{V}$ input signal. This is a significant improvement over similar devices that can typically sink approximately 0.01 mA under similar conditions. The $\mu$ A798 comes in three versions: the $\mu \mathrm{A} 798 \mathrm{HC}$ (commercial grade in a metal can), the $\mu \mathrm{A} 798 \mathrm{HM}$ (military grade, metal can) and the $\mu \mathrm{A} 798 \mathrm{TC}$ (commercial grade, molded DIP).

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## INTEGRATED CIRCUITS

## Precision dual op amp tops 747 performance

Precision Monolithics, 1500 Space Park Dr., Santa Clara, CA 95050. Donn Soderquist (408) 246-9222. From \$3.70 (100-up); stock.

The OP-04, a dual matched high performance operations amplifier, provides improved specifications over the industry standard 747. Input-offset voltage and commonmode rejection ratio of the two op amps in the 04 are matched to within $1 \mathrm{mV}(\max )$ and 94 dB ( $\min$ ), respectively. Key maximum specifications for the individual amplifiers include input-offset voltage of 0.75 mV , input-offset current of 2 nA and input bias current of 50 nA for the OP-04A and OP-04E. All models are pin-forpin improved replacements for 747 types in both the hermetic 14-pin DIPs (Y suffix) and 10-lead TO100 packages (K suffix).

CIRCLE NO. 333

## Watch circuits display time in four languages



Intersil, 10900 N. Tantau Ave., Cupertino, CA 95014. (408) 9965000. From \$8 (100-up); stock.

Foreign-language display versions of the ICM7214, five and sixfunction alphanumeric timekeeping circuits, are now available. The ICM7214, a five-function circuit with alphanumeric capability, provides readout of hours, minutes, day, date and seconds. Its perpetual calendar must be reset only once every four years. The ICM7214A is a six-function version, which also provides a readout of the month. Both circuits interface directly with existing nine-segment LED displays. The foreign versions display the day of the week in German, French, or Italian.

CIRCLE NO. 334

## Multiplying converters settle in only 85 ns



Signetics, 811 E. Arques Ave., Sunnyvale, CA 94086. Peter Guest (408) 739-7700. From \$3.45 (100up) ; stock.

Designated NE5007/8 and SE5088, the multiplying d/a converters handle 8 -bit inputs and settle in 85 ns . The $5007 / 8$ units are pin and functionally compatible with monoDAC-08 converters originally introduced by Precision Monolithics Inc. Monotonic multiplying performance in the $5007 / 8$ units is attained over a 40-to-1 referencecurrent range. Full-scale current is prematched to $\pm 1$ LSB. Linearities are as close as $0.19 \%$ over the entire operating temperature range, which is -55 to +125 C for the SE5008. Operation from $\pm 4.5$ to $\pm 18-\mathrm{V}$ supplies is possible and the units are housed in 16 -pin plastic DIPs.

CIRCLE NO. 335

## Video i-f circuits give discrete performance

N. V. Philips Gloeilampenfabrieken, Elcoma Div., P. O. Box 523, Eindhoven, the Netherlands, J. Geel.

The TDA2540 and 2541 ICs, designed for color and high-quality monochrome television receivers, contain full video i-f circuitry. They offer performance equal to any discrete solution. The only difference between the two circuits lies in the tuner agc outputs: the TDA2540 has an output compatible with npn tuners and the TDA2541 with pnp tuners. Contained in the circuits is a noise protected agc detector that generates an age voltage for the i-f amplifier and an agc current for the tuner. A built-in afc circuit generates a voltage from the reference signal. It has a range of 10 V and is accurate to within 100 kHz .

CIRCLE NO. 336

## Dual line driver made for differential lines

Texas Instruments, P.O. Box 5012, Dallas, TX 75222. Dale Pippenger (214) 238-2011. From \$2.12 (100up) ; stock.

A dual differential line driver, the SN75158, meets the Electronic Industries Association's RS422 specification. It operates from a 5-V supply, has short-circuit protection and input clamp diodes. Inputs are standard TTL and the outputs provide differential signals with high current capability for driving balanced lines. The output stages are TTL totem-pole types that provide a high impedance state in the power off condition. The SN75158 is characterized for a 0 -to- $70-\mathrm{C}$ operating range and is offered in either plastic or ceramic 8 -pin DIPs.

CIRCLE NO. 337
Image sensors come with 128 to 1728 elements


Reticon, 910 Benicia Ave., Sunnyvale, CA 94086. John Rado (408) 738-4266. From \$25 (OEM qty.); stock.

Two families of line scanners, designated the $H$ series and the $G$ series, are available with $15 \mu \mathrm{~m}$ and $25 \mu \mathrm{~m}$ element-to-element spacings. The $G$ series is offered in $128,256,512,768$ and 1024element configurations and contains on chip monolithic drivers and video amplifier circuits. And, instead of critical multiphase clocks, the $G$ series units accept signal phase TTL drive. Device architecture is such that cascaded devices readily drive one another for very high resolution applications. The "H" series units come in 1024 and 1728 element formats for high resolution single-chip facsimile and OCR use. Both units also offer on-chip noise cancellation circuitry, which significantly improves the dynamic range otherwise limited by clock glitches.

CIRCLE NO. 338

## Multidecade counter delivers parallel BCD

LSI Computer Systems, 1235 Walt Whitman Rd., Melville, NY 11746. (516) 271-0400. $\$ 7.50$ (100-up); stock to 6 wks.

Able to be used as either a dual three-decade or a six-decade up/ down counter, the LS7040 delivers BCD data in parallel. This data format enables off-chip comparisons to be made with a minimum
of hardware. The synchronous counter operates from dc to 350 kHz , is equipped with output latches and provides a carry/borrow output for synchronous or asynchronous cascading with another LS7040. The count input may have infinite rise and fall times. All inputs are CMOS, TTL and DTL compatible when the LS7040 operates from 5 V . The circuit operates from a 5 -to- $15-\mathrm{V}$-dc supply and comes in a 40 -pin DIP.

CIRCLE NO. 339






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## POWER SOURCES

## Power-supply series offers 1, 2 or 3 outputs

SGR Corp., Neponset Valley Industrial Park, P.O. Box 391, Canton, MA 02021. (617) 828-7773. \$49 to \$159; stock to 15 days.

Members of the 140 series of power supplies deliver single outputs for logic circuits, dual tracking outputs for power op amps and other function modules, and triple
outputs for $a / d$ and $d / a$ conversion. The series contains two economy models that offer line regulation of $0.01 \%$ and load regulation of $0.05 \%$. Premium models provide $0.005 \%$-line and $0.02 \%$ load regulation. The plug-in modules operate from 105 to 125 V ac , 50 to 400 Hz and include overvoltage protection. Other features are a transient response of $1 \mu \mathrm{~s}$ and a dynamic output impedance of $1 \Omega$ at 100 kHz .

CIRCLE NO. 340


Two-output switcher pours out 750 W


LH Research, 1821 Langley Ave., Irvine, CA 92714. (213) 843-8465. $\$ 685$ (10-24); 9 wks.

The Model MMX-420 is a twooutput switching-regulated power suppy that delivers a total of up to 750 W from a package that measures $5.1 \times 7 \times 12.75 \mathrm{in}$. Primary output of the unit is 5 V at 150 A . The second output can be any one of the following: 2 V at $24 \mathrm{~A}, 5 \mathrm{~V}$ at $24 \mathrm{~A}, 12 \mathrm{~V}$ at $20 \mathrm{~A}, 15 \mathrm{~V}$ at 20 $\mathrm{A}, 18 \mathrm{~V}$ at 16 A or 24 V at 10 A . The combined power of both outputs is 750 W max. The up-to- $80 \%$ efficient device features $1 \%$ or 50 mV pk-pk ripple and noise on the output, $0.4 \%$ line regulation over the entire input range, and $0.4 \%$ load regulation from no-load to full-load. Response is $200 \mu$ s to $1 \%$ after a $25 \%$ load change. The supply operates at full rating from 0 to 40 C and derated to $50 \%$ at 70 C. The switcher contains an integral fan.

CIRCLE NO. 341

## Line regulators are efficient at all loads

Topaz Electronics, 3855 Ruffin Rd., San Diego, CA 92123. J. Pedlow (714) 279-0111. From \$265; stock.

Three series of ac line regulators boast efficiencies greater than $98 \%$ from no-load to full-load. The 73000,75000 and 77000 series offer regulations of $\pm 3.3 \%, \pm 5 \%$ and $\pm 7 \%$, respectively. These desktop models are primarily intended to protect minicomputers and related equipment from brownouts and other severe voltage fluctuations. Regulators in these series handle power in the 600 -to- 1600 -VA range. Other models are available with ratings through 100 kVA .

CIRCLE NO. 342

## 3- $\phi$ line sentinel has UL recognition

Time Mark, P. O. Box 15127, Tulsa, OK 74112. L. Fawcett (918) 9395811. $\$ 55.87$ to $\$ 68.50$; stock to 2 wks.

UL recognition has been granted the Model 263 three-phase power monitor. The unit monitors each phase of a three-phase line. Low voltage, loss of phase, or phase reversal causes the device to trip. The trip point is adjustable from the front panel. A trip light is provided. The instrument is stocked in four ranges: 85 to 125 V ac, 160 to 240 V ac, 340 to 480 V ac and 420 to 575 V ac. It is usable on either wye or delta systems.

CIRCLE NO. 343
Unit supplies 14 W and stays cool


Semiconductor Circuits, 306 River St., Haverhill, MA 01830. (617) 373-9104. $\$ 86.95$ (singles); stock.

The chassis mountable power supply, Model ES12S1200, features a typical case-temperature rise of 15 C above ambient when delivering its full-rated output of 12 V dc at 1.2 A . No derating is required over the -25 to +71 C range. The input can range from 105 to 125 V at 50 to 440 Hz . Regulation is $0.3 \%$ for line and load, and output ripple and noise is 7 mV rms . The output responds smoothly to inputpower switching and abrupt changes of load. The output terminals float, and the unit has more than 60 dB of line-transient immunity. The output is short-circuit protected by a power-foldback circuit, and the MTBF exceeds 150,000 hours at 25 C under highline and full-load conditions. The module is $2.5 \times 3.5 \times 2 \mathrm{in}$.

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CIRCLE NUMBER 91


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Bodine Electric Company, 2500 W. Bradley Place, Chicago, IL 60618 CIRCLE NUMBER 95


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## Zener diodes absorb very fast transients



Siemens Corp., 186 Wood Ave. S, Iselin, NJ 08830. (201) 494-1000. \$3.35: 1N5555 (250 up); stock; 4 uks.

A transient-absorption zenerdiode, the TAZ, has an extremely fast response time of $10^{-12} \mathrm{~s}$ and a very high surge-handling capability of 1.5 kW for less than $10^{-12} \mathrm{~s}$. The zener series is available in the hermetically sealed DO-13 caseJEDEC series 1N5555 to 1N5558, 1N5629 to 1N5665A and 1N5907and covers a range of 6.2 to 200 V . Because of the fast response time, protection characteristics and high discharge capability, the TAZ units have a wide application range.

CIRCLE NO. 345

## Power Darlingtons driven directly from ICs

NEC America, Inc., 3070 Lawrence Expressway, Santa Clara, CA 95051. (408) 738-2180.

NEC's line of $2 \mathrm{SD} 4 \mathrm{XX} / 4 \mathrm{YY}$ low-frequency amplifier and lowspeed switching transistors are npn silicon power Darlingtons that can operate directly from IC outputs without predrivers. The Darlingtons feature high-breakdown voltage and low collector saturation voltage. Common applications include hammer, pulse-motor and relay drivers. The 2SD405/410 series is particularly useful in printers, and the 2SD411/412 series is for automotive ignition and similar high-voltage applications.

CIRCLE NO. 346

## Power transistors handle 175 W

International Rectifier, 233 Kansas St., El Segundo, CA 90245. (213) 322-3331. \$7.45: 2N6546, \$10.70: 2N6547 (100-999); stock.

A new series of npn power transistors, 2 N 6546 and 2 N 6547 , rated for $175-W$ operation features exceptionally fast inductive switching even at elevated temperatures. The transistors have maximum fall times of $0.71 \mu \mathrm{~s}$ at $10-\mathrm{A}$ peak col-
lector current and $100-\mathrm{C}$ junction temperature. The units can operate at 850 V (collector to base) with a peak-collector current to 30 A . The transistors are packaged in JEDEC TO-3 metal cases. Dc current gain for the 2 N 6546 is 12 to 60 at 2 V (collector-emitter) and 5-A collector current. Gain for 2 N 6547 is 6 to 30 at 2 V and 10 A . Inductive switching times for both types is just $5 \mu \mathrm{~s}$ at 10 -A collector current and $100-\mathrm{C}$ case temperature.

CIRCLE NO. 347

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## PACKAGING \& MATERIALS

## Flat-cable connector comes fully assembled

AP Products, 24500 Chagrin Blvd., Cleveland, $O H$ 44122. (216) 464 3636. $\$ 24$ (1000); stock.

Cable-connector assemblies with the flat cable of your choice plug directly into 26 -pin shielded receptacles made by 3 M , Berg, AMP and others. The glass-filled nylon connectors have probe holes, and bal-anced-force floating contacts. They are compatible with high-speed (1.5 $\mathrm{ns} / \mathrm{ft}$ ) ribbon cables. A two-ended cable assembly runs $\$ 24$ plus the cost of the cable, in 1000-quantities.

CIRCLE NO. 348

## Cables meet EIA RS232C specifications

International Data Sciences, 100 Nashua St., Providence, RI 02904. (401) 274-5100. See text; stock-30 days.

The 8520 series of 25 -conductor cables meets the Electronic Industries Association's RS232C specification. The cable length and connector sexes on each end can be specified. One cable having a $25-\mathrm{ft}$ long conductor with a male connector on one end and female on the other, costs $\$ 27.50$ in single qty (Model No. 3520-25-MF).

CIRCLE NO. 349

## These plastic trays give you no static

Wescorp, 1501 Stierlin Rd., Mountain View, CA 94040. (415) 9697717. From $\$ 10$ (1-24); 3 wks.

Wescorp has added seven new configurations to the award-winning "Scat" line of trays, designed to prevent static failure of microcircuits during manufacture or storage. Anti-static trays range in size from $1-5 / 8$ to $9-\mathrm{in}$. dimensions, with 3 to 9 compartments per tray. Wall thickness is 40 mils. The transparent polystyrene contains a permanent agent that interacts with atmospheric moisture to produce a conductive surface coating. Therefore no conductive particles can flake off the trays.

CIRCLE NO. 350

## Heat-shrunk jackets encapsulate splices



Niemand Co., 45-10 94th St., Elmhurst NY 11373. Bob Harris (212) 592-2300. 3-4 wks.

A versatile, convenient cap for moisture-sensitive wire splices consists of a heat-shrinking Mylar jacket with a heat-expanding liner. After a few seconds at 200 to 250 F , the End-Capsulator is completely filled with a thermoplastic polyamide. The forces created by the shrinking cap and expanding liner cause the liner to flow in and around the splices, forming a waterproof seal.

CIRCLE NO. 351

## Reliable DIP socket keeps low profile

Textool Products Inc., 1410 W. Pioneer $D r$., Irving, TX 75061. (214) 259-2676. See text; stock.

The compact "Low Side" DIP socket requires up to $15 \%$ less board area than similar sockets, and features low-insertion force contacts for damage-free loading and unloading. Since the contacts do not extend above the top of the socket, they are protected from possible bending or breaking. The low sidewalls leave the device body exposed for better heat dissipation and more uniform airflow, but are high enough to act as a guide for devices with bent or distorted leads. A center slot accepts all current loading and unloading tools. The polysulfone body tolerates up to 300 C . In quantities of 10,000 , the sockets costs $74 \phi$ and $81 \phi$ each for 14 and 16 -pin versions, respectively.

CIRCLE NO. 352

## Screen printer speeds thick-film deposition

C. W. Price Co., Inc., S. Service Rd., Rt. I-78 at Jutland, R. D. 1, Hampton, NJ 08827. (201) 7359797. \$4200; 4 wks.

The Model 505 thick-film screen printer provides stable operation with tolerances in all three axes within $\pm 0.001$ in. Conductors, glasses, resists, or epoxies can be applied at a rate of 400 pieces per hour, in semi-automatic operation. When fully automated, production rates can reach 3000 pieces per hour. The Model 505 accommodates a square print area up to $2-7 / 8 \mathrm{in}$. It consists of the printer $(15 \times 30 \times 19$ in. $)$ and a controller $(12 \times 18 \times 20 \mathrm{in}$.) that provides pre-programming of power flooding, double printing and squeeze up/down adjustment.

CIRCLE NO. 353

## Card cage holds 23 PC boards in 19-in. rack

Garry Manufacturing Co., 1010 Jersey Ave., New Brunswick, NJ 08902. (201) 545-2424. \$1 to \$2 per card position; 2-4 wk.

A rack assembly, called the ECM 72 series, holds up to 23 PC boards measuring $4.375 \times 4.862 \mathrm{in}$. The board center-to-center spacing is 1.2 in . The over-all rack length is 19 in . The rack assembly also holds a 23-connector back panel. Each card connector has 35 contacts with two wire-wrappable posts per contact.

CIRCLE NO. 354

## Cases offer custom look at bargain cost

Instant Instruments, 306 River St., Haverhill, MA 01830. E. Eastman (617) 373-9260. From \$12.90 (1-9); stock.

A new series of custom-look instrument cases at bargain prices is available in two configurations. The PV-series has a handle that doubles as a stand, while the NVseries does not. Each case consists of black-textured $0.090-\mathrm{in}$. aluminum covers with 0.063 -in. brushanodized aluminum front and back panels that are easy to remove and punch. Six sizes are currently available.

CIRCLE NO. 355

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## INSTRUMENTATION

## Function generator comes with output meter

Philips Test \& Measuring Instruments, 400 Crossways Park Dr., Woodbury, NY 11797. (516) 9218880. \$675; 6-8 wks.

A new multifunction generator (sine, triangle and squarewave waveforms), the PM 5108L, offers a frequency range of 1 Hz to 1 MHz , and two outputs of 50 and $600-\Omega$ impedance. A calibrated output meter and fixed attenuators of 20 and 40 dB are also standard. The PM 5108L includes $\pm 5-\mathrm{V}-\mathrm{dc}$ offset and an externai sweep input with which the output signal can be modulated in many ways. The function generator is short-circuit proof. The output meter is calibrated in both volts and decibels. The PM 5108L has six ranges of frequency selection plus a vernier dial.

CIRCLE NO. 356

## Bit-error set detects errors at 325 MHz



Tau-Tron, 11 Esquire Rd., North Billerica, MA 01862. (617) 6673874. $M B-301, \$ 6000 ; M N-301$, \$3900; 8-10 wks.

The MN-301 transmitter and MB-301 receiver form a state-of-the-art pseudorandom generator and error-rate detector pair for use from 1 to 325 MHz . The MN-301 provides for two different PN sequence lengths, a fixed test pattern and local or remote error injection. The MB-301 receiver features automatic synchronization, variable threshold adjust and a four-digit counter and display, which allows for totalize, autorange bit-error rate or manual range BER. In addition, the counter features BCD printer output and can be used as an independent frequency or timerate counter.

CIRCLE NO. 357

Four-digit DVM is computer compatible


McKee-Pedersen Instruments, P.O. Box 322, Danville, CA 94526. (415) 937-3630. \$350.

The MP-1045 four-digit DVM features a 14 -bit BCD output and can be remotely controlled with logic levels. All necessary data and control lines are available for interfacing the DVM with a computer or peripherals such as a printer. Data can be transferred in byteparallel or in serial form. Three calibrated and three variable input ranges go from $\pm 0.3000$ to $\pm 30.00$ V full scale. The variable ranges allow you to adjust meter readings to correspond to pH units, absorbence, etc.

CIRCLE NO. 358

## Broadband amplifiers deliver up to 50 W

Ailtech, 19535 E. Walnut Dr., City of Industry, CA 91748. (213) 9654911. 2020, $\$ 4475$; 5020, $\$ 4175$; 6 whs.
Two new solid-state wideband amplifiers cover the 1 -to- $200-\mathrm{MHz}$ frequency range. Model 2020 features a linear output of 20 W with up to 35 W available. Model 5020 features linear output of 50 W with up to 75 W available. The units are usable from 500 kHz to above 225 MHz . Designed to be driven by signal sources such as milliwatt sweepers and frequency synthesizers, Models 2020 and 5020 feature a true directional wattmeter to measure forward and reflected power delivered to the load.

CIRCLE NO. 359

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Pocket size 48-page catalog includes prices, dimensional drawings, operating specifications and ordering numbers. Also covers wire, tubing, SAE right angle adapters and the Tapeless Measure distance recorder.

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CIRCLE NUMBER 108

## AUTHOR'S GUIDE



If you've solved a tricky design problem, if you have developed special expertise in a specific area, if you have information that will aid the design process.. share it with your fellow engineerreaders of Electronic Design. Articles you have authored not only raise your own professional status, but help build your company image as well. The readers benefit, your company benefits.

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Circle No.
250

## COMPONENTS

## Solid-state relay features low cost

International Sensor Systems, P.O. Box 345, Industrial Park, Aurora, NE 68818. (402) 694-6111. $\$ 8.50$ (50 up), \$3.50 (10,000 up).

A low-cost optically isolated solid-state relay, Model 750 A , features TTL compatible inputs, no contact bounce, DIP packaging, no moving parts and solid-state speed and reliability. Input/output isolation is 1500 V ac over an operating range of -55 to 100 C . The relay will typically handle a $0.5-\mathrm{A}-\mathrm{dc}$ load current with a control current of 15 mA at 2.7 V dc . The off-state breakdown voltage across the output is greater than 60 V dc. Hybrid thick-film technology ensures an input-output isolation resistance of greater than $10^{10} \Omega$.

CIRCLE NO. 360

## Miniature attenuator pad is multiturn unit



TRW, 2801 72nd St. N., St. Petersburg, FL 33733. (813) 347-2181. $\$ 2.40$ (1000 up).

A low-cost miniature multiturn bridge-T pad with $75 \Omega \pm 20 \%$ impedance (input and output) featuring five adjustment turns $\left(1800^{\circ}\right)$, provides better resolution than single-turn attenuators. The resistive pattern on the ceramic substrate of the device is a metal glaze that provides four to five times the temperature stability of carbon composition, according to TRW. Type 9950 mounts horizontally; Type 9955 mounts vertically; and each is $11 / 16-\mathrm{in}$. rectangular. They provide attenuation from 0.25 to 20 dB over a frequency range from dc to 300 MHz ; power rating is 0.125 W at 20 C ; and operating temperature range is -55 to 125 C .

## 12-button keyboard supplies 2-of-7 code



Grayhill, Inc., 561 Hillgrove Ave., La Grange, IL 60525. (312) 3541040. $\$ 7.07: 0.5 \mathrm{in} ., \$ 7.70: 0.75 \mathrm{in}$. ( 100 up ) ; 4 to 6 wks.

Low profile, 12 -button Series 87 keyboard pads feature a 2 -out-of-7 (row/column) coded output. Suited for telecommunication applications, the compact keyboard is arranged in a $3 \times 4$ button array and is available with either 0.5 or 0.75 -in. button centers. The keyboard's contact system is rated for 3 -million operations per button. The contacts have excellent audio and tactile feedback characteristics with a total button travel of only 0.015 in. Molded-in legends are standard for numbers 1 through 9 on the first three rows and $*, 0$ and \# on the bottom row. Switches may be ordered with clear snap-on caps for user legending.

CIRCLE NO. 362
Ultra-stable capacitors meet MIL-C-55514


Reliable Capacitors, 7409 Bellaire Ave. N., Hollywood, CA 91605. (213) 983-1970. \$0.79: 316 pF at $1 \%$ (1000 up) ; 4 to 6 wks.

Ultra-stable polystyrene wrap-and-fill capacitors exhibit only a $1 \%$ capacitance change at -55 C and $0.5 \%$ at 85 C . The dissipation factor varies from $0.05 \%$ to $0.06 \%$ between -55 and 85 C . Voltage derating is $50 \%$ from -65 to 85 C . Insulation resistance is $10^{6} \Omega$ at 25 C and $10^{4} \Omega$ at 85 C. The capacitors meet MIL-C-55514. Models are available from 100 pF through $2 \mu \mathrm{~F}$.


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CIRCLE NUMBER 109

## SpaceSaver: New RCA Flameproof Film Resistor Kit



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# Application Notes 

## Vector voltmeters

Various methods in which a vector voltmeter may be used, such as scattering-parameter, phase, ampgain, harmonic, group-delay and component measurements, are covered in a 12 -page paper. PRD Electronics, Syosset, NY

CIRCLE NO. 364

## V/f and f/v converters

Applications bulletin AN-20 discusses solving your measurement problems with $\mathrm{v} / \mathrm{f}$ and $\mathrm{f} / \mathrm{v}$ converters. Teledyne Philbrick, Dedham, MA

CIRCLE NO. 365

## Impatt diodes

Details of the design procedure for waveguide and coaxial amplifiers using HP Impatt diodes are given in an application note. Circuit drawings and design-aid graphs are included. Hewlett-Packard, Palo Alto, CA

CIRCLE NO. 366

## Passive filters

Applications information as well as a tutorial look at the design of passive filters and their functions are found in "Using Passive Filters." Comstron Seg, Freeport, NY

CIRCLE NO. 367

## Q standards

The theory and technique for design and construction of $1-\mathrm{MHz}$ capacitive Q standards are described in a 10-page application note. Boonton Electronics, Parsippany, NJ

CIRCLE NO. 368

## Spectrum analyzers

"Notes on Spectrum Analysis," a 22-pager, discusses the important design and operational features concerning spectrum analyzers. A simple-to-follow question-and-answer format is used. Ailtech, Farmingdale, NY

CIRCLE NO. 369

## Vendors Report

Annual and interim reports can provide much more than financial position information. They often include the first public disclosure of new products, new techniques and new directions of our vendors and customers. Further, they often contain superb analyses of segments of industry that a company serves.

Selected companies with recent reports are listed here with their main electronic products or services. For a copy, circle the indicated number.

Solid State Scientific. CMOS, memories, CBs, mobile communication and photo-mask fabrication.

CIRCLE NO. 370

Sykes. Data storage systems.
CIRCLE NO. 371

Wyly Corp. Data communication services.

CIRCLE NO. 372

WUI. International and domestic communications.

CIRCLE NO. 373

Unitrode. Power semiconductors.
CIRCLE NO. 374

Prime Computer. Small to med-ium-sized computers.

CIRCLE NO. 375

Telefile Computer. Computer accessories.

CIRCLE NO. 376

Anderson Jacobson. Data-communications equipment, keyboardprinter computer terminals and small business computer systems.

CIRCLE NO. 377

Comten. Communications computer systems and computer performance evaluation products.

CIRCLE NO. 378
Data 100. Data processing terminal systems.

CIRCLE NO. 379


## Microcomputers

An eight-page brochure describes the Mini-Micro Designer, MMD-1 education and development $\mu \mathrm{C}$ and its optional accessories. E\&L Instruments, Derby, CT

CIRCLE NO. 380

## Instrumentation recorder

Features and specifications of the Ana-log 714 instrumentation recorder can be found in a 24 -page catalog. Philips, Eindhoven, the Netherlands.

CIRCLE NO. 381

## Power supplies

Electrical and mechanical parameters on more than 180 power supplies are detailed in a 40-page handbook. Datel Systems, Canton, MA

CIRCLE NO. 382

## Arc lamp power supplies

Xenon-and-mercury-arc lamp power supplies are described in a four-page brochure. Specifications are given as well as applications in the following areas: optical instrumentation, image projection, graphic arts and medical. Electronic Measurements, Neptune, NJ

CIRCLE NO. 383

## Video equipment

Closed-circuit TV cameras and video products are highlighted in an eight-page brochure. RCA Solid State Div., Somerville, NJ

CIRCLE NO. 384

## Rack, panel connectors

Input/output rack and panel connectors are covered in a 32-page catalog. Elco, Huntingdon, PA

## Int'l switch approvals

The complexities of national standards covering snap-action switches are dealt with in a 16 page handbook. Procedures for application, specifications, markings, creepage and clearance distances and electrical-life requirements are covered. Copies may be obtained by sending $\$ 2$ in cash, check or money order to Cherry Electrical Products, P.O. Box 718, Waukegan, IL 60085

INQUIRE DIRECT

## Is there a recorder just for spectrum analyzers?



The new $19^{\prime \prime}$ rack-mounting SPECTRUM ANALYSIS RECORDER from Raytheon. It's the first dry paper line scanning recorder specifically developed for direct plug-in operation with commercially available spectrum analyzers.

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If you design and build-or buy and use-spectrum analyzers, you don't have to settle for multi-purpose recorders any more. The SAR-097 is here. For full details write the Marketing Manager, Raytheon Company, Ocean Systems Center, Portsmouth, Rhode Island, 02871. U.S.A.

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- Self-healing contacts
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