Maddocks Photo for Librascope Division, GP, Inc.

#### **OUTPUT**

		No. of Multi-
Media	Speed	plexed Channel:
Flexowriter	10 chars/sec	
Teletype via Buffer	10 char/sec	7
Charactron via disp	lay	
console	15,000-20,000 ch	/s 30
Flight strip via dia		
play console	15,000-20,000 ch	/s 30
Flight strip punch		
and printer	10 char/sec	1.2
Data Link	30 char/sec	7
Analog-Digital conv		
to radar trackers	30 char/sec	2
Inter Computer via		
buffer	200,000 char/sec	: 1

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

(For a minimum	system)
Туре	Quantity
Tubes	0
Diodes	
About 5 types	1,500
Transistors	23,000
2N393	
2N599	
2N416	
2 <b>n49</b> 8	
SN404	
2N595	
and a few other	's

#### CHECKING FEATURES

Checking features include parity on all registers, and all information exchanges between units. A dual adder is used in the arithmetic unit. Complete checking is performed.

#### POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer	3	Kw	
Power, air conditioner	2	Kw	
Volume, computer	140	cu ft	
Area, computer		sq ft	
Floor loading	20	lbs/sq :	ft
Air conditioner is inte	rnal		
Weight, computer	3,000	lbs	
Air conditioner is incl	uded in	above	

#### PRODUCTION RECORD

Number produced to date	2	
Number in current operation	1.	
Number in current production	2	
Time required for delivery	12	months

#### RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Construction techniques utilized to insure reliability includes "NOR" circuitry, RTL logic, 100% incoming inspection, rigid testing, "worse, worse" case type of design, extensive field reports on failures plus immediate corrective action, and the use of double rank registers.

#### ADDITIONAL FEATURES AND REMARKS

System is particularly suited to systems requiring random retrieval from large unsorted files and systems with large numbers of input-output devices.

Maddocks Photo for Librascope Division, GP, Inc.

#### **FUTURE PLANS**

It is planned to change to a 6 microsecond memory cycle time and increase the pulse rate, which will reduce the operation times by a factor of 4. Also, index registers will be added and the drum capacity will be increased.

#### **INSTALLATIONS**

Librascope Division of General Precision, Inc. 808 Western Avenue Glendale 1, California

### LIBRASCOPE ASN 24

Librascope ASN 24 Airborne Digital Computer

**MANUFACTURER** 

Librascope Division General Precision, Incorporated

#### **APPLICATIONS**

The ASN-24 Computer is a highly versatile general-purpose electronic digital computer which by virtue of its non-fixed internally-stored program, is easily adaptable to many commercial, scientific and military uses. In addition, its small size and weight and low power requirements make it particularly well suited for application in compact systems.

While the ASN-24 Computer can be utilized for extensive on-line general purpose computing applications, it has been designed primarily to satisfy the complex environmental and operational performance requirements of airborne/spaceborne systems realtime applications. The computations may be made from doppler derived ground speed, manually fed fixes, true heading, celestial position determination, and radio aids. Automatic inputs of the following form may be accepted. (When utilized with appropriate complementing input-output equipment):

Compass heading Astro compass heading Photo by Librascope Division, GP, Inc.

True air speed
Doppler ground speed and drift angle
Inertial velocity
Radio aids
TACAN range and bearing
Automatic sextant (Celestial altitude and azimuth)
Altitude above terrain or above sea level
An internal standard for both sideral and solar
time

Star tracker New equipment as it is developed

Information may also be fed into the computer manually. Manually stored information may be latitude, longitude, range, bearing, wind force or angle, or any direct fix data not available from the aircraft's instrument.

Basic data necessary for navigation may be set manually into the computer before take-off, or in the air.

The ASN-24 will perform the following basic computations as well as solve other desired navigational problems:

Ground position in latitude and longitude with computing errors not to exceed 0.01% of distance traveled.

Ground track

Polar navigation

Great circle course and distance to alternate destinations

Magnetic variation and true heading

Wind direction and velocity (and has provision for wind memory)

Celestial fixes

Position from radar or radio aids (and will check these fixes for credibility)

Range and bearing to a moving target

Range and bearing to a collision point with a moving target

Time to destination

Altitude and azimuth of a celestial body Image motion compensation and timing for aerial photography

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary Binary digits/word 25 Binary digits/instruction 25 Instructions/word

Instructions decoded not applicable Fixed point Arithmetic system Instruction type Two-address (One-plus-one)

The "one-plus-one" addresses are of the operand and the next instruction.

-1 to +1 - 2<sup>-21</sup> Number range

Instruction word format

_	# HOLOCOLOH	OIG IOIMGO			
	P24P23P22P21P20	P19 <sup>P</sup> 18 <sup>P</sup> 17 <sup>P</sup> 16 <sup>P</sup> 15	P14 <sup>P</sup> 13 <sup>P</sup> 12 <sup>P</sup> 11 <sup>P</sup> 10 <sup>P</sup> 9	P8 <sup>P</sup> 7 <sup>P</sup> 6 <sup>P</sup> 5 <sup>P</sup> 4 <sup>P</sup> 3	P <sub>2</sub> P <sub>1</sub> P <sub>0</sub>
	$^{ ext{T}}\!lpha$	Τβ	s β	<sup>S</sup> α	0

 $T_{\alpha} (P_{24} - P_{20})$ represents the track address of the next instruction

 $T_{\beta} (P_{19} - P_{15})$ represents the track address of the operand (except for transfer and store orders)

 $s_{\beta} (P_{14} - P_{9})$ represents the sector address of the operand (except for transfer and store

orders) s<sub>α</sub> (P<sub>8</sub> - P<sub>3</sub>) represents the sector address of the

next instruction  $0 (P_2 - P_0)$ represents the order to be performed

Transfer Orders:  $(T_{\beta}, S_{\beta})$  represents the track and sector

addresses of the next instruction if the contents of the accumulator is positive

Store Orders:

 $(T_{\beta}, S_{\beta})$  defines the location into which the contents of the accumulator is stored, or defines the modified store order to be performed

Automatic built-in subroutines include Add, Subtract, Multiply, Divide, Extract, Clear and Add, Conditional Transfer on Sign of Accumulator, Store, and Modified Store (Multiple).

Registers include 4 recirculating registers. These are the Instruction, Accumulator, Multiplier, and Multiplicand.

#### ARITHMETIC UNIT

Incl Stor Access Exclud Stor Access Microsec Microsec Add 625 156 Mult 4219 3907 4375 4063 Div Construction (Arithmetic unit only) Transistors 382 Diodes 3553 Capacitors 347 87 Transformers 1894 Resistors Arithmetic mode Serial Timing Synchronous Sequential Operation

#### **STORAGE**

No. of No. of Access Microsec Medium Words Digits 64,000 Magnetic Drum 10,000 Max 2,560 156 Min

#### INPUT

Media Speed Incremental Pulse 0-6000 pps Train Shaft Position to Sample: 100/sec Binary Coded Discs Slew Rate: 800 bits/sec Speed can be made higher

Input/output equipment must be designed for each particular application; however, the particular design and wide applicability of the ASN-24 Computer insures minimum required design effort for input/output equipment.

#### OUTPUT

Speed Media Discretes Max 100 pps

(voltage pulses)

Signals of various time lengths and amplitudes are possible. Signals used to excite other equipment, close relays, etc.
Encoder Disc Sample: 100 or 200/sec Slew Rate: 800 bits/sec

Can be coupled to synchro, potentiometer, or other similar type shaft mechanism.

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Туре Quantity Diodes 3,553 Transistors

Only silicon diodes and transistors are used for high temperature operation capability. These units have high back resistance and low leakage characteristics at high temperatures. The higher collector voltage ratings of silicon transistors permit larger logic swings, these reducing the susceptibility of the computer to noise.

The resistors are 1/4 watt, carbon composition type, have low dielectric loss, DC resistivity, and high thermal shock resistance.

Most of the capacitors are a solid tantalum type which have high dielectric strength and

have no derating of voltage over a large temperature range. The very small capacitors are the subminiature ceramic type.

#### CHECKING FEATURES

Routines programmed to check all instructions or order codes and the contents of the memory. Discrete signals, suitable for driving indicators, generated to indicate successful completion of check routines. The support equipment includes a Fill-Test Unit, which will fill and check memory contents in conjunction with a tape reader and control the computer program with one-step or loop operation. It also provides test route and synchronization signals for oscilloscope presentation of computer information and Card Checker will check operation of individual circuit and logic cards.

#### POWER, SPACE, WEIGHT, AND SITE PREPARATION

•	-				
Power, computer		v 0.189	KVA	0.7 p	ır
Does not include	1/0				
Volume, computer	·	0.55	cu ft		
Area, computer		1.42	sq ft		
Weight, computer		37	lbs		
System requires	andtahla	curfoce	table	eta	

System requires a suitable surface, table, etc., that is fairly steady, can support 31 lbs. etc. System requires only electrical power outlets, 28v DC and 3 phase, 400 cycle AC.

#### PRODUCTION RECORD

Number	produced to date	4
Number	in current operation	3
Number	in current production	12
Number	on order	16

#### PERSONNEL REQUIREMENTS

	One o-hour Shift
Supervisors	4
Analysts	1
Programmers	3
Clerks	4
Engineers	20
Technicians	7
Draftsmen	13

#### ADDITIONAL FEATURES AND REMARKS

Outstanding features include extremely wide variety of applications, operation under sea-level to space environments, light weight, low power drain, in actual operation in field, programmable high speed (200 times (sec). Integration of inputs and/or extrapolation of outputs independent of main authentic section, and data read-out for telemeters.

Basic computer unit designed and in field operation, input-output can be designed to meet a multitude of applications with minimum cost and time expenditures. Tie-in with pulse integrating accelerometers.

Magnetic Memory Drum

Capacity and Tracks:

41 tracks (1600 bits/track) of non-volatile main memory, arranged as follows: 38 tracks with 1 read head each; 2 tracks with 1 read and 1 write head each; 1 track with 1 read head and a 200 bit recirculating register.

- 1 1600 bit clock track
- 2 25 bit recirculating registers (2 registers on each of 2 tracks)
- 1 25 bit recirculating register with 4 additional heads on the same track
- 1 track with head spacings for either 200 bit or  $800\ \mbox{bit}$  recirculation.

Speed: 6,000 rpm Clock Frequency: 160 kc

Motor: Location: Contained within drum

Power: 35 watts from 3 phase, 400 cycle, 208 volt Line-Line (60 watts starting power)

Runout: 0.0001 T.I.R.

Drum Assembly Dimensions (including shroud, a head mounting surface surrounding the drum proper; cover: and heads):

6 1/2 inch diameter x 5 11/16 inches long

Drum Assembly Weight (including shroud, cover and heads):

11 1/2 lbs

Drum Surface:

The entire drum surface is milled, similar to the clock track on many other drums (i.e. slotted), with the slots parallel to the axis of rotation. There are 1600 slots around the drum periphery. After milling, the slots are filled with 3M iron oxide.

#### Heads:

Separate read and write heads are used with this drum. Minimum readback from read head is 0.4 volts peak to peak. The write head requires a 300 ma peak current of 2 microseconds duration through a half-winding. Storage tracks with only read heads requires special techniques.

Environmental Specifications

Ambient Temperature Range: -55° C to +100° C

Humidity:

Entire assembly can be hermetically sealed

Altitude:

Sea-level to space

Shock:

20g for 11 milliseconds

Vibration:

6g from 15 cps to 2000 cps

Constant Acceleration: 10g radially, 3g axially

#### **FUTURE PLANS**

Many possible new applications being investigated and radiation testing of circuitry is being planned.

### LIBRASCOPE CP 209

Librascope Model CP 209

#### **MANUFACTURER**

Librascope Division General Precision, Inc.

#### **APPLICATIONS**

System is used for airborne navigation and bombing ballistics, including loft, and real-time, high speed tracking problems.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary Binary digits/word 14 Binary digits/instruction 6

Variable-One, Two or Three Instructions/word

Instructions decoded Arithmetic system

Fixed point Incremental or Digital Differential Analyzer

Instruction type

System can process 8 operands, storing them in 3

parallel positions.

 $+(2^{27}-1)$ Number range

Automatic built-in subroutines include integration and sine-cosine.

#### ADITUMETIC UNIT

AKIIIMLIIOOMI		
	Incl Stor Access	Exclud Stor Access
	Microsec	Microsec
Add	59	5 <b>9</b>
Mult	59	59
Div	177	177 (by sub
		routine

Construction (Arithmetic unit only)

Vacuum-Tubes 304 100 Transistors Condenser-Diodes 4,500 12 A-D inputs 14 D-A outputs

Arithmetic mode Parallel Arithmetic Units

operating Serially

Timing Synchronous Operation Sequential

**STORAGE** 

Medium Magnetic Drum

No. of Words 85 Computational Blocks with 4 Integrand Lines

#### **INPUT**

Media Paper Tape Analog Digital Converters Manual Inputs

Used to fill Memory 200 divisions/sec

#### OUTPUT

Medium Speed Digital-Analog Converters 200 increments/sec

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Туре	Quantity
Tubes	
6021	33
6111	197
5784-WA	<b>3</b> 6
5639	<b>3</b> 6
Diodes	
406621	
Transistors	
2N338	
2N657	

#### CHECKING FEATURES

Checking features include a diagnostic routine programmed for maintenance.

#### POWER. SPACE, WEIGHT, AND SITE PREPARATION

Power, computer	1.76 Kw
Volume, computer	3.0 cu ft
Area, computer	1.77 sq ft
Floor loading	133 lbs concen max
Weight, computer	133 lbs

#### PRODUCTION RECORD

Number produced to date	48
Number in current operation	21
Number in current production	6
Number on order	5
Anticipated production rates	5/month
Time required for delivery	10 months

#### PERSONNEL REQUIREMENTS

One 8-Hour Shift

Operators 1 1 Technicians

Training made available by manufacturer to users includes a factory training course for maintenance

# RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Fleet service records indicate that failure-free operation time averages 90%.

#### ADDITIONAL FEATURES AND REMARKS

Outstanding features include a stored program, retraceable sine-cosine operation, K-Line scaling for flexibility and exact multiplication.

Photo by Librascope Division, General Precision, Inc.

# LIBRASCOPE MK 38

#### **MANUFACTURER**

Librascope Attack Console Mk 38 (U. S. Navy)

Librascope Division General Precision, Inc.

#### **APPLICATIONS**

The system consists of a serial, incremental, computer consisting of two identical sections working from a common control and input-output section. It is used for real-time fire control problems.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system	Binary
Number of binary digits/word	18
Number of binary digits/instruction	5

#### Photo by Librascope Division

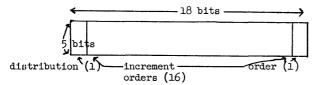
Number of instructions per word 18 Number of instructions decoded 70 Arithmetic system Fixed point Instruction type One-address operation orders Two-address increment orders Four-address distribution orders Operation orders consist of integration, remainder, digital servo, transfer, and sine-cosine generation. Distribution orders take the increment outputs of the operation orders and store them in temporary registers. Increment orders communicate the increment outputs

between the operation orders and make decisions on incremental transfers.

Number range

± 2<sup>15</sup>

Instruction word format



#### ARITHMETIC UNIT

Operation	Time
	Microseconds
Integration	72
Remainder	72
Servo	72
Transfer	72
Sine-Cosine	144
Arithmetic mode	Serial
Timing	Synchronous
Operation	Sequential

#### **STORAGE**

	0.0.0.02		
•	No. of	No. of	Access
Medium	Words	Digits	Microsec
Magnetic Drum	6,874	122,112	4/bit

#### INPUT

Media	Speed.
Analog-digital converters	Each sampled every 10
32 max, 18 used	millisec.
Switches 16 used	Each sampled every 10
	millisec.
Paper Tape	20 char/sec
Tone reader is used for	initial fill only

#### **OUTPUT**

Media	Speed
Servo output 32 max,	Repositioned every 10
1.5 used	millisec.
Relay Lighter 16 used	Repositioned every 10
	millisec.

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

OTROOTI ELLINENTS	OI LIVITIV
Туре	Quantity
Tubes	None
Diodes	
1N621	11,087
1N663	4,275
1N914	128
10Z10.7A	4
1Z12	72
SV128	1
1Z4.7	4
1N647	40
SU122	40
Transistors	
2N697	1,686
2n699	29
2N1252	144
2N1253	704
s)inli8	

#### CHECKING FEATURES

Built in Test Program. Marginal Check Power Supplies. Card Tester.

#### POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power,	computer					
Volts	cps		Kw	KVA	PF	
115	400	3	2.74	3.92	0.70	
115	400	1	1.109	2.64	0.42	
115	60	1	0.032	0.115	0.28	
28	60	1	0.29	0.29	1.00	
28	DC		0.294	0.294	1.00	
Volume	, computer		2	6.67 cu	ft	
Area,	computer			4.67 sq	ft	
Room s	lze, compu	ter	8	x 9 ft		
Power,	air condi	tion	er F	orced ai	.r	
Weight	, computer		3	,000 lbs	, Total	
			(	Includes	Analog S	ection)

Refer to OP 2687 for installation requirements.

#### PRODUCTION RECORD

Number produced to date	14
Number in current operation	3
Number in current production	6
Number on order	54
Anticipated production rates	3 per month

Above data is as of 29 June 60

#### PERSONNEL REQUIREMENTS

	One	O-Hour	Shirt
Supervisors		1	
Operators		2	
Technicians		1	

Training made available by manufacturer to users includes a 6 month course for Navy personnel at Key West for operation and maintenance of entire system.

# RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

System features and construction techniques utilized by manufacturer to insure required reliability include built in test programs, giving both identical sections the same program for comparison testing, a card tester with fixed pattern for testing all circuit cards, silicon components used for greater heat stability, and pluggable etched circuits cards for quick replacement.

#### ADDITIONAL FEATURES AND REMARKS

Outstanding features include the option to reprogram the fixed program gives the computer the facility of handling a weapons system compatible with the number and range of analog-digital converters.

#### **FUTURE PLANS**

Plans include continued adaptation of computing equipment to any rocket thrown torpedo or similar missile requirement. Incorporation of electric set and wire-guide torpedoes as well as other short range weapons.

# LIBRASCOPE MK 130

#### **MANUFACTURER**

Librascope Digital Computer Mk 130 Mod O (U.S. Navy)

Librascope Division General Precision, Inc.

#### **APPLICATIONS**

Computer performs target motion analysis, target prediction, and data smoothing for Fire Control System Mk 113.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Number of binary digits/word Number of digits per instruc-

tion

Binary 18 bits plus sign

5 bits for address orders

7 bits for non-address orders

Arithmetic system Fixed point

Floating point is programmed as a subroutine of two consecutive words; 8 bits are used for the exponent and 30 bits are used for sign and numeric value.

Instruction type

Number range

One address

0 to (2<sup>19</sup> - 1)

#### Photo by Librascope Division

#### Instruction word format

#### Addressable order

1	5	1	12
Sign	0rder	В	Address

#### Non-address order

13	5
Non-Address Orde	r I/O Device Designation or Number of places for shifts
	13 Non-Address Orde

#### Automatic built-in subroutines

The trapping of control is dependent on the overflow of the delay line (relative clock). When trapped, computer obeys the instruction in location (0002)<sub>10</sub> of memory.

Registers and B-boxes

One B Modification register, usually known as

index register.

Approximately 3,650 instructions are decoded for a fire control program.

There is also an indirect addressing feature available. A bit in the sign position is utilized for this purpose. This differs in that instead of referring to an address 0001 the desired address is found in location 0001. This process may be carried

#### ARITHMETIC UNIT

	Incl. Stor. Access	Exclud. Stor. Access
	Microsec.	Microsec.
Add	40	16
Mult	40-424	16-400
Div	40-460	16-436
Construct Arithmeti Timing	cion (Arithmetic unit conce mode	only) Transistors Parallel Synchronous

Operational Sequential The machine has a microprogram unit which controls its function. The microprogram unit is synchronous, the arithmetic unit is asynchronous.

#### **STORAGE**

	No. of	No. of	Access
Medium	Words	Digits	Microsec.
Magnetic Core Memory	4,096	Sign digit +	20
		18 digite	

No magnetic tape is associated with the Mk 130 Mod O.

#### INPUT

Media	Speed
Switch Bank on test panel	Instantaneous
Information is read direct	tly into the accumulator.
Analog Modules (Digital	157 times per second
Data from Shaft encoders)	
Information is converted in	
binary via I/O Buffer, is	read into the K Register.
From the K Register, the p	program gets the informa-
tion into the Accumulator	•
Flexowriter or Ferranti	Photo reader original
tape reader	rate 530 chars/sec
The speed is dependent on	the amount of tape on
the reel.	
Switches in Mk 50, Mk 51,	128 micro sec pulses
and Mk 75 of FCS Mk 113 and	
all Mods, and Sensor opera-	
tor's Mark Signals	
Some of these signals are	stretched to more than

#### **OUTPUT**

ricula	DPGGG
Analog Modules (Digital 1	L57 times/sec
data from shaft encoder)	
Program transfers information	
K Register, then via I/O Buf	fer to analog components.
Light Banks on test panel	Instantaneous
The accumulator and the cour	nter register are dis-
ml orrod	•

Lights on Mk 51 of FCS Instantaneous

Mk 113 all mods

2.25 seconds.

For quality of sol'n lights, relative course light, constraint lights, etc.

To relays in Mk 50, 51 and Instantaneous 75 of FCS Mk 113 all mods

To signal that range, course, speed, and bearing for a particular channel has been calculated and are available as analog information.

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

٠.				· -				**	
Туре			Quantit	ъy					
Tubes			None						
Diodes									
1N251			97						
1N270			76						
1N429			38						
1N537			11						
1N645			97						
1 <b>N</b> 659			11,762						
1 <b>N</b> 663			2,412						
1N749			22						
Does	not	include	diodes	in w	hich	less	than	10 p	er
type	are	used.							
Transia	store	3							
2N335			20						
2 <b>n38</b> 8			1,317						
2N501			3,232						
2N597			68						
2N599			2,361						
2N665			17						
Does	not	include	transia	stors	in	which	less	than	
10 pe	er ty	πpe are ι	ısed.						

82,000 Magnetic Cores

Used in computer memory and switching.

The Mk 130 Mod 0 uses "resistor coupled transistor logic" (RCTL) or "nor" logic throughout.

#### CHECKING FEATURES

Fixed checking features include a card test panel, capable of checking all circuit boards, a computer test panel, providing manual communication with all portions of computer, and a margin check panel, wherein switch settings determine voltage variation for marginal checking.

Optional checking features include a test console, which can duplicate computer test panel and in addition can furnish input/output facilities of flexowriter, high speed punch, switch inputs, camp outputs, digital shaft encoders, and output servo modules. It is used for factory checkout only.

Existing computer circuitry can be utilized to activate portable flexowriter or high speed punch without use of test console if desired. Portable input/output devices not available at this time but can be developed with minimum design effort.

#### POWER, SPACE, WEIGHT, AND SITE PREPARATION

			Power				
	Kw	KVA F	actor		Remai	rks	
Power, computer	2.85	3.15	0.9	400	срв,	зø,	115 <b>V</b>
	1.97	1.54	0.8	60	срв,	ıø,	115V
	.21	.21	1.0	261	, DC		
Volume, computer			cu ft				
Area, computer			sq ft				
Floor loading, co	244	lbs/s	1 ft				
		244	lbs c	oncer	itrate	ed me	ix.
Weight, computer	Approx. 1,647 lbs						

#### PRODUCTION RECORD

Number produced to date	1
Number in current operation	1
Number in current production	1
Number on order	8
Anticipated production rates	l per month
Time required for delivery	6-8 months

#### PERSONNEL REQUIREMENTS

Training made available by manufacturer to users includes a factory maintenance course on the Mk 130 digital computer, which comprises 3 to 4 weeks of instruction. The Mk 130 digital computer is an unmaned piece of equipment in its tactical application. Maintenance will be performed by user personnel (U.S. Navy) aided by Librascope Field Service.

# RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

All circuitry is completely transistorized and mounted in readily accessible modules. Test points have been provided on all circuit modules and chassis assemblies for ease of maintenance. A circuit module tester capable of testing all circuit modules is provided as part of the digital computer. All subassemblies are accessible from the front of the computer. Diagnostic routines are available to the user. Circuitry is conservatively designed and will operate over an ambient temperature range of O°F to 110°F. During prototype evaluation only two failures have occurred in over 1000 hours of operation

#### ADDITIONAL FEATURES AND REMARKS

Unique system advantages include a micro-program unit which can be modified to create new or variations of existing commands to tailor computer operation to suit individual situations without major redesign.

Photo by Librascope Division, General Precision, Inc.

Photos by Librascope Division, General Precision, Inc.

# LIBRATROL 500

#### **MANUFACTURER**

Librascope Division General Precision Equipment Corporation

#### **APPLICATIONS**

Manufacturer

General purpose computing where computing equipment must communicate directly with equipment external to the computer, via digital inputs or via voltage inputs.

General purpose computing where computing equipment must send control signals to equipment external to the computer.

Examples of applications are quality control for both continuous and batch production processes-real time, process control for both continuous and batch processes, and equipment test stand instrumentation (data acquisition, logging and calculation).

Frankford Arsenal

This computer is being incorporated into a breadboard of an automatic checkout system, the purpose of which is to automatically and rapidly test and evaluate the performance of combat vehicles. The above tasks include fault isolation of malfunctioning components or parts, the preparation of logictics Photo by Public Service Company of Colorado

data in the English language, and record keeping of items which pass through the test station. The first item to be so tested will be of the engine and transmission system of the M48Al combat tank.

Public Service Co. of Colorado
Located on West 3rd Avenue and Lipan Street, Denver,
Colorado, the system is used on-line for calculation
of hourly gas loads delivered into company systems by
suppliers. Input by telemeter from remote stations
through digital converter to computer. It is also
used off-line for calculation and printing of orifice
meter deliveries to gas customers, input by paper
tape, and miscellaneous engineering problems, as they
arise.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary
Binary digits/word 30 bits plus sign
Binary digits/instruction 4
Instructions per word 1

#### Integrated System

Instructions decoded 16 Fixed point Arithmetic system

Floating point is programmable. Instruction type

One address

0	1 12	13 16	17	18	19	24	25	30	31	Sp
Si.gn		Instruc- tion			Tre	ick	Sec	tor		Spacer

A complete set of compiler and utility programs are available.

Registers include counter register, accumulator, and instruction register.

#### ARITHMETIC UNIT

Incl Stor Access Exclud Stor Access Microsec Microsec 7,750 (Mean access)
23,000 (Mean access)
23,000 (Mean access) Ad.d. 250 15,000 15,000

Construction (Arithmetic unit only)

Vacuum tubes 175 Diodes 1,750 Arithmetic mode Serial Timing Synchronous Operation Sequential

#### Photo by Frankford Arsenal

Though operation is listed as being sequential, the input system of the LIBRATROL 500, since it is independent of the computing portion of the machine, is capable of inputting information while calculation is proceeding concurrently.

#### **STORAGE**

#### Manufacturer

No. of Words No. of Digits Media Magnetic Drum (Main) Magnetic Drum (Buffer) 4,096 64 126,976 1,984 Access time is variable between 500 and 15,000 microseconds.

Magnetic tape will be developed. Frankford Arsenal

No. of No. of Access Medium Words Digits Microsec Magnetic Drum 4,096 31 binary 9,000 (avg) Public Service Magnetic Drum 4,096 32 binary

615 LIBRATROL 500 Photo by Librascope Division, General Precision

1,450 approx

#### **INPUT**

Manufacturer Media Speed Analog 60 samples/sec Digital 60 char/sec Paper Tape 10 or 60 char/sec Typewriter 10 char/sec Above items are standard. Frankford Arsenal Paper Tape approx 5 char/sec Mechanical tape reader. Flexowriter used and input also available through Flexowriter keyboard. Public Service Electric Typewriter 570 char/min Analog-Digital Converter 75 words/sec Data can be supplied by punched paper tape.

#### **OUTPUT**

Manufacturer

Medium Speed Paper Tape 10 char/sec Frankford Arsenal approx 8 char/sec Flexowriter Public Service

Electric Typewriter

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Manufacturer

Туре Quantity Tubes 2D21 6an8 5915 5965

6197 3RP1A

Total 175 approx

POWER, SPACE, WEIGHT, AND SITE PREPARATION Manufacturer Power, computer 2.5 Kw 49 cu ft 13.7 sq ft Volume, computer Area, computer Room size 24 sq ft Floor loading 78 lbs/sq ft Weight, computer 1,000 lbs (nominal) A separate 115 volt, 20 ampere circuit is recommended. Frankford Arsenal 2.3 KVA Power, computer 30 cu ft Volume, computer Area, computer 10 sq ft Room size 20 ft x 60 ft Weight, computer
Public Service 1,000 lbs Power, computer 2 Kw Power, air condi (2 required) 7.25 Kw each Volume, computer 47.4 cu ft Volume, air conditioner Area, computer Area, air conditioner Floor loading 89.4 cu ft each 13.22 sq ft 29.8 sq ft each 110 lbs/sq ft 7.5 Tons, each

1,450 lbs

LIBRATROL 500

568**7** 

Capacity, air conditioner

Reinforced floor (wood).

Weight, computer

Diodes

1N617

#### PRODUCTION RECORD

Manufacturer	
Number produced to date	Over 400
Number in current operation	380
Number in current production	15
Number on order	15
Time required for delivery	3 months

#### COST, PRICE AND RENTAL RATES

#### Manufacturer

1 Libratrol 500 Computer with 120 input channels and analog to digital converter Frankford Arsenal Basic System	Cost \$84,500
Computer and Flexowriter	89,000
Additional Equipment Commutator extender Digital inputs & high speed input mode 300 magnetic latching mercury wetted relays Spare parts	5,000 20,000 20,000 5,000
D/A converters (5) Total	1,000 \$51,000
Public Service Basic System	Ψ), 000
Computer, Digitizer, 1-Flexowriter	90,000
Additional Equipment 1-Flexowriter	4,000

#### PERSONNEL REQUIREMENTS

Manuf	acturer		
	One 8-Hour	Two 8-Hour	Three 8-Hour
	Shift	Shifts	Shifts
Supervisors	1	1	1
Programmers	1	1	1
Operators	1	2	3
Engineers	1	1	1

Thirty days of instruction time is included in the sale price for programming and maintenance training of customer personnel.

Frankford Arsenal

	One 8-Ho	ur Shift
	Used	Recommended
Programmers	1.5	1.5
Method of training i	is informal	

Method of training is informal.

Public Service
The department, which is the principal user of this computer has 16 employees. The two engineers in the department, program and maintain the computer. Two girls prepare tapes and, to a large extent, operate the computer, and file tapes and work sheets. While it is planned to train additional department personnel to work with the computer, there is no need for increasing the number of employees doing any one computer.

Methods of training includes on-the-job training.

# RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Manufacturer

The combination of conservatively rated, carefully engineered components, with simplicity of design, conspires to allow the computing elements to function with only 113 vacuum tubes. Reliability should normally exceed 99% up-time over a 6 month period.

Public Service

Good time 166 Hours/Week (Average) Attempted to run time 168 Hours/Week (Average) Operating ratio (Good/Attempted to run time) 0.988 Above figures based on period 1 May 60 to present Time is available for rent to qualified outside organizations.

The reliability figures refer only to basic computer operation. We have had difficulty with input of data through the analog-digital system which has been combined with the computer. Reliability figures on the total system would be greatly lower. Because of the input problem the system has not as yet been accepted.

#### ADDITIONAL FEATURES AND REMARKS

Public Service

An outstanding feature is that the system permits computer-controlled input of telemetered values. It has two Flexowriters (off-line and on-line) which are controlled by the program.

The fact that the system permits working off-line programs and a continuing on-line program is a unique advantage.

#### **FUTURE PLANS**

Frankford Arsenal
It should be noted that the Libratrol 500 Computer has been assimilated into the Automatic Checkout equipment. It is now merely a component of the system, and is no longer identifiable as a Libratrol 500 Computer. The questionnaire answers, however, are with respect to the computer portion of the checkout system only, i. e., only the computer memory is discussed although additional memory capability is inherent in the checkout system.

Public Service Possible replacement in 5 or 6 years is planned.

#### **INSTALLATIONS**

U. S. Army Ordnance Arsenal, Frankford Bridge and Tacony Streets Philadelphia 37, Pennsylvania

Public Service Company of Colorado 900 15th Street Denver, Colorado

# LIBRATROL 1000

Libratrol Computing System Model 1000

#### **MANUFACTURER**

Librascope Division General Precision Equipment Corporation

Photo by Librascope Division, General Precision, Inc.

#### **APPLICATIONS**

System is intended for general purpose computing, where computing equipment must communicate directly with equipment external to the computer via digital or voltage (analog) inputs and where computing equipment must develop control signals to equipment external to the computer. Examples of applications are quality control for both contonuous and batch production processes-real time, process control for both continuous and batch processes, and equipment test stand instrumentation - data acquisition, logging, and calculations.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary
Binary digits/word 32
Binary digits/instruction 5
Instructions per word 1
Instructions decoded 32
Arithmetic system Fixed point

Floating point is programmable.

Instruction type Two address

Instruction word format

I			Ope	rand	Add	ress	Next Inst Address				
I	Sl	4	5	11	12	17	18	24	25	30	31
	Command		Tr	ack	Sec	tor	Tra	ck	Se <b>c</b>	tor	Address Modify Flag

A complete set of compiler and utility programs are available.

Additive index register and double length accumulator.

Lower accumulator can be made to operate on eight words at a time.

#### ARITHMETIC UNIT

	Incl Stor Access	Exclud Stor Access
	Microsec	Microsec
Ad.d.	1,000	250
Mult	17,000	16 <b>,</b> 250
Div	17,000	16,250
Arithmetic mod	e Serial	
Timing	Synchron	ous

Operation Sequential Though operation is listed as being sequential, the input system of the L-1000, since it is independent of the computing portion of the machine, is capable of receiving information while calculation is proceeding concurrently.

#### **STORAGE**

			No. of	No. of	Access
Media			Words	Digits	Microsec
Magnetic	Drum	(Main)	8,000	256,000	250
Magnetic				2,016	250
Magnetic	Tane			•	

No. of units that can be connected 64 Units Magnetic tape is a future development.

#### **INPUT**

Media	Speed
Analog	60 samples/sec (2,000 samples/
	sec optional)
Digital	60 char/sec (Standard)
Paper Tape	10/60 char/sec (Standard)
Typewriter	10 char/sec (Standard)

#### **OUTPUT**

Media	Speed
Paper Tape	60 char/sec (Optional)
Typewriter	10 char/sec (Standard)
Control (analog	120 char/sec (Standard)
or digital)	·
Line Printer	300 char/sec (Optional)

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Туре	Quantit	у
Tubes CRT Diodes	1	(digital display for monitoring)
1N617	2,400	
Transistor	s 650	(basic system)
2N1301		
2N393 2N404		
2N357		
2N597		
2N1130		

#### POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer	2 Kw
Volume, computer	48 cu ft
Area, computer	12 sq ft
Room size	24 sq ft
Capacity, air conditioner	1 Ton
Weight, computer 1,000-1,	200 lbs
Air conditioner is includ	led and self-contained

#### PERSONNEL REQUIREMENTS

One operator required for each shift. Training made available by the manufacturer to the user includes programming and maintenance.

#### **FUTURE PLANS**

Magnetic tape input and a core buffer unit are planned.

#### INSTALLATIONS

Librascope Division General Precision Equipment Corporation 808 Western Avenue Glendale, California

# LINCOLN CG 24

**MANUFACTURER** 

Massachusetts Institute of Technology Lincoln Laboratory

#### **APPLICATIONS**

CG 24 is a general purpose computer attached to a long-range radar both for receiving detected echoes and for directing the antenna. It is operated in real time primarily for the collection and processing of radar tracking data. Storage of such data is made directly into high-speed memory under program control. The research was supported jointly by the Department of the Army, the Department of the Navy, and the Department of the Air Force under Air Force Contract No. AF 19(122)-458.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system

Sinary

Number of binary digits/word

Number of binary digits/instruction

Number of instructions/word

Total number of instructions decoded

Arithmetic system

Arithmetic system

Sinary

24 plus sign

24

Fixed point

Photo by Massachusetts Institute of Technology

Instruction type

(Floating point sub-routines)

One address

Number range

 $-1 \le n \le 1 - 2^{-24}$ 

Instruction word format

Bit	0-3	4-9	10-24	
Index		Instruction	Address	

Registers and B-boxes include 5 sets of registers of 8 bits each and a real time clock register.

Negative numbers are treated in two's complement

form.

Arithmetic algorithms handle either positive or negative numbers.

#### CG-24 CHARACTERISTICS

#### **GENERAL**

CONSTRUCTION: SOLID STATE

APPLICATION · · · · · · GENERAL-PURPOSE PLUS REAL-TIME CONTROL

TIMING .....SYNCHRONOUS, 330 Kcps.

OPERATION · · · · · · SEQUENTIAL, SUBJECT TO SELF-MODIFICATION

#### NUMERICAL SYSTEM

INTERNAL NUMBER SYSTEM · · · · 27-BIT BINARY WORDS, INCLUDING TWO PARITY BITS

SINGLE-ADDRESS INSTRUCTIONS.

FIXED-POINT ARITHMETIC SYSTEM, PROGRAMMED FLOATING POINT SUBROUTINE.

#### ARITHMETIC UNIT

ADDITION TIME  $\cdots 24 \,\mu s$  (incl. memory access) MULT-DIV. TIME  $\cdots 84 \,\mu s$  ( " " ) SQUARE ROOT TIME  $\cdots 300 \,\mu s$  ( " " )

#### STORAGE SYSTEM

8192 WORDS, COINCIDENT CURRENT MAGNETIC CORES, 12 µs

#### TERMINAL EQUIPMENT

FLEXOWRITER
CRT DISPLAY WITH NUMERIC GENERATOR
PHOTOELECTRIC TAPE READER.

C314-16

Photo by Massachusetts Institute of Technology

#### ARITHMETIC UNIT

	Incl. Stor. Access	Exclud. Stor. Acces
	Microsec	Microsec
Add	24	12
Mult	84	74
Div	84	74

Construction, arithmetic unit only
Arithmetic unit consists of transistors and
diodes.

Arithmetic mode Parallel

Multiplication and division operations consist of serially adding or subtracting. Addition and subtraction are parallel operations.

Timing Synchronous

Operation Sequential and concurrent

#### **STORAGE**

		_	
	No. of	No. of	Access
Media	Words	Digits	Microsec
Magnetic Core	8,192	27/word	12
	5		
	5 x 10 <sup>5</sup>		272
binar	y words/tap	е	
No. of units that	can be conn	ected 7	Units
No. of characters/	linear inch	200	Chars/inch
Channels or tracks	on tape	7	Tracks/tape
Blank tape separat	ing each re-	cord 0.75	Inches
Tape speed		75	Inches/sec
Transfer rate		15,000	Chars/sec
Start time		5	Millisec
Stop time		1.5	Millisec
Average time for e	xperienced		
operator to change :	reel of tap	e 90	Seconds
Physical properties	s of tape		
Width		1/2	Inches
Length of reel		1,200	Feet
Composition		0.0015	in mylar

The 7-channel digital tape units are Ampex FR-300 with packing density of 200 bits/inch in each channel. These are operated at 75 in. per second. Two units were installed in August 1960.

#### **INPUT**

Media Speed
Magnetic Tape 15,000 char/sec 6 binary digits/char
Paper Tape 200 char/sec 6 binary digits/char
Ferranti Photoreader
Keyboard Flexowriter
Manual Toggle switch

#### OUTPUT

Media Speed
Magnetic Tape 15,000 char/sec
6 binary digits/char
Paper Tape 135 char/sec
6 binary digits/char
Soroban Punch
Keyboard 570 ltrs/min
on line
Flexowriter
Display with camera 18,000 octal digits/sec

Numbers are formed as Lissajou

figures from X-Y inputs

Two servo units are connected to the lower accumulator in such a fashion as to provide for program control of elevation and azimuth angle synchrodata for a radar antenna.

In June 1960 an alpha-numeric display (using the 6 bit Flexowriter code) was connected to CG 24. It has added photographic facilities. It includes two CRT's. The speed is 75 microseconds per figure or letter.

An input buffer provides for real time input of radar position and range rate data.

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Туре	Quantity
Tubes	
K1354P11M	1
K1354P7M	1
5965	1 6 2
6080	2
6073	7 4
5651	4
6BL7	1
8013	2
12AX7	2 2
2D21	2
CRT display	circuits
Diodes	
S347G	21,700
SG22	7,900
HD2085	3 <b>,</b> 600
Total	<i>3</i> 3 <b>,</b> 200
Transistors	
4JD2A6	7,950
2N123	6,250
2 <b>n</b> 385	2 <b>,</b> 850
M201Z	875
GT34	500
CK750	320
904A	185
GT83	60
Total	18,930

Magnetic Cores S-1 Ferrite

229,376

Component count as of May 1958

#### CHECKING FEATURES

#### Fixed

Core memory: parity check on each half word Magnetic tapes: parity check (IBM mode)
Perforated tapes: modulo 25 check sum.

#### Optional

Magnetic tapes: Programmer may use error correcting mode. This provides 2 error detection, 1 error correction. The mode gives 3 of the 7 tracks for data.

#### POWER, SPACE, WEIGHT, AND SITE PREPARATION

4.6 Kw (May 1960) 4.5 Kw (Including Room) 680 cu ft 110 sq ft 1,200 sq ft 5 Tons 5 000 lbs
000 lbs
1

The computer requires 110 sq. feet of floor space. A set of 3 rooms (total area 1200 sq. ft.) is devoted to computer, tapes, maintenance, stock and input buffers. The air conditioner supplies 2500 cu. ft/min. from an 11-inch high plenum underneath the computer proper. The air conditioner services other parts of the building. The 5 ton capacity is an estimated fair proportion. Computer logic power is derived from 400 cycle rotating machinery.

#### PRODUCTION RECORD

		•
Number	produced to date	1
Number	in current operation	1

System is not being produced.

#### COST, PRICE AND RENTAL RATES

CG 24 was built as part of an experimental prototype system. It cost approximately \$1,000,000.

#### PERSONNEL REQUIREMENTS

Typical Personnel	One 8-Hour Shift
Supervisors	1
Programmers	14
Operators	1
Engineers	1
Technicians	1

# RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

CG 24 has been operating for about 2 years. It has been on power almost constantly, being operated an average of 8 hours per day.

Faults have been primarily due to connections (Arkless wiring originally unsoldered has been soldered), other contacts and receptacle pins, memory adjustments (generally not component failures), and photo reader (generally not component failures). Qualitatively, it is difficult to assign many (if any) semiconductor failures to aging. Rather, most are traceable to man-made shorts.

#### ADDITIONAL FEATURES AND REMARKS

Unique system advantages include a very flexible arrangement for receiving and processing long range radar echo data, for directing antenna, and for simulating major parts of receiving and processing equipment.

#### **INSTALLATIONS**

Lincoln Laboratory
Massachusetts Institute of Technology
Lexington, Massachusetts

### LINCOLN TX Lincoln Test-Experimental Computer Model O

#### **MANUFACTURER**

Lincoln Laboratory Massachusetts Institute of Technology

Photo by Lincoln Laboratory, Massachusetts Institute of Technology

#### **APPLICATIONS**

Manufacturer

An experimental digital computer used to test ad-

vance design techniques, including very large core storage and transistor circuitry.

The research reported in this computing system description was sponsored jointly by the Army, Navy and Air Force under contract with the Massachusetts Institute of Technology.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system	Binary
Binary digits/word	18
Binary digits/instruction	18
Instructions/word	1
Instructions decoded	25
Arithmetic system	Ring-adder
Instruction type	One address
Number range	Not appropriate

Three instructions are addressable and 1 is microprogrammable.

#### ARITHMETIC UNIT

Incl Stor Access	Exclud Stor Access				
Microsec	Microsec				
6	1				
1,000	1,000				
1,000	1,000				
1,	000 transistors				
Pa	rallel				
Sy	nchronous				
Co	ncurrent				
orms 83,000 additi	ons per second.				
vide is programmed					
	Microsec 6 1,000 1,000 1, Pa Sy Co				

#### **STORAGE**

Media	Words	Digits	Microsec	
Magnetic Core	65 <b>,</b> 536	,536 18/word		
Flip-flop	1	18/word	0.5	
Toggle Switch	16	18/word	3	
A parity bit is	additional.	Read-rewrite	time is	
6 microseconds.				

LINCOLN TX 0 and TX 2 Memory Plane

Photo by Lincoln Laboratory, Massachusetts Institute of Technology

#### **INPUT**

Media	Speed.
Photo Reader	250 lines/sec
Flexowriter	Manual
Toggle Switch	Manual

#### **OUTPUT**

Media	Speed.
Flexowriter	10 char/sec
Display (CRT)	16 microsec/spot

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Tubes	440
Tube types	3
Crystal diodes	350
Magnetic cores	1,245,773
Transistors	3,500
Separate cabinets	. 5

Three major tube types, a small number of others. Most tubes are used in the large memory. The transistors are the Philco L-5122 Surface Barrier Transistor.

#### CHECKING FEATURES

Parity check on memory systems. Marginal checking is built in.

### POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer	lo Kw	
Volume, computer	1,000 cu ft	
Area, computer	200 sq ft	
Capacity, air conditioner	40 Tons	
Weight, computer	4,000 lbs	
Above figures are appro-	ximate. Air conditioner	18
necessary for memory only	•	

#### PRODUCTION RECORD

Number	produced	1
Number	in operation	J.

#### ADDITIONAL FEATURES AND REMARKS

One picture shows close-up view of magnetic core memory plane and other picture shows random-access core memory, frame of memory-core selection-switch drivers, computer arithmetic element and control element, and computer operating console.

#### **INSTALLATIONS**

Lincoln Laboratory Massachusetts Institute of Technology Lexington 73, Massachusetts

### LINCOLN TX

Lincoln Test Experimental Computer 2

#### MANUFACTURER

Lincoln Laboratory Massachusetts Institute of Technology

#### APPLICATIONS

Computing system is used for scientific research and for the simulation, analysis, and control of real time systems.

#### PROGRAMMING AND NUMERICAL SYSTEM

Binary Internal number system Binary digits/word 36 + 1 + 1 Binary digits/instruction 36 + 1 + 1

Instructions per word Instructions decoded 64

Arithmetic system

Fixed point (Ones complement binary) Instruction type

Indexable; Indirect addressing on all instructions

Number range  $-(1 - 2^{-35})$  to  $(1 - 2^{-35})$ 

Instruction word format

1	1	5	6.	6	1	17
meta bit		configu- ration reg. no.			indirect address bit	

Photo by Lincoln Laboratory, MIT

All fixed programs are in toggle switch or plugboard storage.

Automatic coding includes standard compiler, which provides full symbolic coding facilities.

All four arithmetic registers and the exchange register are addressable as part of memory. There are sixty-four 18-bit parity-checked index registers.

Indirect addressing can be repeated indefinitely. 33 program (instruction) counters are provided, only one of which is used at a time.

Each in-out unit is associated with a program counter. Choice of program counter is determined by in-out unit, by program, and by relative priority of program counters.

Any instruction can specify a configuration of the computer during the execution of the instruction. A 36 bit operand word can be divided into one 36, one 27 and one 9, two 18, or four 9 bit subwords formed from the 9 bit quarters. The 9 bit quarters can be permuted among themselves. Any or all of the subwords can be used simultaneously. For example, two 18 bit multiplications are done by one multiply instruction in less time than one 36 bit multiplication.

#### Memory Stall

Word Length in Bits

Arithmetic Operation

One bit of each computer memory word is used for parity checking. The other is used as a tag bit for program debugging.

#### ARITHMETIC UNIT

	Incl. Stor. A	Access Exclud. Stor. Access
	Microsec	Microsec
Add.	4.8	1.4
Mult	9.6 <b>- 1</b> 9.2	5 - 17 (9 bit-36 bit)
D±v	19.6 - 80.0	) 17.2 - 75 (9 bit-36 bit)
Construc	tion (Arithmet	ic unit only)
Transi	stors	8,800
Arithmet	ic mode	Parallel
Timing		Synchronous
Operatio	n	Concurrent
of arith		lists the number of thousands ons of a given type which can

36 + 200 x 50 ÷ 13

Photo by Lincoln Laboratory, MIT

#### **STORAGE**

		Read-				
	No. of	Write	No	. of		
Media	Words	Time	Digi			Microsec
Magnetic Core	65,536	6.4	36	+ 1 +	1	3.4
S Memory						
Magnetic Core	4,096	4.4	36	+1+	1	2.2
T Memory						
Toggle switch,	80	4.8	36	+ 1		2.6
plugboard, et						
Magnetic Core	64	3.4	18	+ 1		0.6
Index Memory						
Magnetic Film	32	0.8	9	+ 1		0.3
Config. Memor	Ϋ́					
Magnetic Tape						
No. of units t						Units
No. of lines /						Lines/inch
Channels or tr						Tracks/tape
Blank tape ser	parating	each r			-	Inches
Tape speed						Inches/sec
Transfer rate			3,300			Chars/sec
Start time						Millisec
Stop time			10,	000-25	00	Millisec
Physical prope	rties of	tape		_	۸.	
Width	_					Inches
Length of re	eT			7,200		
Composition				мутал	: 1	type 189 3M

Tape reels are not changed.

9 800

400 200

400 17 48 Fixed address system (like drum). Variable read speed.

 $^{5/2}$  tape unit drives can be treated as  $10^{10}$ -bits of internal storage.

14" NARTB reel.

Recording channels are paired. One pair is used for timing marks, another for block marks, and the remaining three for information. Three lines of information form the standard unit of information, a 9 bit character.

#### INPUT

Media Speed

Paper Tape 3000 7 bit lines/sec peak speed Speed is not constant. Accelerates slowly compared to line width.

Keyboard 10 6 bit char/sec

Lincoln Writer input

Analog-Digital Converter 40,000 11 bit samples/sec

Epsco Datrac converter

Light pen/eye Manual Signals selected by operator

Random No. Gen. 18,000 9 bit words/sec

Radioactive source

PACE plotter

Miscellaneous Input 80 KC

9 channel pulse input to computer from miscellaneous devices.

#### **OUTPUT**

Media	Speed
Paper Tape	180 7 bit lines/sec
Soroban punch	•
Xerox printer	20 lines/sec
	1300 char/sec
88 characters can be	printed in 2 sizes. 6 bit
vert. & 9 bit horiz.	axes resolution.
Typewriter	10 6 bit char/sec
Lincoln Writer output	;
CRT point display & Can	nera 10 KC - 40 KC
10 bit resolution in	both axes
Miscellaneous output	Up to 500 cycles
9 channel switch for	computer control of low rate
devices	
Large board plotter	15 in/sec slew speed

Several input-output units can operate simultaneously so long as the time required by all the units operating does not saturate the central computer. Each unit has at most a single-line buffer; whenever a line of data needs to be transmitted to or from the central computer the unit causes the central computer to use its associated program counter. The machine can compute while in-out units are operating.

At peak rate, about 80,000 computer words/sec can be transferred into or out of the computer.

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Туре	Quantity	Use
Tubes 6888 5998 Z <b>-</b> 2177	69 312 296	Clock pulse amplifiers S memory
Misc. Types	296 88	S memory
Diodes		
CTP592	3,000	Input-output circuitry
1N625	736	Input-output circuitry
Misc. Types	1.488	Input-output circuitry

Trans	istors			
L5122		26,042		
L5134		31,928		
2N501		320		
2N357		1,016		
Misc.	Types	2,227		
Magne	tic Cor	es		
_		2,490,880	S	memory
		155,648	T	memory
		2.432	Х	memory

All the vacuum tubes are used in the 65,536 word memory and in the generation of the computer clock pulses.

Resistor coupled transistor logic in the central computer operates at a 5 megapulse per second rate.

Thin magnetic film memory contains 320 magnetic spots.

#### CHECKING FEATURES

Checking features include a single bit odd parity check on all memories, a voltage margin check on all bias voltages, and a manual switching system selects circuits to be checked. A built-in sync system facilitates locating machine errors. A library of test programs are used which check the operation of the computer and which attempt to induce errors.

#### POWER, SPACE, WEIGHT, AND SITE PREPARATION

20 Kw	25.6	KVA	0.8 pf
17 Kw			
	1,500	sq ft	
	54	x 29	
er	17	x 20	
•	25	Tons	
	17 Kw	17 Kw 1,500 350 54 er 17	1,500 sq ft 350 sq ft 54 x 29 27 x 20

Cables run through overhead wireways. Air conditioning ducts also runoverhead. An 8 ft high false ceiling is hung to cover these. Otherwise building is standard. Most power supplies are solid state. Principally required for memories.

#### PRODUCTION RECORD

Number produced to date	1.
Number in current operation	1
One-of-a-kind research computer	

#### PERSONNEL REQUIREMENTS

Problem originators are trained to use the computer. Paper-tape preparation facilities and utility programs available to all computer users.

Three engineering assistants and one technician are available to do routine maintenance and to make changes in the computer system.

# RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

As a research machine, TX-2 operating experience is good but though data is kept on machine failures, no reliability figures have been computed.

Basic circuits and components are similar to MIT's TX-O machine.

#### ADDITIONAL FEATURES AND REMARKS

Outstanding features include an operating thin magnetic film memory; 65,536 word magnetic core memory. Fixed address magnetic tape system. Multiple-sequence operation of computer and simultaneous operation of input-output units permits considerable flexibility in use of in-out units. Maximum execution time for any one arithmetic instruction can be reduced to one memory cycle time by overlapping instructions and memories.

Unique system advantages include multiple-sequence operation, configuration control over operands, thin magnetic film memory used in control element of computer, and 64 index registers stored in random access magnetic core memory.

magnetic core memory.

The Lincoln Writer input-output unit permits considerable flexibility in communicating with the computer.

#### **FUTURE PLANS**

Another 4096 word magnetic core memory will be installed in order to increase opportunities for overlapping operation of memories.

A magnetic tape unit will be installed which will be compatible with units used on many commercial computers.

Input-output units will be added as the needs develop.

A new control console will replace the present console.

#### INSTALLATIONS

Lincoln Laboratory Massachusetts Institute of Technology P. O. Box 73 Lexington 73, Mass.

629 LINCOLN TX 2

### ITTON C 7000

Litton Industries, Model C 7000

#### APPLICATIONS

System is designed for real time control systems applications requiring very high computing rates.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary Binary digits/word Binary digits/instruction 21 Instructions per word Instructions decoded

Arithmetic system Fixed point

Two's complement system of arithmetic is used

Instruction type One address - 1 to + 1 - 2<sup>-20</sup> Number range

Instruction word format

00	01	<b>0</b> 6	07	09	10	20
Break- point	Operation Code			lex lg	Add	ress

Automatic built-in subroutines include square root and gray code to binary conversion.

Three index registers are included.

#### ARITHMETIC UNIT

Incl Stor Access Exclud Stor Access Microsec Microsec Add 4 2 Mult 26 (Average) 22 46 42 Div

Arithmetic mode Parallel Synchronous Timing Operation Sequential

Special hi speed multiplication technique is used. There is extensive overlapping in the execution of sequential instructions resulting in a very high speed computer.

#### **MANUFACTURER**

Litton Industries Electronic Equipment Division

#### **STORAGE**

	No. of	No. of	
Media	Words	Digits(Binary)	Microsec
Cores	1,280	28,160	4
Magnetic Drum	12,800	281,600	5,000 (Avg)

#### INPUT

Media Speed 128 Word Drum Buffer 250 microsec avg to 1st word 4 microsec thereafter

Flexowriter 10 char/sec Paper Tape 200 char/sec

#### **OUTPUT**

Media Speed 512 Word Drum Buffer 1,000 microsec avg to 1st word 4 microsec thereafter

Flexowriter 10 char/sec

#### CHECKING FEATURES

Parity checks are built in on all core and drum memory transfer and storage. Program is automatically interrupted when error occurs and is detected.

#### POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer 0.950 Kw 9.5 cu ft 4 sq ft Volume, computer Area, computer Weight, computer 320 1bs

# LITTON DATA ASSESSOR

**MANUFACTURER** 

Litton Industries Data Assessor System

Electronic Equipments Division Litton Industries

Photo by Litton Industries Electronic Equipments Division

#### **APPLICATIONS**

System is designed for general purpose computing and for special purpose problems, which take advantage of the internal information transfer in the computer. The useful characteristics are dual half word arithmetic (simultaneous operation on two half words), real time input-output functions, large amounts of data comparison, external control and communication, and computer-computer communication.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary Binary digits/word 32 Binary digits/instruction 32 Instructions per word 1 Instructions decoded 55

Arithmetic system Fixed point

30 bits plus sign or two half words each of 15 bits

plus sign Instruction type Number range

One address Either  $\pm$  (2<sup>30</sup> - 1) or two half words of  $\pm$  (2<sup>15</sup> - 1)

Input-output functions are automatic. All B-box operations are included within each order. These include choice of B-box and whether the B-box itself should be modified.

Each command associated with information transfer contains stencil bits which allow full word, half word, or shifted half word transfer. In addition, it is possible to transfer a word logically multiplied by a stencil word.

#### ARITHMETIC UNIT

	Incl Stor Access	Exclud Stor Access Microsec
Add	12	6
Mult	60-102	48-90 (half or full word
Div	60 <b>-1</b> 02	48-90 (half or full word
A square ro	ot order is inclu	ided. It takes 96 or 186
microsecond	s.	
Constructio	n (Arithmetic uni	it only)
Transisto	rs 1,600	)
Diodes	17,000	)
Arithmetic:	mode Parall	lel
Timing	Synchi	ronous
Operation	Concur	rrent

#### **STORAGE**

	No. of	No. of	Access
Media	Words	Digits	Microsec
Cores	1,024	32	6
Drum	2,560	32	6-17,000

Up to eight independent magnetic tape units could be connected to the computer.

#### INPUT

Media	Speed
Data Link	750 bits/sec
Analog/Digital Converter	20 bits/6 microsec
Control Panel	•

Link and converter are programmable.

#### **OUTPUT**

Media	Speed
Cathode Ray Tube	20 bits/6 microsec
Control Equipment	30 bits/6 microsec

Outputs are programmable. The input-output equipment specified is for a special purpose application. The input-output buffers can accept serial or parallel information up to a total maximum rate of 32 bits each 6 microseconds. The ability of each input buffer is programmable, but the various functions are controlled by the availability of external data.

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM FUTURE PLANS

	I OTOKE I LANG	
Туре	Quantit	У
Diodes	23,000	
	1,000	memory
	16,000	logic
	6,000	clamps on the out-
		puts or inputs of
		flip-flops or boosters
		or double inverters
Transistors	3,100	
	275	flip-flops at 4 each
	200	power boosters at 2

each
800 double inverters at

2 each

Magnetic Cores 34,000

The basic building blocks of the system are 4 transistor flip-flops, 2 transistor power boosters, and 2 transistor double inverters. In addition, there are several cards of fast adder carry propogation logic. The major number of cards in the system contain diode logic. There are numerous cards which are required for the mechanization of the core memory. Plans include provision of a separate memory for input -output to avoid interference with computation, additional memory modules, and geranium modules with silicon for wide ambient temperature range.

#### **CHECKING FEATURES**

There exists a parity bit in each word in core storage. If a parity bit failure occurs, or an incorrect order occurs, the error flip-flop turns on. Program can be used to interpret error information.

Program can be used to interpret error information Under operator control, an error will either turn off the computer or cause the program to tally and classify such errors.

#### POWER, SPACE, WEIGHT, AND SITE PREPARATION

ŀ	OWER, SPACE,	WEIGHT, AND SITE PREPARATION
	Power, computer	1.5 Kw 2.0 KVA 0.75 pf
	Volume, computer	21 cu ft
	Area, computer	9 sq ft
	Room size	7 ft x 7 ft x 7 ft high
		(Suitable for Helihut use)
	Floor loading	60 lbs/sq ft
	Weight, computer	500 lbs
	The computer w	lll work in an environment from 20°F
	to 100°F, and as	such, no air conditioning was

The computer will work in an environment from 20°F to 100°F, and as such, no air conditioning was planned for the initial installation. Present system operates at 60 cps. A 400 cps model can be made available.

#### PRODUCTION RECORD

Number in current production

Number on order

Time required for delivery

6-8 months

#### PERSONNEL REQUIREMENTS

One operator is required for each shift. The present mechanization contains a self checking program and as it works on a real-time problem, it proceeds automatically. Because of this, a single operator is all

That 15 FROM THE OPERATING EXPERIENCE.
AND TIME AVAILABILITY
ADDITIONAL FEATURES AND REMARKS

The design of the circuits is based upon very conservative techniques in order to assure a high degree of reliability. The worst-case method of analysis has been used to insure that the circuits operate satisfactorily while being subjected to the most adverse combination of component tolerances and parameter variations. In addition, all of the components have been derated with respect to allowable dissipation, voltage, etc. Component characteristics, such as current gain, are based upon history-derived estimate of the end-of-life value.

Provision is made in the Data Assessor for automatic checking. If a parity error should occur in the core memory, or if an incorrect order should be read, the appropriate error flip flop will turn on and this information would be entered into the computer. A switch on the control panel can be set to either stop the computer after an error, or allow the program to analyze the error. The program can be set to determine and tally the various types of errors. The program can then either correct them if they occur rarely or stop the computer and alert the operator if they occur often.

The Data Assessor is mechanized to provide all the error checking information necessary for complete error detection and checking. The amount of error checking performed depends on the type of program used. Outstanding features include self modifying B-box operation, built in stencil in each order, dual half word arithmetic, programmable (serial or parallel) input-outputs, alarm clock, and simplified communication between computer components. The computer operates at the rate of about 75,000 operations per second. Each operation can occur between two sets of independent variables, such as X, Y navigation. Inputs-outputs do not interrupt the program.

63

# LOGISTICS

#### **MANUFACTURER**

Engineering Research Associates, Inc.

#### **APPLICATIONS**

Located at the Staughton Hall, 707 22nd Street, NW, Washington 7, D.C., the system is used for the numerical simulation of naval operations in the area of supply, mathematical studies in the area of matrix games, situations of attrition, and certain kinds of war game studies. System is also used directly as a research instrument in the development of concepts bearing upon data processing operations by the military.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary coded decimal Digits per word 12

Arithmetic system Fixed point

Instruction type Three address (approx)

Number range  $-(5.10^{11} - 1) \le N \le (5.10^{11} - 1)$ 

Instruction word format

Normal mode of program storage is by way of wired instructions (many address) on a plugboard. A program may be stored internally and decoded by means of a control program wired on a plugboard. This latter method has been little used because of its low speed.

There are 15 registers.

Photo by George Washington University

#### ARITHMETIC UNIT

Operation Incl. Stor. Access Microsec Add 500

Mult 500 - 1000 Div (Programmed division)

Construction (Arithmetic unit only)
Vacuum-Tubes 4,500
Condenser-Diodes 5,000

Arithmetic mode Serial
Timing Synchronous
Operation Sequential

System is serial in decimal digits, parallel in bits for each decimal digit.

#### **STORAGE**

No. of No. of Access Media Words Dig/Word Microsec Magnetic Drum var. 14,000-37,000 12-4 16,000 16,000 220 Ke Magnetic Drum 7,000-80,000 60-4 15 12 High speed Shift rate registers Total digit capacities of drums are about 185,000

and 400,000 respectively.

LOGISTICS 634

Second drum has usual read and write. The location or absence of a given word may be determined in one revolution by means of one of a system of SEARCH instructions.

#### INPUT

Media Speed
Card (collator) 240 cards/min
Paper Tape 200 columns/sec

Paper tape input are Ferranti readers. Card rate is alternating from each of two pockets.

#### OUTPUT

Media Speed
Card 50 cards/min
Paper Tape 60 char/sec two
Two paper tape outputs are teletype perforators.

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Type Quantity
Tubes 4,500
Diodes 5,000
Transistors 10,000
Magnetic Cores 3,240 (bits)

#### CHECKING FEATURES

Most commonly used checking feature is that machine will detect presence of non-numeric binary codes and will stop or branch.

#### POWER. SPACE, WEIGHT, AND SITE PREPARATION

KVA, computer 19
KVA, air conditioner 22
Volume, computer 910 cu ft
Volume, air conditioner 130 cu ft
Area, computer 150 sq ft
Area, air conditioner 18 sq ft
Room size, computer and 918 sq ft

air conditioner
Floor loading 7.4 lbs/sq ft
86 lbs concen max

Capacity, air conditioner 11 Tons

Computer was installed in a gutted area of building with heavy floor. Further ceiling support was provided. Motor generator set was installed in a small addition to main building. Building was an apartment house.

#### PRODUCTION RECORD

Number produced to date 1
Number in current operation 1

#### COST, PRICE AND RENTAL RATES

Cost/Price for basic system

Computer and one drum \$350,000 Ferranti readers and 4,000 tape punches

Total \$354,000

Cost/Price for additional equipment Input Output buffer \$ 95,000 Second drum (see above) 200,000

Computer and equipment cited is owned by Office of Naval Research.

Rental rates for additional equipment includes \$200/monthly for collator and punch used for Input-Output.

#### PERSONNEL REQUIREMENTS

One 8-Hour Shift
Analysts 3
Programmers 4

Clerks 2 keypunch operators Engineers 2

2

Operation tends towards open shop.

Technicians

Method of training used is "write, load and debug."
Additional shift operations are filled in by programming staff or by the hiring on temporary basis of "computer Watchers" when tending is all that is required.

No increase in engineering staff would probably be required if we ran 2 shifts all the time.

Computer is capable of protracted good time. Personnel cited above is approximation of relatively informal organizational setup.

The engineers cited are capable of work at all levels of electronic engineering and have extensively modified the original computer (delivery February 1953) both logically and in the matter of capacity of certain elements.

# RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Good time 37 Hours/Week (Average) Operating ratio (Good/Attempted to run time) 0.9 Above figures based on period 1 Jan 56 to 30 Jun 56 Time is available for rent to outside qualified organizations.

Our down time figures would indicate that our "would have run if computer were OK time" would be about 10% of our good time, hence the indicated ratio. During 1958 and 1959 a heavy modification program has reduced good time. Our current (last six months) good time is 16, with a somewhat lower operating ratio, i.e. 0.75.

#### ADDITIONAL FEATURES AND REMARKS

The search logic as noted above. Given that a certain register contained the word w, one or another of the search instructions would determine its absence, the address of its location )somewhere), or of its "next" location, or of its "first" location in about 16 milliseconds.

#### **FUTURE PLANS**

A new adder is being built for this machine. The operation is serial and digital addition will occur every two drum (shift) clocks instead of during the 7 now used.

Division, ll decimal digits divided by ll decimal digits plus signs will be installed.

#### INSTALLATIONS

The George Washington University Logistics Research Project 707 22nd Street, N. W. Washington 7, D. C.

635 LOGISTICS

## MAGNEFILE B

Magnefile Electronic Data Processing System B

#### **MANUFACTURER**

Electronics Corporation of America Business Machines Division

## **APPLICATIONS**

Inventory control for retail sales department store. Special purpose, no longer in production.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Decimal Decimal digits/word 8

Instructions/word Instructions wired in

Instructions used 7
Arithmetic system Fixed point
Instruction type One address

Three addresses are entered simultaneously.

## **ARITHMETIC UNIT**

Add time (exclud stor access)

Construction

Basic pulse repetition rate
Arithmetic mode
Timing
Operation

150,000 microsec
Vacuum tubes
30 Kc/sec
Serial
Asynchronous
Sequential

#### **STORAGE**

Medium Words Access Microsec Magnetic Drum 4,040 300,000

## INPUT OUTPUT

Medium Speed
Full Keyboard 4 char/sec
Typewriter 7 char/sec

Photo by Electronics Corporation of America

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Tubes 130 6 Types Crystal diodes 40 Different plug-in units 10 Separate cabinets 1

#### CHECKING FEATURES

Continuous checking total

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer 0.6 Kw
Area, computer 3.5 ft x 2.5 ft
Weight, computer 400 lbs

## COST, PRICE AND RENTAL RATES

Approximate cost of basic system \$20,000.

#### PERSONNEL REQUIREMENTS

One operator required during operation. A service technician is called when needed.

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Passed Customer Acceptance Test 15 February 1954.

## INSTALLATIONS

B. Altman and Company Fifth Avenue New York, New York

MAGNEFILE B 636

## MAGNEFILE

e Electronic Data Processing System Model D

### **MANUFACTURER**

Electronics Corporation of America Business Machines Division

#### **APPLICATIONS**

Inventory control. No longer manufactured.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Decimal Decimal digits/word 42

Instructions/word Instructions wired in Instructions used 77 Fixed point Arithmetic system

Instruction type One address Three one-address commands are entered simultaneously.

## ARITHMETIC UNIT

Add time (exclud stor access) 100,000 microsec Construction Vacuum tubes Basic pulse repetition rate 40 Kc/sec Arithmetic mode Serial Timing Asynchronous Sequential Operation

#### **STORAGE**

Media Words Access Microsec Magnetic Drum 8,000 50,000 Magnetic Drum 500 50,000 The larger drum stores 8,000 21 dec dig words. The smaller drum stores 500 42 dec dig words.

## INPUT OUTPUT

Media Speed

Full Keyboard Manual (4 char/sec) Typewriter 10 char/sec

Remote keyboards may be added.

Photo by Electronics Corporation of America

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

140 4 Types Tubes Crystal diodes 240 Different plug-in units 12 Separate cabinets 1.

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer 1. Kw 5 ft x 3 ft Area, computer 700 lbs Weight, computer

## PRODUCTION RECORD

Number produced No longer in production

## COST, PRICE AND RENTAL RATES

Approximate cost of basic system \$50,000. No longer in production.

## PERSONNEL REQUIREMENTS

One operator. On call technician.

RELIABILITY, OPERATING EXPERIENCE.

AND TIME AVAILABILITY

Passed Customer Acceptance Test 5 August 1953.

#### INSTALLATIONS

B. Altman and Company Fifth Avenue New York, New York

## MANIAC I

Mathematical Analyzer Numerical Integrator and Computer Model I

## **MANUFACTURER**

University of California Los Alamos Scientific Laboratory

Photo by University of California, Los Alamos Scientific Laboratory

## **APPLICATIONS**

University of New Mexico Research Center Located at University of New Mexico Research Center, 2206 Lomas Blvd., N.E. (Box 181), albuquerque, New Mexico, the system is used for general purpose scientific computations, providing a computer service to faculty and students of the university.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system	Binary
Binary digits per word	40
Binary digits/instruction	8
Instructions per word	2
Instructions decoded	<b>3</b> 6
Binary digits/instruction	
not decoded	2
Instructions used	35
Arithmetic system	Fixed point
Instruction type	One address
Number range	$-1 \leq n < 1$

## ARITHMETIC UNIT

	Exclud Stor Access
	Microsec
Add time	.80
Mult time	1,000
Div time	1,000
Construction	Vacuum tubes
Arithmetic mode	Parallel
Timing	Asynchronous
Operation	Concurrent

## **STORAGE**

0

MANIAC I 638

## **INPUT**

Media Speed
Paper Tape 1,024 words in 48 seconds
Magnetic Tape 1,024 words in 45 seconds

#### **OUTPUT**

Media Speed
Printer (Teletype) 36 words/min
Printer (Anelex) 3,600 words/min
Paper Tape 81 words/min
Magnetic Tape 81,024 words in 45 seconds

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Tubes 2,400
Tube types 7
Crystal diodes 500
Different plug-in units 1 Electrostatic 6 Magnetic Drum
Separate cabinets 4

Type 2BP1 cathode ray tubes (Williams) are used in the storage unit.

## CHECKING FEATURES

Check sum on filling storage by paper tape and magnetic tape.

Check sum on filling magnetic drum.

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

University of California
Power, computer 35 Kw

Volume, computer 128 cu ft Area, computer 20 sq ft Capacity, air conditioner 10 Tons

U of New Mexico

Site preparation include channeled floor for air conditioning and power, ceiling ducts for air conditioning, and a special room for the motor-generators (D.C. supply).

## COST, PRICE AND RENTAL RATES

Approximate cost of basic system \$250,000. Approximate cost of high speed printer and magnetic drum \$48,000.

Prices include development, construction and overhead.

## PERSONNEL REQUIREMENTS

U of New Mexico

System is to be operated and maintained by one person. It is expected to be operating prior to 1 Jan 1961 at the University of New Mexico.

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

U of New Mexico

Average error-free running period 5 Hours
Good time 11,493 Hours

Attempted to run time 12,399 Hours
Operating ratio (Good/Attempted to run time) 0.93

Above figures based on period from Mar 52 to Jan 57

Passed Customer Acceptance Test Mar 52

Time is available for rent to outside organizations.

The machine was moved to the University of New
Mexico in 1958. Installation was started in 1959.

#### **FUTURE PLANS**

U of New Mexico
The machine is being studied by approximately six graduate students at the Master of Science level, with three masters thesis being written on proposed system changes, including, replacing the single channel tape system with a multi-channel system, a logical study of a program interchange between MANTAC I and a NATIONAL 102A, and modifications to the existing adder in the machine.

#### INSTALLATIONS

University of New Mexico Research Center 2206 Lomas Blvd. N.E. Albuquerque, New Mexico

## MANIAC II

Mathematical Analyzer Numerical Integrator and Computer Model II

## **MANUFACTURER**

University of California Los Alamos Scientific Laboratory

Photo by University of California, Los Alamos Scientific Laboratory

## **APPLICATIONS**

University of California, Los Alamos Scientific Laboratory

Located at Los Alamos, N. M., the system is used for studies in automatic programming, symbolic manipulations (e.g., algebra, differential calculus), mathematics, esp. combinatorial and algebraic transformations, Monte Carlo, crystallography, and general applied mathematics.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system	Binary
Binary digits/word Binary digits/instruction	48
Binary digits/instruction	24
Instructions per word	2
Instructions decoded	94
Arithmetic system	Fixed and floating point
Instruction type	One address
Number range	2 <sup>112</sup>

Instruction word format

1	2	8	9	10	11	24
Break Point	Or	der	Ind	lex	Add	ress

Automatic coding includes MADCAP (86 characters, full sub- and superscripting). Display quotients are planned.

Registers and B-boxes include 3 B-boxes, a universal register, a storage register and a remainder register.

## **ARITHMETIC UNIT**

	Incl Stor Access	Exclud Stor Access
	Microsec	Microsec
Mult	180 avg	180 avg
Div	300 avg	300 avg
Construction	(Arithmetic unit on	ıly)
Vacuum tub	es 2,850	• •
Diodes	1,040	

MANIAC II 640

Arithmetic mode Timing Operation

Parallel Asynchronous Sequential

## STORAGE

	No. of	No. of Bin			
Media	Words	Dig/Word	Microsec		
Cathode Ray Tube	12,288	49	15 (avg)		
(Barrier Grid)	•				
Magnetic Cores	4,096	49	2.4		
Magnetic Tape	• •				
No. of units that			Units		
No. of chars/linear	inch of		Char/inch		
Channels or tracks	on the ta	pe 1	Track/tape		
Blank tape separat:	ing each r	ecord 6	Inches		
Tape speed		. 60	Inches/sec		
Transfer rate		18,000	Char/sec		
Start time		25	Millisec		
Stop time			Millisec		
Average time for experienced					
operator to change	reel of ta	<b>pe</b> 60	Seconds .		
Physical properties of tape					
Width		. 0.25	Inch		
Length of reel		1,200			
Composition			ar sandwich		
Two physical track		combine to	form a sin-		
gle information char	mel.				

## **INPUT**

Media	Speed
Magnetic Tape	270 words/sec
Paper Tape	250 char/sec
Keyboard	Manual

#### **OUTPUT**

Media	Speed
Magnetic Tape	270 words/sec
Printer	77,400 char/sec (max)
Punch	60 char/sec
Electric Typewriter	10 char/sec
Frinter speed is 900	lines/min.

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Туре	Quantity
Tubes	5,190
Diodes	3,050
Transistors	1,160
Magnetic Cores	200,700

95% of the tubes are Type 5965. 5% are high power

#### CHECKING FEATURES

Parity check on electrostatic storage and magnetic tape. Load sums for identification (+) Exponent spill Overflow (-) Exponent spill

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer	37 KVA
Volume, computer	1,000 cu ft
Area, computer	150 sq ft
Capacity, air conditioner	15 Tons

#### PRODUCTION RECORD

Number produced to date Number in current operation

## COST, PRICE AND RENTAL RATES

The total cost is about \$350,000.

## PERSONNEL REQUIREMENTS

				0ne	8-Hour	Shift
Supervisors					3	
Analysts					4	
Programmers					8	
Coders					2	
Clerks					1	
Operators					1	
Engineers					4	
Technicians					4	
Operation	tends	toward	open	shor	·	

## RELIABILITY, OPERATING EXPERIENCE. AND TIME AVAILABILITY

Average error-free running period Several Hours 50 Hours/Week (Average) 52 Hours/Week (Average) Good time Attempted to run time Operating ratio (Good/Attempted to run time) 0.96 Above figures based on period from 1958 to present Time is not available for rent to outside organization.

## ADDITIONAL FEATURES AND REMARKS

3 indexing registers (B registers) for automatic, address modification and cycle counting (independent of arithmetic elements).

Semi-automatic exit from sub-routines. Large base for floating point operation to increase

speed of floating point additions.
"Madcap", Mathematical and Descriptions Coding Assembly Program, will translate a series of logical and algebraic statements into a computer ready code, this will use a seven hole tape, standard coding uses five holes. Tape reader can handle either.

## INSTALLATIONS

University of California Los Alamos Scientific Laboratory P. 0. Box 1663 Los Alamos, New Mexico

## MANIAC III

Mathematical Analyzer Numerical Integrator and Computer

## **APPLICATIONS**

This system will be used by all interested departments of the University of Chicago.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary digits/word Instructions per word Instructions decoded Arithmetic system Instruction type

Number range

Floating point Two address

Instruction word format

Fraction range  $(1 - 2^{-39})$  to -1. Exponent range +127 to -127

1.	7	1	5	14	1	5	14
Tag	Opera-	Inflec-	Modi-	Address	Inflec-	Modi-	Address
Ŭ	tion		fier			fier	

Registers and B-boxes

3 Arithmetic Registers 8 Transistor Storage Registers

8 Index Registers

2 Indicator Registers

Four different types of arithmetic (significant digit floating point, specified point, normalized, basic), all using same number format (exponent-fraction).

Special exponent used to denote absolute zero (essentially zero with exponent -(x).

#### ARITHMETIC UNIT

Incl Stor Access Exclud Stor Access Microsec Microsec 24 + n/21.8 + n/271 65 81 75

Div n = difference of exponents. Construction (Arithmetic unit only)

Transistors 10,000 Condenser-diodes 16,000 Arithmetic mode Timing Operation

Add

Mult

Parallel Asynchronous Concurent

## **MANUFACTURER**

University of Chicago Institute for Computer Research

## **STORAGE**

Access No. of Digits No. of Words Microsec 8,192 48 Plans in progress to include an additional 8,192 words. Magnetic Tape No. of units that can be connected 4 Units No. of char/linear inch of tape 250 Char/inch 8 Tracks/tape Channels or tracks on the tape Blank tape separating each record 5 Inches 150 Inches/sec Tape speed 37,500 Char/sec Transfer rate Start time 5 Millisec Stop time 5 Millisec Average time for experienced operator to change reel of tape 60 Seconds Physical properties of tape 0.5 Inches Width 2,500 Feet Length of reel Composition Mylar

#### INPUT

Media Speed Paper Tape 350 char/sec Keyboard (Type-Manual Alphabetic and numeric writer) Magnetic Tape 37,500 char/sec

## **OUTPUT**

Media	Speed		
Paper Tape	60 char/sec		
Typewriter	10 char/sec		
Line Printer	600 lines/min		
Magnetic Tape	37,500 char/sec		

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Туре	Quantity
Diodes	20,000
Transistors	12,000
Magnetic cores	500,000

#### CHECKING FEATURES

Parity on tapes and core storage.

MANIAC III 642

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer 2 Area, computer Rocm size, computer 2 Kw 2 KVA 1.0 pf 64 sq ft 25 ft x 30 ft 50 lbs/sq ft max Floor loading 20 lbs/sq ft 50 lbs concen max Weight, computer
Room temperature controlled to 75°F., humidity 40%
- 60%. Cable ways under floor, or false floor.

## PERSONNEL REQUIREMENTS

Open shop policy.

## ADDITIONAL FEATURES AND REMARKS

All arithmetic performed on operands in exponent-coefficient form; several options for sealing of result allow calculation to be performed, "generalized fixed point", "normalized", significance-mode, or multiple-precision as convenient.

## INSTALLATIONS

University of Chicago Institute for Computer Research Chicago 37, Illinois

## MERLIN

MERTAN

## **MANUFACTURER**

Brookhaven National Laboratory

## **APPLICATIONS**

Located at Upton, New York, the system is used for Atomic Energy Commission programs, including areas of physics, chemistry, biology, medicine, reactor studies, acceleration design and meteorology.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system	Binary
Number of binary digits per word	a. 48
Number of binary digits per	
instruction	48
Number of instructions per word	1
Number of instructions decoded	Approx 90
Arithmetic system	Floating point
-	Fixed point
Instruction type	One address (mostly)
	Two address (some)
Number range	2-120 2120
	c - c

Instruction word format

r			r			
١	Y	Z	ъ	ъı	m.	m."
L						

Automatic built-in subroutines Square-root

2nd memory address

Registers and B-boxes

6 B-boxes

3 shifting and 4 non-shifting registers, the latter for fast access storage, in Arithmetic Section Pathfinder for subroutine return 16 bit Sense Register

48 bit word has one multi-address instruction. The numerical operand represents number in the form  $2^{8e}$  X: four bits specify magnitude of exponent e, one bit its sign; 40 bits the magnitude of the fractional part X and one bit its sign. Of the remaining two bits of the word (tag bits), one may be automatically detected by control. Fetching of next sequenced instruction begins before completion of operation. MERLIN is patterned after MANIAC II (Los Alamos).

## **ARITHMETIC UNIT**

	Incl. Stor. Access	s Exclud. Stor. Access
	Microsec.	Microsec.
Add	8 μs <b>(3.</b> 5)	-
Mult.	140 µв	130
Div.	330 µs	320

Construction, Type Vacuum tubes	arithmetic unit only Quantity
5956 6197	800 2 <b>7</b> 5
Diodes T3G	7,000

Arithmetic mode

Parallel

SI	$\sim$	n	A	$\sim$	г
•		ĸ	ш		
	•		П	v	_

	No. of	No. of	Access
Medium	Words	Digits	Microsec
Electrostatic Barrier Grid Tube	8,192	49	6

#### **INPUT**

Media.	Speed	
Flexowriter	10 char/sec	Friden
Paper Tape	200 char/sec	Ferranti TR 2
Magnetic Tape	20K char/sec	Ampex FR 300 - 4 units

#### **OUTPUT**

Media	Speed.	
Flexowriter	10 char/sec	Friden
Paper Tape	60 char/sec	Teletype
Magnetic Tape	20K char/sec	Ampex FR 300
Printer	10 lines (96 char)/	Shepard
	deo	

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Type Tubes	Quantity	
5965	2,500	Blocking Osc. Flip Flops and Cathode followers
6197	400	Pulse Drivers
12E1	18	Deflection Amplifiers and Pulse Amps.
OA2	24	Regulators
Diodes		
T3G	16,000	Gating and Switching
T5G	350	Gating and Switching
IN643	150	Gating and Switching
Zenor Dio	des	
Various	40	Bias Supplies
Transisto	rs	
2N247	400	Memory Read Amplifier and Buffer
2N393	100	Memory Discriminator
2N344	300	Memory Strobe and Parity
2N1091	20	Emitter Followers
Magnetic	Cores	
Various	1,200	Blocking Oscillator and Pulse Transformers

#### CHECKING FEATURES

Parity check at input/output and memory output. Single bit error correction to be added at memory output.

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

I Officia, Dirace, Hickory	,,	<i></i>	
Power, computer	40 Kw	45 KVA	0.9
Power, air conditioner	16 Kw	18 KVA	0.9
Volume, computer		1500 cu	ft
Area, computer		165 sq	ft
Room size, computer		1400 sq	ft
Volume, air conditioner		150 cu	ft
Area, air conditioner		25 sq	
Room size, air condition	ner	1200 sq	ft
Capacity, air conditions	er	20 To	as

## PRODUCTION RECORD

Number produced to date 1
Number in current operation 1

## **COST. PRICE AND RENTAL RATES**

Cost

\$600,000

## PERSONNEL REQUIREMENTS

One 8-Hour Shift

	Use
Supervisors	1
Analysts	2
Programmers	7
Clerks	1
Engineers	2
Technicians	3

Operation tends toward open shop.

Formal two week course (1 1/2 hr/day). Individual assistance as required.

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Time is available for rent to outside organizations on a qualified basis.

Operating figures are not yet available.

Computer is in final stages of debugging and is available on a limited basis.

## ADDITIONAL FEATURES AND REMARKS

Outstanding features include 4 fast access temporary storage registers.

## **FUTURE PLANS**

An additional 8000 words of Radechon memory will be incorporated with a self-correcting code. An 8000 word magnetic core memory will also be added.

Modifications, including specialized input-output equipment, will be undertaken on the basis of specific research requirements.

## **INSTALLATIONS**

Brookhaven National Laboratory Upton, New York

## MINIAC II

#### **MANUFACTURER**

Marchant Calculators, Incorporated (Now Smith-Corona Marchant, Inc.) Data Processing Systems Division

Picture by The Atlantic Refining Company, Incorporated, Dallas, Texas

#### **APPLICATIONS**

The Atlantic Refining Company Scientific and engineering. Utilized by the Atlantic Refining Company's Research and Development Department for research and development in oil exploration and production. Scientific and engineering applications include synthetic seismograms, geophone responses, chemical process designs and fluid flow in porous media.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Digits per word Digits per instruction Instructions per word Instructions decoded Arithmetic system Instruction type Bin coded and Hexa dec 10 decimal 10 decimal 1 71 Fixed point

One address

#### ARITHMETIC UNIT

Includ. Stor. Access Exclud. Stor. Access Microsec Microsec Maximum Average Add time 11,200 Mult time 24,300 21,200 450 41,400 13,600 14,800 Div time 25,600 43,200 Construction Vacuum tubes Basic pulse repetition rate 300 Kc/sec Arithmetic mode Serial Timing Synchronous (clocking channels on the drum) Sequential Operation

The add time, excluding storage access, given above, is equivalent to 3 word times. The operand and instruction times are included in all the above values.

#### STORAGE

Microsec Media Words Digits Access 1,200-10,000 Magnetic drum 4,096 10 plus sign 256 words, 2,500 microsec average access, is optional.

#### **INPUT**

Speed Paper Tape (Flexowriter) 600 char/min (6 channel tape) 300 words/min (limited Ferranti Photoelectric Reader by loading program) Keyboard Manual

#### OUTPUT

Media Speed 600 dig/min Paper Tape (Flexowriter) 30 char/sec (6 channel) Friden Punch

Spare Flexowriter can also be used for the separate preparation of data and programs.

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Tubes 7 cover 95% Tube types Crystal diodes Separate cabinets

There are 75 types of plug-ins at \$10 each. 50% of the machine uses 7 types of plug-ins. The major types of tubes are the 5963, 5687, 12BH7,

12AT7, 5965, 5915, 2D21.

A cold water supply and a desk for the Flexowriter is included.

#### CHECKING FEATURES

Timing circuits Twenty jacks for applying marginal voltages

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

5 KW, 220 volt Power, computer 91 cu ft, 20 sq ft Space, computer Space, air cond. Dimensions 4.5 x 4.5 x 4.5 ft

plus desk 2,000 lbs Weight, computer Capacity, air cond. 2 tons

Designed for cooling by water between 60° and 65°F.

#### PRODUCTION RECORD

Produced 1 Model C and 1 Model II Operating 1 Model C and 1 Model II Delivery time No longer manufactured

## COST. PRICE AND RENTAL RATES

Approximate cost of basic system \$85,000 Approximate cost of Flexowriter \$ 2,950 Approximate cost of Spare Flexowriter \$ 2,950 No maintenance contract.

## PERSONNEL REQUIREMENTS

	One 8-Hour Shift			Two 8-Hour Shifts		Three 8-Hour Shifts	
Operators	Used 1	Recom 1	Used 1	Recom 2	Used 1	Recom 2	
Typist	0	1	0	1	0	1	

Methods of training used On the Job

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY 42 Hours/Week

48 Hours/Week Attempted to run time Operating ratio (Good/Attempted 0.875 to run time) Above figures based on period from 26 Feb 60 to 26 Mar 60 1 Mar 55 Acceptance test

Time is not available for rent to outside organizations.

Magnetic drum replaced in 1958.

Good time

Converted from one-address to two-address operation in 1959.

## **FUTURE PLANS**

Incorporation of a Moseley system, to have the following items: Tape Translator X-Y Recorder Character Printer Curve Follower

## **INSTALLATIONS**

Atlantic Refining Company Research and Development Laboratory 4500 W. Mockingbird Lane Dallas, Texas

# MISTIC Michigan State Digital Computer

## **MANUFACTURER**

Michigan State University

## **APPLICATIONS**

Service facility for University staff and students on an open shop basis for general purpose computation.

Used for instructional purposes in several programming and numerical analysis courses which are offered for credit by the University.

Available for use on applicable sponsored research projects.

Photo by Michigan State University

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary
Number of binary digits per word 40
Number of binary digits per instruction
Number of instructions per word 2
Total number of instructions decoded 186, of which 139
are unique
Arithmetic system Fixed point
Instruction type One-address
Number range -1 to +(1-2-39)

MISTIC 648

# Instruction word format ORDER ADDRESS ORDER ADDRESS Type Variant Type Variant 4 bits 4 bits 12 bits 4 bits 4 bits 12 bits

Two 40-bit shifting registers and one 40-bit fixed register for arithmetic operations.

Two separate 2-bit registers will hold a bank address for 16,384 word core memory, one register each for operands and instructions.

#### ARITHMETIC UNIT

Operation, Incl stor. access	Microseconde
Add time	100
Mult time	1,000
Div time	1,100
Excl stor. access	1,100
	_
Add time	80
Mult time	980
Div time	1,080
Construction (Arithmetic unit only)	•
Vacuum tubes type	Quantity
5844	580
7044	236
5670	120
Arithmetic mode	Serial

#### **STORAGE**

Media	Number	Digits per Access	
	of Words	Word	Microseconds
Cathode Ray Tube	1,024	40 bin	20
Magnetic Core	16,384	40 bin	20
The MC memory	will repl	ace the CRT	memory.

## **INPUT**

MediaSpeed5-level Photodiode Paper Tape300 char/secCards200 cards/min

Above speeds are maximum. Card decoding is programmed so that input is 100 cards/minute for most applications.

## OUTPUT

Med1a	Speed
Paper Tape	60 char/sec
Teletypewriter	10 char/sec
Cards	100 cards/min, max
habaaah mamann ana ahaal	• •

Cards are program decoded.
CRT output is under construction.

## **CIRCUIT ELEMENTS OF ENTIRE SYSTEM**

Tubes	
Туре	Quan
5844	1,300
5670	650
7044	400
5726	100
Misc.	160
Total	2,610

#### CHECKING FEATURES

Division algorithm automatically checks for overflow and division by 0.

## POWER. SPACE, WEIGHT, AND SITE PREPARATION

Power, computer	18.5 K.W., input to com-
Power, air conditioner	10 K.W., including fan, water pump and cooling
	tower fan
Volume, computer	500 cu ft
Volume, power supplies	200 cu ft
Volume, air conditioner	150 cu ft
Volume, cooling tower	320 cu ft
Area, computer	75 sq ft
Area, power supplies	30 sq ft
Area, air conditioner	32 sq ft
Area, cooling tower	32 sq ft
Room, computer	12 ft x 18 ft
Room, power supplies	8 ft x 9 ft
Air conditioner	10 Horsepower

## PRODUCTION RECORD

Number	produced to date	1
Number	in current operation	1

#### PERSONNEL REQUIREMENTS

Typical Personnel One 8-Ho Supervisors	
Analysts	2
Programmers 2	_
Clerks	
Librarians ]	-
Operators 1	_
Engineers 1	
Technicians	-

The computer is operated on an open-shop basis so that most of the functions other than direct operation and maintenance of the computer are taken care of by the various users.

#### INSTALLATIONS

Michigan State University East Lansing, Michigan

## **FUTURE PLANS**

A 40-bit 16,384 word core memory is under construction and will replace the existing 1,024 word CRT memory in the Fall of 1960.

A CRT output with an attached camera is also under construction which will permit analog output directly from the computer. This will be made available in the Fall of 1960.

## MOBIDIC A

Mobile Digital Computer A AN/MYK-1(v)

## **MANUFACTURER**

Sylvania Electric Products, Incorporated

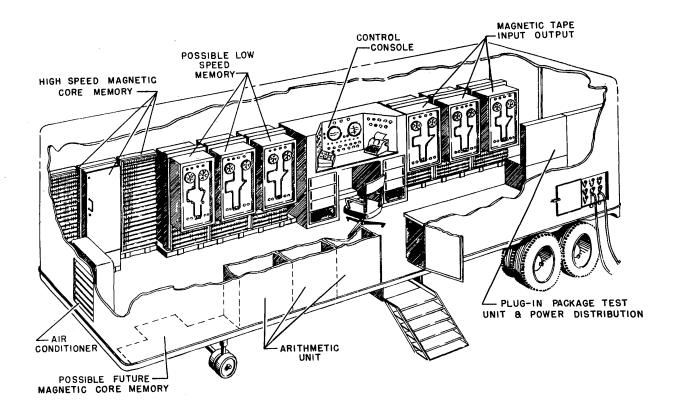


Photo by Sylvania Electric Products, Inc.

## **APPLICATIONS**

Presently located at the Needham Industrial Park, the MOBIDIC "A" is a mobile, highly-reliable, high speed, general purpose computing facility for use by field commanders for combat support data processing, combat control data processing, combat computation, and logistic computations.

The Real Time System consist of Real Time Input-Output registers, both of which are capable of communicating with an external device (including another MOBIDIC) over nine lines (6 data, 1 parity, 2 control). The Input system incorporates a program interupt feature.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary

Binary digits/word 36 plus sign & parity bits
Binary digits/instruction 36 plus spare & parity
bits

Instructions per word 1 Instructions decoded 52

Arithmetic system Fixed point

Assumed binary point at left end of word, between bits 36 & 37

Instruction type One address

Some instructions are two address, e.g. load, move, etc.

Number range  $-(1-2^{-36})$  to  $+(1-2^{-36})$ 

Standard Computer Instruction Word

<b>3</b> 8	37	36 31	30 28	27 16	15 13 12 1
Parity	Spare		Index Reg. Selection	Minor Address	Major Address

#### Input-Output Instruction Word

38	37	36 31	30 22	21 16	15 13 12 1
Parity	Spare		Word-Block Counter	Device Selec- tion	Storage Address

Automatic coding includes the Mobidic Assembly Program.

Registers include 6 in the Central Processor, 4 Index, 2 in the Communications Converter, and 2 in the In/Out Converter.

Instructions consist of 15 Arithmetic, 8 Transfer, 17 Logical, 3 Sense, and 9 Input-Output instructions. Index Registers are expansible to a total of 7.

## **ARITHMETIC UNIT**

	Incl. Stor. Access	Exclud. Stor. Access
	Microsec	Microsec
Add.	16	8
Mult	86	78
$\mathtt{Div}$	88	88
Constructi	on (Arithmetic unit	only)
Transist	ors 6,000	

Transistors 6,000
Arithmetic mode Parallel
Timing Synchronous
Operation Sequential

Mostly sequential, however processing may proceed during input-output operations.

#### **STORAGE**

	No. of	No. of		Access
Media			rd	Microsec
Core Memory (2)	4096 each	<b>i</b> 40		8
Expandable to a			s.	
Magnetic Tape				
No. of units the				Units
No. of chars/li	near inch c	f tape	300	Chars/inch
Channels or tra	cks on the	tape	8	Tracks/tape
Blank tape sepa	rating each	record	1.5	Inches
Tape speed			150	Inches/sec
Transfer rate		45	,000	Chars/sec
Start time			1.5	Millisec
Stop time			1.5	Millisec
Average time for	r experienc	ed		
operator to change	e reel		120	Seconds
Physical proper	ties of tap	e		
Width			1/2	Inches
Length of ree	1.		3600	Feet
Composition			l mi	l mylar

The 40 bit word length in storage is made up of:

36 bit magnitude

l bit sign

1 bit parity

1 busy-bit

l spare-bit

40 Total in storage

## **INPUT**

Media	Speed.
Paper Tape 5 channel	200 char/sec (start-stop) to
11/16 inch tape	270 char/sec
Paper Tape 8 channel	200-270 char/sec
L inch tape	,
Real Time Channel	120,000 char/sec
character by characte	יירב יי

Cards 200 cards/min

The Real Time System was designed to operate with
the Collins Kineplex equipment whose speed is 300
characters/sec. or with another MOBIDIC; however,
it can operate with any compatible transmission
equipment.

## **OUTPUT**

Media		Speed	
Paper Tape 5	channel	100 char/sec	11/16" tape
Paper Tape 8	channel		1 inch tape
Real Time			ec char by char
Flexowriter		10 char/sec	

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Type	Quantit
Diodes	
SG-225	6,000
Transistors	
2 <b>N</b> 393	32,000
Magnetic Cores	311 <b>,</b> 200

#### **CHECKING FEATURES**

Checking features include parity on memory transfer and input-output, overflow, non-existant memory, non-existant instruction, and non-existant device (I/0). Marginal checking may also be performed. Diagnostic Routines to check the machine and indicate instruction which fail and aid in the localization of failures are available.

## POWER. SPACE, WEIGHT, AND SITE PREPARATION

V	,,					
	er, computer					
Pow	er, air conditione	rs 4.64	Kw 5.8	KVA	0.8	ρf
	ume, computer		1,440	cu ft		
Are	a, computer			sq ft		
Flo	or loading			lbs/s		
			240	lbs co	oncen	ma.x
Vol:	ume, two air condi	tioners		cu ft		
	a, two air conditi			sq ft		
Cap	acity, two air con	ditione	rs 6	Tons		
Wei	ght, computer		12,000	lbs		
	ght, air condition		1,200			
T	he computer is mou	inted in	a 30 fc	ot van	, air	con-

The computer is mounted in a 30 foot van, air conditioning ducts on the right and left ceilings, and the heater duct at floor level behind the I/O converters, Central Processor, and memory units.

Air-conditioning is for operator comfort only.

#### PRODUCTION RECORD

Number produced to date 1
Number in current operation 1
Time required for delivery 18 months
System is being tested and evaluated.

## COST, PRICE AND RENTAL RATES

Large computer system such as this one is seldom duplicated from one installation to another. Individual problem and application normally requires unique configuration and special features that establish either purchase or lease price. Upon completion of a feasibility study when the requirements are known, along with a calculated growth, costs could be determined.

#### PERSONNEL REQUIREMENTS

Training will be dependent on the requirements of the user. However, it is recommended that personnel have a minimum of 4 weeks of formal class room lectures followed, if possible, by closely supervised on-the-job-training.

Number of operating personnel will depend on the requirements of the user.

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Time is not available for rent to outside organizations.

## ADDITIONAL FEATURES AND REMARKS

Outstanding features include ruggedization for field use. System operates between -25°F to 125°F and 0 to 97% relative humidity. It is built on a modular basis, expansible in memory (to 7), I/O converter (up to 4), and I/O equipment (to 63).

Unique system advantages include containment in 240 sq ft. It is a completely mobile, large scale, general purpose system. System is designed for reliability, mobility, flexibility, fixed plant, and strategic installation, with minimum space requirements, and minimum pre-installation cost, such as air conditioning and power. This machine is a member of the Army FIELDATA Family of Computers. It uses the FIELDATA code and is compatible with other FIELDATA machines.

#### INSTALLATIONS

One MOBIDIC will be installed for the 7th Army Stock Control Center in Germany. Other systems will be installed to cover a wide range of applications.

## MOBIDIC

Mobile Digital Computer B AN/MYK-2(v)

## **APPLICATIONS**

The MOBIDIC B is a duplexed general purpose computer being developed for inclusion into a tectical army operation center. The machine's mechanized instruction list was selected for optimized operation in the processing of data rather than for scientific calculations. The two machines may be synchronized together and run as one machine or they may be used separately.

The MOBIDIC B has two real time in-out registers and possible sources of such real time data are radar equipment, weather stations, drone aircraft and other MOBIDICS. This computer is also suited for applications such as message switching centers. One processor receives the incoming message and does the incoming processing and then stores the message in the common mass memory. The second processor would serve as the output message processor, taking the message from the common mass memory completing the processing and transmitting it out. If one machine failed, the second could handle the work at a reduced system speed.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary

Binary digits/word 36 plus sign bit & parity Binary digits/instruction 36 plus spare bit & parity

Instructions/word Instructions decoded

55 + 9 special subroutine

instructions

Arithmetic system Fixed point

Assumed binary point at left end of word, between

bits 36 and 37

Instruction type One address

Some instructions are two address, e.g. load, move,

etc.

 $-(1 - 2^{-36})$  to  $+(1 - 2^{-36})$ Number range

Instruction word format

<i>3</i> 8	37	36 31	30 ·	28	27	16	15	13	12	1
Parity	Spare		Index Selec		Min Addr		,	jor	Add	ress

#### Standard Computer Instruction Word

38	37	36 31	30 22	21 16	15 13 12 1
Parity	Spare		Word-Block Counter	Device Selection	Storage Address

Input-Output Instruction Word

Automatic coding includes MOBIDIC Assembly Program. Registers include 8 in the central processor, 2 in the communications converter, 2 in the In/Out Converter and 7 index registers.

Instructions consist of 15 Arithmetic, 9 Transfer, 18 Logical, 3 Sense, 10 Input-Output, and 9 Special instructions.

#### MANUFACTURER

Sylvania Electric Products, Incorporated

#### ARITHMETIC UNIT

Incl. Stor. Access Exclud. Stor. Access Microsec Microsec Add 42 8o 88 Mult Div Not mechanized Construction (Arithmetic unit only) 6,000 Transistors Arithmetic mode Parallel Timing Synchronous Sequential Operation

## **STORAGE**

	No. oi	No. or		Access
Media	Words	Bits/word		Microsec
Magnetic Core	8,192 6.25 x 10 <sup>6</sup>	•		8 6
Disk	6.25 x 10°	8		1 x 10 <sup>0</sup>
Expansible to	a total of	7 core mem	orie	s of
4,096 words ea	ach			
Magnetic Tape				
No. of units				Units
No. of chars/	linear inch	of tape		
Channels or to	racks on the	e tape	8	Tracks/tape
Blank tape seg	parating eac			Inches
Tape speed				Inches/sec
Transfer rate				Chars/sec
Start time		Millisec		
Stop time				Millisec
Average time	for experie	aced	120	Seconds
operator to c	hange reel			
Physical prop	erties of t	ape		
Width				Inches
Length of re		-,		Feet
Composition	1	mil	mylar	

#### INPUT

Media Speed Paper Tape 5 channel 200 char/sec (start-stop) 11/16 inch tape 270 char/sec Paper Tape 8 channel 200-270 char/sec 1 inch tape 120,000 char/sec Real Time Channel character by character Cards 200 cards/min

The Real Time System was designed to operate with the Collins Kineplex equipment whose speed is 300 characters/sec. or with another MOBIDIC; however, it can operate with any compatible transmission equipment.

#### **OUTPUT**

Media	Speed.	
Paper Tape 5 channel	100 char/sec	11/16" tape
Paper Tape 8 channel	100 char/sec	1 inch tape
Real Time	120,000 char/sec	char by char
Flexowriter	10 char/sec	

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Туре Quantity Diodes SG225 6,000 Transistors 30,000 2N393 Approx. Magnetic Cores 655,360

#### CHECKING FEATURES

Checking features include parity on memory transfer and input-output, overflow, non-existant memory, non-existant instruction, and non-existant device (I/O). Marginal checking may also be performed. Diagnostic Routines to check the machine and indicate instruction which fail and aid in the localization of failures are available.

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer 34	.2 Kw 42.75 KVA 0.8 pf
Power, air conditioner 6	.6 Kw 8.25 KVA 0.8 pf
Volume, computer	690 cu ft
Volume, air conditioner	72 cu ft
Area, computer	115 sq ft
Area, air conditioner	48 sq ft
Weight, computer	12,600 lbs
Weight, air conditioner	1,200 lbs
Capacity, air conditioner	Two at 1.5 Tons each

Air conditioners are for personnel comfort only Computer is mounted in a 30 ft van and a shelter

of approximately the size of an S-109.

#### PRODUCTION RECORD

Number in current production Number on order 18 months Time required for delivery

## ADDITIONAL FEATURES AND REMARKS

Outstanding features include reggedization for field use. System operates between -250°F to +125°F and 0 to 97% relative humidity. It is built on a modular basis, expansible in memory (to 4), I/0 converter (up to 4), and I/O equipment (to 63).

Unique system advantages include containment in 335 sq ft. It is a completely mobile, large scale, general purpose system. System is designed for reliability, mobility, flexibility, fixed plant, and strategic installation, with minimum space requirements, and minimum pre-installation cost, such as air conditioning and power. This machine is a member of the Army FIELDATA Family of Computers. It uses the FIELDATA code and is compatible with other FIELDATA machines.

The 40 bit word length in storage is made up of:

36 bit magnitude

l bit sign

1 bit parity

1 busy-bit

l spare-bit

40 Total in storage

## MOBIDIC

## **MANUFACTURER**

Mobile Digital Computer C, D, 7A AN/MYK-1(v)

Sylvania Electric Products, Incorporated

#### **APPLICATIONS**

MOBIDIC C, D, and 7A are mobile highly reliable, high speed, general purpose computing systems for use by the field commanders for combat support data processing, combat control data processing, combat computations and logistic computations.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary digits/word Binary digits/instruction 36 plus spare & parity bits

Binary 36 plus sign & parity bits

Instruction per word Instructions decoded

Fixed point

Arithmetic system Assumed binary point at left end of word, between

bits 36 & 37 Instruction type

One address

Some instructions are two address, e.g. load, move,

Number range

-(1 - 2<sup>-36</sup>) to +(1 - 2<sup>-36</sup>)

Instruction word format

#### Standard Computer Instruction Word

<b>3</b> 8	37	36 31	<b>3</b> 0 28	27 16	15 13 12 1	]
Parity	Spare			Minor Address	Major Address	1

#### Input-Output Instruction Word

<b>3</b> 8	37	36 31	30	22	21.	16	15	13	12	1
Parity	Spare		Word Coun	-Block ter		ice ec-				

Automatic coding includes the Mobidic Assembly Program.

Registers include 6 in the Central Processor, 4 Index, 2 in the Communications Converter, and 2 in the In/Out Converter.

Instructions consist of 15 Arithmetic, 8 Transfer, 17 Logical, 3 Sense, and 9 Input-Output instructions. Index registers are expansible to a total of 7.

## **ARITHMETIC UNIT**

Incl. Stor. Access Exclud. Stor. Access Microsec Microsec 16 8 Add 78 86 Mult 88 Div 80 Construction (Arithmetic unit only)

Transistors 6,000 Arithmetic mode Parallel Timing

Synchronous Sequential

Operation is primarily sequential, however processing may proceed duting input-output operations.

#### **STORAGE**

No. of Access Bits/word No. of Words Medium Microsec 2 ea (4096) Magnetic Core 40 total 8192

Expansible to a total of 7 magnetic core memories of 4,096 words each.

Magnetic Tape
No. of units that can be connected 63 Units 300 Chars/inch No. of chars/linear inch 16 Tracks/tape Channels or tracks on the tape Blank tape separating each record 1-1/2 Inches Tape speed 1-150 Inches/sec 300-45,000 Chars/sec Transfer rate Start time 3 Millisec Stop time 3 Millisec Average time for experienced operator to change reel 120 Seconds Physical properties of tape Width 1 Inch Length of reel 3,600 Feet Composition Mylar

MOBIDIC C & MOBIDIC D each have (8) tape units while MOBIDIC 7A has 11.

## INPUT

Media Speed 200 char/sec (start-stop) to Paper Tape 5 channel 11/16 inch tape 270 char/sec Paper Tape 8 channel 200-270 char/sec 1 inch tape Real Time Channel 120,000 char/sec character by character Cards 200 cards/min

## OUTPUT

Media Speed 11/16" tape Paper Tape 5 channel 100 char/sec Paper Tape 8 channel 100 char/sec 1 inch tape Real Time 120,000 char/sec Flexowriter 10 char/sec char by char

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Туре Quantity Diodes SG225 6,000 Transistors 30,000 2N393 Magnetic Cores 335,872 MOBIDIC is a completely Solid State computer

#### CHECKING FEATURES

Parity on memory transfer and input-output, over flow, non-existant memory, non-existant instruction, non-existant device (I/O), marginal checking, and diagnostic programs to check the machine and indicate instructions which fail and aid in the localization of failures.

Operation

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer 44.	8 Kw	56	KVA	0.8	рf
Power, air conditioner 9.	28 Kw	11.6	KVA	0.8	pf
Volume, computer		2,880	cu ft		
Volume, air conditioner		72	cu ft		
Area, computer		480	sq ft		
Area, air conditioner		48	sq ft		
Weight, computer		7,000	lbs		
Weight, air conditioner		600	lbs e	8.	
1200 lbs - Total for 1	van	•			
2400 lbs - Total for 2	vans				,
Capacity, air conditioner	•	1 1/2	Tons		

18,000 BTU/hr each
No special site requirements. MOBIDIC C, D, 7A are
mounted in two 30' vans. KVA & Kw ratings include
air conditioning for two vans. Kw and KVA maximum
for entire system. Air conditioning is for operator
comfort only. Air conditioners are two per van.
All figures are for two vans.

## PRODUCTION RECORD

Number produced to date	1 - AN - MYK - 1(v)
Number in current operation	1
Number in current production	3
Time required for delivery	18 months

## ADDITIONAL FEATURES AND REMARKS

Outstanding features include reggedization for field use. System operates between -25 F to +125 F and 0 to 97% relative humidity. It is built on a modular basis, expansible in memory (to 7), I/O converter (up to 4), and I/O equipment (to 63). Unique system advantages include containment in

Unique system advantages include containment in 480 sq ft. It is a completely mobile, large scale, general purpose system. System is designed for reliability, mobility, flexibility, fixed plant, and strategic installation, with minimum space requirements, and minimum pre-installation cost, such as air conditioning and power. This machine is a member of the Army FIELDATA Family of Computers. It uses the FIELDATA code and is compatible with other FIELDATA machines.

The 40 bit word length in storage is made up of:

- 36 bit magnitude
- l bit sign
- 1 bit parity
- l busy-bit
- 1 spare-bit
- 40 Total in storage

# MODAC 404 Mountain Systems Digital Automatic Compute

## **MANUFACTURER**

Airborne Instruments Laboratory, Incorporated (Parent) Mountain Systems, Incorporated

#### **APPLICATIONS**

Statistical and business data processing, accounting, coding and controls.

System is no longer being manufactured.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary coded decimal Decimal digits/word 6
Decimal digits/instruction 2
Instructions decoded 8
Arithmetic system Fixed point Instruction type One address Number range 0 to 999,999

Programming system is designed for special application. Operations include addition, subtraction, unit entry, bulk entry and transfer.

Photo by Reader's Digest Association

of two 6-digit decimal numbers.

## **ARITHMETIC UNIT**

Incl Stor Access Exclud Stor Access Microsec Microsec 25,000 Add time 240 Construction Vacuum tubes Basic pulse repetition rate 150 Kc/sec Arithmetic mode Serial Asynchronous Timing Operation Sequential The addition time given above is for the addition

## **STORAGE**

					AC	cess:
Medium		Word	B	Digits	Mic	rosec
Magnetic D	rum	20,0	000	120,000	25	,000
Access t	ime	given ab	ove i	s average.	System s	tores
500,000 bis	nary	digits	in 50	millisecon	ds access	time.

MODAC 404 658

Photo by Reader's Digest Association

## INPUT

Media Speed
Paper Tape 200 char/sec
Punched Cards (Rem. Rand Tab) 4 cards/sec

#### OUTPUT

Media Speed
Paper Tape 200 char/sec
Punched Cards (Rem. Rand Tab) 4 cards/sec

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Tubes 1,000
Tube types 3
Crystal diodes 2,000
Separate cabinets 1

## **CHECKING FEATURES**

Address check

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer 3 Kw
Volume, computer 120 cu ft
Area, computer 20 sq ft
Weight, computer 1,500 lbs

## PRODUCTION RECORD

Reader's Digest Association Number produced 1 Number in operation 1

## COST, PRICE AND RENTAL RATES

Reader's Digest Association Approximate cost of basic system \$100,000. System is no longer in production.

#### PERSONNEL REQUIREMENTS

Reader's Digest Association
Daily Operation Engineers Tech and Operators
One 8-hour shift 0 1
RFIIARIIITY OPERATING EXPERIENCE

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Reader's Digest Association

Good time 6,000 Hours
Attempted to run time 6,188 Hours
Operating ratio (Good/Attempted to run time) 0.97
Figures based on period from Sep 55 to Jan 57
Passed Customer Acceptance Test Sep 54

Additional features include external programming, dual entry to memory with single address and an address check.

System no longer manufactured.

## MODAC 410

Mountain Systems Digital Automatic Computer Model 410

## **APPLICATIONS**

Business data processing. System is no longer being manufactured.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system
Decimal digits per word
Arithmetic system

10 Fixed point

Instruction type Number range

One address
O to 10 decimal digits

Decimal - Excess 3

Program is stored internally and on tape.

### **ARITHMETIC UNIT**

Exclud Stor Access

Microsec 600 7,000 7,000

Div time Construction

Add time

Mult time

Vacuum tubes, magnetic elements and diodes

Rapid access word registers Basic pulse repetition rate Arithmetic mode

ion rate 150 Kc/sec Serial Asynchronous

Timing Operation

peration Sequential Computer is serial with buffer storage.

#### STORAGE

Medium Words Microsec
Magnetic Drum 5,000 7,500
50,000 decimal digits stored.

50,000 decimal digits stored. Buffer storage in magnetic cores.

#### INPUT

MediaSpeedPunched Tape400 char/secPunched Cards600 cards/min

#### OUTPUT

Media Speed
Punched Tape 60 char/sec
Punched Cards 600 cards/min

## **CIRCUIT ELEMENTS OF ENTIRE SYSTEM**

Tubes 600
Tube types 3
Crystal diodes 3,000
Magnetic elements 1,000
Number of different plug in units
Number of separate cabinets

#### **MANUFACTURER**

Airborne Instruments Laboratory, Incorporated Mountain Systems Incorporated

#### **CHECKING FEATURES**

Number checks Address checks Odd number check

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer 4 Kw
Volume, computer 40 cu ft
Area, computer 20 sq ft
Weight, computer 1,000 lbs

## PRODUCTION RECORD

Number produced 1 Number in operation System out of production.

## COST, PRICE AND RENTAL RATES

Approximate cost of basic system \$120,000.

Price includes input and output equipment described.

Other equipment dependent upon specific application.

System is no longer being manufactured.

### PERSONNEL REQUIREMENTS

One operator required during operation.

## **ADDITIONAL FEATURES AND REMARKS**

Special translator or converter feature reads an abbreviation on a punched card, looks up corresponding code from a list of 5,000 and punches a code number into the same card at a reading and punching rate of 500 per minute.

High speed tallying feature performs 1,440,000 unit additions per hour into selected registers.

Transactions, from a total of 4,000 categories, can be read at random and added to an appropriate one of 4,000 registers.

System no longer being manufactured.

#### INSTALLATIONS

Readers Digest Association, Incorporated Condensed Book Club Pleasantville, New York

## MODAC 414

Mountain Systems Digital Automatic Computer Model 414

## **APPLICATIONS**

Reader's Digest Association, Incorporated Large scale translation, statistical processing and general purpose computation. System is no longer being manufactured.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system

Binary coded decimal and alphanumeric

Decimal digits/word Decimal digits/instruction Instructions decoded 6 2 12

Arithmetic system
Instruction type

Fixed point One address (for general purpose applications)

288

8,000

8,000

#### **ARITHMETIC UNIT**

Incl Stor Access Exclud Stor Access
Microsec Microsec

Microsec
Add time 8,000
Mult time 8,000
Div time 8,000

Construction
Arithmetic mode

Vacuum tubes and magnetic cores Serial

Timing Asynchronous
Operation Sequential

Concurrent for punched cards in any of three modes.

The multiply and divide times given above include re-record time.

#### **STORAGE**

				Access
Media		Words	Character	s Microsec
Magnetic	Drum	6,000	36,000	8,000
Magnetic	Drum	4	24	576
Magnetic	Cores	2	12	288

## INPUT

Media Punched Cards Paper Tape

Speed 360 cards/min

Paper tape is used for report programming and testing.

#### **MANUFACTURER**

Airborne Instruments Laboratory, Incorporated (Parent)
Mountain Systems, Incorporated

#### OUTPUT

Media Punched Cards Speed
360 cards/min
20 char/sec

Paper Tape 20 char/sec
Punched cards are used for translation and paper
tape for reports.

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Tubes 2,000 (approx)
Tube types 3 (major)
Crystal diodes 3,000 (approx)
Magnetic cores 396

#### CHECKING FEATURES

Odd-even checks on numerical calculations are used.

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer 5 Kw
Volume, computer 240 cu ft
Area, computer 40 sq ft
Weight, computer 3,000 lbs

## **PRODUCTION RECORD**

Number produced 1 Number in operation 1

System is no longer being manufactured.

## COST, PRICE AND RENTAL RATES

Approximate cost of basic system \$150,000. System is out of production.

## PERSONNEL REQUIREMENTS

Daily Operation One 8-hour shift Engineers T

Tech and Operators

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Passed Customer Acceptance Test October 1956. System is no longer being manufactured.

## **INSTALLATIONS**

Reader's Digest Association, Incorporated Condensed Book Club Pleasantville, New York

## **MANUFACTURER**

Airborne Instruments Laboratory, Incorporated (Parent) Mountain Systems, Incorporated

## **APPLICATIONS**

Manufacturer

Business data processor. System no longer being manufactured.

Hickok Manufacturing Company, Incorporated A perpetual inventory, furnishing reports to the Central Planning and Packaging Departments.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary digits/word Binary digits/instruction

Arithmetic system Instruction type

Binary 20 60

Decimal-binary

One address, consisting of two parts. Address "A" selects a magnetic drum read-re-cord head and address "B" selects a particular location on the drum.

Photo by Airborne Instruments Laboratory, Inc.

Number range

"A" ranges from 0 to 66
"B" ranges from 0 to 150

## **ARITHMETIC UNIT**

Add time (Includ stor access)

32

Microsec

Construction Vacuum tubes, using a combination

of trigger pairs, pullers, and cath-ode followers. A crystal diode matrix is also used.

Arithmetic mode A combination series-parallel shift

register is utilized.

Timing Synchronous Operation Sequential

Three types of pulses are used to control operation. Serial feed is by use of 5 channel paper tape. Basic operations are addition, subtraction and

"reading out" a balance.

#### **STORAGE**

			Access
Media	Words	Digits	Microsec
Magnetic Drum	10,000	50,000	
Shift Register	1.	5	<b>3</b> 2
Paper Tape			

Faper tape is utilized for permanent storage in order to release the magnetic drum for other purposes.

The magnetic drum is 8 inches long and 7 inches in diameter. There are 66 recording heads. The address system is composed of a relay pyramid and an electronic counter.

#### INPUT

Medium Speed Paper Tape 600 char/min

Above tape is 5-channel tape, which is prepared by an IBM 063 Card-to-Tape Converter or a Flexowriter typewriter.

#### OUTPUT

Medium Speed Paper Tape 600 char/min

Direct to paper tape or via a Flexowriter type-writer.

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Tubes 535
Tube types 8
Crystal diodes 150
Separate cabinets 4

Crystal diode types used are the 1N35 and the 1N116. Tube types used are the 5965, 5915, 6AN5, 12BF7, 12AX7, 2D21, 5963, and 6AS6. All four cabinets are inter-cabled.

The IBM 063 Card-to-Tape Converter and the Flexowriter are located in an adjoining room. The Flexowriter can be cabled directly to the computer so as to print out in hard copy as the computer is in operation.

#### CHECKING FEATURES

Checking is performed by using predetermined "heads" and "spots" on the drum and tapes with known answers. A visual check is made.

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Volume, computer 120 cu ft
Area, computer 16 sq ft
Four cabinets 2 x 2 x 7.5 ft each
Weight, computer 600 lbs

#### COST, PRICE AND RENTAL RATES

Hickok Manufacturing Company, Inc.
Approximate cost of basic system \$85,000
Approximate cost of Flexowriter 1,200
Rental rates of IBM 063 Card to Tape Converter (\$65.00 plus \$6.50 tax)/month.
System is no longer manufactured.

## PERSONNEL REQUIREMENTS

Hickok Manufacturing Company, Inc.
One operator and 1 clerk are utilized to operate the system on a one 8-hour shift/week basis. One engineer is utilized for developing methods and procedures.

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Average error-free running period 30 Hours Good time 145 Hours/Week Attempted to run time 168 Hours/Week Operating ratio (Good/Attempted to run time) 0.87 Figures based on the last three years. Passed Customer Acceptance Test July 1954 System is no longer being manufactured.

#### INSTALLATIONS

Hickok Manufacturing Company, Incorporated Rochester, New York



## **MANUFACTURER**

Monroe Calculating Machine Company Electronics Division

Photo by Monroe Calculating Machine Company, Electronics Division

## **APPLICATIONS**

Air Force Cambridge Research Center Scientific calculation.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system
Decimal digits/word
Decimal digits/instruction
Instructions per word
Instructions decoded
Instructions used
Arithmetic system
Instruction type
Number range

JMERICAL SYSTEM
Binary coded decimal
20
10
1.
11
11
Fixed point
Four address
$10^{-10} \le n \le 10^{10} - 1$
•

## ARITHMETIC UNIT

	Incl Stor Access
	Microsec
Add time	120,000
Mult time	540,000
Div time	540,000
Construction	Vacuum tubes
Basic pulse repetition rate	10 Kc/sec
Arithmetic mode	Serial
Timing	Synchronous
Operation	Sequential

## **STORAGE**

Media Magnetic Drum Magnetic Drum

Words 100 numbers 100 instructions

Access Microsec 15,000 15,000

PRODUCTION RECORD

Number produced No longer in production.

INPUT

Media Keyboard Paper Tape

Media

Typewriter (Flexowriter)
Paper Tape (Flexowriter)

Speed Manual 10 dig/sec

Speed

10 dig/sec

PERSONNEL REQUIREMENTS

One technician or mathematician is required to operate system.

> RELIABILITY. OPERATING EXPERIENCE. AND TIME AVAILABILITY

Good time Attempted to run time Operating ratio (Good/Attempted to run time)

28 Hours 35 Hours 0.80

Figures based on period 1 Feb 55 to 1 Sep 56
Passed Customer Acceptance Test 1 Feb 55

Approximately 1 hour/day is required for maintenance, therefore "attempted to run time" is considered to be 40-5, or 35 hours.

CIRCUIT ELEMENTS OF ENTIRE SYSTEM

**OUTPUT** 

Tube types Crystal diodes 800 4 100

POWER, SPACE, WEIGHT, AND SITE PREPARATION Power, computer Space, computer Weight, computer

Desk size 1,000 lbs

INSTALLATIONS

Air Force Cambridge Research Center Computing Laboratory Cambridge 39, Massachusetts

# MONROBOT **V**Monroe Computer Model V

## MANUFACTURER

Monroe Calculating Machine Company Electronics Division

Photo by Monroe Calculating Machine Company, Electronics Division

## **APPLICATIONS**

Computing problems normally encountered by Topographic Troops in surveying operations.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Decimal digits/word
Decimal digits/instruction

Arithmetic system

Instruction type

Binary coded decimal 20

10

Fixed point Four address

## ARITHMETIC UNIT

Includ Stor Access

Microsec 120 (approx) Add time 540 (approx) 540 (approx) Mult time Div time Construction Vacuum tubes 10 Kc/sec

Basic pulse repetition rate Timing Synchronous Sequential Operation

## **STORAGE**

Media Words Magnetic Drum 300 Paper Tape

Access Microsec 18,000

Access time on drum is for 100 twenty digit numbers. Drum is 6 inches in diameter, 20 inches long and rotates at a speed of 3,550 rev/min.

#### INPUT

Media Keyboard Paper Tape (Reader)

Speed Manual 570 char/min

## **OUTPUT**

Medium Typewriter (Flexowriter)

Reader 570 char/min Printer 400 char/min

Photo by U. S. Army Corps of Engineers, Engineer Research and Development Laboratories

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Tubes 800 (approx)

Crystal diodes
Tube types

1
5814A, 5726, 5751, 5844, 6005, and
5725

#### CHECKING FEATURES

Storage selection indicators.

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer
Area, computer

44 1/2 in x 72 in x 31 in Desk

Area, computer 44 1/2 in x 72 in x 31 in Desk Weight, computer 1,686 lbs, including Flexowriter

## PRODUCTION RECORD

Number produced 1 No longer in production.

## COST, PRICE AND RENTAL RATES

Manufacturer

System is no longer being manufactured. Engineer Research and Development Laboratories Approximate cost of basic system \$86,074.

## PERSONNEL REQUIREMENTS

Engineer Research and Development Laboratories One person required for operation and one person required for servicing unless one person is trained to perform both operation and servicing.

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Operating ratio (Good/Attempted to run time) 0.85 Passed Customer Acceptance Test March 1955

Computer has operated for several days without any down time; however, the only actual figure is the 2,069 hours taken from the running time meter.

## ADDITIONAL FEATURES AND REMARKS

Pre-addressed tapes
Single cycle operation where program is checked line for line.
Pre-determined automatic sequencing.
Shock-mounted for van installation; mobile.

## **INSTALLATIONS**

U. S. Army Corps of Engineers Topographic Engineering Department Engineer Research and Development Laboratories Fort Belvoir, Virginia

667 MONROBOT V

# MONROBOT VI Monroe General Purpose Computer Model VI

#### **MANUFACTURER**

Monroe Calculating Machine Company Electronics Division

Photo by Monroe Calculating Machine Company, Electronics Division

## **APPLICATIONS**

Scientific calculation.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system
Decimal digits/word

Binary coded decimal

Decimal digits/word
Decimal digits/instruction

20

Instructions per word Instructions used

2 200

Arithmetic system Instruction type Fixed point Four address

Instruction type Four add:
Number range ± xxxxx xxxx

+ xxxxx xxxxx . xxxxx xxxxx

Fixed point is centrally located

#### ARITHMETIC UNIT

Incl Stor Access

Add time Mult time Div time Construction Microsec 135,000 600,000 600,000

Vacuum tubes and crystal diodes

Basic pulse repetition rate Arithmetic mode

Timing Operation 10 Kc/sec Serial Synchronous Sequential

Automatic positioning of numerical results about the decimal point.

## STORAGE

Medium Magnetic Drum Words 200 Access Microsec 16,670 max

4,000 digits of magnetic drum storage.

## **INPUT**

Media Keyboard Punched Tape

Speed Manual 10 char/sec 17 cards/sec

Punched Card 17 cards/sec Punched tape is optional to 60 char/sec.

Standard teletype or Kleinschmidt units for tape

MONROBOT VI

668

Photo by Monroe Calculating Machine Company, Electronics Division

## **OUTPUT**

Media Printed Copy Speed 10 char/sec 10 char/sec 17 char/sec Punched Tape Punched Card

Standard teletype or Kleinschmidt units. 60 char/sec. Punched tape and punched card is optional.

## **INSTALLATIONS**

Several systems are at U. S. Air Force installations in Japan and Germany. These are under Monroe maintenance. The systems installed in the United States are not under Monroe maintenance.

Howard University

## **CHECKING FEATURES**

Parity checks MAID (Monrobot Automatic Internal Diagnosis) and dual arithmetic and control units.

## MONROBOT IX

Monroe Calculating Machine Company

MANUFACTURER

## **APPLICATIONS**

System is used primarily for billing, and invoice writing.

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary Binary digits/word 62 bits A plugboard program is used.

Instructions decoded 16 Arithmetic system Fixed point One address

Instruction type

0 to 10<sup>18</sup> Number range System has 15 registers.

## **ARITHMETIC UNIT**

Incl. Stor. Access Exclud. Stor. Access Microsec

Microsec 12,000

Add Mult 13,500

per decimal digit of multiplier 54,000 per decimal digit of

3,000

quotient

Construction (Arithmetic unit only) Vacuum-Tubes 64

Diodes Arithmetic mode Timing

Div

1,000 Serial Synchronous Sequential

Operation Multiplication is accomplished by manual input of the multiplier digits. Therefore, actual speed is operator limited.

During division the quotient digits are printed, and the actual speed is therefore printer limited.

#### **STORAGE**

	No. of	No. of	Access
Medium	Words	Digits	Microsec
Magnetic Drum	15	18 decimal dig	12,500 avg

#### INPUT

Media Electric Typewriter Plugboard

Speed 10 char/sec 10 char/sec

Plugboard is used for constants and alphabetic characters.

Typewriter input is operator limited.

## **OUTPUT**

Speed Media 10 char/sec Electric Typewriter Numerical and alphabetic

IBM 024 Card Punch

10 char/sec

Numerical

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Quantity Туре

Tubes

Primarily 5965 Diodes

74

Primarily 1N636

1,000

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

KVA, computer 0.75

32 cu ft Volume, computer 10.6 sq ft Area, computer 40 lbs/sq ft Floor loading 150 lbs concen max

450 lbs Weight, computer Site preparation not necessary.

#### PRODUCTION RECORD

Number produced to date Number in current operation

Time required for delivery

## COST, PRICE AND RENTAL RATES

\$9,650 Computer with Typewriter List of additional equipment IBM (024) Intercoupler 500 (IBM 024 - see IBM price list)

90-day guarantee parts and labor. Service contract price is \$500.

## PERSONNEL REQUIREMENTS

One clerk is required for each 8-hour shift. Ordinary typists are trained at customer location in one day.

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Ambient temperature to 130°F Voltage margins, ±25% Pre-aged tubes used throughout.

## **FUTURE PLANS**

Punched tape output planned soon.

## ADDITIONAL FEATURES AND REMARKS

Computer has fewer tubes (74) than any other product known to us. Calculation is so fast operator experiences no delay. System handles fractions of any kind, feet, inches, gross, dozen, board feet, etc. Foreign currency such as pounds, shillings, pence, are handled. Step-rate utility billing is automatically performed.

System operates as a decimal machine externally. Input and output conversion are automatic. Decimal shift left and shift right instructions make this possible.

## MONROBOT XI

Monroe Calculating Machine Mark XI

#### **MANUFACTURER**

Litton Industries Monroe Calculating Machine Division

## Photo by Monroe Calculating Machine Division

## **APPLICATIONS**

The Monrobot Mark XI is a stored-program, general purpose electronic business computer capable of operation with a wide variety of input-output equipment.

## PROGRAMMING AND NUMERICAL SYSTEM

PROGRAMMING	CAND NU	MERICAL SYS	STEM
Internal number sys	tem	Binary	•
Binary digits/word		32 including	sign
Binary digits/instr	uction	16	
Instructions/word		2	
Instructions decode	eđ.	27	
Instructions used		27	
Arithmetic system		Fixed point;	programmed
		floating poin	nt
Instruction type		One address	
Number range	0 to 2 <sup>31</sup>	- 1 or 0 to ± 3	10 <sup>9</sup> or
	0 to ± 2,	147, 483, 647	

- 11	BOL	4C U.	LOH	MOJ	. u _ 1	OTIMO	••								
16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
	Ins	str	ıcti	on					Ad	dre	88				

#### **ARITHMETIC UNIT** Incl. Stor. Access Exclud. Stor. Access Microsec Microsec 9,000 34,000 3,000 28,000 Add Mult 500,000 500,000 DivDivision is programmed. Construction (Arithmetic unit only) 190 1,675 Transistors Diodes Serial Arithmetic mode Synchronous Sequential Timing Operation

#### **STORAGE**

	No. of	No. of	Average Access
Medium	Words	Digits	Microsec
Magnetic Drum	1,024	<i>3</i> 2,768	6,000

#### **INPUT**

Media	Speed
Punched Paper Tape	20 char/sec
Electric typewriter	10 char/sec
16-key numeric keyboard	10 char/sec
80-column card	16 col/sec
Tel.etypewriter	10 char/sec

The machine can accomodate any three of the above input devices simultaneously. Punched paper tape may be any code, 5 to 8 level. Quoted input and output speeds include conversion to and from binary as well as translation of any tape language to machine code. Higher speeds are possible using pure binary input and output.

#### **OUTPUT**

Media.	Speed
Punched Paper Tape	20 char/sec
Electric Typewriter	10 char/sec
80-column Card	16 col/sec
Teletypewriter	10 char/sec

The machine can accomodate any three of the above output devices simultaneously. Punched paper tape may be any code, 5 to 8 level.

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

V	
Туре	Quantity
Tubes	70 70 /70 / 7 / 7 / 7
5727	10 - 30 (10 tubes/output device)
Diodes Primarily 1N636	0. 700
Transistors	2,300
Primarily 2N412	383
TI.IIIGH IIJ ZII-FIZ	909

#### **CHECKING FEATURES**

Parity check on input and output. Parity may be omitted.

Action taken on parity failure depends upon program. With Teletype or other parity-less codes, parity is not used.

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer	0.85	O Kw	0.940 KV <i>I</i>	1 0.9 p.f
Volume, computer			48 cu	ft
Area, computer			15 sq	ft
Room size			10 ft	x 10 ft
Floor loading				s/sq ft
			100 lbs	concen max
Weight, computer			3 <b>7</b> 5 1ba	
System requires	15 amp,	110	volt, AC, 6	0 cps line.

#### PRODUCTION RECORD

Number produced to date	7
Number in current operation	6
Time required for delivery	6 - 9 months

## COST, PRICE AND RENTAL RATES

Cost of computer with operator desk,	
1 typewriter, 1 tape reader, and 1	
tape punch	<b>\$</b> 24 <b>,</b> 500
Additional Equipment	• •
16-key numeric keyboard	300
Tape Reader	1,250
Tape Punch	700
Typewriter	2,350
Buffer for third device	600
Above prices are approximate.	
Monthly rental of computer with opera	tor desk,
1 typewriter, 1 tape reader, and 1 tape	punch,
including service	\$700
Maintenance contracting is \$1,200/yea	r after

#### PERSONNEL REQUIREMENTS

90-day service guarantee.

Manufacturer makes a programmers' school available to users. One operator is required for each 8-hour shift. Virtually no operator training is required.

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Conservative solid-state design assures long life and wide margins under all but the most extreme operating conditions. Pluggable printed circuit boards provide trouble-free operation as well as ease of maintenance. Monrobot XI operates with full ±25% voltage margins at 110°F ambient.

#### ADDITIONAL FEATURES AND REMARKS

The Monrobot Mark XI accepts alpha-numeric information in any code from up to three independent input devices and can output information to any combination of three independent devices.

System can simultaneously prepare independent output documents in any format, and can merge transaction and unit record input tapes in any format.

# MONROBOT MU Monroe Multiple-Unit General Purpose Computer

## **MANUFACTURER**

Monroe Calculating Machine Company Electronics Division

Photo by Monroe Calculating Machine Company, Electronics Division

## **APPLICATIONS**

Item inventory and monetary accounting.

## PROGRAMMING AND NUMERICAL SYSTEM

PROGRAMMING F	AND NUMERICAL STOTEM
Internal number system	Binary coded decimal
	and sexadecimal
Binary digits/word	96
Decimal digits/instruct	ion 12
Instructions per word	2
Instructions decoded	<b>3</b> 6
Instructions used	36
Arithmetic system	Fixed point (arbitrarily
•	located)
Instruction type	Three address (modified)
Number range	Variable
J	

Number range Variable
Words may be made up of either numeric or alphanumeric characters.

## ARITHMETIC UNIT

	Includ Stor Access
	\ Microsec
Add time	8,000
Mult time	68,000
Div time	77,000
Construction	Vacuum tubes and crystal
	diodes
Basic pulse repetition rate	60 Kc/sec (rapid access)
	104 Kc/sec (general
	storage)
Arithmetic mode	Serial
Timing	Synchronous
Operation	Sequential
-	<del>-</del>

## **STORAGE**

The large capacity drums are for general storage and utilize saturable core reactors for track selection. The number of drums utilized is based upon application requirements.

#### **INPUT**

Media Speed
Keyboard (Flexowriter) 10 char/sec
Keyboard (Model 28 6 char/sec
Teletype 12 Units)
Paper Tape (Ferranti) 200 char/sec
Magnetic Tape 400 char/sec

#### **OUTPUT**

Media Speed
Paper Tape (Flexowriter) 10 char/sec
Printed Page (Flexowriter) 10 char/sec
and Model 28 Teletype)
Magnetic Tape 400 char/sec

## **CHECKING FEATURES**

Parity checks
MAID (Monroe Automatic Internal Diagnosis)
System used for malfunction detection and location.

#### **INSTALLATIONS**

System was installed for the U.S. Air Force under Contract No. AF33(616)-2158.

## NAREC

Naval Research Electronic Computer

#### **MANUFACTURER**

U. S. Naval Research Laboratory

#### **APPLICATIONS**

General purpose scientific calculation and data processing.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system	Binary
Binary digits/word	48
Binary digits/instruction	24
Instructions/word	2
Instructions decoded	44
Arithmetic system	Fixed point
Instruction type	One address
Number range	-1 to +1
Instruction word format	

Address			Order		
4	4	4	4	4	4

Half-word, six four-bit sexadecimal characters

Automatic built-in subroutines include punched tape input and output routines and variable length data transfer instructions (2 address).

Automatic coding includes floating point interpretive routines, containing standard mathematical subroutines as basic instructions.

#### Registers:

- 7 48 bit parallel registers in arithmetic section, including one adder and one inverter of which two are directly programmable.

  1 - 48 bit parallel register in control section

Official United States Navy Photo

- 1 16 bit parallel register in control section (program counter)
- 1 48 bit serial parallel output buffer register
- 2 48 bit multiple use comparator registers
- 1 48 bit core memory information register 1 - 14 bit core memory address register
- 2 48 bit and 1 16 bit manual switch registers

#### **ARITHMETIC UNIT**

Incl. St	or. Access	Exclud.	Stor. Access
Micr	osec	M	licrosec
Add 2	2		6
Mult 300-	600	same (appr	ox range)=450
	<b>7</b> 25		ox range)=650
Division = 10 p	er digit (e	excluding a	ccess) for
numerator = 0			
Construction (Ari	thmetic un:	it only)	
Туре	Quantity		
Vacuum tubes			
568 <b>7</b>	600		
56 <b>70</b>	600		
6an5	100		
Total	1,300		
Transistors	0		
Crystal diodes	16,000		
Arithmetic mode	Parallel		
Timing	Asynchrono	ous	
Operation	Sequential	l.	
Input-output is	partially	concurrent	;

NAREC 676

#### STORAGE

Media Magnetic core Memory cycle time Magnetic drum Maximum drum acces Magnetic tape	is 8 microsecond 8,192	D: 78 Is	o. of Lgits 86,432 93,216 onds
No. of units that No. of chars/line Each line acros	ar inch s the tape contai Packing density s on the tape ting each record	600 ins 2 7 = 30 16 •75 120 72 KC	Chars/inch characters OO lines/inch Tracks/tape
Physical propertic Width Length of reel Composition	-	1 -3600	Inch Feet Mylar

The above information on magnetic tape is preliminary only, as definite plans are now being formulated. The tape system should be installed in the NAREC by July 1961.

#### INPUT

Media Speed
Magnetic Tape 120 in/sec
Paper Tape 25-100 in/sec

Dual speed photoelectric reader at 250 and 1000 char/sec using mylar-aluminum foil and paper tape.

#### **OUTPUT**

Media Speed
Paper Tape 60-llo char/sec
High and medium speed paper tape punches
Magnetic Tape 120 in/sec
Line Printer

Line Printer will be installed by July 1961. Speed of 600-1200 lines/minute is anticipated.

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Туре	Quantity
Tubes	•
5687	600
5670	600
6an5	100
•	1.300

Does not include electrostatic memory which is being replaced by magnetic core memory.

Diodes		
1N89	16,000	
SG22	1,800	
DR211	7,000	
1N690	4,000	
650 <b>C</b> 5	250	Zener Diodes
651CO	350	(Texas Instruments)
65107	<u>600</u>	
	30,000	

1N89 used in arithmetic section. All others used in magnetic core memory.

Transistors
2N1478 1,000
2N600 500
2N1122 3,000
2N1123 300
4,800

Resistors 20,000 (core memory)
7,000 (arith. section)

## **CHECKING FEATURES**

Automatic comparison bit by bit of all transfers between registers in arithmetic and control sections by means of 2 - 48 bit comparator registers.

Magnetic tape system will have conventional parity checks and sense instructions.

#### POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer	25	Κw		
Power, air conditioner	15	Κw		
Volume, computer	1,000	cu	ft	
Area, computer	125	вq	ft	
Room size, computer	30	ft	x 80	ft
Capacity, air conditioner	25	Tor	ıs	
Weight, computer 5.000-1	LO.000	lbs	3	

Site preparation included concrete trenches in floor for power wiring and coaxial cables. Power includes both M-G sets and electronic power supplies.

#### PRODUCTION RECORD

Number produced to date 1
Number in current operation 1

#### COST, PRICE AND RENTAL RATES

Total system cost approximately \$1,500,000.

## PERSONNEL REQUIREMENTS

, =110011112= ::= 401112=::-1110				
	One 8-Hour	Shift Two	8-Hour	Shifts
Supervisors	2		2	
Programmers	10		20	
Clerks	2		2	
Engineers	1		2	
Techniciona	ス		5	

Operation tends toward semi-open shop. Includes programmers in central facility and in other groups of the Laboratory. Programmers above includes analysts and coders. The above does not provide for magnetic tape or printer operation. Technicians above includes operators.

Training made available by manufacturer includes in-service programming courses by Research Computation Center for rest of the NRL.

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Approximately 1 hour per shift is required for scheduled maintenance.

Operating efficiency (ratio of good time to scheduled operating time) has averaged 85% over the past four years (1956-1960) of full time operation. This is expected to improve considerably in the future due to the current replacement of electrostatic storage by magnetic core storage.

## ADDITIONAL FEATURES AND REMARKS

Outstanding features include a special console, which displays the contents of core memory address abd information registers and permits direct manual read and write to core memory locations.

Unique system advantages include computation monitored from control console without loss of time to stop computer at desired location or instruction in many different ways in order to facilitate program and machine checking. Instruction code and layout is very simple to use and remember, yet is extremely powerful and flexible.

Flexowriters are used in parallel to print results of several problems simultaneously. A centralized operating area consists of photoelectric tape readers, high speed punch, Flexowriter and core memory console adjacent to main control console.

#### **FUTURE PLANS**

Magnetic core memory installed in October 1960. Magnetic tape system and line printer will be installed by July 1961.

## **INSTALLATIONS**

U. S. Naval Research Laboratory Washington 25, D. C.

NAREC

678

679 NAREC

## NATIONAL 102A

National Cash Register Company Model CRC 102A Built by former Computer Research Company

## **MANUFACTURER**

National Cash Register Company

#### **APPLICATIONS**

Manufacturer
General purpose scientific applications
Chemical Warfare Laboratories, U.S. Army
Chemical Center
Scientific

Scientific
U.S. Naval Ordnance Test Station (China Lake)
Data reduction general purpose
U.S. Naval Postgraduate School
Located at Monterey, California, the system is used

for scientific applications, including student and faculty research in practically all phases of the physical sciences, for data processing, including weather prediction, and for simulation, including electronics systems and games (business, industrial and military).

Photo by U. S. Army Chemical Center

Holloman Air Development Center (ARDC) Trajectory calculations, heat transfer problems, solution of various kinds of linear simultaneous equations and other algebraic equations.

U.S. Air Force School of Aviation Medicine Located at Brooks Air Force Base, Texas, the system is used for matrix algebra (Covariance, symmetric and non-symmetric mult. and inversion programs, corr), factor analysis (Factoring the correlation matrix and rotating the factor vector), for general analysis statistics (Mean, variance, and st. deviations; and analysis of variance), time series analysis (Circular serial correlation, autocorrelations, periodograms), and for Monte Carlo methods, generating pseudo random fractions (runs, means, frequency).

## PROGRAMMING AND NUMERICAL SYSTEM

Manufacturer

Internal number system Binary Binary digits/word Binary digits/instruction 42 (One for timing) 41 Instructions per word ı 27 27 Instructions decoded Instructions used Fixed point Arithmetic system Instruction type Three address Number range  $-(1-2^{-36})$  to  $(1-2^{-36})$  Photo by U.S. Naval Post Graduate School Official Photograph U.S. Navy

#### **ARITHMETIC UNIT**

	Incl Stor Acce	ss Exclud Stor Access
	Microsec	Microsec
Add	19,900	7,400
Mult	37,500	25,000
Div	38,500	25,800
Construction	3	00 Vacuum tubes
	4,0	00 Diodes
Rapid access	word registers	8
Basic pulse r	epetition rate	100 Kc/sec
Arithmetic mo	de	Serial
Timing		Synchronous
Operation		Sequential
The speed c	an be increased	by a factor of 8 when
a buffer is u	sed.	

## **STORAGE**

Media Magnetic Drum	Words 1,024	Microsec Access 12,500 (avg)
Magnetic Tape	112,800/reel	6,000
		with one computer.
Magnetic drum	rotates at 40 rev	/sec.

#### INPUT

Manufacturer	
Media	Speed
Flexowriter	Typing Speed
Punched Paper Tape	10 char/sec
Magnetic Tape	1.6 char/millsec
U.S. Army Chemical C	enter
Flexowriter	Typing Speed
Punched Paper Tape	10 char/sec
Magnetic Tape	64 words/sec
U.S. Naval Ordnance	Test Station
Punched Paper Tape	10 char/sec
IBM Cards (523 modified)	50 cards/min
U.S. Naval Post Grad	uate School
Paper Tape	10 char/sec
Cards	60 cards/min
	240 words/min
Magnetic Tape	60 words/sec
School of Aviation M	[edicine
Flexowriter	Typing Speed
Punched Paper Tape	10 char/sec
IBM Cards	50 or 100 cards/min

#### **OUTPUT**

1.6 char/millisec

3.5-	 +	nrer

Magnetic Tape

Media	Speed.
Magnetic Tape	600 char/sec
Flexowriter	10 char/sec
Punched Paper Tape	10 char/sec
U.S. Army Chemical Cent	er
Magnetic Tape	64 words/sec
Flexowriter	10 char/sec
Punched Paper Tape	10 char/sec
U.S. Naval Ordnance Tes	t Station
Flexowriter	10 char/sec
Punched Paper Tape	10 char/sec
IRM Cards (523 modified)	
U.S. Naval Post Graduat	e School
A digital point is used, which	
ly of computer. No conversion	
plotting. The plotter is man	ufactured by the Cali-
fornia Computer Products Corp	
Paper Tape	10 char/sec
Cards	60 cards/min
Magnetic Tape	60 words/sec
School of Aviation Medi	,
Flexowriter	10 char/sec
Paper Tape	10 char/sec
Cards	50 or 100 cards/min
Magnetic Tape	600 char/sec

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Tubes	400	
Tube types	12	
Crystal diodes	8,000	
U.S. Army	Chemical Center	
Tube types used	include 12AT7, 1	2BH7, 6BQ7, 5687,
5963, 6080, 588	l, and 6AN5. Sys	tem utilizes 265
tubes and 6,000	diodes and consi	sts of operators
console, compute	er proper, and ma	gnetic tape unit.

#### **CHECKING FEATURES**

Duplicate recording on magnetic tape "Overflow" alarm"
"No command" alarm

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Manufacturer	
Power, computer	7.7 Kw
Weight, computer	2,700 lbs
U.S. Army Chemic	cal Center
Computing system occur	pies 135 cu ft, air conditioner
48 cu ft. The comput:	ing system weighs 3,400 lbs,
	s 12,000 lbs. The capacity of
the air conditioner is	

U.S. Naval Ordnance Station System operates a 230v ±5% line, liberates 35,000 BTU/hour. Computer occupies 72 cu ft and 12 sq ft (Dimensions are 30 by 59 by 73 inches).

U.S. Naval Post Graduate School

Power, computer 5.5 Kw	7.7 KVA	0.71 pf
Volume, computer	108 cu ft	
Volume, air conditioner	360 cu ft	
Area, computer	18 sq ft	
Area, air conditioner	36 sq ft	
Room size Approx	2,800 sq ft	
Floor loading	200 lbs/sq	ft
	700 lbs cor	cen max

25 Tons Capacity, air conditioner

Weight, computer Approx 2,000 lbs, total Lobby section of one of the school buildings was partitioned. False flooring, air conditioning and power were installed in the laboratory section which houses two computers (CDC 1604 and NCR-CRC-102A) and their associated peripheral equipment. Sahool of Arristion Modiains

DOMOOT OF WATSCION	Medicine
Power, computer 5.5 Kw	7.7 KVA 0.7 pf
Volume, computer	72 cu ft
Area, computer	12 sq ft
Room size	20 ft x 12 ft
Floor loading	225 lbs/sq ft
	60 lbs concen max
Capacity, air conditioner	5 Tons
Weight, computer	2,700 lbs
Air conditioner	central unit

System required primary power source due to sensitivity to power fluctuations.

#### PRODUCTION RECORD

Manufacturer Produced Operating 16

## COST, PRICE AND RENTAL RATES

U.S. Army Chemical Center

Approximate cost of basic system	\$70,000
Approximate cost of additional equip-	
ment	\$25,000
Rental rates for basic system	\$ 2,400/month
U.S. Naval Post Graduate School	
Computer, 2 tape drives, console, F	lexowriter
(paper tape reader and punch), point ]	plotter cost
approximately \$100,000.	
Rental rates for additional equipmen	
the 2 IBM 523, IBM 402, IBM 082, and 3	IBM 026 is
\$600 per month.	
Approximately \$9,500 per year is par	id to the Na-
tional Cash Register Company for main	tenance service
School of Aviation Medicine	

The 102A Computer cost approximately \$80,000. The 126A Magnetic Tape Unit cost approximately \$15,000.
Two IBM Summary Punches (No. 523) rent at approximate-

ly \$2,100 per year.

#### PERSONNEL REQUIREMENTS

U.S. Army Chemical Center

One 8-hour shift requires 1 engineer and 1 technician-operator.

U.S. Naval Ordnance Test Station

One 8-hour shift requires 1 engineer, 2 programmers, and 1 "open shop" personnel.

U.S. Naval Post Graduate School

The computers are available for student and faculty research 24 hours per day. Those students and faculty who have been checked-out on the operation of the computers and peripheral equipment are permitted outof-hours production runs on the computers. Potentially the school has approximately 1,000 programmeroperators under this system. At the present time the CDC 1604 operates approximately 14 hours per day and the NCR 102A 20 hours per day, 7 days per week. Course work is given in the Engineering School on

programming, operation and applications.

Seminars are given at the school.

School of Aviation Medicine

			(	One	8-Hour	Shift
Analysts					1	
Programmers					1	
Operators					1	
Engineers					1	
Ā 1.	-	-		•		

Operation tends toward open shop. On-the-job training (OJT) is given.

Gulf Research and Development Company Central computing section consists of 1 engineer, 1 operator, 1 secretary-librarian, and 5 administrative, numerical analysis and programming personnel, for slightly more than an 8-hour shift.

## RELIABILITY, OPERATING EXPERIENCE. AND TIME AVAILABILITY

U.S. Army Chemical Center 3,380 hours Good time Attempted to run time 5,200 hours Operating ratio (Good/Attempted to run time) 0.65 Figures based on a 2.5 year period Passed Acceptance Test

U.S. Naval Ordnance Test Station 80 hours Average error-free running period Operating ratio (Good/Attempted to run time) 0.90 Figures based on period 1 Jan 55 to 10 Nov 56 Passed Acceptance Test 1 Jun 54

U.S. Naval Post Graduate School Passed Acceptance Test Summer of 1953 Time is not available for rent to outside organizations.

Since August 1958, the NCR 102A has averaged in excess of 100 hours per week operating time and has averaged less than 2 hours per week of forced downtime due to equipment failure. Each working day there is a two hour preventive maintenance period.

School of Aviation Medicine 36 Hours/Week (Average) 40 Hours/Week (Average) Good time Attempted to run time Operating ratio (Good/Attempted to run time) 0.90 Above figures based on period from Fall 54 to 1 Jul 59 Time is not available for rent to outside organizations.

## ADDITIONAL FEATURES AND REMARKS

Holloman Air Development Center (ARDC) Two systems have been in use since 1953. Additional input/output equipment includes 3 magnetic tape units (NCR 128), and 1 summary card punch (IBM 523).

U.S. Army Chemical Center

The three-address system permits ease of programming and coding not present in one or two address systems.

Additional features include: Bit positions available in the structure of commands for flagging internal addresses for automatic coding and repositioning of programmed routines. Logic designed such that. automatic links to and from subroutines are accomplished easily. Large storage capacity and threeaddress commands. Tape unit searches independently of the computer, once the block address is set up. High speed paper tape input reader. A visual display of the control register while computing and when idle. All logical diodes accessible, diodes are of clip-in type, plug-in units replaceable for repair.

U.S. Naval Post Graduate School The system is used primarily for the education of the officers in the Engineering School. Other computing time after normal training hours is used by government agencies in the Monterey area on a non-interference basis.

The plotter output is considered to be a very valuable part of the system permitting rapid observation of results. The binary information is plotted directly on graph paper, thus avoiding the problem of converting to decimal. The plotter has three

plotting symbols. Gulf Research and Development Company Auxiliary equipment consists of: 2 NCR 126 Magnetic Tape Units. Two Flexowriters, each capable of serving as the in-

put-output device for the computer. A third Flexowriter, not modified, for input-output functions.

One IBM 514 Reproducing Punch, modified for use as an output device.

A photoelectric paper tape system based upon a Ferranti reader has been constructed.

Two more one-word recirculating registers have been made addressable, making them correspond roughly to the accumulator and Q-register of a typical oneaddress computer.

#### INSTALLATIONS

Holloman Air Development Center (ARDC) Holloman Air Force Base, New Mexico

School of Aviation Medicine Randolph Air Force Base, Texas

Chemical Warfare Laboratories U. S. Army Chemical Center, Maryland

U. S. Naval Ordnance Test Station China Lake, California

U. S. Naval Post Graduate School Monterey, California

A. V. Roe, Ltd. Malton, Ontario, Canada

Great Lakes Pipe Line Company Kansas City, Missouri

Gulf Research and Development P. O. Drawer 2038 Pittsburgh 30, Pennsylvania

Polytechnic Institute of Milan Milan, Italy

Royal Canadian Air Force Edmunton, Alberta, Canada

## NATIONAL 102D

NCR CRC Model 102 D

#### **MANUFACTURER**

The National Cash Register Company

#### **APPLICATIONS**

Manufacturer

General purpose scientific application Pitman-Dunn Laboratories

The system is used for general scientific computing, to include trajectories, other differential equations, interior and exterior ballistics, fire control problems, curvefitting, solution of algebraic equations, simulations, etc.

tions, simulations, etc.

The National Cash Register Company
Located at the Research and Development Division,
The National Cash Register Company, Dayton, Ohio,
the system is used to support the physical and chemical investigations of the Research and Development
Division by processing experimental data originating
in the research and engineering laboratories. Work
in such fields as logical design, wiring diagrams for
logical systems, cam design, and statistical analysis
of data are typical of the applications made.

Photo by The National Cash Register Company

## PROGRAMMING AND NUMERICAL SYSTEM

Internal number system
Decimal digits/word
Binary digits/instruction
Instructions per word
Instructions decoded
Arithmetic system
Instruction type

Number range
Or

Binary coded decimal
9 plus 6 bin dig/word
42
27
Fixed point
Three address
-(1-10<sup>-9</sup>) to +(1-10<sup>-9</sup>)

 $-(1-2^{-36})$  to  $+(1-2^{-36})$ 

1. Computer, 2 Control Consol, 3 High Speed Paper Tape Reader, 4 Magnetic Tape Units, 5 High Speed Paper Tape Punch

## **ARITHMETIC UNIT**

Manurac	turer	
	Incl Stor Acces	s Exclud Stor Access
	Microsec	Microsec
Add	7,800	4,000
Mult	21,100-49,100	15,000
Div	21,100-53,200	15,500
Construction		Vacuum tubes
Rapid access	word registers	8
Basic pulse re	epetition rate	100 Kc/sec
Arithmetic mo	đe	Serial
Timing		Synchronous
Operation		Sequential

## **STORAGE**

Manufacti	ırer		Access
Media.	Words	Digits	Microsec
Magnetic Drum	1,024	43,008	12,500 avg
Magnetic Tape	102,000/reel	918,000/ree	
Pitman-Du	nn Laboratorie	s	
	No. of	No. of	Min Access
Media.	Words	Digits/Word	Microsec
Magnetic Drum	1,032	14 octal or	390
-	•	9 decimal	
Magnetic Tane	110-000	Same	

Photo by The National Cash Register Company

Magnetic tape searches approx. 90"/sec., reads/writes 15"/sec. (approx. 59 words/sec)

## INPUT

Manufacturer	
Media	Speed
Paper Tape (Flexowriter)	10 char/sec
Paper Tape (Photoelectric)	200 char/sec
Magnwtic Tape	600 char/sec
Keyboard (Flexowriter)	Manual.
Punched Cards	4,000 char/min
Pitman-Dunn Laborator	ries
Keyboard or Paper Tape	6 char/sec
Hi-Speed Tape Reader	160 char/sec
Magnetic Tape	59 words/sec
IBM Card Reader	100 cards/min
Six channel Flexowriter	is used. Cards contain 4
words each.	

Photo by The National Cash Register Company

## **OUTPUT**

Manufacturer		
Media		Speed
Paper Tape (Flexowriter)		char/sec
High Speed Punch		char/sec
Magnetic Tape	600	char/sec
Punched Cards	4,000	char/min
Pitman-Dunn Laboratories		
Typed Page	6	char/sec
Paper Tape (High Speed)	60	char/sec
Card (IBM)	100	cards/min

## CIRCUIT ELEMENTS OF ENTIRE SYSTEM

## Manufacturer

Tubes 425
Tube types 15
Crystal diodes 8,500

## **CHECKING FEATURES**

Manufacturer
Duplicate recording on magnetic tape
"Overflow" alarm
"No command" alarm

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Manufacturer	
Power, computer	7.7 Kw
Area, computer	250 sq ft
Weight, computer 2,	,700 lbs
Pitman-Dunn Laborator	ries
Power, computer 8.2 Kw	11.6 KVA 0.71 pf
Power, air conditioner	61.5 KVA
Volume, computer	154.5 cu ft
Volume, air conditioner	2,318 cu ft
Area, computer	49 sq ft
Area, air conditioner	206.5 sq ft
Room size, computer	19 ft x 39 ft
Room size, air conditioner	
Floor loading	84 lbs/sq ft
	425 lbs concen max
Capacity, air conditioner	60 Tons 720,000 BTU
Weight, computer	4,110 lbs
Weight, air conditioner	6,600 lbs
Weight, cooling tower	2,500 lbs
Power includes Input-Outp	out systems.
One 5 KVA and one 10 KVA	Sol A transformers are
used for voltage regulation	a. Power outlets for main
computer and auxiliary equi	ipment were installed. Air

686

conditioner services personnel and an analog computer also.

## COST, PRICE AND RENTAL RATES

Manufacturer	* *			
Approximate cost of basic system	<b>\$</b> 65 <b>,</b> 000			
Approximate cost of additional equipme	ent			
Magnetic Tape Unit Model 126	<b>\$1</b> 6,000			
High Speed Reader Model 160	9,500			
High Speed Punch Model 170	5,000			
Flexowriter	2,900			
Rental rates for basic system	\$2,400/month			
Rental rates for additional equipment				
High Speed Reader Model 160	2 <b>7</b> 5/month			
High Speed Punch Model 170	220/month			
Flexowriter	150/month			
Pitman-Dunn Laboratories				
Computer w/console, Flexowriter mag	. tape unit, hi-			
speed reader, hi speed punch, including transportation				
and installation cost \$83,370.				
Off-line Flexowriter cost \$2,900 additional.				
IBM equipment includes 2-523's, 2-026's, 1-082,				
1-085, 1-514, and 1-402, which rents at \$12,624/year.				
Maintenance for purchased equipment is \$12,624/year.				
DED CONNET DECITIONE	ITC			

#### PERSONNEL REQUIREMENTS

Pitman-Dunn Laboratories

One 8-Hour Shift Analysts 1 Programmers 2 Technicians

Photo by the Georgia Institute of Technology

Operation tends toward closed shop. Methods of training includes basic programming course and use of the programming manuals. Programmers code and run their own problems.

The National Cash Register Company One 8-Hour Shift

Supervisors 1 Coders, Programmers & Analysts

Operation tends toward open shop. Individuals may have programs entirely written by department personnel, written by themselves with assistance from department, or written entirely by thwmselves. Individuals desiring running time for a problem most commonly supply their own operator.

Methods of training used includes on-the-job training of department personnel, informal training of other research personnel.

RELIABILITY. OPERATING EXPERIENCE,

AND TIME AVAILABILITY

Pitman-Dunn Laboratories Average error-free running period 4 Hours 26 Hours/Week (Average) Good time Attempted to run time 39 Hours/Week (Average) Operating ratio (Good/Attempted to run time) 0.67 Above figures based on period 1 Apr 59 to 30 Apr 60 Passed Customer Acceptance Test Jan 58 Time is not available for rent to outside organizations.

4 hours of preventive maintenance is performed.

The National Cash Register Company 28.5 Hours/Week (Average) 32 Hours/Week (Average) Good time Attempted to run time

High Speed Reader

Operating ratio (Good/Attempted to run time) 0.89 Above figures based on period from Jul 59 to Aug 60 Time is not available for rent to outside organizations.

## ADDITIONAL FEATURES AND REMARKS

Pitman-Dunn Laboratories Outstanding features include 3 address system, which is easy to use. Photo by The National Cash Register Company

## **FUTURE PLANS**

Pitman-Dunn Laboratories System expected to be replaced by second quarter Fiscal Year 61 with system comparable with DATATRON 205, IBM 650, or UNIVAC Solid State.

High Speed Punch

## **INSTALLATIONS**

Dow Chemical Company Midland, Michigan

Rice Electronic Computer Center Georgia Institute of Technology Atlanta, Georgia

Research and Development Division The National Cash Register Company Dayton, Ohio Photo by The National Cash Register Company

The National Cash Register Company Hawthorne, California Pitman-Dunn Laboratories Frankford Arsenal Philadelphia, Pennsylvania

## NATIONAL 107

National Cash Register Corporation Model 107

## **MANUFACTURER**

National Cash Register Corporation

Photo by U. S. Naval Academy

## **APPLICATIONS**

Located in Ward Hall, U. S. Naval Academy, the system is used to demonstrate to Naval Academy faculty and midshipmen, automatic calculations using a large data processing/scientific type digital computer.

## PROGRAMMING AND NUMERICAL SYSTEM

	MENIONE SISIUM
Internal number system	Binary coded decimal
Decimal digits/word	11
Decimal digits/instruction	2
Instructions/word	l + sign
Instructions decoded	22
Arithmetic system	Fixed point
Instruction type	Three address
Number range	$-(1 - 10^{-9}) \le x \le 1$

The excess-three system of binary coded decimal notation is used.

#### ARITHMETIC LINIT

AKIIRW	IETTC UNIT
	Incl Stor Access
Operation Time	Microsec
Add	15,000
Mult	40,000
Div	40,000
Construction (Arithmeti	c unit only)
Vacuum-tubes	625
Diodes	2,040
Arithmetic mode	Parallel
Parallel by bit, seri	al by digit.
Timing	Synchronous
Operation	Sequential

#### **STORAGE**

	No. of	No. of		Access
Media,	Words	Digits		Microsec
Dicum	11,000	121,000		3,000
Magnetic	: Tape	·		•
		n be connecte	d 99	Units
No. of	char/linear i	nch of tape	134	Char/inch
Channel	s or tracks o	n the tape	10	Tracks/tape
Blank t	ape separatin	g each record	10	Inches
Tape sp	eed		15	Inches/sec
Transfe	r rate		2,010	Char/sec
Start t	ime		3	Millisec
Stop ti	me		3	Millisec
Average	time for exp	erienced		
operator	· to change re	el of tape	120	Seconds
Physics	l properties	of tape		
Width	L		1	Inch
Lengt	h of reel		3,600	Feet
Compo	sition		Mylar	or Acetate
All te	pe must initi	ally be pre-c	locked	off-line.

#### INPUT

	• • •	
Media	5	Speed
Card (IBM)	100	cards/min
Magnetic Tape	15	in/sec, 121 char/in
Paper Tape	120	char/min
Keyboard (Flexowrite	r)	Manual

#### **OUTPUT**

Media	Speed
High Speed Printer	600 lines/min, 120 char/line
Magnetic Tape	15 in/sec, 121 char/in
Cards (IBM)	100 cards/min
Typewriter (Flexo)	120 char/min
Paper Tape	120 char/min

#### CIRCUIT ELEMENTS OF ENTIRE SYSTEM

Туре	Quantity
Tubes	800
Diodes	2,500

#### **CHECKING FEATURES**

Unwanted Digit Command Check Overflow Conditional Halt

## POWER, SPACE, WEIGHT, AND SITE PREPARATION

Power, computer	208 Volts	3	phase	60 cps
Power, air cond	208 Volts	3	phase	60 <b>c</b> ps
Volume, computer		65,774	cu ft	
Volume, air condi	tioner	126	cu ft	
Area, computer		1,711	sq ft	
Area, air conditi	oner	18	sq ft	
Room size, comput	er	1,800	sq ft	
Capacity, air con	ditioner	15	Tons	
Weight, computer		12,000	lbs	
Weight, air condi	tioner	1,000	lbs	
Building was mo	dified for	compute	er insta	llation.

#### PRODUCTION RECORD

Number produced to date 1
Number in current operation 1
Produced for U. S. Navy Bureau of Aeronautics to their design specifications.

## COST, PRICE AND RENTAL RATES

Original cost, \$1,000,000 to Navy BUAER (original owner). System consists of control console, arithmetic section, high speed printer, 8 magnetic tape units, and memory section.

Additional Equipment

For the IBM 514, IBM 523, IBM 024, the total rent is \$108.00 per month (including educational discount 60%).

#### PERSONNEL REQUIREMENTS

One 8-Hour Shift
Supervisors 1
Operators 2
Technicians 5
Operation tends toward open shop.

Methods of training used is classroom and on-thelob training.

## RELIABILITY, OPERATING EXPERIENCE, AND TIME AVAILABILITY

Operating experience over four year period approximately 98% of scheduled production at previous location in Washington, D. C. (Navy Bureau of Weapons).

#### ADDITIONAL FEATURES AND REMARKS

Outstanding features are block search on tape handles at rate of 600 words/sec, off-line preparation and verification of tape input, and large memory with maximum of 10 word access.

#### **FUTURE PLANS**

Addition of X-Y plotter and weapons system simulation for educational purposes.

## INSTALLATIONS

U. S. Naval Academy Weapons Department Annapolis, Maryland

## NATIONAL 304

National Cash Register 304

## **MANUFACTURER**

National Cash Register Company

Photo by National Cash Register Company

#### APPLICATIONS

Manufacturer

The system is a general-purpose data processing system for industrial, governmental and educational organizations. It is intended for commercial and scientific applications. All commonly-used inputs and outputs, a magnetic tape system with unique characteristics, and flexibility in system organization result in a powerful system with versatility of application.

U.S. Marine Corps, Camp Lejeune Located on the base, the system is used for USMC personnel accounting.

U.S. Marine Corps, Camp Pendleton
Located at the U.S. Marine Corps, Camp Pendleton
the system will be used to maintain current magnetic
tape records on approximately one half the Active
and Organized Reserve Establishments of the Marine
Corps. Weekly, Changed Record Tapes will be forwarded to Headquarters, Marine Corps to overlay

Headquarters' Tape Records. Headquarters will prepare personnel reports for itself, Navy Department, and Department of Defense from its Magnetic Tape Files. Meanwhile, this installation will prepare "Field Personnel Reports" for 14 major field commands in the western complex. (DPT, MCB, Camp Lejeune, North Carolina performs the same function for the eastern complex, i.e., the other half of the Marine Corps.) Implementation of the system is scheduled for August 1960.

U.S. Marine Corps, Headquarters
Located at the Arlington Navy Annex, the system
s utilized for military personnel accounting.

is utilized for military personnel accounting.

The three U.S. Marine Corps NCR 304 Data Processing Systems are utilized in an integrated Military Personnel Accounting and Reporting System. The systems located in California and North Carolina servicing the two major Marine Corps bases and reporting to the system located in Washington D. C.

American United Life Insurance Company
Located at 30 W. Fall Creek Parkway, N. Dr. Indianapolis, Indiana, the system is used to maintain Master Tape Files of policyholders, agents,
payroll, general ledger, mortgages, securities, update daily any transactions that affect any master
record or file, compute commissions, journalize
daily - Maintain accounting controls, balance books
of account. Perform premium billing and policy conservation advices, control disbursements, bank reconciliations, and compute dividends, cash values,
premiums, reserves, interest, etc.
S. C. Johnson & Son, Inc.

Located at 1525 Howe Street, Racine, Wisconsin, the system is used for order processing, inventory control, accounts receivable, credit and collection, freight allocation, sales statistics, and allied financial reports.

National Cash Register Company, Hawthorne
Located at the National Cash Register's Electronics
Division, Hawthorne, California, the system is used
for electronic data processing service to business,
industry and government. Complete facilities for
handling customer created input - punched paper
tape and cards. It is used for program check out
for 304 customers. The system is used internally
by the National Cash Register Company for programming
research, automated logical design, and data processing. Back up support for other 304 systems is
additional use.

Photo by National Cash Register Company

Bureau of Yards and Docks
Located at the Bureau of Yards and Docks, Washington D. C., the system is used for the management of the Military Construction Program, the inventory of Automotive Equipment, the inventory of Construction Equipment, and for engineering applications.

#### PROGRAMMING AND NUMERICAL SYSTEM

Internal number system Binary Coded Decimal Alphanum char/word 10 Alphanum char/word 5 - 60 1/2 - 6 83 one address 37 three address w/variations Words/instruction Instructions decoded Floating point Arithmetic system MICROFLOW provides exponents in range of -50 to + 49 and automatic normalizing of result Fixed point Automatic alignment takes place One address (MICROFLOW) Instruction type (Scientific-type) Three address (Multi-address) (Business-type) Number range  $-(1-10^{-10})$  to  $+(1-10^{-10})$ 

#### Instruction word format

The command structure of the 304 system is unique in a number of aspects. The instructions are basically 3 address plus a number of other functions or capabilities that are built into the execution of the instruction. The basic instruction word format for arithmetical operations such as add, subtract, etc., is illustrated:

	9	8	7	6	5	4	_ 3	2	1	, 0
ı	Οp		A			В			C	. 1
				I ,						
i		М	<u> </u>	R	Al	Ar	BI	Br	CI	Cr

Op = Operation Code

A = Address of Operand

B = Address of Operand

C = Putaway or Jump Address

V = Variation & Self-Linking designator

M = Auto-monitor level

S = Combinations of A, B, or C to be relative to Index Register

Photo by American United Life Insurance Company

R = Index Register

Al, Ar = left and right field of A Address

B1, Br = left and right field of B Address

Cl, Cr = left and right field of C Address

The programmer in translating procedures for the 304 System will use the NEAT (National's Electronic Autocoding Technique) format. The programmer might write the following to add the Old Quantity on Hand to the Number Received to arrive at the New Quantity on Hand:

ADD (V) (R) OQOH QREC NQOH

The NEAT assembly process will translate the NEAT format into the necessary absolute machine language.

It could be considered that all instructions in the basic 304 Command List are automatic built-in subroutines. There are a number of operation codes that were designed specifically for business data processing such as Edit, Merge, Move, Sift (or table look-up), and Summarize that are powerful instructions and in some cases are self-incrementing.

As an example, the Merge instruction will serve to illustrate the nature of these business-type opera-

#### Official Marine Corps Photo

tions. Specified in NEAT format would be the following mnemonic designators and parameters: Designate first word, first item, each string Number of items in each string Relative position of Major Key (if any), within

Relative position of Major Key (if any), with item Relative position of Minor Key within item

Relative position of Minor Key within item Number of keys (1 or 2) for the Merge Length of each item Specify three exits (Cutoff Merge only)

NEAT (National's Electronic Autocoding Technique) was in operation before the first deliveries of the National 304 System. This system enables systematic organization in the approach to a problem, an assembly-compiler and a complete library of generators, service programs, and subroutines. COBOL or COBOL-like language will become part of the system in a reasonable time.

Each instruction may be relative to one of 10 Relative Index Registers. The particular Index Register and the portions of the instruction which are to be relative are specified within the instruction.

Camp Lejeune, N. C.

#### **ARITHMETIC UNIT**

Operation	on Incl Stor Access	Exclud Stor Access
_	Microsec	Microsec
MICRO-FI	LOW SINGLE ADDRESS MODE	
Add	120	60
Mult	1,320	1,260
Div	3,480	3,420
BUSINESS Add Mult Div	5-TYPE THREE ADDRESS 60 (10+R+L) 60 (17+R+L+X) 60 (14+R+L+2Q+Y)	60 (9+R+L) 60 (16+R+L+X) 60 (13+R+L+2Q+Y)
R = 1 R = 0 L = 2 L = 0 X =	If not Sum plus significant digi	ts of multiplier
Q = Y =	Number of digits in quoti- Sum of digits of the quot	ient

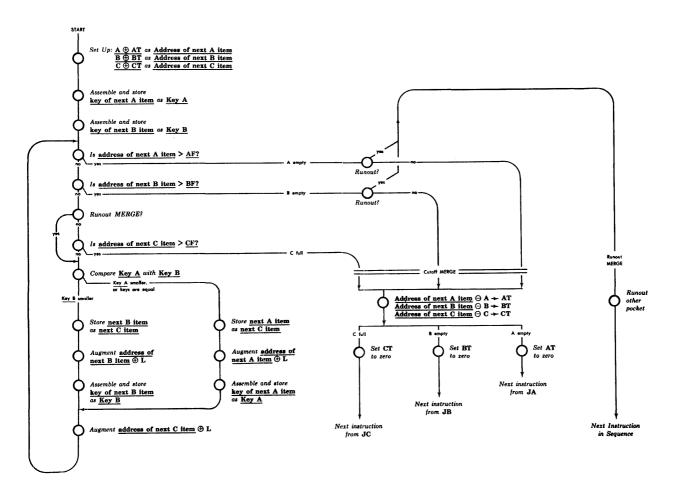
Construction (Arithmetic unit only)

Due to interlaced circuitry, number of elements are included in the total for the 304 Processor.

Arithmetic mode Serial by word, parallel by character The adder is implemented with two diode-matrix

half adders and one diode full adder.
Timing Synchronous in Central Processor

Synchronous in Central Processor Asynchronous to or from peripheral units



Merge Flow Diagram

Operation

Sequential internally Concurrent with copy or search of magnetic tape or printing

#### **STORAGE**

#### Manufacturer

	No. of	No. c	of	Access
Media	Words			Microsec
Magnetic Core	2400-4800	24,000-4	.8 <b>,000</b>	6/alphanum
Magnetic Tape,	850,000	8,500	,000	2 1/2 min
per reel	-	•		•
No. of units th	at can be	connected	64	Units
No. of chars/li	near inch	of tape	200	Chars/inch
Channels or tra	cks on the	tape	8	Tracks/tape
(including pari	ty and mar	kers)		•
Blank tape sepa	rating eac	h record		None
Tape speed	_		150	Inches/sec
Transfer rate (	alpha-nume	ric	30,000	Chars/sec
characters)			-	-
Start time			3.5	Millisec
Stop time			3.5	Millisec
Average time for	r experien	ced		
operator to chan	ge reel		30	Seconds

Physical properties of tape
Width 1/2 Inch
Length of reel 3,600 Feet
Composition 1 mil mylar, laminated

The unique 304 magnetic tape system is composed of sub-systems of National 330 Controllers and National 332 Magnetic Tape Handlers. The Central 304 Processor may handle up to eight 330 Controllers, and each Controller may handle up to eight Handlers to provide an upper limit of 64 Handlers available.

A record on magnetic tape may contain 10 or more words. Multiple variable-length records may be written onto magnetic tape with a single acceleration. The contents of one magnetic tape may be read and simultaneously written onto another tape unit until a desired record is reached. The copying may be shared with computing, printing, input or output. Several copy operations may be in progress simultaneously.

All conditions which may arise during magnetic file operations are automatically detected and identified by the Processor without programmed testing. Then a standard routine, called STEP (Standard Tape Executive Program), will perform all tape management

#### Production

operations including:

Repeat in case of error, write "Skip" record after proper number of tries

Repeat, or jump, on busy Handler or Controller Indicate attempt to use locked-out tape Alternate Tape Units at end of tape Tape identification, and label-checking Memory dump, and rescue points Log of tape operations

Executive control over sequencing from one program to the next, or overlays

The time-sharing ability of magnetic tape copy allows for convenient file-splitting, or multi-programming of different jobs where reference to the file is for less than every record. Thus, completely independent programs can be written and checked out, then tied together with the timing interlace being performed by the equipment.

In order to improve the efficiency of Magnetic File Operations, any quantity of numeric information may be packed from 6-bit code to 4-bit code with a sin-

#### Photo by National Cash Register Company

gle instruction. Thus, utilization of tape storage, and transport speed, will both be increased by 50% for numeric information.

When the packed information is brought into memory, a single instruction will reverse the transformation. Only that information requiring arithmetic operation need be unpacked; sorting, table lookup, and all other logical operations, can be performed on packed information.

U.S. Marine Corps, Camp Lejeune Access
Medium No. of Words No. of Digits Microsec
Core Memory 2400 10 60/word time

U.S. Marine Corps, Camp Pendleton

Magnetic Core 2400

U.S Marine Corps, Headquarters

Magnetic Core 2400 Magnetic Tape 864,000

American United Life Insurance Company

Magnetic Core 4800 Magnetic Tape 850,000

697 NATIONAL 304

#### Official Marine Corps Photo

S. C. Johnson & Son, Inc. Access No. of Words No. of Digits Microsec Medium Magnetic Core 2400 10 National Cash Register Company, Hawthorne netic Core 4800 6 Magnetic Core 6/alphanum char Magnetic Tape 1.08 million word/reel 3 min (10 char/word) char/word)
Bureau of Yards and Docks
11800 48,000 Magnetic Core 60 Magnetic Tape may be considered as storage medium.

#### INPUT

Manufacturer
Media Speed
Punched Cards 2000 cards/min Photo-electric
Punched Paper Tape 1800 char/sec Photo-electric
Magnetic Tape 30 Kc alpha-numeric
Punched Cards and Punched Paper Tape may be handled
on-line with the 304 Processor or converted to magnetic tape with the 320 Multi-Purpose Converter.

Camp Pendleton, California

U.S. Marine Corps, Camp Lejeune Media Speed Magnetic Tape Punched Cards Flexowriter typing Flexowriter Paper Tape U.S. Marine Corps, Camp Pendleton Magnetic Tape Punched Cards Console Typewriter
U.S. Marine Corps, Headquarters Magnetic Tape Punched Cards Punched Paper Tape Flexowriter American United Life Insurance Company Paper Tape Reader 2,000 char/sec Optical Reader - choice of 3 codes Punched Card Reader 2,500 cards/min 90 column cards - read optically Magnetic Tape 30,000 char/sec 200 char/in - 3,600' reels Console Typewriter 10 char/sec Modified Flexowriter

#### Official Marine Corps Photo

S. C. Johnson & Son, Inc. Speed Media 30,000 char/sec Magnetic Tape Punched Paper Tape 1,800 char/sec Punched Cards 2,000 cards/min Console Flexowriter 10 char/sec National Cash Register Company, Hawthorne 380 Card Reader 2,000 cards/min NCR 380 Card Reader 2,000 cards/min NCR 360 Paper Tape Reader 1,800 char/sec Conscle 10 char/sec Magnetic Tape 30,000 char/sec The Paper Tape Reader can handle 6 codes. core matrices are on the unit. Bureau of Yards and Docks Magnetic Tape  $3600 \text{ ft} = 8.5 \times 10^6 \text{ char}$ 30,000 char/sec 1,800 char/sec Paper Tape 3 code option, 10 char/in Punched Cards 2 2,000 cards/min 1 card = 80 chars Console Typewriter 10 char/sec max. Manual type-in or paper tape read

## **OUTPUT**

Manufacturer Media Speed Punched Paper Tape 60 char/sec 850-1200 printing 850 alpha numeric 5040 spacing 1200 numeric 100 cards/min (lines/min) Line Printer Punched Cards 100 cards/min Magnetic Tape 30,000 char/sec The Paper Tape Punch or the High Speed Line Printer may be controlled on-line by the 304 Processor or offline by the 320 Multi-Purpose Converter. The Line Printer may also be controlled off-line by the 322 Printer Converter. Punched cards can be produced off-line using an IBM 523 with source information on magnetic tape and under control of the 320 Converter. U.S. Marine Corps, Camp Lejeune Magnetic Tape Flexowriter typing & punching Punch Cards Printer IBM Type 523 as IBM card output media