

CONTROL DATA
CORPORATION

CONTROL DATA[®]
1700 COMPUTER SYSTEMS

1700 MSOS 4
CUSTOMIZATION MANUAL

①

PRELIMINARY

1700 MSOS 4

CUSTOMIZATION MANUAL

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Preface

This manual is intended to aid the user in understanding the parameterization of 1700 MS0S 4 so that he can modify his system according to his own needs if necessary. A thorough knowledge of MS0S is not assumed on the part of the reader, but a general acquaintance with the aspects of the system will be helpful.



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SYSDAT DESCRIPTION AND CUSTOMIZATION OPTIONS

The MSOS Data Base is a set of code from which a SYSDAT for any given installation may be derived. The selection of which card images in the Data Base are to be a part of a given SYSDAT is determined according to the information supplied by the user when he completes the 1700 MSOS 4 Ordering Form.

Each section of the Data Base, and consequently each section of a given SYSDAT, is identified by a unique combination of three letters. This identifier appears in columns 74-76 of each line of code. Each article of this chapter deals with a particular section of SYSDAT. The heading of an article indicates the contents of the SYSDAT section to be discussed and the corresponding three letter identifier. Either the original Data Base listing of the section or a listing of the section from a sample SYSDAT is found with each description. Sections AAA through AAU, sections ACR, ACT, ACV and ACW, and sections ADB through ADD are contained in the SYSDAT of every system. Other sections are contained in a given SYSDAT only if needed by the particular system.

A general description of the mandatory entries in a physical device table are discussed in Article 1.19. It is assumed that the reader is familiar with the information in this article when he reads about

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any specific physical device table. The individual physical device tables are described in Articles 1.20 through 1.57.

1.1 SYSDAT Table of Contents, Section AAA

Section AAA {Figure 1-1} is a list of the contents of SYSDAT. This section would normally not be modified by the user.

NAM SYSDAT COPYRIGHT CONTROL DATA CORPORATION 1973
 SYSTEM DATA PROGRAM - MSCS 4.1
 1700 MASS STORAGE OPERATING SYSTEM VERSION 4.1
 SMALL COMPUTER DEVELOPMENT DIVISION, LA JOLLA, CALIFORNIA
 COPYRIGHT CONTROL DATA CORPORATION 1973

AAA00100
 AAA00200
 AAA00300
 AAA00400
 AAA00500
 AAA00600
 AAA00700
 AAA00800
 AAA00900
 AAA01000
 AAA01100
 AAA01200
 AAA01300
 AAA01400
 AAA01500
 AAA01600
 AAA01700
 AAA01800
 AAA01900
 AAA02000
 AAA02100
 AAA02200
 AAA02300
 AAA02400
 AAA02500
 AAA02600
 AAA02700
 AAA02800
 AAA02900
 AAA03000
 AAA03100
 AAA03200
 AAA03300
 AAA03400
 AAA03500
 AAA03600
 AAA03700
 AAA03800
 AAA03900
 AAA04000
 AAA04100
 AAA04200
 AAA04300
 AAA04400
 AAA04500
 AAA04600
 AAA04700
 AAA04800
 AAA04900
 AAA05000
 AAA05100

PROGRAM BASE - MSOS 4.0

S Y S T E M D A T A P R O G R A M

TABLE OF CONTENTS

- 1. COMMUNICATION EXTERNALS
- 2. COMMUNICATION REGION (INCLUDING APPLICATIONS AREA)
- 3. INTERRUPT REGION
- 4. INTERRUPT MASK TABLE (MASK1)
- 5. EXTENDED COMMUNICATIONS REGION
- 6. STORAGE STACKS (INTSTK, VOLFLK, SCHSTK)
- 7. LOGICAL UNIT TABLES (LOG1A, LOG1, LOG2)
- 8. DIAGNOSTIC TABLES (DGNTAB, ALTERR)
- 9. STANDARD LOGICAL UNIT DEFINITIONS AND LINE 1 TABLE
- 10. PHYSICAL DEVICE TABLES WITH INTERRUPT RESPONSE ROUTINES
- 11. CORE ALLOCATION INFORMATION (CALTHD, LVLSTR, NN#S)
- 12. CORE PARTITION INFORMATION (PARTBL, THDS, USE)
- 13. SYSTEM COMMON DECLARATION
- 14. MISCELLANEOUS PROGRAMS
- 15. MISCELLANEOUS INFORMATION
- 16. SYSTEM FILE INFORMATION
- 17. PRESET REGION
- 18. START OF SYSTEM DIRECTORY

FJI

Figure 1-1. SYSDAT Table of Contents, Section AAA

1.2 Core Locations Zero and One and the Mask Tables, Section AAB

The first part of Section AAB is locations zero and one. These locations are filled by the system restart portion of the program, SPACE, at the time of an autoloading. They will contain $18FF_{16}$ and zero, respectively. These two locations must not be altered.

The masks in Section AAB, Figure 1-2, are used extensively by the operating system. They must not be altered. The mask table consists of five parts:

- {1} logical product masks {locations $2-11_{16}$ }
- {2} complements of the logical product masks {locations $12_{16}-21_{16}$ }
- {3} masks each with a single bit set {locations $22_{16}-32_{16}$ }
- {4} masks with all bits set except one {locations $33_{16}-42_{16}$ }
- {5} integer values that are commonly used, but which are not contained in the first four parts of the mask table {locations $43_{16}-46_{16}$ }.

* *		C O M M U N I C A T I O N R E G I O N	AAR00100
			AAR00200
	CRG	1 LOCATION 0 SET TO \$18FF BY SYSTEM INITIALIZER	AAR00300
	NUM	0	AAR00400
* LPMSK	NUM	0 LOGICAL PRODUCT MASK TABLE OF ONES	AAR00500
ONE	NUM	1 ONE	AAR00600
THREE	NUM	3 THREE	AAR00700
SEVEN	NUM	7 SEVEN	AAR00800
	NUM	\$F	AAR00900
	NUM	\$1F	AAR01000
	NUM	\$3F	AAR01100
	NUM	\$7F	AAR01200
	NUM	\$FF	AAR01300
	NUM	\$1FF	AAR01400
	NUM	\$3FF	AAR01500
	NUM	\$7FF	AAR01600
	NUM	\$FFF	AAR01700
	NUM	\$1FFF	AAR01800
	NUM	\$3FFF	AAR01900
	NUM	\$7FFF	AAR02000
	NUM	\$7FFF	AAR02100
* NZERO	NUM	\$FFFF LOGICAL PRODUCT MASK TABLE OF ZEROS (NFG ZERO)	AAR02200
	NUM	\$FFFE	AAR02300
	NUM	\$FFFC	AAR02400
	NUM	\$FFF8	AAR02500
	NUM	\$FFF0	AAR02600
	NUM	\$FFE0	AAR02700
	NUM	\$FFC0	AAR02800
	NUM	\$FF80	AAR02900
	NUM	\$FF00	AAR03000
	NUM	\$FE00	AAR03100
	NUM	\$FC00	AAR03200
	NUM	\$F800	AAR03300
	NUM	\$F000	AAR03400
	NUM	\$E000	AAR03500
	NUM	\$C000	AAR03600
	NUM	\$8000	AAR03700
	EJT		AAR03800
			AAR03900
* *		C O M M U N I C A T I O N R E G I O N	AAR04000
			AAR04100
ZERO	NUM	0 ZERO	AAR04200
*			AAR04300

Figure 1-2. Core Locations Zero and One and the Mask Tables, Section AAB.

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ONEBIT	NUM	1		ONE BIT TABLE		AAR04400
TWO	NUM	2		TWO		AAR04500
FOUR	NUM	4		FOUR		AAR04600
EIGHT	NUM	8		EIGHT		AAR04700
	NUM	\$10				AAR04800
	NUM	\$20				AAR04900
	NUM	\$40				AAR05000
	NUM	\$80				AAR05100
	NUM	\$100				AAR05200
	NUM	\$200				AAR05300
	NUM	\$400				AAR05400
	NUM	\$800				AAR05500
	NUM	\$1000				AAR05600
	NUM	\$2000				AAR05700
	NUM	\$4000				AAR05800
	NUM	\$8000				AAR05900
*						AAR06000
ZRORIT	NUM	\$FFFE		ZERO BIT TABLE		AAR06100
	NUM	\$FFFD				AAR06200
	NUM	\$FFFB				AAR06300
	NUM	\$FFF7				AAR06400
	NUM	\$FFEF				AAR06500
	NUM	\$FFDF				AAR06600
	NUM	\$FFBF				AAR06700
	NUM	\$FF7F				AAR06800
	NUM	\$FFFF				AAR06900
	NUM	\$FDFE				AAR07000
	NUM	\$FDFB				AAR07100
	NUM	\$F7FF				AAR07200
	NUM	\$EFFF				AAR07300
	NUM	\$CFFF				AAR07400
	NUM	\$BFFF				AAR07500
	NUM	\$7FFF				AAR07600
*						AAR07700
FIVE	NUM	5		FIVE		AAR07800
SIX	NUM	6		SIX		AAR07900
NINE	NUM	9		NINE		AAR08000
TEN	NUM	10		TEN		AAR08100
	FJT					AAR08200

Figure 1-2. Core Locations Zero and One and the Mask Tables, Section AAB {Continued}

1.3 Interactive GRAPHICS Region, Section AAC

This section appears in SYSDAT only if GRAPHICS and IMPORT are a part of the system {Figure 1-3}. If section AAC is present in an unmodified state, it provides for either one or two 1744/1706/274 Digigraphic Consoles.

The number of non-zero entries in the beginning of this section is the number of digigraphic consoles used by the system. If only one console was specified in the original SYSDAT section and the user wishes to add a second console, the second entry in the console list should be set to the address constant, LU2741-LOG1A. A maximum of six consoles may be specified, however if more than two consoles are to be used, the physical device table for each console beyond console two must be defined by the user. For example, if the user wished to add consoles three and four, he might define physical device tables labeled LU2742 and LU2743. The two cards inserted in Section AAC would then be:

```
ADC LU2742-LOG1A  CONSOLE NUMBER 1
ADC LU2743-LOG1A  CONSOLE NUMBER 2
```

MASK44 is the mask used to clear all GRAPHICS interrupts. Each bit of MASK44 should be one except those bits corresponding to GRAPHICS interrupt lines. Each 274 GRAPHICS terminal that is used has its own interrupt line. The following table gives the standard interrupt lines and the value of MASK44 for systems with one or two GRAPHICS consoles.

<u>Console Numbers in System</u>	<u>Interrupt Lines</u>	<u>MASK 44</u>
1	12	FFFF ₁₆
1,2	12,13	CFFF ₁₆

If more than two GRAPHICS consoles are in a system, interrupt lines for additional consoles are defined by the user. MASK44 must be defined by the user according to the above definition.

```

*
* COMMUNICATION REGION
*
* INTERACTIVE GRAPHICS REGION
*
SPC 2
EXT FUNC44 1744 FUNCTION ROUTINE
EXT IING INHIBIT GRAPHICS INTERRUPT ROUTINE
EXT FING ENABLE GRAPHICS INTERRUPT ROUTINE
SPC 1
*
* TABLE OF 274 CRI LOGICAL UNITS
* NOTE - THERE MUST BE SIX ENTRIES IN THIS TABLE
*
SPC 1
ADC LL2740-LOG1A CONSOLE NUMBER 1
ADC LL2741-LOG1A CONSOLE NUMBER 2
ADC 0 CONSOLE NUMBER 2
ADC 0 CONSOLE NUMBER 3
ADC 0 CONSOLE NUMBER 4
ADC 0 CONSOLE NUMBER 5
ADC 0 CONSOLE NUMBER 6
*
ADC FUNC44 1744 FUNCTION ROUTINE
*
* MASK USED TO CLEAR GRAPHICS INTERRUPT. ALL BITS SHOULD BE
* SET EXCEPT THOSE CORRESPONDING TO THE 1744 INTERRUPT LINES.
*
* INTERRUPT LINE
*
15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
*****
* MASK44 1 1 0 0 1 1 1 1 1 1 1 1 1 1 1
* MASK44 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1
*
*****
*
* MASK44 NUM $CFFF GRAPHICS INTERRUPT MASK
* MASK44 NUM $EFFF GRAPHICS INTERRUPT MASK
*
* THE FOLLOWING ENTRIES ARE INITIALIZED BY THE INTERACTIVE
* GRAPHICS SYSTEM DURING GRAPHICS OPERATION.
*
ADC 0 MCRTAH - TABLE OF A/N MACROS (RELATIVE)
ADC 0 AATADR - CURRENT AAT ADDRESS
ADC 0 AATLNG - AAT LENGTH
ADC 0 ANADR - ADDRESS OF A/N MACROS (D1744)
ADC 0 ANPIK - ADDRESS OF A/N PICK DISPLAY (D1744)
ADC 0 ANSFLG - GAINS COMMUNICATION FLAG
ADC 0 CFX - T. C. X-COORDINATE
ADC 0 CFY - T. C. Y-COORDINATE
ADC 0 CTRLSZ - LAST WORD ADDRESS OF D1744
ADC 0 DIPSTA - CURRENT STATUS OF THE 1744
ADC 0 STRLOV - AVAILABLE MACRO S-JUMP ENTRIES
ADC 0 FAVAL - CURRENT DISPLAY BYTES END ADDRESS
ADC 0 INX - X-COORDINATE OF TRACKED ITEM
ADC 0 INY - Y-COORDINATE OF TRACKED ITEM
EJT

```

Figure 1-3. Interactive GRAPHICS, Section AAC.

I N T E R A C T I V E G R A P H I C S R E G I O N			
SPC	2		AAC05300
ADC	0	STBLSZ - SIZE OF S-JUMP ENTRY TABLE	AAC05400
ADC	0	MAXBUF - LAST WCRD ADDRESS OF 1744 BUFFER	AAC05500
ADC	0	MCRBTS - TOTAL NUMBER OF MACRO BYTES	AAC05600
ADC	0	MVMCR - CLEAR/ATTACH MACRO FROM T.C.	AAC05700
ADC	0	MVTM - CLEAR/ATTACH DISPLAY ITEM FROM T.C.	AAC05800
ADC	0	PLPS - P-REGISTER AT LAST L.P. PICK	AAC05900
ADC	0	PRX - X-COORDINATE AT LAST L.P. PICK	AAC06000
ADC	0	PRY - Y-COORDINATE AT LAST L.P. PICK	AAC06100
ADC	0	TMMVAD - ADDRESS OF MACRO ATTACH AREA (D1744)	AAC06200
ADC	0	TCADR - ADDRESS OF T.C. DISPLAY AREA (D1744)	AAC06300
ADC	0	SLPS - S-REGISTER AT LAST L.P. PICK	AAC06400
ADC	0	TMPADR - FAVAL (SAVED BY GIDIS)	AAC06500
ADC	0	TRFLG - TRACKING/NOT TRACKING FLAG	AAC06600
ADC	0	XLPS - X-REGISTER AT LAST L.P. PICK	AAC06700
ADC	0	YLPS - Y-REGISTER AT LAST L.P. PICK	AAC06800
ADC	0	AD1744 - DISPLAY ITEM ADDRESS (USED BY TRACD)	AAC06900
ADC	0	MM1744 - MACRO ITEM ADDRESS (USED BY TRACD)	AAC07000
ADC	0	PROGRS - CHARACTER COUNT (USED BY GIANC)	AAC07100
ADC	0	LIMIT - CHARACTER LIMIT (USED BY GIANC)	AAC07200
ADC	0	GIANIH - I H VALUE (USED BY GIANC)	AAC07300
ADC	0	GIANIV - I V VALUE (USED BY GIANC)	AAC07400
ADC	0	NCFC - CHARACTER COUNT (USED BY GIANS)	AAC07500
ADC	0	CCODE - CURRENT CALL CODE	AAC07600
ADC	0	GRAPHE - RETURN ADDRESS FOR ALL OVERLAYS	AAC07700
ADC	0	CALFS - CLEAR ALPHANUMERIC CONTRCL BLOCK	AAC07800
ADC	0	GHLP1 - LIGHTPEN INTERRUPT PROCESSOR ADDRESS	AAC07900
ADC	0	GHKBI - KEYBOARD INTERRUPT PROCESSOR ADDRESS	AAC08000
ADC	0	GH1KI - LIGHTPEN KEY INTERRUPT PROCESSOR	AAC08100
ADC	0	GH1DR - SINGLE PICK ID BLOCK PROCESSOR	AAC08200
ADC	0	GH1LF - ALPHANUMERIC PICK PROCESSOR ADDRESS	AAC08300
ADC	0	GH1LKA - FREE STORAGE BLOCK ALLOCATOR ADDRESS	AAC08400
ADC	0	GH1LKR - FREE STORAGE BLOCK RELEASOR ADDRESS	AAC08500
ADC	0	GH1DRS - STRING PICK PROCESSOR ADDRESS	AAC08600
ADC	0	GH1DRE - DELETE ID BLOCK PROCESSOR ADDRESS	AAC08700
ADC	0	IGM - IGNORE MASK TABLE ADDRESS	AAC08800
ADC	0	SPM - SINGLE PICK MASK TABLE ADDRESS	AAC08900
ADC	0	STPM - STRING PICK MASK TABLE ADDRESS	AAC09000
ADC	0	RM - BUTTON PICK MASK TABLE ADDRESS	AAC09100
ADC	0	MM - MARKER MASK TABLE ADDRESS	AAC09200
ADC	0	YGIL - SWITCH FOR GILPKY	AAC09300
ADC	0	ZGIL - ID BLOCK ADDRESS FOR GILPKY	AAC09400
ADC	0	YGIK - SWITCH FOR GIKPYD	AAC09500
ADC	0	ZGIK - ID BLOCK ADDRESS FOR GIKPYD	AAC09600
ADC	0	YGIP - SWITCH FOR GIPRUT	AAC09700
ADC	0	ZGIP - ID BLOCK ADDRESS FOR GIPRUT	AAC09800
ADC	0	YGIE - SWITCH FOR GIECM	AAC09900
ADC	0	ZGIE - ID BLOCK ADDRESS FOR GIECM	AAC10000
EJ1			AAC10100
			AAC10200

Figure 1-3. Interactive GRAPHICS, Section AAC {Continued}

I N T E R A C T I V E G R A P H I C S R E G I O N			AAC10300
SPC	2		AAC10400
ADC	0		AAC10500
ADC	0	CICL - FLAG WORD FOR GICLR	AAC10600
ADC	0	RELSTG - RELEASE LAST BLOCK RETURN ADDRESS	AAC10700
ADC	0	GFALFF - PICKED CHARACTER FETCH ROUTINE	AAC10800
ADC	0	GFSTK - FLAG FOR BUTTON ON FETCH W	AAC10900
ADC	0	GFIDRF - SINGLE PICK ID BLOCK FETCH ROUTINE	AAC11000
ADC	0	WAIT - START OF WAIT CONTROL BLOCKS	AAC11100
ADC	0	FETCH - START OF FETCH CONTROL BLOCKS	AAC11200
ADC	0	XCOLRL - X-COORDINATE OF LAST BUTTON	AAC11300
ADC	0	YCOLRL - Y-COORDINATE OF LAST BUTTON	AAC11400
ADC	0	TRACD - T. C. PROCESSOR ADDRESS	AAC11500
ADC	0	REQSTK - REQUEST STACK	AAC11600
ADC	0	- RESERVED	AAC11700
ADC	0	- RESERVED	AAC11800
ADC	0	- LPRFLAG	AAC11900
ADC	0	- X0-COORDINATE OF BEAM	AAC12000
ADC	0	- Y0-COORDINATE OF BEAM	AAC12100
ADC	0	- XB0-COORDINATE OF DECODERS	AAC12200
ADC	0	- YB0-COORDINATE OF DECODERS	AAC12300
ADC	IING	IING - INIFIT GRAPHIC INTERRUPT ROUTINE	AAC12400
ADC	FING	EING - ENABLE GRAPHIC INTERRUPT ROUTINE	AAC12500
ADC	0	GICON - CONSOLE (NCON SAVED BY IGSPO)	AAC12600
ADC	0	GLELE -	

Figure 1-3. Interactive GRAPHICS, Section AAC {Continued}

1.4 Area for Applications Use, Section AAD

Section AAD, Figure 1-4, includes locations 70_{1b} through B2_{1b}.
This area is available to the user to be used as desired.

C O M M U N I C A T I O N R E G I O N

THIS AREA IS AVATIABLE FOR APPLICATIONS USE

THIS AREA IS AVATIABLE FOR APPLICATIONS USE

NUM	0	\$47	AAD00100
NUM	0	\$48	AAD00200
NUM	0	\$49	AAD00300
NUM	0	\$4A	AAD00400
NUM	0	\$4B	AAD00500
NUM	0	\$4C	AAD00600
NUM	0	\$4D	AAC00700
NUM	0	\$4E	AAD00800
NUM	0	\$4F	AAD00900
NUM	0	\$50	AAD01000
NUM	0	\$51	AAD01100
NUM	0	\$52	AAD01200
NUM	0	\$53	AAD01300
NUM	0	\$54	AAD01400
NUM	0	\$55	AAD01500
NUM	0	\$56	AAD01600
NUM	0	\$57	AAD01700
NUM	0	\$58	AAD01800
NUM	0	\$59	AAD01900
NUM	0	\$5A	AAD02000
NUM	0	\$5B	AAD02100
NUM	0	\$5C	AAD02200
NUM	0	\$5D	AAD02300
NUM	0	\$5E	AAD02400
NUM	0	\$5F	AAD02500
NUM	0	\$60	AAD02600
NUM	0	\$61	AAD02700
NUM	0	\$63	AAD02800
NUM	0	\$64	AAD02900
NUM	0	\$65	AAD03000
NUM	0	\$66	AAD03100
NUM	0	\$67	AAD03200
NUM	0	\$68	AAD03300
NUM	0	\$69	AAD03400
NUM	0	\$6A	AAD03500
NUM	0	\$6B	AAD03600
NUM	0	\$6C	AAD03700
NUM	0	\$6D	AAD03800
NUM	0	\$6E	AAD03900
NUM	0	\$6F	AAD04000
EJT			AAD04100
SPC	4		AAD04200
			AAD04300
			AAD04400
			AAD04500
			AAD04600
			AAD04700
			AAD04800

Figure 1-4. Area for Applications Use, Section AAD

C O M M U N I C A T I O N R E G I O N

NUM	0	\$70	AAD04900
NUM	0	\$71	AAD05000
NUM	0	\$72	AAD05100
NUM	0	\$73	AAD05200
NUM	0	\$74	AAD05300
NUM	0	\$75	AAD05400
NUM	0	\$76	AAD05500
NUM	0	\$77	AAD05600
NUM	0	\$78	AAD05700
NUM	0	\$79	AAD05800
NUM	0	\$7A	AAD05900
NUM	0	\$7B	AAD06000
NUM	0	\$7C	AAD06100
NUM	0	\$7D	AAD06200
NUM	0	\$7E	AAD06300
NUM	0	\$7F	AAD06400
NUM	0	\$80	AAD06500
NUM	0	\$82	AAD06600
NUM	0	\$83	AAD06700
NUM	0	\$84	AAD06800
NUM	0	\$85	AAD06900
NUM	0	\$86	AAD07000
NUM	0	\$87	AAD07100
NUM	0	\$88	AAD07200
NUM	0	\$89	AAD07300
NUM	0	\$8B	AAD07400
NUM	0	\$8C	AAD07500
NUM	0	\$8D	AAD07600
NUM	0	\$8E	AAD07700
NUM	0	\$8F	AAD07800
EJT			AAD07900
SPC	2		AAD08000

C O M M U N I C A T I O N R E G I O N

THIS AREA IS AVAILARLE FOR APPLICATIONS USE

NUM	0	\$90	AAD08100
NUM	0	\$91	AAD08200
NUM	0	\$92	AAD08300
NUM	0	\$93	AAD08400
NUM	0	\$94	AAD08500
NUM	0	\$95	AAD08600
NUM	0	\$96	AAD08700
NUM	0	\$97	AAD08800
NUM	0	\$98	AAD08900
NUM	0		AAD09000
NUM	0		AAD09100
NUM	0		AAD09200
NUM	0		AAD09300
NUM	0		AAD09400
NUM	0		AAD09500
NUM	0		AAD09600

Figure 1-4. Area for Applications Use, Section AAD {Continued}

NUM	0	\$99	AAD09700
NUM	0	\$9A	AAD09800
NUM	0	\$9C	AAD09900
NUM	0	\$9D	AAD10000
NUM	0	\$9E	AAD10100
NUM	0	\$9F	AAD10200
NUM	0	\$A0	AAD10300
NUM	0	\$A2	AAD10400
NUM	0	\$A3	AAD10500
NUM	0	\$A4	AAD10600
NUM	0	\$A5	AAD10700
NUM	0	\$A6	AAD10800
NUM	0	\$A7	AAD10900
NUM	0	\$A8	AAD11000
NUM	0	\$A9	AAD11100
NUM	0	\$AA	AAD11200
NUM	0	\$AB	AAD11300
NUM	0	\$AC	AAD11400
NUM	0	\$AD	AAD11500
NUM	0	\$AE	AAD11600
NUM	0	\$AF	AAD11700
NUM	0	\$F0	AAD11800
NUM	0	\$F1	AAD11900
NUM	0	\$F2	AAD12000
EJT			AAD12100

Figure 1-4. Area for Applications Use, Section AAD (Continued)

1.5 Communication Region, Section AAE

The first part of Section AAE, Figure 1-5, lists the externals which are referenced in the communications region. Each external corresponds to a frequently used module. By placing the entry point address of each of these externals in the communications region one-word indirect addressing may be used to enter these modules. The rest of Section AAE is the communications region itself. It consists of locations $B3_{16}$ through FF_{16} . This area includes entry point addresses, temporary storage locations, logical unit numbers, the real time clock counter, and index register I. This section must not be modified by the user.

		COMMUNICATIONS EXTERNALS	AAE00100
			AAE00200
			AAE00300
EXT	FNR	FIND NEXT REQUEST	AAE00400
EXT	COMPRG	COMPLETE REQUEST	AAE00500
EXT	REQXT	REQUEST EXIT	AAE00600
EXT	VCLR	VCLATILE RELEASE	AAE00700
EXT	VOLA	VCLATILE ASSIGNMENT	AAE00800
EXT	LLABS	LOGICAL UNIT ABSOLUTIZING	AAE00900
EXT	SABS	STARTING ADDRESS ABSOLUTIZING	AAE01000
EXT	CABS	COMPLETION ADDRESS ABSOLUTIZING	AAE01100
EXT	NABS	NUMBER OF WRDS ABSOLUTIZING	AAE01200
EXT	DISPXX	DISPATCHER	AAE01300
EXT	MONI	MONITOR	AAE01400
EXT	MSIZV4	HIGHEST CORE LOCATION USED BY SYSTEM	AAE01500
EXT	IPROC	INTERNAL INTERRUPT PROCESSOR	AAE01600
EXT	ALLIN	COMMON INTERRUPT HANDLER	AAE01700
EJT			AAF01800
		COMMUNICATION REGION	AAE01900
			AAE02000
			AAE02100
	CRG \$B3		AAE02200
	ADC SCRCH	LOGICAL UNIT OF STANDARD SCRATCH DEVICE	AAE02300
	ADC SCHSTK	ADR OF TOP OF SCHEDULER STACK	AAE02400
AFNR	ADC FNR	ADR OF FIND NEXT REQUEST	AAE02500
ACOMPR	ADC COMPRG	ADR OF COMPLETE REQUEST	AAE02600
	ADC MASKT	ADR OF MASK TABLE	AAE02700
	ADC INTSTK	ADR OF TOP OF INTERRUPT STACK	AAE02800
	ADC REQXT	ADR OF EXIT FOR MONITOR REQUESTS	AAE02900
AVOLR	ADC VCLR	ADR OF RELEASE VOLATILE ROUTINE	AAE03000
AVOLA	ADC VOLA	ADR OF ASSIGN VOLATILE ROUTINE	AAE03100
	ADC LLABS	ADR OF ABSOLUTIZING ROUTINE FOR LOGICAL UNIT	AAE03200
	ADC SABS	ADR OF ABSOLUTIZING ROUTINE FOR STARTING ADR	AAE03300
	ADC CABS	ADR OF ABSOLUTIZING ROUTINE FOR COMPLETION ADRA	AAE03400
	ADC NABS	ADR OF ABSOLUTIZING ROUTINE FOR NUMBER OF WRDS	AAE03500
	NUM 0	MSB OF STARTING SCRATCH SECTOR (ALWAYS ZERO)	AAE03600
	NUM 0	LSB OF STARTING SCRATCH SECTOR (SET BY SI)	AAE03700
	ADC LBUNIT	LOGICAL UNIT OF STANDARD LIBRARY DEVICE	AAE03800
	NUM 0	MSB OF PGM LIR DIRECTORY SECTOR (ALWAYS ZERO)	AAE03900
	NUM 0	LSB OF PGM LIR DIRECTORY SECTOR (SET BY SI)	AAE04000
	BZS (\$E3-\$C5+1)	RESERVED FOR FTN (UNPROTECTED)	AAE04100
	NUM 0	RESERVED FOR FTN + LOAD/GO SECTOR (UNPROTECTED)	AAE04200
	NUM 0	RESERVED FOR FTN (UNPROTECTED)	AAE04300
			AAE04400

Figure 1-5. Communications Region, Section AAE

	BSS	(1)	LENGTH OF MASS RESIDENT SYSTEM DIR. (SET BY SI)	AAE04500
	BSS	(1)	LENGTH OF CORE RESIDENT SYSTEM DIR. (SET BY SI)	AAE04600
	NUM	0	REAL TIME CLOCK COUNTER	AAE04700
	ADC	EXTRV4	ADDR OF EXTENDED CORE TABLE	AAE04800
ADISP	ADC	DISPXX	ADR OF DISPATCHER	AAE04900
	ADC	SLDIRY	ADR OF SYSTEM DIRECTORY	AAE05000
	NUM	0	TEMPORARY TOP+1 OF UNPROTECTED (SET BY SI)	AAE05100
	NUM	0	TEMPORARY BOTTOM-1 OF UNPROTECTED (SET BY SI)	AAE05200
	NUM	0	USED BY JOB PROCESSOR FOR LOADER RETURNS	AAE05300
	NUM	-1	CURRENT PRIORITY LEVEL	AAE05400
	ADC	VOLBLK	STARTING LOCATION OF VOLATILE STORAGE	AAE05500
	ADC	LPRSET	LENGTH OF PRESETS TABLE	AAE05600
	ADC	APRSET	STARTING LOCATION OF PRESETS TABLE	AAE05700
	ADC	0	ADR OF BREAKPOINT PROGRAM IN CORE (UNPROTECTED)	AAE05800
AMONI	ADC	MONI	ADR OF MONITOR ENTRY FOR REQUESTS	AAE05900
	ADC	MSIZV4	HIGHEST CORE LOCATION USED BY SYSTEM	AAE06000
	NUM	0	TOP+1 OF UNPROTECTED (SET BY SI)	AAE06100
	NUM	0	BOTTOM-1 OF UNPROTECTED (SET BY SI)	AAE06200
	ADC	IFROC	ADR OF INTERNAL INTERRUPT PROCESSOR	AAE06300
	ADC	STDINP	LOGICAL UNIT OF STANDARD INPUT DEVICE (FTN 1)	AAE06400
	ADC	RINOUT	LOGICAL UNIT OF STANDARD BINARY DEVICE (FTN 2)	AAE06500
	ADC	LSTOUT	LOGICAL UNIT OF STANDARD PRINT DEVICE (FTN 3)	AAE06600
	ADC	OUTCOM	LOGICAL UNIT OF OUTPUT COMMENT DEVICE (FTN 4)	AAE06700
	ADC	INPCOM	LOGICAL UNIT OF INPUT COMMENT DEVICE (FTN 4)	AAE06800
	ADC	ALLIN	ADR OF COMMON INTERRUPT HANDLER	AAE06900
	BSS	(1)	I (MEMORY INDEX) REGISTER (UNPROTECTED)	AAE07000
	EJT			AAE07100

Figure 1-5. Communications Region, Section AAE (Continued)

1.6 Interrupt Trap Region, Section AAF

Section AAF, Figure 1-6, the interrupt trap region, must reside in locations 100_{16} - $13F_{16}$, since the interrupt locations used by the hardware are located in this region. This section consists of sixteen four-word entries, one entry for each interrupt line.

Each four-word entry is as follows:

Word 0 The value of the Program Address Register, P, is stored by the hardware in word 0 at the time an interrupt occurs. This enables return to the next unexecuted instruction after interrupt processing. The overflow status is saved in bit 15 of word 0 in a 32K system. {See the description of word 2 for overflow handling in a 65K system.}

Word 1 Word 1 is the first instruction to be executed after an interrupt occurs. This instruction is a $54F8_{16}$ to transfer control to the internal interrupt processor. It is a $54FE_{16}$ to transfer control to the common interrupt handler. The internal interrupt processor is used for interrupt line 0. The common interrupt handler is normally used for interrupt lines 1-15. It is possible that the user may wish to bypass the common interrupt handler. This would be the case for an interrupt which must be processed before the common interrupt handler could transfer control to the primary interrupt response routine. In this case, word 1 should contain an instruction to transfer control directly to the interrupt response routine.

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Word 2 Word 2 contains the priority level associated with this interrupt line. For a 65K system, the overflow status at the time of an interrupt is saved in bit 15 of word 2.

Word 3 Word 3 contains the address of the interrupt response routine. This word is used by the common interrupt handler to determine where control is to be passed after resetting the overflow indicator, saving the interrupted program in the interrupt stack and altering the system priority level.

* I N T E R R U P T R E G I O N			
			AAF00100
			AAF00200
			AAF00300
LINE00	NUM 0	INTERRUPT LINE ENTRY	AAF00400
	RTU- (\$F8)	GO TO INTERRUPT HANDLER ROUTINE	AAF00500
	NUM 15	PRIORITY LEVEL OF INTERRUPT	AAF00600
	ADC IPROC	INTERRUPT RESPONSE FOR THE PROTECT/PARITY ERR.	AAF00700
LINE01	NUM 0	INTERRUPT LINE ENTRY	AAF00800
	RTU- (\$FE)	GO TO INTERRUPT HANDLER ROUTINE	AAF00900
	NUM 14	PRIORITY LEVEL OF INTERRUPT	AAF01000
	ADC LIN1V4	INTERRUPT RESPONSE FOR THE LOW SPEED I/O	AAF01100
LINE02	NUM 0	INTERRUPT LINE ENTRY	AAF01200
	RTU- (\$FE)	GO TO INTERRUPT HANDLER ROUTINE	AAF01300
	NUM 09	PRIORITY LEVEL OF INTERRUPT	AAF01400
	ADC P1752	INTERRUPT RESPONSE FOR THE 1752 DRUM	AAF01500
LINE02	NUM 0	INTERRUPT LINE ENTRY	AAF01600
	RTU- (\$FE)	GO TO INTERRUPT HANDLER ROUTINE	AAF01700
	NUM 09	PRIORITY LEVEL OF INTERRUPT	AAF01800
	ADC P1751	INTERRUPT RESPONSE FOR THE 1751 DRUM	AAF01900
			AAF02000
LINE03	NUM 0	INTERRUPT LINE ENTRY	AAF02100
	RTU- (\$FE)	GO TO INTERRUPT HANDLER ROUTINE	AAF02200
	NUM 09	PRIORITY LEVEL OF INTERRUPT	AAF02300
	ADC P17332	INTERRUPT RESPONSE FOR THE 1733-2/856-2/4 DISK	AAF02400
			AAF02500
LINE03	NUM 0	INTERRUPT LINE ENTRY	AAF02600
	RTU- (\$FE)	GO TO INTERRUPT HANDLER ROUTINE	AAF02700
	NUM 09	PRIORITY LEVEL OF INTERRUPT	AAF02800
	ADC P1738	INTERRUPT RESPONSE FOR THE 1738/853-4 DISK	AAF02900
			AAF03000
LINE03	NUM 0	INTERRUPT LINE ENTRY	AAF03100
	RTU- (\$FE)	GO TO INTERRUPT HANDLER	AAF03200
	NUM 09	PRIORITY LEVEL OF INTERRUPT	AAF03300
	ADC R17331	INTERRUPT RESPONSE FOR THE 1733-1/853-4 DISK	AAF03400
			AAF03500
LINE03	NUM 0	INTERRUPT LINE ENTRY	AAF03600
	RTU- (\$FE)	GO TO INTERRUPT HANDLER ROUTINE	AAF03700
	NUM 09	PRIORITY LEVEL OF INTERRUPT	AAF03800
	ADC P17391	INTERRUPT RESPONSE FOR THE 1739-1 CART. DISK	AAF03900
			AAF04000
LINE04	NUM 0	INTERRUPT LINE ENTRY	AAF04100
	RTU- (\$FE)	GO TO INTERRUPT HANDLER ROUTINE	AAF04200
	NUM 10	PRIORITY LEVEL OF INTERRUPT	AAF04300
	ADC R42312	INTERRUPT RESPONSE FOR THE 1742-30/120 PRINTER	AAF04400
			AAF04500
LINE04	NUM 0	INTERRUPT LINE ENTRY	AAF04600
	RTU- (\$FE)	GO TO INTERRUPT HANDLER ROUTINE	AAF04700
	NUM 10	PRIORITY LEVEL OF INTERRUPT	AAF04800
	ADC R40421	INTERRUPT RESPONSE FOR THE 1740/1742-1 PRINTER	AAF04900
			AAF05000

Figure 1-b. Interrupt Trap Region, Section AAF

LINE05	NUM 0	INTERRUPT LINE ENTRY	AAF05100
	RTU- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF05200
	NUM 12	PRIORITY LEVEL OF INTERRUPT	AAF05300
	ADC R3644	INTERRUPT RESPONSE FOR THE 364-4 COMM. MUX.	AAF05400
			AAF05500
LINE07	NUM 0	INTERRUPT LINE ENTRY	AAF05600
	RTU- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF05700
	NUM 10	PRIORITY LEVEL OF INTERRUPT	AAF05800
	ADC R17322	INTERRUPT RESPONSE FOR THE 1732-2/615 MAG TAPE	AAF05900
			AAF06000
LINE07	NUM 0	INTERRUPT LINE ENTRY	AAF06100
	RTU- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF06200
	NUM 14	PRIORITY LEVEL OF INTERRUPT	AAF06300
	ADC R1732L	INTERRUPT RESPONSE FOR THE 1732/608/9 MAG TAPE	AAF06400
			AAF06500
LINE07	NUM 0	INTERRUPT LINE ENTRY	AAF06600
	RTU- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF06700
	NUM 10	PRIORITY LEVEL OF INTERRUPT	AAF06800
	ADC R1732B	INTERRUPT RESPONSE FOR THE 1732/608/9 MAG TAPE	AAF06900
			AAF07000
LINE07	NUM 0	INTERRUPT LINE ENTRY	AAF07100
	RTU- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF07200
	NUM 14	PRIORITY LEVEL OF INTERRUPT	AAF07300
	ADC R1731L	INTERRUPT RESPONSE FOR THE 1731/601 MAG TAPE	AAF07400
			AAF07500
LINE07	NUM 0	INTERRUPT LINE ENTRY	AAF07600
	RTU- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF07700
	NUM 10	PRIORITY LEVEL OF INTERRUPT	AAF07800
	ADC R1731B	INTERRUPT RESPONSE FOR THE 1731/601 MAG TAPE	AAF07900
LINE08	NUM 0	INTERRUPT LINE ENTRY	AAF08000
	RTU- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF08100
	NUM 13	PRIORITY LEVEL OF INTERRUPT	AAF08200
	ADC TIMINT	INTERRUPT RESPONSE FOR THE SYSTEM TIME BASE	AAF08300
			AAF08400
LINE09	NUM 0	INTERRUPT LINE ENTRY	AAF08500
	RTU- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF08600
	NUM 09	PRIORITY LEVEL OF INTERRUPT	AAF08700
	ADC R1536	INTERRUPT RESPONSE FOR THE 1536-2 A/D MUX.	AAF08800
			AAF08900
LINE10	NUM 0	INTERRUPT LINE ENTRY	AAF09000
	RTU- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF09100
	NUM 10	PRIORITY LEVEL OF INTERRUPT	AAF09200
	ADC R1747	INTERRUPT RESPONSE FOR THE 1747 DATA SET CONT.	AAF09300
			AAF09400
LINE11	NUM 0	INTERRUPT LINE ENTRY	AAF09500
	RTU- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF09600
	NUM 14	PRIORITY LEVEL OF INTERRUPT	AAF09700
	ADC R17293	INTERRUPT RESPONSE FOR THE 1729-3 CARD READER	AAF09800
			AAF09900
LINE11	NUM 0	INTERRUPT LINE ENTRY	AAF10000
	RTU- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF10100
	NUM 14	PRIORITY LEVEL OF INTERRUPT	AAF10200
	ADC R1728	INTERRUPT RESPONSE FOR THE 1728-430 READ/PNCH	AAF10300
			AAF10400

Figure 1-b. Interrupt Trap Region, Section AAF (Continued)

LINE11	NUM 0	INTERRUPT LINE ENTRY	AAF10500
	RTJ- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF10600
	NUM 14	PRIORITY LEVEL OF INTERRUPT	AAF10700
	ADC R17292	INTERRUPT RESPONSE FOR THE 1729-2 CARD READER	AAF10800
			AAF10900
LINE11	NUM 0	INTERRUPT LINE ENTRY	AAF11000
	RTJ- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF11100
	NUM 10	PRIORITY LEVEL OF INTERRUPT	AAF11200
	ADC R1726L	INTERRUPT RESPONSE FOR THE 1726-405 CARD RDR.	AAF11300
			AAF11400
LINE11	NUM 0	INTERRUPT LINE ENTRY	AAF11500
	RTJ- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF11600
	NUM 10	PRIORITY LEVEL OF INTERRUPT	AAF11700
	ADC R1726B	INTERRUPT RESPONSE FOR THE 1726-405 CARD RDR.	AAF11800
			AAF11900
LINE12	NUM 0	INTERRUPT LINE ENTRY	AAF12000
	RTJ- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF12100
	NUM 11	PRIORITY LEVEL OF INTERRUPT	AAF12200
	ADC R17441	INTERRUPT RESPONSE FOR THE 1744 D-GRAPH. CONT.	AAF12300
			AAF12400
LINE13	NUM 0	INTERRUPT LINE ENTRY	AAF12500
	RTJ- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF12600
	NUM 11	PRIORITY LEVEL OF INTERRUPT	AAF12700
	ADC R17442	INTERRUPT RESPONSE FOR THE 1744 D-GRAPH. CONT.	AAF12800
			AAF12900
LINEXX	NUM 0	INTERRUPT LINE ENTRY	AAF13000
	RTJ- (\$FE)	GC TO INTERRUPT HANDLER ROUTINE	AAF13100
	NUM 0	PRIORITY LEVEL OF INTERRUPT	AAF13200
	ADC INVINT	INTERRUPT RESPONSE FOR THE INVALID INTERRUPTS	AAF13300
	SPC 1		AAF13400
	EJT		AAF13500

Figure 1-b. Interrupt Trap Region, Section AAF (Continued)

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1.7 Core Resident Debugging Aids Entries, Section AAG

Section AAG, Figure 1-7, always starts at core location 140₁₆. At 140₁₆ is a two-word jump to COUTV4, the off-line core dump routine, assuming COUTV4 is a part of the system. If COUTV4 is not in the system a jump to location zero is located at 140₁₆. At 142₁₆ is a two-word jump to COBOP, the SYSCOP bootstrap which transfers the failed core image to mass memory. If COBOP is not included in the system, location 142₁₆ contains a jump to location zero.

These two-word jumps are intended for use by the programmer as on-line debugging aids. {Refer to 1700 MSOS Version 4 Instant, Publication Number 39520500.}

* CORE RESIDENT DEBUG ENTRIES			
SPC	2		AAG00100
CRG	\$140		AAG00200
			AAG00300
SPC	1		AAG00400
JMP+	0	DEBUG / CHECKOUT NOT SELECTED	AAG00500
JMP+	0		AAG00600
EXT	CCUTV4		AAG00700
EXT	CCROP		AAG00800
SPC	2		AAG00900
JMP+	CCUTV4	OFF-LINE CORE DUMP	AAG01000
SPC	4		AAG01100
JMP+	CCROP	SYSTEM CHECKOUT BOOTSTRAP	AAG01200
EJT			AAG01300

Figure 1-7. Core Resident Debugging Aids Entries, Section AAG

1.8 Interrupt Mask Table, Section AAH

The interrupt mask table is a set of masks, one for each of the 17 priority levels allowed by the operating system. For a given priority level, p, each bit of the corresponding interrupt mask is defined as follows:

interrupt mask p, bit m = 1 if an interrupt from interrupt line m is to be recognized when the system is operating at level p, that is, if line m has a priority level greater than p.

= 0 if an interrupt from interrupt line m is to be saved by the hardware but is not to be recognized when the system is operating at level p, i.e., bit m is zero if line m has an interrupt trap priority less than or equal to p. Note that bit m is zero if an interrupt from line m causes the system to run at priority level p. Bit m is zero for all values of p if line m is not used.

for m=0, 1, 2, ... 15.

When an interrupt occurs at level p one of the functions of the common interrupt handler is to alter the system priority level. This is done by selecting interrupt mask p from the interrupt mask table and storing it into the 1700 interrupt mask register {M register}. The interrupt masks are defined so that drivers within the system do not need to be reentrant.

Suppose a driver responds to interrupts at priority level seven from line 15. The driver runs at level seven. The system interrupt trap priorities are as follows:

<u>Interrupt Line</u>	<u>Priority Level</u>
15	7
12	6
4	5
1	14
0	15

For simplicity we will assume the other interrupt lines are unused.

According to the definition of the interrupt masks, interrupt mask 7 would allow interrupt recognition for priority levels 14 and 15 only. Thus, bits 0 and 1 would be one and all other bits of interrupt mask 7 would be zero. {Interrupt mask 7 = 0003₁₆.}

When an interrupt is recognized at level 7, the common interrupt handler stores 0003₁₆ in the M register before transferring control to the driver. As long as this value is in the M register the driver cannot be interrupted by a line 15 interrupt. Furthermore, the only interrupts recognized at level 7 are from lines 0 and 1. The interrupt masks for lines zero and one would be: interrupt mask 15 = 0000₁₆ {for line 0} and interrupt mask 14 = 0001₁₆ {for line 1}. Neither of these interrupt masks allows a line 15 interrupt to be recognized. By defining interrupt masks in this way the need for driver reentrancy is eliminated. Refer to Figure 1-8 for another Interrupt Mask Table example.

If the user modifies the interrupt trap region to add a buffered device, the priority level for the device must be less than 14, since the 1706 Buffered Data Channel Handler runs at level 14. For any device the priority level should be greater than 3 since the Job Processor and the Protect Processor run at levels 1-3.

INTERRUPT MASK TABLE

ENT MASKT INTERRUPT MASKS INDEXED BY PRIORITY LEVEL

		----- INTERRUPT LINE NUMBER																	
		15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
P	-1	*	1	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	*
R	0	*	1	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	*
I	1	*	1	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	*
O	2	*	1	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	*
R	3	*	1	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	*
I	4	*	1	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	*
T	5	*	1	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	*
Y	6	*	1	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	*
	7	*	1	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	*
L	8	*	1	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	*
E	9	*	1	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	*
V	10	*	1	0	0	0	0	0	0	1	0	0	1	0	1	0	1	1	*
E	11	*	1	0	0	0	0	0	0	1	0	0	1	0	1	0	1	1	*
L	12	*	1	0	0	0	0	0	0	1	0	0	0	1	0	1	1	1	*
.	13	*	1	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	*
.	14	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	*
V	15	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*

MASKT	NUM	ADDRESS	PRIORITY LEVEL
	NUM	\$913B	PRIORITY LEVEL -1
	NUM	\$913B	PRIORITY LEVEL 00
	NUM	\$913B	PRIORITY LEVEL 01
	NUM	\$913B	PRIORITY LEVEL 02
	NUM	\$913B	PRIORITY LEVEL 03
	NUM	\$913B	PRIORITY LEVEL 04
	NUM	\$913B	PRIORITY LEVEL 05
	NUM	\$913B	PRIORITY LEVEL 06
	NUM	\$913B	PRIORITY LEVEL 07
	NUM	\$913B	PRIORITY LEVEL 08
	NUM	\$912B	PRIORITY LEVEL 09
	NUM	\$812B	PRIORITY LEVEL 10
	NUM	\$812B	PRIORITY LEVEL 11
	NUM	\$810B	PRIORITY LEVEL 12
	NUM	\$800B	PRIORITY LEVEL 13
	NUM	\$0001	PRIORITY LEVEL 14
	NUM	\$0000	PRIORITY LEVEL 15

Figure 1-8. Interrupt Mask Table, Section AAH

1.9 Extended Communication Region, Section AAI

Section AAI, Figure 1-9, is called the extended communication region. It contains parameters and logical units required by the operating system. The logical unit numbers are supplied by the user when he completes the 1700 MSOS Ordering Form. The various mass memory sector addresses are supplied by the system initializer during system installation. The mode switch {indicating 32K or 65K} is set by the restart portion of the SPACE program.

The only words of the extended communication region which a user might wish to change are words 10 and 11, {the unprotected core flag and the no swapping allowed flag.} Word 10 is always set to zero in a standard system indicating that unprotected core is in Part 0. The user may wish to move unprotected core to Part 1. When unprotected core is in Part 0, it is located adjacent to allocatable core so that when necessary the unprotected area can be swapped out to mass memory to extend the allocatable area. However, when both allocatable core and the background are used extensively by a system, and unprotected core is in Part 0, the number of core swaps required may create undesirable time lags in the system. Each core swap requires not only the time to write the unprotected area to mass memory, but also the time required to change the protect status of each word in the unprotected area. {The latter requires the execution of an 8-instruction loop for each word in the unprotected area.} For these reasons, it may in some systems be desirable to have unprotected core in Part 1. In order to change the system so that unprotected core is in Part 1, word 10, the unprotected core flag must be changed to one. In addition, if the partitioned core driver is not present in the system, word 11, the no swapping allowed flag, must also be one.

EXTENDED COMMUNICATIONS REGION

REFERENCED THRU LOCATION \$E9

	SPC	3			AAI00100
	ENT	MAXSEC			AAI00200
	EXT	JFILV4			AAI00300
	EXT	RCTV			AAI00400
	EXT	ENDOV4			AAI00500
	EXT	DATBAS			AAI00600
	EXT	SECTOR			AAI00700
	EQL	CSYLST(9)			AAI00800
	EQL	CSYINF(10)			AAI00900
	EQL	CSYPUN(11)			AAI01000
	EQL	SECT1()			AAI01100
	EQL	SECT3()			AAI01200
	EQL	SECT4()			AAI01300
	SPC	3			AAI01400
EXTRV4	ADC	0	00	MODE SWITCH 32K=0 65K=1	AAI01500
	ADC	CSYINF	01	STANDARD CCSY INPUT LU NUMRER	AAI01600
	ADC	CSYPUN	02	STANDARD CCSY OUTPUT LU NUMRER	AAI01700
	ADC	CSYLST	03	STANDARD CCSY LIST LU NUMRER	AAI01800
	ADC	0	04	FIRST SECTOR LSH OF SYSTEM CORE IMAGE	AAI01900
	ADC	0	05	FIRST SECTOR LSH OF S. A. T.	AAI02000
	ADC	0	06	FIRST SECTOR LSH OF CREP TABLE	AAI02100
	ADC	0	07	FIRST SECTOR LSH OF CREP1 TABLE	AAI02200
	ADC	JFILV4	08	FIRST SECTOR LSH OF JOB FILE DIRECTORY	AAI02300
	ADC	RCTV	09	ADDRESS OF RCTV TABLE IN THE MONITOR	AAI02400
	ADC	0	10	UNPRCTECTED CORE FLAG 0=PART0 / 1=PART1	AAI02500
	ADC	1	10	UNPRCTECTED CORE FLAG 0=PART0 / 1=PART1	AAI02600
	ADC	0	11	UNPROTECTED SWAP ALLOWED 0=YES / 1=NO	AAI02700
	ADC	AYERTC	12	ADDRESS LOCATION CONTAINING THE YEAR	AAI02800
	ADC	AMONTC	13	ADDRESS LOCATION CONTAINING THE MONTH	AAI02900
	ADC	ADAYTC	14	ADDRESS LOCATION CONTAINING THE DAY	AAI03000
	ADC	ENDOV4	15	LAST ADDRESS OF PART 0 CORE	AAI03100
	ADC	0	16	FIRST ADDRESS OF BLANK (SYSTEM) COMMON	AAI03200
	ADC	DATBAS	17	FIRST ADDRESS OF LARELED COMMON	AAI03300
	ADC	0	18	COSY DRIVER CURRENT PHYSTAB ADDRESS	AAI03400
	ADC	0	19	JOB TABLE INITIALIZATION FLAG	AAI03500
	ADC	0	20	MASS MEMORY LOCATION OF ENGINEERING FILE	AAI03600
	ADC	SECT1	21	MSB OF MAXIMUM SCRATCH SECTOR	AAI03700
MAXSEC	ADC	SECTOR	22	LSB OF MAXIMUM SCRATCH SECTOR	AAI03800
	ADC	SECT3	23	MSB OF MAXIMUM LIBRARY SECTOR	AAI03900
	ADC	SECT4	24	LSB OF MAXIMUM LIBRARY SECTOR	AAI04000
	EJT				AAI04100
					AAI04200
					AAI04300
					AAI04400

Figure 1-9. Extended Communication Region, Section AAI

1.10 System Identification, Section AAJ

This section contains the system name provided by the user in the 1700 MSOS Ordering Form.

It also contains ASCII characters representing the date the system was built. These characters are supplied by the system initializer. The system name and date of build are printed with each autoload and at the beginning of each job on the job name identification sheet. Section AAJ is shown in Figure 1-10.

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```
*
*
      S Y S T E M   I D E N T I F I C A T I O N
SPC  1
ENT  SYSID
EXT  SYSMON      MONTH SYSTEM WAS BUILT
EXT  SYSDAY      DAY   SYSTEM WAS BUILT
EXT  SYSYER      YEAR  SYSTEM WAS BUILT
SPC  4
SYSID ALF 16,
      ADC SYSMON
      ADC SYSDAY
      ADC SYSYER
      EJT
AAJ00100
AAJ00200
AAJ00300
AAJ00400
AAJ00500
AAJ00600
AAJ00700
AAJ00800
AAJ00900
AAJ01000
AAJ01100
AAJ01200
AAJ01300
```

Figure 1-10. System Identification, Section AAJ

1-11 Interrupt Stack and Volatile Block Stack, Section AAK

The interrupt stack contains room for one entry for each priority level used by the system. Each entry consists of the values of the Q, A, I, and P registers, the overflow status, and the priority level at the time of an interrupt. The standard interrupt stack contains room for 16 priority levels. The size of the interrupt stack can be decreased by the user if it is certain that some priority levels will not be used by the system. However, care must be taken in doing so. The user must be sure there is room in the interrupt stack for each distinct priority level found in the interrupt trap region, Section AAF.

The volatile block stack, or volatile storage area, is an area that is used by MSOS to preserve reentrancy. It is used by the reentrant FORTRAN run-time package if reentrant FORTRAN is included in the system. It is used by the reentrant ENCODE/DECODE packages if these packages are in the system. If a reentrant user applications program is written, the volatile block stack may be used by such a program. For each priority level in the system the number of words of volatile storage is the sum of the following:

<u>Use</u>	<u>Number Words</u>
Basic Volatile	16
Reentrant FORTRAN	96, if this is a FORTRAN level 0, otherwise
Reentrant ENCODE/DECODE	57, if ENCODE/DECODE can run on this level 0, otherwise
Reentrant user applica- tions	Number of words of volatile needed by any reentrant user applications programs which can run at this level
One extra word needed to store Q in volatile before the check is made to see if there is room in volatile for this entry.	1

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The error message, 0V, on the output comment device indicates volatile storage has overflowed. No recovery from this error is possible. Before the system is restarted the number of words of volatile should be increased as necessary. It is possible that the message 0V may appear if the system deteriorates for some reason other than volatile overflow. Thus, the user should ascertain that some other feature of the system did not cause the system to fail, before assuming volatile overflow caused the problem.

* S T O R A G E S T A C K S				AAK00100
* AAK00200				
NUMPRI	EQL	NUMPRI(16)	NUMBER OF SYSTEM PRIORITY LEVELS	AAK00300
EXTVOL	EQL	EXTVOL(00)	AMOUNT OF EXTRA VOLATILE STORAGE	AAK00400
* AAK00500				
NFTNLV	EQL	NFTNLV(0)	NUMBER OF REENTRANT FORTRAN LEVELS	AAK00600
NEDLVL	EQL	NEDLVL(0)	NUMBER OF REENTRANT ENCODE/DECODE LEVELS	AAK00700
NFTNLV	EQL	NFTNLV(0)	NUMBER OF REENTRANT FORTRAN LEVELS	AAK00800
NEDLVL	EQL	NEDLVL(0)	NUMBER OF REENTRANT ENCODE/DECODE LEVELS	AAK00900
	SPC	3		AAK01000
* AAK01100				
* I N T E R R U P T S T A C K				AAK01200
* AAK01300				
ENT	INTSTK	CONTENTS,	1 = G-REGISTER	AAK01400
			2 = A-REGISTER	AAK01500
			3 = I-REGISTER	AAK01600
			4 = F-REGISTER	AAK01700
			5 = PRIORITY LEVEL AND OVERFLOW	AAK01800
			INDICATOR (BIT 15)	AAK01900
* AAK02000				
INTSTK	BZS	INTSTK(R*NUMPRI)		AAK02100
	SPC	3		AAK02200
* AAK02300				
* V O L A T I L E B L O C K S T A C K				AAK02400
* AAK02500				
ENT	VOLBLK	CONTENTS,	1 = G-REGISTER	AAK02600
			3 = I-REGISTER	AAK02700
			4 = USER ASSIGNMENTS	AAK02800
			.	AAK02900
			N = USER ASSIGNMENTS	AAK03000
* AAK03100				
VOLBLK	BZS	VOLBLK(18*NUMPRI+98*NFTNLV+57*NEDLVL+EXTVOL+1)		AAK03200
VOLEND	EQL	VOLEND(*)	END OF VOLATILE	AAK03300
	SPC	3		AAK03400
* AAK03500				

Figure 1-11. Interrupt Stack and Volatile Block Stack, Section AAK

1.12 Scheduler/Timer Stack, Section AAL

This section {Figure 1-12} is used by both the scheduler and the timer programs. The waiting list of programs that have been requested by a scheduler request are threaded by priority. Within each priority they are threaded in a first-in first-out basis. The waiting list of timer requests are threaded according to the amount of time remaining before the program is to be scheduled. An entry in either stack is four words long as indicated in Figure 1-12. The number of entries in this stack is 24 in a standard system without IMPORT. If IMPORT is in the system, an additional 28 entries are included in the standard stack.

Since the stack must be large enough to contain the maximum number of expected simultaneous requests, it is sometimes necessary to increase the size of the scheduler/timer stack. However, the maximum number of expected simultaneous requests is generally difficult to compute. Therefore, the usual procedure is to use the standard stack size until a stack overflow is detected. Such an overflow is indicated by a non-zero value for the variable ERRCNT located in the scheduler/dispatcher program {NDISP or RDISP}. If this overflow occurs, one or more scheduler or timer calls will be lost. The user should then increase the stack size and again monitor ERRCNT.

SCHEDULER / TIMER STACK

ENT	SCHSTK	CONTENTS		
ENT	SCHLN6	1 = SCHEDULER CALL		AAL00100
		2 = STARTING ADDRESS		AAL00200
		3 = THREAD TO NEXT CALL		AAL00300
		4 = G-REGISTER CONTENTS		AAL00400
SCHSTK	EQL	SCHSTK(*)		AAL00500
	SPC	1		AAL00600
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL00700
	EJT			AAL00800
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL00900
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL01000
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL01100
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL01200
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL01300
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL01400
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL01500
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL01600
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL01700
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL01800
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL01900
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL02000
	EJT			AAL02100
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL02200
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL02300
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL02400
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL02500
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL02600
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL02700
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL02800
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL02900
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL03000
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL03100
	ADC	0,0,(-0),0	SCHEDULER STACK ENTRY	AAL03200
	EJT			AAL03300
		SCHEDULER / TIMER STACK		AAL03400
		28 ENTRIES REQUIRED FOR HIGH SPEED IMPORT		AAL03500
	SPC	4		AAL03600
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL03700
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL03800
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL03900
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL04000
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL04100
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL04200
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL04300
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL04400
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL04500
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL04600
	EJT			AAL04700
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL04800
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL04900
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL05000
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL05100
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL05200
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL05300
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL05400
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL05500
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL05600
	ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	AAL05700
	EJT			AAL05800

Figure 1-12. Scheduler/Timer Stack, Section AAL

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ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	045	AAL05900
ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	046	AAL06000
ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	047	AAL06100
ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	048	AAL06200
ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	049	AAL06300
ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	050	AAL06400
ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	051	AAL06500
ADC	0,0,#+2,0	SCHEDULER STACK ENTRY	052	AAL06600
ADC	0,0,(-0),0	SCHEDULER STACK ENTRY	053	AAL06700
SCHLNG	EQU	SCHLNG(*-SCHSTK)	SCHEDULER STACK LENGTH	AAL06800
EJT				AAL06900

Figure 1-12. Scheduler/Timer Stack, Section AAL {Continued}

1.13 LOG1A Logical Unit Table, Section AAM

The first word of Section AAM must contain the number of logical units in the system. This must be followed by one entry for each logical unit in the system. An entry for a given logical unit is the address of the physical device table for that logical unit.

Logical units are of two types, diagnostic and non-diagnostic. A diagnostic logical unit differs from a non-diagnostic logical unit for the same device in that a device error is handled differently. For a non-diagnostic logical unit, a device error causes the alternate device handler to be called. One of the functions of the alternate device handler is to print an error message indicating the logical unit and an error code. For a diagnostic logical unit the alternate device handler is not called. An error message is printed by SCMM. Such an error message contains more information than the corresponding message printed by the alternate device handler since SCMM performs additional error analysis on the device.

There are twelve standard logical units. These are always non-diagnostic logical units. The twelve are as follows:

<u>Logical Unit</u>	<u>Function</u>
1	Core allocator
2	Dummy
3	Dummy
4	Comment input/output
5	COSY driver - Unit 0 *
6	Mag tape - Unit 0 *
7	Pseudo tape - Unit 0 *
8	Library unit
9	Standard list unit
10	Standard input unit
11	Standard output unit
12	FORTTRAN list unit

* If not present in a system, dummy is substituted.

Logical units 2 and 3 are always dummy logical units. This is due to the following reasons. FORTRAN allows its users to specify logical units 1-4 for input/output. There is no problem in distinguishing FORTRAN logical unit one from the system logical unit one, since the latter is the core allocator and does not correspond to an input/output device. Also, there is no confusion with regard to FORTRAN logical unit four, since logical unit four is the comment input/output device for both FORTRAN and the operating system. However, ambiguities would arise in regard to FORTRAN logical units two and three if the corresponding operating system logical units were non-dummy logical units. Therefore, these two logical units are always specified as dummies.

The set of all possible entries which may appear in a given LOG1A table is shown in Figure 1-13. The characters, XXX, in the comment field of each line are changed to a numerical value when a line is selected for a given SYSDAT. This value is an actual logical unit number. Refer to Figure 1-14 for a sample SYSDAT LOG1A Table. Logical units are selected according to the user's specifications in the 1700 MSOS Ordering Form.

The lines in the first part of this section have been grouped as indicated by brackets in Figure 1-13. One line is selected from each group as the corresponding standard logical unit. The reason the dummy logical unit is indicated as the first line of some groups is so that the Configurator can substitute the dummy logical unit for one or more of the twelve standard logical units if necessary. This preserves the standard logical unit numbers following an unused logical unit, so that the system may refer to the first twelve logical units by number, independent of system configuration.

In most systems, the remaining non-diagnostic logical units are listed after the first twelve logical units. Then the diagnostic logical units and finally, any non-diagnostic logical units used only as alternate devices, would appear. The order of the logical units after the first twelve is dependent on user specification in the MSOS Ordering Form.

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LOGICAL UNIT TABLES (LOGIA)

LOGIA

AAM00100
AAM00200
AAM00300
AAM00400
AAM00500
AAM00600
AAM00700
AAM00800
AAM00900
AAM01000
AAM01100
AAM01200
AAM01300
AAM01400
AAM01500
AAM01600
AAM01700
AAM01800
AAM01900
AAM02000
AAM02100
AAM02200
AAM02300
AAM02400
AAM02500
AAM02600
AAM02700
AAM02800
AAM02900
AAM03000
AAM03100
AAM03200
AAM03300
AAM03400
AAM03500
AAM03600
AAM03700
AAM03800
AAM03900
AAM04000
AAM04100
AAM04200
AAM04300
AAM04400
AAM04500
AAM04600
AAM04700
AAM04800
AAM04900
AAM05000
AAM05100
AAM05200
AAM05300
AAM05400

PHYSICAL DEVICES ADDRESSES BY LOGICAL UNIT

ENT LOGIA
ENT NUMLU
SPC I
ADC NUMLU
ADC PCORE
ADC PDUMMY
ADC P1711
ADC P1713K
ADC PDUMMY
ADC PCOSY1
ADC PCOSY2
ADC PDUMMY
ADC P73220
ADC P73221
ADC P73222
ADC P73223
ADC P732U0
ADC P732U1
ADC P732U2
ADC P732U3
ADC P732U4
ADC P732U5
ADC P732U6
ADC P732U7
ADC P732B0
ADC P732B1
ADC P732B2
ADC P732B3
ADC P732B4
ADC P732B5
ADC P732B6
ADC P732B7
ADC P731U0
ADC P731U1
ADC P731U2
ADC P731U3
ADC P731U4
ADC P731U5
ADC P731U6
ADC P731U7
ADC P731B0
ADC P731B1
ADC P731B2
ADC P731B3
ADC P731B4
ADC P731B5
ADC P731B6
ADC P731B7
ADC PDUMMY
ADC PSUD00
ADC PSUD01
ADC PSUD02

1 } NUMBER OF LOGICAL UNITS
2 } XXX CORE ALLOCATOR
4 } XXX DUMMY LOGICAL UNIT
4 } XXX 1711 TELETYPE, 713-10 CRT
4 } XXX 1713 TELETYPE KEYBOARD/PRINTER
5 } XXX DUMMY LOGICAL UNIT
5 } XXX COSY DRIVER, FIRST UNIT
5 } XXX COSY DRIVER, SECOND UNIT
6 } XXX DUMMY LOGICAL UNIT
6 } XXX 1732-2 615-73/93 MAG TAPE UNIT 0
6 } XXX 1732-2 615-73/93 MAG TAPE UNIT 1
6 } XXX 1732-2 615-73/93 MAG TAPE UNIT 2
6 } XXX 1732-2 615-73/93 MAG TAPE UNIT 3
6 } XXX UNBUFFERED 1732-1-608/9 MAG TAPE, UNIT 0
6 } XXX UNBUFFERED 1732-1-608/9 MAG TAPE, UNIT 1
6 } XXX UNBUFFERED 1732-1-608/9 MAG TAPE, UNIT 2
6 } XXX UNBUFFERED 1732-1-608/9 MAG TAPE, UNIT 3
6 } XXX UNBUFFERED 1732-1-608/9 MAG TAPE, UNIT 4
6 } XXX UNBUFFERED 1732-1-608/9 MAG TAPE, UNIT 5
6 } XXX UNBUFFERED 1732-1-608/9 MAG TAPE, UNIT 6
6 } XXX UNBUFFERED 1732-1-608/9 MAG TAPE, UNIT 7
6 } XXX BUFFERED 1732-1-608/9 MAG TAPE, UNIT 0
6 } XXX BUFFERED 1732-1-608/9 MAG TAPE, UNIT 1
6 } XXX BUFFERED 1732-1-608/9 MAG TAPE, UNIT 2
6 } XXX BUFFERED 1732-1-608/9 MAG TAPE, UNIT 3
6 } XXX BUFFERED 1732-1-608/9 MAG TAPE, UNIT 4
6 } XXX BUFFERED 1732-1-608/9 MAG TAPE, UNIT 5
6 } XXX BUFFERED 1732-1-608/9 MAG TAPE, UNIT 6
6 } XXX BUFFERED 1732-1-608/9 MAG TAPE, UNIT 7
6 } XXX UNBUFFERED 1731-601 MAG TAPE, UNIT 0
6 } XXX UNBUFFERED 1731-601 MAG TAPE, UNIT 1
6 } XXX UNBUFFERED 1731-601 MAG TAPE, UNIT 2
6 } XXX UNBUFFERED 1731-601 MAG TAPE, UNIT 3
6 } XXX UNBUFFERED 1731-601 MAG TAPE, UNIT 4
6 } XXX UNBUFFERED 1731-601 MAG TAPE, UNIT 5
6 } XXX UNBUFFERED 1731-601 MAG TAPE, UNIT 6
6 } XXX UNBUFFERED 1731-601 MAG TAPE, UNIT 7
6 } XXX BUFFERED 1731-601 MAG TAPE, UNIT 0
6 } XXX BUFFERED 1731-601 MAG TAPE, UNIT 1
6 } XXX BUFFERED 1731-601 MAG TAPE, UNIT 2
6 } XXX BUFFERED 1731-601 MAG TAPE, UNIT 3
6 } XXX BUFFERED 1731-601 MAG TAPE, UNIT 4
6 } XXX BUFFERED 1731-601 MAG TAPE, UNIT 5
6 } XXX BUFFERED 1731-601 MAG TAPE, UNIT 6
6 } XXX BUFFERED 1731-601 MAG TAPE, UNIT 7
6 } XXX DUMMY LOGICAL UNIT
6 } XXX PSEUDO TAPE, UNIT 0
6 } XXX PSEUDO TAPE, UNIT 1
6 } XXX PSEUDO TAPE, UNIT 2

Figure 1-13. LOGIA Logical Unit Table, Section AAM

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ADC	PSUD03	xxx	PSEUDO TAPE, UNIT 3	AAM05500	
ADC	PSUD04	xxx	PSEUDO TAPE, UNIT 4	AAM05600	
ADC	PSUD06	xxx	PSEUDO TAPE, UNIT 6	AAM05700	
ADC	PSUD07	xxx	PSEUDO TAPE, UNIT 7	AAM05800	
ADC	P73320	xxx	1733-2 856-2/4 DISK, UNIT 0	AAM05900	
ADC	P73321	xxx	1733-2 856-2/4 DISK, UNIT 1	AAM06000	
ADC	P73322	xxx	1733-2 856-2/4 DISK, UNIT 2	AAM06100	
ADC	P73323	xxx	1733-2 856-2/4 DISK, UNIT 3	AAM06200	
ADC	P17380	xxx	1738 853/4 DISK, UNIT 0	AAM06300	
ADC	P17381	xxx	1738 853/4 DISK, UNIT 1	AAM06400	
ADC	P73310	xxx	1733-1 853/4 DISK, UNIT 0	AAM06500	
ADC	P73311	xxx	1733-1 853/4 DISK, UNIT 1	AAM06600	
ADC	P73312	xxx	1733-1 853/4 DISK, UNIT 2	AAM06700	
ADC	P73313	xxx	1733-1 853/4 DISK, UNIT 3	AAM06800	
ADC	P73314	xxx	1733-1 853/4 DISK, UNIT 4	AAM06900	
ADC	P73315	xxx	1733-1 853/4 DISK, UNIT 5	AAM07000	
ADC	P73316	xxx	1733-1 853/4 DISK, UNIT 6	AAM07100	
ADC	P73317	xxx	1733-1 853/4 DISK, UNIT 7	AAM07200	
ADC	P17391	xxx	1739-1 CARTRIDGE DISK	AAM07300	
ADC	P1752	xxx	1752 DRUM	AAM07400	
ADC	P1751	xxx	1751 DRUM	AAM07500	
ADC	P42312	xxx	1742-30/120 LINE PRINTER	AAM07600	
ADC	P40421	xxx	1740-501/1742 LINE PRINTER	AAM07700	
ADC	P1711	xxx	1711 TELETYPE, 713-10 CRT	AAM07800	
ADC	P1713K	xxx	1713 TELETYPE KEYBOARD/PRINTER	AAM07900	
ADC	P17293	xxx	1729-3 CARD READER	AAM08000	
ADC	P1728	xxx	1728-430 CARD READER	AAM08100	
ADC	P17292	xxx	1729-2 CARD READER	AAM08200	
ADC	P1726L	xxx	UNBUFFERED 1726-405 CARD READER	AAM08300	
ADC	P1726R	xxx	HUFFERED 1726-405 CARD READER	AAM08400	
ADC	P73220	xxx	1732-2 615-73/93 MAG TAPE UNIT 0	AAM08500	
ADC	P732U0	xxx	UNBUFFERED 1732-1-608/9 MAG TAPE, UNIT 0	AAM08600	
ADC	P732R0	xxx	BUFFERED 1732-1-608/9 MAG TAPE, UNIT 0	AAM08700	
ADC	P731U0	xxx	UNBUFFERED 1731-601 MAG TAPE, UNIT 0	AAM08800	
ADC	P731R0	xxx	BUFFERED 1731-601 MAG TAPE, UNIT 0	AAM08900	
ADC	P1713R	xxx	1713 TELETYPE PAPER TAPE READER	AAM09000	
ADC	P1777R	xxx	1777/1721 PAPER TAPE READER	AAM09100	
ADC	P1728	xxx	1728-430 CARD PUNCH	AAM09200	
ADC	P1728S	xxx	1728/430 CARD PUNCH - S/W BUFFERED	AAM09300	
ADC	P73220	xxx	1732-2 615-73/93 MAG TAPE UNIT 0	AAM09400	
ADC	P732U0	xxx	UNBUFFERED 1732-1-608/9 MAG TAPE, UNIT 0	AAM09500	
ADC	P732R0	xxx	BUFFERED 1732-1-608/9 MAG TAPE, UNIT 0	AAM09600	
ADC	P731U0	xxx	UNBUFFERED 1731-601 MAG TAPE, UNIT 0	AAM09700	
ADC	P731R0	xxx	BUFFERED 1731-601 MAG TAPE, UNIT 0	AAM09800	
ADC	P1713P	xxx	1713 TELETYPE PAPER TAPE PUNCH	AAM09900	
ADC	P171PS	xxx	1713 PAPER TAPE PUNCH - S/W BUFFERED	AAM10000	
ADC	P1777P	xxx	1777/1723 PAPER TAPE PUNCH	AAM10100	
ADC	P777PS	xxx	1777/23 PAPER TAPE PUNCH - S/W BUFFERED	AAM10200	
FTN742	ADC	P42312	xxx	1742-30/120 FORTRAN LINE PRINTER	AAM10300
FTN740	ADC	P40421	xxx	1740-501/1742 FORTRAN LINE PRINTER	AAM10400
	ADC	P1711	xxx	1711 TELETYPE, 713-10 CRT	AAM10500
	ADC	P1713K	xxx	1713 TELETYPE KEYBOARD/PRINTER	AAM10600
	ADC	P1728S	xxx	1728/430 CARD PUNCH - S/W BUFFERED	AAM10700
	ADC	P1713R	xxx	1713 TELETYPE PAPER TAPE READER	AAM10800
	ADC	P1777R	xxx	1777/1721 PAPER TAPE READER	AAM10900
PTRDNP	ADC	P1777R	xxx	1777/1721 PAPER TAPE READER - NO PARITY	AAM11000
	ADC	P1713P	xxx	1713 TELETYPE PAPER TAPE PUNCH	AAM11100
	ADC	P171PS	xxx	1713 PAPER TAPE PUNCH - S/W BUFFERED	AAM11200
	ADC	P1777P	xxx	1777/1723 PAPER TAPE PUNCH	AAM11300
	ADC	P777PS	xxx	1777/23 PAPER TAPE PUNCH - S/W BUFFERED	AAM11400

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ADC	PCOM00	XXX 364-4	COMMUNICATIONS MUX, UNIT 0	AAM11500
ADC	PCOM01	XXX 364-4	COMMUNICATIONS MUX, UNIT 1	AAM11600
ADC	PCOM02	XXX 364-4	COMMUNICATIONS MUX, UNIT 2	AAM11700
ADC	PCOM03	XXX 364-4	COMMUNICATIONS MUX, UNIT 3	AAM11800
ADC	PCOM04	XXX 364-4	COMMUNICATIONS MUX, UNIT 4	AAM11900
ADC	PCOM05	XXX 364-4	COMMUNICATIONS MUX, UNIT 5	AAM12000
ADC	PCOM06	XXX 364-4	COMMUNICATIONS MUX, UNIT 6	AAM12100
ADC	PCOM07	XXX 364-4	COMMUNICATIONS MUX, UNIT 7	AAM12200
ADC	PCOM08	XXX 364-4	COMMUNICATIONS MUX, UNIT 8	AAM12300
ADC	PCOM09	XXX 364-4	COMMUNICATIONS MUX, UNIT 9	AAM12400
ADC	PCOM10	XXX 364-4	COMMUNICATIONS MUX, UNIT 10	AAM12500
ADC	PCOM11	XXX 364-4	COMMUNICATIONS MUX, UNIT 11	AAM12600
ADC	PCOM12	XXX 364-4	COMMUNICATIONS MUX, UNIT 12	AAM12700
ADC	PCOM13	XXX 364-4	COMMUNICATIONS MUX, UNIT 13	AAM12800
ADC	PCOM14	XXX 364-4	COMMUNICATIONS MUX, UNIT 14	AAM12900
ADC	PCOM15	XXX 364-4	COMMUNICATIONS MUX, UNIT 15	AAM13000
ADC	PCMS00	XXX 364-4	COMM. MUX, UNIT 0 - S/W BUFFERED	AAM13100
ADC	PCMS01	XXX 364-4	COMM. MUX, UNIT 1 - S/W BUFFERED	AAM13200
ADC	PCMS02	XXX 364-4	COMM. MUX, UNIT 2 - S/W BUFFERED	AAM13300
ADC	PCMS03	XXX 364-4	COMM. MUX, UNIT 3 - S/W BUFFERED	AAM13400
ADC	PCMS04	XXX 364-4	COMM. MUX, UNIT 4 - S/W BUFFERED	AAM13500
ADC	PCMS05	XXX 364-4	COMM. MUX, UNIT 5 - S/W BUFFERED	AAM13600
ADC	PCMS06	XXX 364-4	COMM. MUX, UNIT 6 - S/W BUFFERED	AAM13700
ADC	PCMS07	XXX 364-4	COMM. MUX, UNIT 7 - S/W BUFFERED	AAM13800
ADC	PCMS08	XXX 364-4	COMM. MUX, UNIT 8 - S/W BUFFERED	AAM13900
ADC	PCMS09	XXX 364-4	COMM. MUX, UNIT 9 - S/W BUFFERED	AAM14000
ADC	PCMS10	XXX 364-4	COMM. MUX, UNIT 10 - S/W BUFFERED	AAM14100
ADC	PCMS11	XXX 364-4	COMM. MUX, UNIT 11 - S/W BUFFERED	AAM14200
ADC	PCMS12	XXX 364-4	COMM. MUX, UNIT 12 - S/W BUFFERED	AAM14300
ADC	PCMS13	XXX 364-4	COMM. MUX, UNIT 13 - S/W BUFFERED	AAM14400
ADC	PCMS14	XXX 364-4	COMM. MUX, UNIT 14 - S/W BUFFERED	AAM14500
ADC	PCMS15	XXX 364-4	COMM. MUX, UNIT 15 - S/W BUFFERED	AAM14600
LU1747	AUC	p1747	XXX 1747 DATA SET CONTROLLER	AAM14700
LU2740	AUC	p17440	XXX DIGIGRAPHICS CONSOLE 0	AAM14800
LU2741	AUC	p17441	XXX DIGIGRAPHICS CONSOLE 1	AAM14900
	ADC	p1501	XXX 1501 HIGH SPEED ANALOG MUX CONTROLLER	AAM15000
	ADC	p1536	XXX 1536-2 RELAY ANALOG MUX CONTROLLER	AAM15100
IMPSFD	ADC	PCUMMY	XXX HIGH SPEED IMPORT UNIT 0	AAM15200
IMPTC	ADC	PCUMMY	XXX HIGH SPEED IMPORT UNIT 1	AAM15300
IMPTC	ADC	PCUMMY	XXX HIGH SPEED IMPORT UNIT 2	AAM15400
IMSTR1	ADC	PCUMMY	XXX HIGH SPEED IMPORT UNIT 3	AAM15500
	ADC	PCUMMY	XXX HIGH SPEED IMPORT UNIT 4	AAM15600
	ADC	PCUMMY	XXX HIGH SPEED IMPORT UNIT 5	AAM15700
	ADC	PCUMMY	XXX HIGH SPEED IMPORT UNIT 6	AAM15800
	ADC	PCUMMY	XXX HIGH SPEED IMPORT UNIT 7	AAM15900
IMLAST	ADC	PCUMMY	XXX HIGH SPEED IMPORT UNIT 8	AAM16000
X1711	ADC	p1711	XXX DIAGNOSTIC 1711 TELETYPE, 713-10 CRT	AAM16100
X1713K	ADC	p1713K	XXX DIAGNOSTIC 1713 TTY KEYBOARD / PRINTER	AAM16200
X73220	ADC	p73220	XXX DIAGNOSTIC 1732-2 615 MAG TAPE, UNIT 0	AAM16300
X73221	ADC	p73221	XXX DIAGNOSTIC 1732-2 615 MAG TAPE, UNIT 1	AAM16400
X73222	ADC	p73222	XXX DIAGNOSTIC 1732-2 615 MAG TAPE, UNIT 2	AAM16500
X73223	ADC	p73223	XXX DIAGNOSTIC 1732-2 615 MAG TAPE, UNIT 3	AAM16600
X732U0	ADC	p732U0	XXX DIAGNOSTIC 1732-1 608/9 MAG TAPE, UNIT 0	AAM16700
X732U1	ADC	p732U1	XXX DIAGNOSTIC 1732-1 608/9 MAG TAPE, UNIT 1	AAM16800
X732U2	ADC	p732U2	XXX DIAGNOSTIC 1732-1 608/9 MAG TAPE, UNIT 2	AAM16900
X732U3	ADC	p732U3	XXX DIAGNOSTIC 1732-1 608/9 MAG TAPE, UNIT 3	AAM17000
X732U4	ADC	p732U4	XXX DIAGNOSTIC 1732-1 608/9 MAG TAPE, UNIT 4	AAM17100
X732U5	ADC	p732U5	XXX DIAGNOSTIC 1732-1 608/9 MAG TAPE, UNIT 5	AAM17200
X732U6	ADC	p732U6	XXX DIAGNOSTIC 1732-1 608/9 MAG TAPE, UNIT 6	AAM17300
X732U7	ADC	p732U7	XXX DIAGNOSTIC 1732-1 608/9 MAG TAPE, UNIT 7	AAM17400

Figure 1-13. LOG1A Logical Unit Table, Section AAM (Continued)

X732H0	ADC	P732H0	xxx	DIAGNOSTIC	1732-1	608/9	MAG TAPE, UNIT 0	AAM17500
X732H1	ADC	P732H1	xxx	DIAGNOSTIC	1732-1	608/9	MAG TAPE, UNIT 1	AAM17600
X732H2	ADC	P732H2	xxx	DIAGNOSTIC	1732-1	608/9	MAG TAPE, UNIT 2	AAM17700
X732H3	ADC	P732H3	xxx	DIAGNOSTIC	1732-1	608/9	MAG TAPE, UNIT 3	AAM17800
X732H4	ADC	P732H4	xxx	DIAGNOSTIC	1732-1	608/9	MAG TAPE, UNIT 4	AAM17900
X732H5	ADC	P732H5	xxx	DIAGNOSTIC	1732-1	608/9	MAG TAPE, UNIT 5	AAM18000
X732H6	ADC	P732H6	xxx	DIAGNOSTIC	1732-1	608/9	MAG TAPE, UNIT 6	AAM18100
X732H7	ADC	P732H7	xxx	DIAGNOSTIC	1732-1	608/9	MAG TAPE, UNIT 7	AAM18200
X731U1	ADC	P731U1	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 1	AAM18300
X731U2	ADC	P731U2	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 2	AAM18400
X731U3	ADC	P731U3	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 3	AAM18500
X731U4	ADC	P731U4	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 4	AAM18600
X731U5	ADC	P731U5	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 5	AAM18700
X731U6	ADC	P731U6	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 6	AAM18800
X731U7	ADC	P731U7	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 7	AAM18900
X731R0	ADC	P731R0	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 0	AAM19000
X731R1	ADC	P731R1	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 1	AAM19100
X731R2	ADC	P731R2	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 2	AAM19200
X731R3	ADC	P731R3	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 3	AAM19300
X731R4	ADC	P731R4	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 4	AAM19400
X731R5	ADC	P731R5	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 5	AAM19500
X731R6	ADC	P731R6	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 6	AAM19600
X731R7	ADC	P731R7	xxx	DIAGNOSTIC	1731-601		MAG TAPE, UNIT 7	AAM19700
X17380	ADC	P17380	xxx	DIAGNOSTIC	1738	853/4	DISK, UNIT 0	AAM19800
X17381	ADC	P17381	xxx	DIAGNOSTIC	1738	853/4	DISK, UNIT 1	AAM19900
X42312	ADC	P42312	xxx	DIAGNOSTIC	1742-30/120		LINE PRINTER	AAM20000
X40421	ADC	P40421	xxx	DIAGNOSTIC	1740-501/1742		LINE PRINTER	AAM20100
X17293	ADC	P17293	xxx	DIAGNOSTIC	1729-3		CARD READER	AAM20200
X1728	ADC	P1728	xxx	DIAGNOSTIC	1728-430		READER / PUNCH	AAM20300
X17292	ADC	P17292	xxx	DIAGNOSTIC	1729-2		CARD READER	AAM20400
X1726U	ADC	P1726U	xxx	DIAGNOSTIC	UNRUFFERED	1726	CARD READER	AAM20500
X1726B	ADC	P1726B	xxx	DIAGNOSTIC	RUFFERED	1726	CARD READER	AAM20600
X1713R	ADC	P1713R	xxx	DIAGNOSTIC	1713		TTY READER	AAM20700
X1777R	ADC	P1777R	xxx	DIAGNOSTIC	1777/1721		PAPER TAPE READER	AAM20800
X1713P	ADC	P1713P	xxx	DIAGNOSTIC	1713		TTY PUNCH	AAM20900
X1777P	ADC	P1777P	xxx	DIAGNOSTIC	1777/1723		PAPER TAPE PUNCH	AAM21000
	ADC	P DUMMY	xxx	DUMMY LOGICAL			UNIT	AAM21100
NUMLU	EQL	NUMLU(*-LOG1A-1)						AAM21200
	EJT							AAM21300

Figure 1-13. LOG1A Logical Unit Table, Section AAM (Continued)

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LOGICAL UNIT TABLES (LOGIA)

ENT	LOGIA	PHYSICAL DEVICES ADDRESSES BY LOGICAL UNIT	
ENT	NUMLU		
SPC	1		
LOGIA	ADC	NUMLU	NUMBER OF LOGICAL UNITS
	ADC	PCORE	1 CORE ALLOCATOR
	ADC	PDUMMY	2 DUMMY LOGICAL UNIT
	ADC	PDUMMY	3 DUMMY LOGICAL UNIT
	ADC	P1711	4 1711 TELETYPE, 713-10 CRT
	ADC	PDUMMY	5 DUMMY LOGICAL UNIT
	ADC	P731U0	6 UNBUFFERED 1731-601 MAG TAPE, UNIT 0
	ADC	PSUD00	7 PSEUDO TAPE, UNIT 0
	ADC	P17380	8 1738 853/4 DISK, UNIT 0
	ADC	P40421	9 1740-501/1742 LINE PRINTER
	ADC	P1728	10 1728-430 CARD READER
	ADC	P1728	11 1728-430 CARD PUNCH
FTN740	ADC	P40421	12 1740-501/1742 FORTRAN LINE PRINTER
	ADC	P731U1	13 UNBUFFERED 1731-601 MAG TAPE, UNIT 1
	ADC	P1777R	14 1777/1721 PAPER TAPE READER
	ADC	P1777P	15 1777/1723 PAPER TAPE PUNCH
PTRDNP	ADC	P1777R	16 1777/1721 PAPER TAPE READER - NO PARITY
	ADC	PCOM00	17 364-4 COMMUNICATIONS MUX, UNIT 0
	ADC	PCOM01	18 364-4 COMMUNICATIONS MUX, UNIT 1
X731U0	ADC	P731U0	33 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 0
X731U1	ADC	P731U1	34 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 1
X17380	ADC	P17380	35 DIAGNOSTIC 1738 853/4 DISK, UNIT 0
X40421	ADC	P40421	36 DIAGNOSTIC 1740-501/1742 LINE PRINTER
X1728	ADC	P1728	37 DIAGNOSTIC 1728-430 READER / PUNCH
X1777R	ADC	P1777R	38 DIAGNOSTIC 1777/1721 PAPER TAPE READER
X1777P	ADC	P1777P	39 DIAGNOSTIC 1777/1723 PAPER TAPE PUNCH
X1711	ADC	P1711	40 DIAGNOSTIC 1711 TELETYPE, 713-10 CRT
NUMLU	EQL	NUMLU(*-LOGIA-1)	
	EJT		

Figure 1-14. Sample SYSDAT LOGIA Table, Section AAM

1.14 LOG1 Logical Unit Table, Section AAN

The first word of the LOG1 table contains the number of logical units in the system. Following this there must be one entry for each logical unit in the system. These entries must correspond in order to the entries in the LOG1A table.

Bit 14 of a given entry indicates whether or not this logical unit shares a physical device table with another logical unit. If bit 14 is one, the logical unit does share a physical device table. An example of a SYSDAT LOG1 table, is shown in Figure 1-15. This example corresponds to the LOG1A table shown in Figure 1-14. In this figure all logical units share physical device tables except logical units 1, 5, 7, 8, 13, 14, 15, and 17. The particular physical device table shared by a set of logical units may be identified by referring to Figure 1-14. For example, logical units 2 and 3 share the physical device table located at PDUMMY; logical units 9, 12 and 21 share the physical device table located at P42312.

Bit 13 of a LOG1 entry is always zero in an original SYSDAT. If, during system operation, the corresponding hardware device driver detects an irrecoverable error, bit 13 will be set to one. At that time, if an alternate device exists, it will be used to process all requests for this logical unit until MSOS is advised that the primary unit has been restored. Bit 13 is not to be modified by the user.

Bits 0-11 of a LOG1 entry give the logical unit number of the alternate device if one exists for this logical unit. If bits 0-11 are zero, there is no alternate device. In the

example in Figure 1-15, there is only one logical unit which has an alternate device. That is logical unit 4, the standard comment input/output device which has logical unit 2, the dummy logical unit, as an alternate. This is a standard system procedure so that a failure on the system comment device will not hang the system in an endless sequence of attempts to print an error message on the comment device.

*
*
*
*
*
*
S

LOGICAL UNIT TABLES (LOG1)

ENI	LOG1	LOGICAL UNIT INFORMATION BY LOGICAL UNIT	
		BIT 14 = 1. IMPLIES LU SHARES DEVICE	
		BIT 13 = 1. IMPLIES LU IS MARKED DOWN	
		BITS 0 - 11 IS ALTERNATE LOGICAL UNIT	
		ALTERNATE = 0. IMPLIES NONE	
	EQL	S(4000)	SHARED BIT
	SPC	1	
LOG1	ADC	NUMLU	NUMBER OF LOGICAL UNITS
	ADC	0	1 CORE ALLOCATOR
	ADC	0+S	2 DUMMY LOGICAL UNIT
	ADC	0+S	3 DUMMY LOGICAL UNIT
	ADC	2+S	4 1711 TELETYPE, 713-10 CRT
	ADC	0+S	5 DUMMY LOGICAL UNIT
	ADC	0+S	6 UNBUFFERED 1731-601 MAG TAPE, UNIT 0
	ADC	0	7 PSEUDO TAPE, UNIT 0
	ADC	0+S	8 1738 853/4 DISK, UNIT 0
	ADC	0+S	9 1740-501/1742 LINE PRINTER
	ADC	0+S	10 1728-430 CARD READER
	ADC	0+S	11 1728-430 CARD PUNCH
	ADC	0+S	12 1740-501/1742 FORTRAN LINE PRINTER
	ADC	0+S	13 UNBUFFERED 1731-601 MAG TAPE, UNIT 1
	ADC	0+S	14 1777/1721 PAPER TAPE READER
	ADC	0+S	15 1777/1723 PAPER TAPE PUNCH
	ADC	0+S	16 1777/1721 PAPER TAPE READER - NO PARITY
	ADC	0	17 364-4 COMMUNICATIONS MUX, UNIT 0
	ADC	0	18 364-4 COMMUNICATIONS MUX, UNIT 1
	ADC	0+S	33 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 0
	ADC	0+S	34 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 1
	ADC	0+S	35 DIAGNOSTIC 1738 853/4 DISK, UNIT 0
	ADC	0+S	36 DIAGNOSTIC 1740-501/1742 LINE PRINTER
	ADC	0+S	37 DIAGNOSTIC 1728-430 READER / PUNCH
	ADC	0+S	38 DIAGNOSTIC 1777/1721 PAPER TAPE READER
	ADC	0+S	39 DIAGNOSTIC 1777/1723 PAPER TAPE PUNCH
	ADC	0+S	40 DIAGNOSTIC 1711 TELETYPE, 713-10 CRT

Figure 1-15. Sample SYSDAT LOG1 Table, Section AAN

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1.15 LOG2 Logical Unit Table, Section AA0

The first word of the LOG2 table contains the number of logical units in the system. Following the first word there must be one entry for each logical unit in the system. These entries are in the same order as in the LOG1A table and the LOG1 table. Each entry is a pointer to the thread word of the next request to be processed for this logical unit. Initially, each entry is FFFF_{1b} signifying there are no requests threaded for this logical unit. An example of a SYSDAT LOG2 table is shown in Figure 1-1b. The logical units correspond to those in the LOG1A Table in Figure 1-14.

LOGICAL UNIT TABLES (LOG2)

*
*
*

ENT SPC		LOG2	TOP OF I/O THREAD ADDRESSES BY LOGICAL UNIT	
LOG2		ADC	NUMLU	NUMBER OF LOGICAL UNITS
		NUM	\$FFFF	1 CORE ALLOCATOR
		NUM	\$FFFF	2 DUMMY LOGICAL UNIT
		NUM	\$FFFF	3 DUMMY LOGICAL UNIT
		NUM	\$FFFF	4 1711 TELETYPE, 713-10 CRT
		NUM	\$FFFF	5 DUMMY LOGICAL UNIT
		NUM	\$FFFF	6 UNBUFFERED 1731-601 MAG TAPE, UNIT 0
		NUM	\$FFFF	7 PSEUDO TAPE, UNIT 0
		NUM	\$FFFF	8 1738 853/4 DISK, UNIT 0
		NUM	\$FFFF	9 1740-501/1742 LINE PRINTER
		NUM	\$FFFF	10 1728-430 CARD READER
		NUM	\$FFFF	11 1728-430 CARD PUNCH
		NUM	\$FFFF	12 1740-501/1742 FORTRAN LINE PRINTER
		NUM	\$FFFF	13 UNBUFFERED 1731-601 MAG TAPE, UNIT 1
		NUM	\$FFFF	14 1777/1721 PAPER TAPE READER
		NUM	\$FFFF	15 1777/1723 PAPER TAPE PUNCH
		NUM	\$FFFF	16 1777/1721 PAPER TAPE READER - NO PARITY
		NUM	\$FFFF	17 364-4 COMMUNICATIONS MUX, UNIT 0
		NUM	\$FFFF	18 364-4 COMMUNICATIONS MUX, UNIT 1
		NUM	\$FFFF	33 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 0
		NUM	\$FFFF	34 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 1
		NUM	\$FFFF	35 DIAGNOSTIC 1738 853/4 DISK, UNIT 0
		NUM	\$FFFF	36 DIAGNOSTIC 1740-501/1742 LINE PRINTER
		NUM	\$FFFF	37 DIAGNOSTIC 1728-430 READER / PUNCH
		NUM	\$FFFF	38 DIAGNOSTIC 1777/1721 PAPER TAPE READER
		NUM	\$FFFF	39 DIAGNOSTIC 1777/1723 PAPER TAPE PUNCH
		NUM	\$FFFF	40 DIAGNOSTIC 1711 TELETYPE, 713-10 CRT

Figure 1-1b. Sample SYSDAT LOG2 Table, Section AA0.

1.1b Diagnostic Tables, Section AAP

The first part of the diagnostic tables is the alternate device error table. This table is used by the system as a temporary storage area in which a driver of a failed device passes information to the alternate device handler. The number of entries in the table is contained in the first word of the table. In a standard system the number of entries is the number of logical units in the system. The number of entries must be at least as large as the maximum number of logical units which could fail simultaneously. If the user can ascertain that this maximum is smaller than the number of logical units in the system, the number of entries in the table may be reduced. For example, in a standard system the number of entries may be reduced by one for logical unit one, the core allocator, and reduced by two more for logical units two and three, dummy logical units. Neither the core allocator nor a dummy logical unit will call the alternate device handler. Care must be taken in reducing the size of the table in that if the table is insufficient, the alternate device handler halts in a $18FF_{1b}$ instruction, thus terminating system operation.

The second part of the diagnostic tables is the diagnostic timer table. This table must contain one entry for each logical unit for which the diagnostic timer is to test for a timeout condition. An entry in the diagnostic timer table is the address of the physical device table for the logical unit. The diagnostic timer program will examine each physical device table whose address is contained in the diagnostic timer table. If the diagnostic clock word of the physical device table is counted down to zero, then the diagnostic timer causes the driver to be scheduled through its error entry. For a hardware device, this is an indication of

a lost interrupt. For the core allocator, a count down to zero indicates a swap of unprotected core to mass memory may be performed at this time if additional allocatable core is needed. The frequency of swaps allowed in a system is determined by the user in the MSOS Ordering Form.

An example of SYSDAT diagnostic tables appears in Figure 1-17.

*
*
*

D I A G N O S T I C T A B L E S

```

ENT  ALTERR  ALTERNATE DEVICE ERROR TABLE
SPC  1
ALTErr  ADC  NUMLU          ERROR TABLE SIZE
      BZS  (NUMLU)        SPACE FOR MAXIMUM SIMULTANEOUS FAILURES
      SPC  3
ENT  DGNTAB  DIAGNOSTIC TIMER TABLE
SPC  1
DGNTAB  EQL  DGNTAB(*)    START OF TABLE
      ADC  PCORE          1    CORE ALLOCATOR
      ADC  P1711          4    1711 TELETYPE, 713-10 CRT
      ADC  P73100         6    UNBUFFERED 1731-601 MAG TAPE, UNIT 0
      ADC  P17380         8    1738 853/4 DISK, UNIT 0
      ADC  P46421         9    1740-501/1742 LINE PRINTER
      ADC  P1728          10   1728-430 CARD READER
      ADC  P73101        13   UNBUFFERED 1731-601 MAG TAPE, UNIT 1
      ADC  P1777R        14   1777/1721 PAPER TAPE READER
      ADC  P1777F        15   1777/1723 PAPER TAPE PUNCH
      ADC  PCOM00        17   364-4 COMMUNICATIONS MUX, UNIT 0
      ADC  PCOM01        18   364-4 COMMUNICATIONS MUX, UNIT 1
      NUM  $FFFF        END OF TABLE

```

Figure 1-17. Sample SYSDAT Diagnostic Tables, Section AAP

1.17 Standard Logical Units, Section AA0

This section defines the standard logical units for the system. Equivalences in this section are referenced in the communication region, Section AAE. The standard equivalences in the data base are shown in Figure 1-1B. These equivalences may be changed if the user desires. Note that the scratch unit is equivalenced to zero in the data base. The user specifies the scratch unit prior to system installation so that in a SYSDAT listing the scratch unit is equivalenced to an actual logical unit instead of to zero.

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* * * * *				
S T A N D A R D L O G I C A L U N I T S				
* * * * *				
ENT DUMALT				
* * * * *				
DUMALT	EQL	DUMALT(2)	STANDARD DUMMY ALTERNATE	AAQ00100
INPCOM	EQL	INPCOM(4)	STANDARD INPUT COMMENT	AAQ00200
OUTCOM	EQL	OUTCOM(4)	STANDARD OUTPUT COMMENT	AAQ00300
LBUNIT	EQL	LBUNIT(8)	STANDARD LIBRARY UNIT	AAQ00400
SCRATCH	EQL	SCRATCH(1)	STANDARD SCRATCH UNIT	AAQ00500
LSTOUT	EQL	LSTOUT(9)	STANDARD LIST OUTPUT	AAQ00600
STDINP	EQL	STDINP(10)	STANDARD INPUT	AAQ00700
BINOUT	EQL	BINOUT(11)	STANDARD BINARY OUTPUT	AAQ00800
EJT				AAQ00900
				AAQ01000
				AAQ01100
				AAQ01200
				AAQ01300

Figure 1-18. Standard Logical Units, Section AAQ

1.18 Line One Table, Section AAR

The line one data base cards are shown in Figure 1-19. A given configuration may require only one hardware device to be connected to the line one {flow speed input/output} interrupt, or it may require several hardware devices to use line one. If only one hardware device uses line one, the first part of the line one table from the data base, cards *AAR0050 through *AAR0100 will be included in SYSDAT together with the physical device table address of the line one device. These cards will then comprise all of the line one interrupt handler. If more than one device uses line one, cards *AAR0050 through *AAR0100 are omitted from SYSDAT, but a list of the system line one devices is included beginning at LNITV4. In this case, the program with entry point LINIV4 must be included in the system outside the SYSDAT area. This is a standard MSOS routine which utilizes the list of physical device table addresses beginning at LNITV4.

LINE ONE TABLE			
	SPC	1	AAR00100
			AAR00200
			AAR00300
	ENT	LINIV4	AAR00400
	SPC	1	AAR00500
		LINE 1 INTERRUPT ENTRY	AAR00600
LINIV4	LOG#	LNITV4	AAR00700
	LDA-	2,0	AAR00800
	STA-	I	AAR00900
	JMF-	(I)	AAR01000
	SPC	3	AAR01100
	ENT	INVINT	AAR01200
	SPC	1	AAR01300
INVINT	JMF-	(ADISP)	AAR01400
	ENT	LNITV4	AAR01500
	EXT	LINIV4	AAR01600
	EXT	INVINT	AAR01700
			AAR01800
	SPC	4	AAR01900
	EQL	LNITV4(*)	AAR02000
	ADC	P1711	AAR02100
		1711 TELETYPE, 713-10 CRT	AAR02200
	ADC	P1713K	AAR02300
		1713 TELETYPE	AAR02400
	ADC	P1777R	AAR02500
		1777 PAPER TAPE READER	AAR02600
	ADC	P1777P	
		1777 PAPER TAPE PUNCH	
	NUM	\$FFFF	
	EJT		
		END OF TABLE	

Figure 1-19. Line One Table, Section AAR

1.19 Physical Device Tables, Mandatory Entries, Section AAS

Section AAS defines the basic structure of a physical device table. Words 0-15 of a physical device table must conform to the definitions in this section. Care must be taken in altering any of the first 16 words of a physical device table. Even if one of these words is not used by the driver, it may be used by the operating system in determining the logical flow of a program. The mandatory entries are shown in Figure 1-20. In a given physical device table additional words may be added for use by the output message buffering package and for special use by drivers.

Word 5 of the mandatory entries is the logical unit currently assigned to this device. Word 6 is the current request parameter list location. Word 7 tells the driver which device to use. This word contains the following information:

<u>Bits</u>	<u>Code</u>
0-6	Station
7-10	Equipment
11-15	Converter

By loading @ with word 7, and performing an input, the equipment status is obtained. The bits of word 8, request status are defined as follows:

<u>Bits</u>	
15	1 if operation is in progress 0 if operation is complete
14	1 if driver detects I/O hardware failure
11-13	Equipment class
	<u>Code</u> <u>Device</u>
	0 Class undefined
	1 Magnetic tape
	2 Mass storage
	3 Card

Bits

Code

Device

4 Paper tape
5 Printer
6 Teletypewriter
7 Reserved for future use

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Equipment type code

Type

Code

Device

0 1711
1 1721/1722
2 1723/1724
3 1752
4 713-10/711-100/713-120
5 1738/853
6 1751
7 1739-1

8 1738/854
9 1731/601
10 Software Buffer
11 COSY Driver
12 1728/430
13 Core Allocator
14 1733-1/854
15 1733-2/856-2
16 1733-2/856-4
17 1742-30
18 1742-120
19 1740/501
20 1732-2/615-73

<u>Code</u>	<u>Device</u>
21	1732-2/615-93
22	1732-1/1706/608
23	1726/405
24	1732-1/608
25	1732-1/609
26	1713 Keyboard
27	1713 Punch
28	1713 Reader
29	1729-2
30	1732-1/1706/609
31	Software Dummy
32	364-4/361-1
33	364-4/361-4
34	1742-1
35	1777 Reader
36	Pseudo Tape
37	1777 Punch
38	1729-3
39	1733-1/853
40	1731/1706/601
41	1726/1706/405
42	1747
43	1744/274
44	1536
45	1501
46-99	For later use
100-127	For user assignment

Bits

3	Not used {Reserved}
2	=1 if device may be written by un-protected programs
1	=1 if device may be read by unpro- tected programs
0	=1 if device not available to unpro- tected programs

The bits of word 9, driver status, are used as follows:

Bits

15	=1 if error condition and/or end-of-file detected	}	Driver
14	=1 if fewer words read than requested		
13	=1 if device remains ready after detecting an error or end-of-file or both		
6-12	Reserved for special use by individual drivers		
5	0 if this is a control character	}	Driver
4	0 if this is first character		
3	1 if ASCII; 0 if binary mode		
2	1 if lower case character; 0 if upper case		
1	1 if format READ or WRITE; 0 if unformatted	}	FNR
0	1 if WRITE request; 0 if READ request		

Word 10 is the location where the driver will next store or obtain data. Word 11 is one more than the location where the driver is to store or obtain data to satisfy a request. Equipment status is stored in word 12. Word 13 contains the driver length in words if the driver is mass memory resident. Word 13 is zero for an optionally core or mass resident driver which is core resident in a given system. In an actual SYSDAT, word 14 will contain an address constant obtained from an external value. This value is the

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sector number of the start of the driver. For a core resident driver, the external will be unpatched, and thus word 14 will contain $7FFF_{16}$. Values for words 13 and 14 are inserted by the system initializer by means of *S statements. Word 15 is used for storing a return address by the programs NFNR, MAKQ, and NCMPRG.

PHYSICAL DEVICE TABLES

THE FOLLOWING SECTION CONTAINS THE PHYSICAL DEVICE TABLES AND INTERRUPT RESPONSE ROUTINES FOR EACH LOGICAL UNIT IN THE SYSTEM.

LISTED BELOW ARE THE MANDATORY ENTRIES FOR ALL PHYSICAL DEVICE TABLES. ADDITIONAL ENTRIES REQUIRED BY EACH DRIVER MAY BE ADDED AFTER THE LAST ENTRY INDICATED.

SPC	3			AAS00100
PHYSTH	EQL	PHYSTB(*)		AAS00200
	EQL	ELVL(0)	00 SCHEDULER CALL WITH DRIVER LEVEL	AAS00300
	EQL	EDIN(1)	01 DRIVER INITIATOR ENTRY	AAS00400
	EQL	EDCN(2)	02 DRIVER CONTINUATOR ENTRY	AAS00500
	EQL	EDPGM(3)	03 DRIVER DIAGNOSTIC ENTRY	AAS00600
	FQL	EDCLK(4)	04 DIAGNOSTIC CLOCK	AAS00700
	EQL	ELU(5)	05 LOGICAL UNIT	AAS00800
	EQL	EPTR(6)	06 PARAMETER LOCATION	AAS00900
	EQL	EWES(7)	07 CONVERTOR, EQUIPMENT, STATION	AAS01000
	EQL	ERFQST(8)	08 REQUEST STATUS	AAS01100
	EQL	ESTAT1(9)	09 DRIVER STATUS	AAS01200
	EQL	ECCOR(10)	10 CURRENT LOCATION	AAS01300
	EQL	ELSTWD(11)	11 LAST LOCATION PLUS ONE	AAS01400
	EQL	FSTAT2(12)	12 DEVICE STATUS	AAS01500
	EQL	MASLGN(13)	13 DRIVER LENGTH (IF MASS MEMORY)	AAS01600
	EQL	MASSEC(14)	14 NAME ASSOCIATED WITH SECTOR NUMBER	AAS01700
	EQL	RETURN(15)	15 RESERVED FOR FNR AND CMR	AAS01800
	EJT			AAS01900
				AAS02000
				AAS02100
				AAS02200
				AAS02300
				AAS02400
				AAS02500
				AAS02600
				AAS02700
				AAS02800
				AAS02900

Figure 1-20. Physical Device Tables, Mandatory Entries, Section AAS

1.20 Core Allocator Physical Device Table, Section AAT

The entries in the core allocator physical device table Figure 1-21, are all fixed except word 13. Word 13 is the minimum number of seconds allowed between swaps. If word 13 is negative, swaps are allowed as often as needed by the system.

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CORE ALLOCATOR

AAT00100
AAT00200
AAT00300
AAT00400
AAT00500
AAT00600
AAT00700
AAT00800
AAT00900
AAT01000
AAT01100
AAT01200
AAT01300
AAT01400
AAT01500
AAT01600
AAT01700
AAT01800
AAT01900
AAT02000
AAT02100
AAT02200
AAT02300
AAT02400

SPC	1				
ENT	PCORE				
EXT	ICORE.FCORE				
EQL	SWAPT()	SWAP TIME			
SPC	1				
PCORE	ADC \$5207	00	SCHEDULER CALL		
	ADC ICORE	01	INITIATOR ADDRESS		
	ADC 0	02	CONTINUATOR ADDRESS - NOT USED		
	ADC FCORE	03	TIMEOUT ERRCR ADDRESS		
	NUM -1	04	DIAGNOSTIC CLOCK		
	NUM 0	05	LOGICAL UNIT		
	NUM 0	06	PARAMETER LOCATION		
	NUM 0	07	CONVERTER, EQUIPMENT, STATION - NONE		
	NUM \$00D6	08	REQUEST STATUS		
	NUM 0	09	DRIVER STATUS		
	NUM 0	10	CURRENT LOCATION		
	NUM 0	11	LAST LOCATION PLUS ONE		
	NUM 0	12	DEVICE STATUS		
	VFL X16/SWAPT-1	13	TIME BETWEEN SWAPS (NONE IF NEGATIVE)		
	NUM \$7FFF	14	RESERVED		
	NUM 0	15	RESERVED FOR FNR AND CMR		
	EJT				

Figure 1-21. Core Allocator Physical Device Table, Section AAT

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1.21 Dummy Physical Device Table, Section AAU

Section AAU, the physical device table for the dummy logical unit, has a fixed structure with no options. This section appears in Figure 1-22.

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D U M M Y L O G I C A L U N I T

AAU00100
AAU00200
AAU00300
AAU00400
AAU00500
AAU00600
AAU00700
AAU00800
AAU00900
AAU01000
AAU01100
AAU01200
AAU01300
AAU01400
AAU01500
AAU01600
AAU01700
AAU01800
AAU01900
AAU02000
AAU02100
AAU02200

SPC 1
EXT IDUMMY,CDUMMY,EDUMMY
SPC i
PDUMMY ADC \$520A 00 SCHEDULER CALL
ADC IDUMMY 01 INITIATOR ADDRESS
ADC CDUMMY 02 CONTINUATOR ADDRESS
ADC EDUMMY 03 TIMEOUT ERROR ADDRESS
NUM -1 04 DIAGNOSTIC CLOCK - NOT USED
NUM 0 05 LOGICAL UNIT
NUM 0 06 PARAMETER LOCATION
NUM 0 07 CONVERTER, EQUIPMENT, STATION - NONE
NUM \$01F6 08 REQUEST STATUS
NUM \$8000 09 DRIVER STATUS
NUM 0 10 CURRENT LOCATION
NUM 0 11 LAST LOCATION PLUS ONE
NUM 0 12 DEVICE STATUS
NUM 0 13 RESERVED
NUM \$7FFF 14 RESERVED
NUM 0 15 RESERVED FOR FNR AND CMR
EJT

Figure 1-22. Dummy Physical Device Table, Section AAU.

1.22 1711 Teletype / 713-10 CRT Physical Device Table,
Section AAV

The entries of this section, Figure 1-23, are fixed except for the type code of the device, bits 4-10 of word 8. The type code specifies whether a 1711 teletype or a 713-10 CRT is to be used. {A given system cannot contain both a 1711 and a 713-10 due to hardware requirements.} Word 18 contains an index into the LOG1A table if there is a corresponding diagnostic logical unit. This index is defined in card *AAV0080.

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1 7 1 1 T E L E T Y P E , 7 1 3 - 1 0 C R T

SPC	1			AAV00100
EXT	I1711,C1711,E1711			AAV00200
EQL	T713(04*\$10)	TYPE CODE - 713-10 CRT		AAV00300
EQL	T1711(00*\$10)	TYPE CODE - 1711 TELETYPE		AAV00400
EQL	U1711(0)			AAV00500
EQL	U1711(X1711-LOG1A)			AAV00600
SPC	1			AAV00700
P1711 ADC	\$520E	00	SCHEDULER CALL	AAV00800
ADC	I1711	01	INITIATOR ADDRESS	AAV00900
ADC	C1711	02	CONTINUATOR ADDRESS	AAV01000
ADC	E1711	03	TIMEOUT ERROR ADDRESS	AAV01100
NUM	-1	04	DIAGNOSTIC CLOCK	AAV01200
NUM	0	05	LOGICAL UNIT	AAV01300
NUM	0	06	PARAMETER LOCATION	AAV01400
NUM	\$0091	07	CONVERTER, EQUIPMENT, STATION	AAV01500
ADC	\$3006+T1711	08	REQUEST STATUS	AAV01600
ADC	\$3006+T713	08	REQUEST STATUS	AAV01700
NUM	0	09	DRIVER STATUS	AAV01800
NUM	0	10	CURRENT LOCATION	AAV01900
NUM	0	11	LAST LOCATION PLUS ONE	AAV02000
NUM	0	12	DEVICE STATUS	AAV02100
NUM	0	13	ERROR CODE AND STARTING LOCATION	AAV02200
NUM	\$7FFF	14	RESERVED	AAV02300
NUM	0	15	RESERVED FOR FNR AND CMR	AAV02400
NUM	0	16	DRIVER FLAGS	AAV02500
NUM	1	17	HARDWARE PARITY CHECK FLAG	AAV02600
ADC	(U1711)	18	DIAGNOSTIC LU	AAV02700
EJT				AAV02800
				AAV02900
				AAV03000

Figure 1-23. 1711 Teletype, 713-10 CRT Physical Device Table, Section AAV

1.23 Interrupt Response Routine for the 1713 Teletype with
Paper Tape Reader/Punch, Section AAW

This interrupt response routine determines whether an interrupt from the 1713 Teletype is due to an operation on the keyboard, the paper tape reader, or the paper tape punch. Control is then transferred to the proper driver continuator entry. {Refer to Figure 1-24.}

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```
*      1 7 1 3  T E L E T Y P E  W I T H  R E A D E R / P U N C H A A W 0 0 1 0 0
*      A A W 0 0 2 0 0
*      C O M M O N  I N T E R R U P T  R E S P O N S E      A A W 0 0 3 0 0
*      A A W 0 0 4 0 0
ENT  S13BZY,S13MOD      A A W 0 0 5 0 0
SPC  ?                  A A W 0 0 6 0 0
S13BZY NUM 0            C U R R E N T L Y  A C T I V E  P D T  A D D R E S S      A A W 0 0 7 0 0
S13MOD NUM 0            C O M M O N  E O P  I N D I C A T O R      A A W 0 0 8 0 0
SPC  ?                  A A W 0 0 9 0 0
CN1713 LDG* S13BZY      I S  A N Y  U N I T  C U R R E N T L Y  A C T I V E      A A W 0 1 0 0 0
SQN  RSY13             Y E S      A A W 0 1 1 0 0
K1713C LDG =XP1713K     N O ,  G O  T O  T H E  K E Y B O A R D  C O N T I N U A T O R      A A W 0 1 2 0 0
JMP+ C1713K           A A W 0 1 3 0 0
BSY13 TRG A            A A W 0 1 4 0 0
EOH =XP1713K         I S  T H I S  A N  I N T E R R U P T  F O R  T H E  K E Y B O A R D      A A W 0 1 5 0 0
SAN  RSY13A          N O      A A W 0 1 6 0 0
JMP* K1713C          Y E S ,  G O  T O  T H E  K E Y B O A R D  C O N T I N U A T O R      A A W 0 1 7 0 0
SPC  1              A A W 0 1 8 0 0
BSY13A LDA- 2,0       A A W 0 1 9 0 0
STA-  I              A A W 0 2 0 0 0
JMP- (I)             G O  T O  T H E  S E L E C T E D  U N I T # S  C O N T I N U A T O R      A A W 0 2 1 0 0
EJT                  A A W 0 2 2 0 0
```

Figure 1-24. Interrupt Response Routine for 1713 Teletype with Paper Tape Reader/Punch, Section AAW

1.24 1713 Teletype Keyboard Physical Device Table, Section AAX

In this physical device table, word 19 contains an index into the LOG1A table if there is a corresponding diagnostic logical unit. The remaining entries are fixed. This section is shown in Figure 1-25.

1 7 1 3 T E L E T Y P E K E Y B O A R D

	EXT	T1713K.C)1713K.E)1713K		AA00100
	EQL	U1713K(0)		AA00200
	EQL	U1713K(X1713K-LCG1A)		AA00300
	SPC	1		AA00400
P1713K	ADC	\$520E	00 SCHEDULER CALI	AA00500
	ADC	T1713K	01 INITIATOR ADDRESS	AA00600
	ADC	CN1713	02 CONTINUATOR ADDRESS	AA00700
	ADC	E1713K	03 TIMEOUT ERROR ADDRESS	AA00800
	NUM	-1	04 DIAGNOSTIC CLOCK	AA00900
	NUM	0	05 LOGICAL UNIT	AA01000
	NUM	0	06 PARAMETER LOCATION	AA01100
	NUM	\$0091	07 CONVERTER, EQUIPMENT, STATION	AA01200
	NUM	\$31A6	08 REQUEST STATUS	AA01300
	NUM	0	09 DRIVER STATUS	AA01400
	NUM	0	10 CURRENT LOCATION	AA01500
	NUM	0	11 LAST LOCATION PLUS ONE	AA01600
	NUM	0	12 DEVICE STATUS	AA01700
	NUM	0	13 ERROR CODE	AA01800
	NUM	\$7FFF	14 RESERVED	AA01900
	NUM	0	15 RESERVED FOR FNR AND CMR	AA02000
	NUM	0	16 RETURN ADDRESS	AA02100
	NUM	0	17 SPARE	AA02200
	NUM	0	18 SPARE	AA02300
	ADC	U1713K	19 DIAGNOSTIC LU	AA02400
	EJT			AA02500
				AA02600
				AA02700

Figure 1-25. 1713 Teletype Keyboard Physical Device Table, Section AAX

1.25 1713 Teletype Paper Tape Reader Physical Device Table, Section AAY

The driver for the 1713 teletype paper tape reader may be either core resident or mass memory resident. This physical device table, Figure 1-26, has the same general form as all physical device tables for which the driver may be either core resident or mass memory resident. The residency option is indicated by the parameter M1713R. This parameter is zero for a core resident driver, one for a mass resident driver. If the driver is core resident, the driver initiator, continuator, and error entries in the physical device table are linked directly to the driver entries. If the driver is mass memory resident the entry points in the physical device table will transfer control to the locations MASDRV, MASCON, and MASERR. These are entries in the mass memory driver executive program, MMEXEC. The cards *AAY0160 - *AAY0210 are therefore assembled into a SYSDAT with a mass resident driver for the 1713 teletype reader.

Word 19 contains an index into the LOG1A table if there is a corresponding diagnostic logical unit. This index is defined in card *AAY0300.

		1 7 1 3 T E L E T Y P E R E A D E R		AAY00100
				AAY00200
	SPC	1		AAY00300
	EQL	M1713R(0)		AAY00400
	EQL	M1713R(1)		AAY00500
				AAY00600
	JFA	M1713R.EQ.0	CORE RESIDENT DRIVER	AAY00700
	EXT	I1713R		AAY00800
	EXT	C1713R		AAY00900
	EXT	F1713R		AAY01000
	EQL	I1713R(0)		AAY01100
	EQL	S1713R(\$7FFF)		AAY01200
	EIF			AAY01300
				AAY01400
	JFA	M1713R.EQ.1	MASS RESIDENT DRIVER	AAY01500
	EXT	MASDRV		AAY01600
	EXT	MASCON		AAY01700
	EXT	MASERR		AAY01800
I1713R	JMF+	MASDRV	INITIATE DRIVER	AAY01900
C1713R	JMF+	MASCON	INTERRUPT RESPONSE	AAY02000
E1713R	JMF+	MASERR	TIMEOUT ERROR	AAY02100
	EXT	I1713R		AAY02200
	EXT	S1713R		AAY02300
	EIF			AAY02400
				AAY02500
	EJT			AAY02600
				AAY02700
		1 7 1 3 T E L E T Y P E R E A D E R		AAY02800
				AAY02900
	EQL	U1713R(0)		AAY03000
	EQL	U1713R(X1713R-LOG1A)		AAY03100
	SPC	1		AAY03200
P1713R	ADC	\$520E	00 SCHEDULER CALL	AAY03300
	ADC	I1713R	01 INITIATOR ADDRESS	AAY03400
	ADC	C1713R	02 CONTINUATOR ADDRESS	AAY03500
	ADC	F1713R	03 TIMEOUT ERROR ADDRESS	AAY03600
	NUM	-1	04 DIAGNOSTIC CLOCK	AAY03700
	NUM	0	05 LOGICAL UNIT	AAY03800
	NUM	0	06 PARAMETER LOCATION	AAY03900
	NUM	\$0091	07 CONVERTER. EQUIPMENT. STATION	AAY04000
	NUM	\$21C2	08 REQUEST STATUS	AAY04100
	NUM	0	09 DRIVER STATUS	AAY04200
	NUM	0	10 CURRENT LOCATION	AAY04300
	NUM	0	11 LAST LOCATION PLUS ONE	AAY04400
	NUM	0	12 DEVICE STATUS	AAY04500
	ADC	L1713R	13 DRIVER LENGTH IF MASS MEMORY	AAY04600
	ADC	S1713R	14 NAME ASSOCIATED WITH SECTOR NUMBER	AAY04700
	NUM	0	15 CHECKSUM AND ERROR CODE	AAY04800
	NUM	0	16 TEMPORARY	AAY04900
	NUM	0	17 RETURN ADDRESS	AAY05000
	NUM	0	18 TEMPORARY ADDRESS	AAY05100
	ADC	U1713R	19 DIAGNOSTIC LU	AAY05200
	EJT			

Figure 1-26. 1713 Teletype Reader Physical Device Table, Section AAY

1.26 1713 Teletype Paper Tape Punch Physical Device Table, Section AAZ

The driver for the 1713 teletype paper tape punch may be either core resident or mass resident. If core resident, M1713R is zero, and words 1, 2, and 3 of the table are driver entries. If mass resident, M1713R is one, and words 1, 2, and 3 are locations which jump to entries within MMEXEC.

Word 19 contains an index into the L0G1A Table if there is a corresponding diagnostic logical unit. This section is shown in Figure 1-27.

```

*          1 7 1 3   T E L E T Y P E   P U N C H
*
SPC      1
EQL     M1713P(0)
EQL     M1713P(1)

IFA     M1713P.EQ.0      CORE RESIDENT DRIVER
EXT     I1713P
EXT     C1713P
EXT     E1713P
EQL     I1713P(0)
EQL     S1713P($7FFF)
EIF

IFA     M1713P.EQ.1      MASS RESIDENT DRIVER
EXT     MASDRV
EXT     MASCON
EXT     MASERR
I1713P JMF+ MASDRV      INITIATE DRIVER
C1713P JMF+ MASCON      INTERRUPT RESPONSE
E1713P JMF+ MASERR      TIMEOUT ERROR
EXT     L1713P
EXT     S1713P
EIF

```

```

*          1 7 1 3   T E L E T Y P E   P U N C H
*
EQL     U1713P(0)
EQL     U1713P(x1713P-LCG1A)
SPC      1
P1713P ADC $520E      00 SCHEDULER CALL
ADC     I1713P      01 INITIATOR ADDRESS
ADC     C1713P      02 CONTINUATOR ADDRESS
ADC     E1713P      03 TIMEOUT ERROR ADDRESS
NUM     -1          04 DIAGNOSTIC CLOCK
NUM     0           05 LOGICAL UNIT
NUM     0           06 PARAMETER LOCATION
NUM     $0091      07 CONVERTER, EQUIPMENT, STATION
NUM     $21B4      08 REQUEST STATUS
NUM     0           09 DRIVER STATUS
NUM     0           10 CURRENT LOCATION
NUM     0           11 LAST LOCATION PLUS ONE
NUM     0           12 DEVICE STATUS
ADC     L1713P      13 DRIVER LENGTH IF MASS MEMORY
ADC     S1713P      14 NAME ASSOCIATED WITH SECTOR NUMBER
NUM     0           15 CHECKSUM AND ERROR CODE
NUM     0           16 TEMPORARY
NUM     0           17 RETURN ADDRESS
NUM     0           18 SPARE
ADC     U1713P      19 DIAGNOSTIC LU
EJT

```

```

AAZ00100
AAZ00200
AAZ00300
AAZ00400
AAZ00500
AAZ00600
AAZ00700
AAZ00800
AAZ00900
AAZ01000
AAZ01100
AAZ01200
AAZ01300
AAZ01400
AAZ01500
AAZ01600
AAZ01700
AAZ01800
AAZ01900
AAZ02000
AAZ02100
AAZ02200
AAZ02300
AAZ02400
AAZ02500
AAZ02600
AAZ02700
AAZ02800
AAZ02900
AAZ03000
AAZ03100
AAZ03200
AAZ03300
AAZ03400
AAZ03500
AAZ03600
AAZ03700
AAZ03800
AAZ03900
AAZ04000
AAZ04100
AAZ04200
AAZ04300
AAZ04400
AAZ04500
AAZ04600
AAZ04700
AAZ04800
AAZ04900
AAZ05000
AAZ05100
AAZ05200

```

Figure 1-27. 1713 Teletype Punch Physical Device Table, Section AAZ

1.27 1713 Paper Tape Punch Software Buffered Section, Section ABA

This section of the data base contains the macro instruction, BUFFER, and a set of parameters for the macro expansion as used by the 1713 paper tape punch {refer to Figure 1-28}. The BUFFER macro when expanded, is a physical device table. The general form of this physical device table is shown in Figure 1-29. This form of physical device table supplies the parameters for the message buffering routine. The function of this routine is to transfer a user's output message from core to mass memory and then to initiate the driver. After the message has been written to mass memory, the user may consider the request to be completed. Subsequent output of the message is independent of the user.

A mass memory resident program can make a buffered output request and release its core area when buffering is completed but prior to actual message output. Thus, software message buffering is useful for slow input/output devices. After the message has been buffered to mass memory, portions of the message are transferred to a core resident character buffer prior to transmission to the output device. The macro parameters are as follows:

- 1} FLSB - Least significant bits of start of mass memory buffer.
- 2} LLSB - Least significant bits of end of buffer.
- 3} FMSB - Most significant bits of start of buffer.
- 4} BLU - Logical unit for eventual output.
- 5} MMLU - Mass memory logical unit.
- 6} CBSIZE - Character buffer size. The size of this buffer is the number of words in a formatted write request that will be made to logical unit, BLU.

For purposes of specifying the mass memory buffer area, mass memory is defined in 32K - word pages. The maximum size of the buffer on

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mass storage is one page or 8000_{16} words. The buffer must reside totally within one page. The most significant bits of the starting address of the buffer, FMSB, are the same as the most significant bits of the last address of the buffer. Therefore, the least significant bits of the starting address, FLSB, must be less than the least significant bits of the last address, LLSB. The following set of parameter values would be valid:

$$\begin{aligned} \text{FMSB} &= 30_{16} \\ \text{FLSB} &= 0 \\ \text{LLSB} &= 7FFF_{16} \end{aligned}$$

The following set of parameter values are invalid:

$$\begin{aligned} \text{FMSB} &= 30_{16} \\ \text{FLSB} &= 6000_{16} \\ \text{LLSB} &= 4000_{16} \end{aligned}$$

The page, p , and word within the page, w_p , corresponding to a given system sector, s , and word within the sector, w_s , may be computed as follows:

$$p = \left\lfloor \frac{(96 \cdot \{s-1\} + w_s)}{32,768} \right\rfloor$$

where $\lfloor x \rfloor$ is the greatest integer less than or equal to x
 $w_p = 96 \cdot \{s-1\} + w_s - 32,768 \cdot p$.

For example, if $s = 633$ and $w_s = 88$, then $p = 1$, and $w_p = 6D58_{16}$.

The form of a physical device table for a device using the message buffering routine differs from a physical device table for a device not using software buffering. Refer to Figure 1-29. The initiator address is an entry to DSBUFR, the message buffering routine. The continuator address and diagnostic error addresses are not used. Beginning at word 20 is a monitor request to transfer the message to mass memory. Beginning at word 29 is a monitor request to transfer a portion of the message from the mass memory buffer to the character buffer in core. Beginning at word 38 is a monitor request to transfer a formatted record from the character buffer to the output device. Beginning at word 45 is the character buffer.

In a standard system the software buffers are always after the end of the scratch area. If the scratch device and the library device are the same, this positioning of the software buffers causes a relatively long seek time between an access of the allocatable core area within the system core image and an access of the software buffer area. {Refer to the standard system mass storage diagram in Figure 1-30.} Thus, placing the software buffers at the end of the scratch area may cause excessive disk seek time due to frequent swapping and simultaneous message buffering.

In this case, the user may wish to move the software buffers. If another disk drive is available

which is not being accessed so frequently, this may be the best place for the software buffers. To move a buffer to another logical unit it is necessary to change all references to FLSB, LLSB, FMSB, and MMLU in the physical device table. If there is no other drive available, the user may wish to move the buffer from the end of the disk to the area beginning after the end of the core image and before the system library. This area is shown in Figure 1-30. This requires rebuilding the system. To make room for a software buffer area after the core image, the following initializer control statement must be changed:

(100)

This must be changed to force the system library to start far enough after the end of the core image to leave room for the software buffers. The system library should start at system sector $s = n_1 + n_2$ where:

n_1 = former starting address of system library

n_2 = length of software buffer area in sectors

Thus, the control statement

```
*M          LIBEDT      1
```

must be changed to:

```
*M115      LIBEDT      1
```

where s is the constant computed as $n_1 + n_2$.

In addition, the value of MAXSEC, maximum scratch sector, must be changed. This value appears in words 21-22 of the Extended Communication Region, Section AAI.

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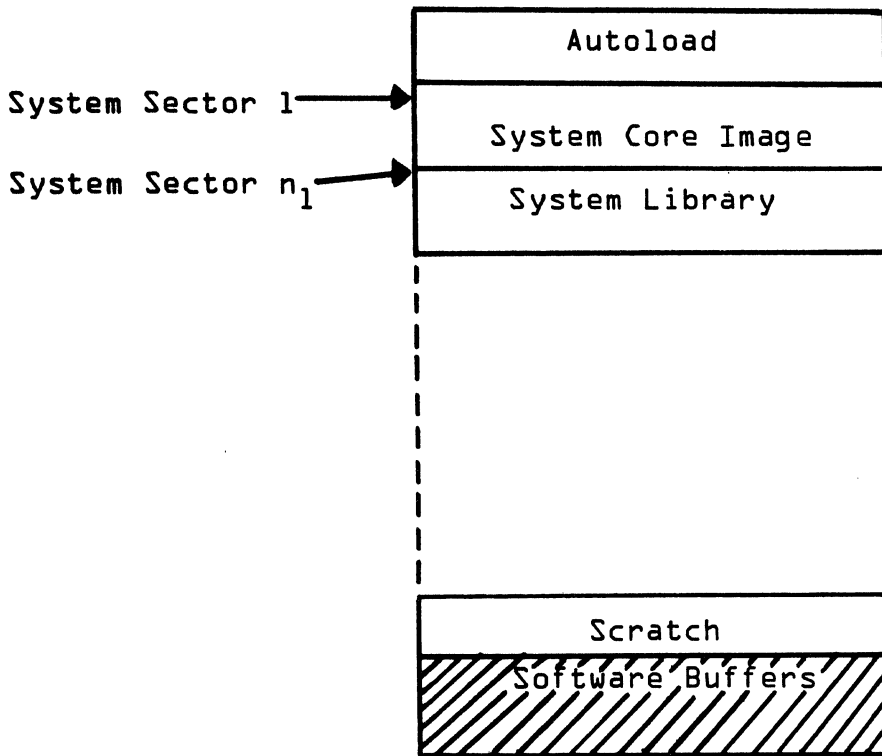
```
*           1 7 1 3   T A P E   P U N C H   S / W   B U F F E R R E D   A R A 0 0 1 0 0
*
P171PS BUFFER $0000,$0000,$0000.00,$000.0096   A R A 0 0 2 0 0
EJT                                               A R A 0 0 3 0 0
                                                A R A 0 0 4 0 0
```

Figure 1-28. 1713 Teletype Paper Tape Punch Software Buffered Section, Section ABA

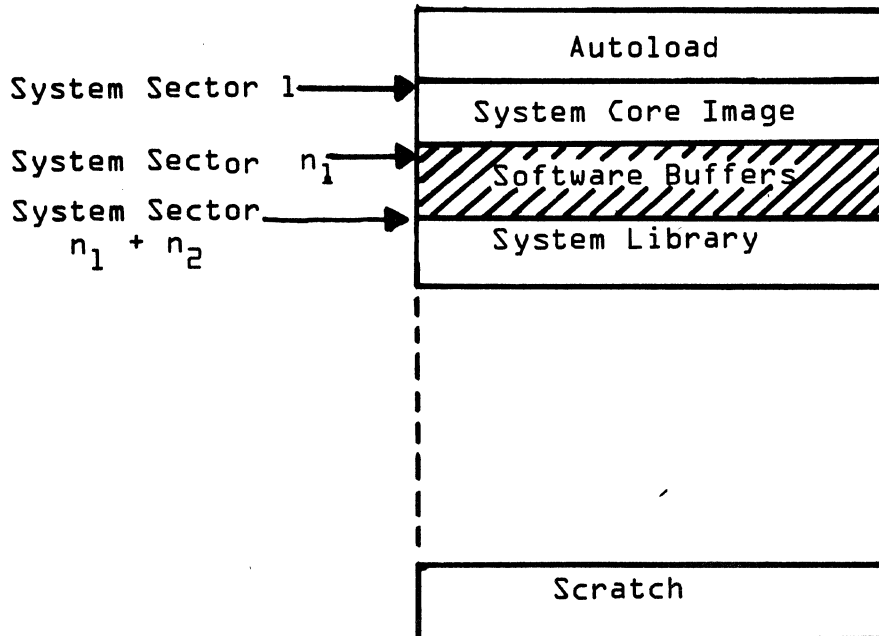
(102)

* BUFFER	MAC	#LSB, #LLSB, #MSB, #MLU, #MMLU, #CBSIZE		C14
*	LOC	#		C14
*	EXT	ISBUFFER	BUFFER DRIVER INITIATOR	C14
*	EXT	CBWRIT	MASS MEMORY WRITE COMPLETION	C14
*	EXT	CBREAD	MASS MEMORY READ COMPLETION	C14
*	EXT	BOUTC	BUFFER OUTPUT COMPLETION	C14
*	NUM	#BZUA	00 SCHEDULER CALL	C14
*	ADC	ISBUFFER	01 INITIATOR ADDRESS	C14
*	ADC	U	02 CONTINUATOR ADDRESS (NOT USED)	C14
*	ADC	U	03 DIAGNOSTIC ERROR ADDRESS (NOT USED)	C14
*	NUM	-1	04 DIAGNOSTIC CLOCK (NOT USED)	C14
*	NUM	U	05 LOGICAL UNIT	C14
*	ADC	U	06 PARAMETER LOCATION	C14
*	NUM	U	07 CONVERTER, EQUIPMENT, STATION (NOT USED)	C14
*	NUM	#A4	08 REQUEST STATUS	C14
*	NUM	U	09 DRIVER STATUS	C14
*	NUM	U	10 CURRENT LOCATION	C14
*	NUM	U	11 LAST LOCATION PLUS ONE	C14
*	NUM	U	12 DEVICE STATUS (NOT USED)	C14
*	NUM	U	13 MASS MEMORY LENGTH (NOT USED)	C14
*	NUM	#7FFF	14 MASS MEMORY LOCATION (NOT USED)	C14
*	NUM	U	15 MONITOR RETURN	C14
*	ADC	#FLSB#	16 LSB START OF BUFFER	C14
*	ADC	#LLSB#	17 LSB END OF BUFFER	C14
*	ADC	#FLSB#	18 CURRENT LSB START OF BUFFER	C14
*	ADC	#LLSB#	19 CURRENT LSB END OF BUFFER	C14
*	NUM	#44AA	20 MASS MEMORY WRITE REQUEST (UNFORMATTED)	C14
*	ADC	CBWRIT	21 COMPLETION	C14
*	NUM	U	22 THREAD	C14
*	ADC	#MMLU#	23 MASS MEMORY LOGICAL UNIT	C14
*	NUM	U	24 NUMBER OF WORDS	C14
*	NUM	U	25 STARTING DATA LOCATION	C14
*	ADC	#FMSB#	26 MSB ADDRESS	C14
*	ADC	#FLSB#	27 LSB ADDRESS	C14
*	NUM	U	28 CONTROL WORD	C14
*	NUM	#42AA	29 MASS MEMORY READ REQUEST (UNFORMATTED)	C14
*	ADC	CBREAD	30 COMPLETION	C14
*	NUM	U	31 THREAD	C14
*	ADC	#MMLU#	32 MASS MEMORY LOGICAL UNIT	C14
*	NUM	U	33 NUMBER OF WORDS	C14
*	ADC	#A#	34 STARTING LOCATION (CHARACTER BUFFER)	C14
*	ADC	#FMSB#	35 MSB ADDRESS	C14
*	ADC	#FLSB#	36 LSB ADDRESS	C14
*	NUM	U	37 PARTIAL WORD OUTPUT FLAG	C14
*	NUM	#4CAA	38 BUFFER WRITE REQUEST (FORMATTED)	C14
*	ADC	BOUTC	39 COMPLETION	C14
*	NUM	U	40 THREAD	C14
*	ADC	#BLU#	41 OUTPUT LOGICAL UNIT	C14
*	NUM	U	42 NUMBER OF WORDS	C14
*	ADC	#A#	43 STARTING LOCATION (CHARACTER BUFFER)	C14
*	ADC	#CBSIZE#	44 CHARACTER BUFFER SIZE	C14
*	NUM	#A# (#CBSIZE#)	45 CHARACTER BUFFER	C14
*	NUM			C14
*	NUM			C14
*	NUM			C14

Figure 1-29. Form of BUFFER Macro Expansion



Standard System



System after software buffer area has been moved.

n_2 = length of software buffer area in sectors.

Figure 1-30. Mass Storage Before and After Moving Software Buffers

1.28 COSY Driver Physical Device Table, Section ABB

The COSY driver may be core resident or mass resident depending on the value of MCOSY1. The initiator address will link directly to the driver if core resident. Otherwise, the initiator address is a jump to MASDRV in the program MMEXEC. There may be two COSY logical units, each with a distinct physical device table. The number of COSY logical units is specified by the user in the MSOS Ordering Form. To use COSY, only one COSY logical unit is necessary, but having two logical units will allow simultaneous input and output, thus increasing throughput.

Section ABB is shown in Figure 1-31.

		C O S Y D R I V E R		ARR00100
				ARR00200
	SPC	1		ARR00300
	EQL	MCOSY1(0)		ARR00400
	EQL	MCOSY1(1)		ARR00500
				ARR00600
	IFA	MCOSY1.EQ,U	CORE RESIDENT DRIVER	ARR00700
	EXT	ICOSY		ARR00800
	EQL	LCOSY(0)		ARR00900
	EQL	SCOSY(\$7FFF)		ARR01000
	EIF			ARR01100
				ARR01200
	IFA	MCOSY1.FQ,I	MASS RESIDENT DRIVER	ARR01300
	EXT	MASDRV		ARR01400
ICOSY	JMP	MASDRV	INITIATE DRIVER	ARR01500
	EXT	LCOSY		ARR01600
	EXT	SCOSY		ARR01700
	EIF			ARR01800
				ARR01900
	EJ1			ARR02000
				ARR02100
		C O S Y D R I V E R , F I R S T U N I T		ARR02200
				ARR02300
PCOSY1	SPC	1		ARR02400
	ADC	\$5208	00 SCHEDULER CALL	ARR02500
	ADC	ICOSY	01 INITIATOR ADDRESS	ARR02600
	ADC	0	02 CONTINUATOR ADDRESS - NOT USED	ARR02700
	ADC	0	03 TIMEOUT ERROR ADDRESS - NOT USED	ARR02800
	NUM	-1	04 DIAGNOSTIC CLOCK	ARR02900
	NUM	0	05 LOGICAL UNIT	ARR03000
	NUM	0	06 PARAMETER LOCATION	ARR03100
	NUM	0	07 CONVERTER, EQUIPMENT, STATION - NONE	ARR03200
	NUM	\$0886	08 REQUEST STATUS	ARR03300
	NUM	0	09 DRIVER STATUS	ARR03400
	NUM	0	10 CURRENT LOCATION	ARR03500
	NUM	0	11 LAST LOCATION PLUS ONE	ARR03600
	NUM	0	12 DEVICE STATUS	ARR03700
	ADC	LCOSY	13 DRIVER LENGTH IF MASS MEMORY	ARR03800
	ADC	SCOSY	14 NAME ASSOCIATED WITH SECTOR NUMBER	ARR03900
	NUM	0	15 RESERVED FOR FNR AND CMR	ARR04000
	ADC	PCOSY2	16 PHYSTB THREAD	ARR04100
	NUM	0	17 SEQUENCE NUMBER	ARR04200
	NUM	0	18 ID	ARR04300
	NUM	0	19 ID-1	ARR04400
	NUM	0	20 ID-2	ARR04500
	NUM	0	21 NUMBER OF WORDS REQUESTED	ARR04600
	NUM	\$5555	22 HCL-CHARACTER POINTER	ARR04700
	NUM	\$5555	23 COSY-CHARACTER POINTER	ARR04800
	NUM	0	24 R/W FLAG	

Figure 1-31. COSY Driver Physical Device Table, Section ABB

NUM	1	25	PON5F	ABB04900
NUM	0	26	FSTCHR	ABB05000
NUM	0	27	ENDDCK	ABB05100
ADC	INPBFA	28	COSY BUFFER LOCATION	ABB05200
ADC	INPBFA	29	NEXT COSY WCRD	ABB05300
NUM	0	30	HCL-BUFFER POINTER	ABB05400
NUM	1	31	INITIAL CALL FLAG	ABB05500
NUM	0	32	SEQUENCE FLAG NONZERO=NO SEQUENCE NUMRER	ABB05600
* BZS INPBFA(192)				ABB05700
* 224 COSY BUFFER				ABB05800
* EJT				ABB05900
* C O S Y D R I V E R , S E C O N D U N I T				ABB06000
* SPC 1				ABB06100
PCOSY2	ADC	\$5208	00 SCHEDULER CALL	ABB06200
	ADC	ICOSY	01 INITIATOR ADDRESS	ABB06300
	ADC	0	02 CONTINUATOR ADDRESS - NOT USED	ABB06400
	ADC	0	03 TIMEOUT ERROR ADDRESS - NOT USED	ABB06500
	NUM	-1	04 DIAGNOSTIC CLOCK	ABB06600
	NUM	0	05 LOGICAL UNIT	ABB06700
	NUM	0	06 PARAMETER LOCATION	ABB06800
	NUM	0	07 CONVERTER, EQUIPMENT, STATION - NONE	ABB06900
	NUM	\$08B6	08 REQUEST STATUS	ABB07000
	NUM	0	09 DRIVER STATUS	ABB07100
	NUM	0	10 CURRENT LOCATION	ABB07200
	NUM	0	11 LAST LOCATION PLUS ONE	ABB07300
	NUM	0	12 DEVICE STATUS	ABB07400
	ADC	LCOSY	13 DRIVER LENGTH IF MASS MEMORY	ABB07500
	ADC	SCOSY	14 NAME ASSOCIATED WITH SECTOR NUMBER	ABB07600
	NUM	0	15 RESERVED FOR FNR AND CMR	ABB07700
	ADC	PCOSY1	16 PHYSTB THREAD	ABB07800
	NUM	0	17 SEQUENCE NUMBER	ABB07900
	NUM	0	18 ID	ABB08000
	NUM	0	19 ID-1	ABB08100
	NUM	0	20 ID-2	ABB08200
	NUM	0	21 NUMBER OF WORDS REQUESTED	ABB08300
	NUM	\$5555	22 HCL-CHARACTER POINTER	ABB08400
	NUM	\$5555	23 COSY-CHARACTER POINTER	ABB08500
	NUM	0	24 R/W FLAG	ABB08600
	NUM	1	25 PON5F	ABB08700
	NUM	0	26 FSTCHR	ABB08800
	NUM	0	27 ENDDCK	ABB08900
	ADC	INPBFB	28 COSY BUFFER LOCATION	ABB09000
	ADC	INPBFB	29 NEXT COSY WCRD	ABB09100
	NUM	0	30 HCL-BUFFER POINTER	ABB09200
	NUM	1	31 INITIAL CALL FLAG	ABB09300
	NUM	0	32 SEQUENCE FLAG NONZERO=NO SEQUENCE NUMRER	ABB09400
* BZS INPBFB(192)				ABB09500
* 224 COSY BUFFER				ABB09600
* EJT				ABB09700
				ABB09800
				ABB09900
				ABB10000

Figure 1-31. COSY Driver Physical Device Table, Section ABB

{Continued}

1.29 1732-2/615-73/93 Magnetic Tape Physical Device Table,
Section ABC

This set of physical device tables can include data for up to four tape drives. Each of these drives may be either 7-track {615-73} or 9-track {615-93}. The number of tracks determines the equipment type in word 8 of each physical device table. {Refer to Figure 1-32.} According to the specifications by the user in the MSOS Ordering Form, word 8 will contain the correct request status. In any given SYSDAT, only one of the word 8 definitions {#0806+T6157 or #0806+T6159} will be assembled into each 615-73/93 physical device table.

The driver is the same for all 615-73/93 magnetic tape units. This driver is core resident if M17322 is zero in which case words 1, 2, and 3 of each table are driver entries. If M17322 is one, the driver is mass resident, and words 1, 2, and 3 of each table point to jumps to the mass memory driver executive program, MMEEXEC.

For each 615-73/93 magnetic tape physical device table, word 17 is an index into the LOG1A table of logical unit numbers if there is a corresponding diagnostic logical unit. Otherwise this word is zero.

Word 23 of each table contains the thread word for the driver. Each table is linked to the table for the next unit, with the thread for the last unit pointing to the table for the first unit. If there is only one unit, the thread points to itself.

In a physical device table for a 7-track drive words 24 and 25 have special meanings. The maximum physical record size, as specified in word 24, is the number of words written to tape at one time. The number of words specified in a READ/WRITE request to the driver defines logical record length. If a logical record is longer than the maximum physical record size, it is segmented and written as a series of physical records. A maximum physical record size of 192 words is standard, since the COSY driver uses 192-word records. The record size may be larger if other aspects of the system require it, but this will require a larger pack/unpack buffer as will be defined. If the COSY driver is not included in the system, a maximum smaller than 192 words may be specified by the user.

Word 25 is a pointer to BF1F32, the buffer used to pack and unpack data for 615-73 requests. A hardware assembly/disassembly feature is available on the 615-73. With this feature, 12 bits may be output into two frames of tape with one output instead of two. Similarly, two frames may be input with one input instead of two.

To use this hardware feature a buffer must be made available to the driver for packing and unpacking data. This is the buffer BF1F32 within the physical device tables. The size of this buffer is defined as follows: If the maximum physical record size is n words, then the length of the pack/unpack buffer is $\left\lfloor \frac{4}{3} \cdot n \right\rfloor + 2$ words, where $\lfloor x \rfloor$ is the greatest integer less than or equal to x .

If a foreground user wishes to temporarily use a larger maximum physical record size, he may include in his program, code to do the following:

- 1) Temporarily change the value of word 24 of the physical device table he will use in making the request. The new value is the larger physical record size.
- 2) Temporarily change word 25 of the physical device table to point to a pack/unpack buffer in the user's own program.

If a foreground user uses this technique, he must take care that no other user makes a request for this physical device table before the table has been restored to its original form.

If all 615-73/93 units in the system are 615-93, i.e., 9-track drives, the driver optional modules for 7-track repacking of binary information will not be included in the system. When these modules are not in the system, lines *ABCD130 and *ABCD140 are included in SYSDAT.

These lines equate the entry points PACK, UNPK, BCDASC, ASCBCD, and PRUCMP with 7FFF₁₆. The purpose of these equivalences is to avoid listing these entry points as unpatched externals at the time of system installation.

The 615-73/93 interrupt response routine is located at lines *ABC0390 and *ABC0400. The routine stores the address of the physical device table in the 0-register, then jumps to the driver continuator entry.

111

*		6 1 5 - 7 3 / 9 3	M A G	T A P E		ARC00100
*						ARC00200
	SPC	1				ARC00300
	EQU	T6157(20*\$10)	TYPE	CODE - 615-73	MAG TAPE	ARC00400
	EQU	T6159(21*\$10)	TYPE	CODE - 615-93	MAG TAPE	ARC00500
	EQU	PHSREC(192)	MAX.	PHYSICAL RECORD SIZE FOR	7 TRACK	ARC00600
	SPC	2				ARC00700
BF1F32	BZS	BF1F32(PHSREC*4/3+2)	PACK/UNPACK	BUFFER (7 TRACK)		ARC00800
	SPC	1				ARC00900
*			LINK	UNSELECTED 7 TRACK	PROCESSORS	ARC01000
	SPC	1				ARC01100
	ENT	PACK,UNPK,BCDASC,ASCHCD,PRUCMP				ARC01200
	EQU	PACK(\$7FFF),UNPK(\$7FFF)				ARC01300
	EQU	BCDASC(\$7FFF),ASCPD(\$7FFF),PRUCMP(\$7FFF)				ARC01400
	SPC	1				ARC01500
	EQU	M17322(0)				ARC01600
	EQU	M17322(1)				ARC01700
*						ARC01800
	IFA	M17322.EQ.0	CORE	RESIDENT DRIVER		ARC01900
	EXT	I17322				ARC02000
	EXT	C17322				ARC02100
	EXT	E17322				ARC02200
	EQU	L17322(0)				ARC02300
	EQU	S17322(\$7FFF)				ARC02400
	EIF					ARC02500
*						ARC02600
	IFA	M17322.EQ.1	MASS	RESIDENT DRIVER		ARC02700
	EXT	MASDRV				ARC02800
	EXT	MASCON				ARC02900
	EXT	MASERR				ARC03000
I17322	JMP+	MASDRV	INITIATE	DRIVER		ARC03100
C17322	JMP+	MASCON	INTERRUPT	RESPONSE		ARC03200
E17322	JMP+	MASERR	TIMEOUT	ERROR		ARC03300
	EXT	L17322				ARC03400
	EXT	S17322				ARC03500
	EIF					ARC03600
*						ARC03700
	SPC	2				ARC03800
R17322	LDG	=XP73220	INTERRUPT	RESPONSE FOR 615-73/93	MAG TAPE	ARC03900
	JMP*	(P73220+2)				ARC04000
	EJT					ARC04100
*		6 1 5 - 7 3 / 9 3	M A G	T A P E ,	U N I T	0
*						ARC04200
	EQU	U73220(0)				ARC04300
	EQU	U73220(X73220-LCG1A)				ARC04400
	SPC	1				ARC04500
P73220	ADC	\$520A	00	SCHEDULER	CALI	ARC04600
	ADC	I17322	01	INITIATOR	ADDRESS	ARC04700
	ADC	C17322	02	CONTINUATOR	ADDRESS	ARC04800
	ADC	E17322	03	TIMEOUT	ERROR ADDRESS	ARC04900
	NUM	-1	04	DIAGNOSTIC	CLOCK	ARC05000
	NUM	0	05	LOGICAL	UNIT	ARC05100
	NUM	0	06	PARAMETER	LOCATION	ARC05200
	NUM	\$0381	07	CONVERTER,	EQUIPMENT, STATION	ARC05300
						ARC05400

Figure 1-32. 1732-2/615-73/93 Magnetic Tape Physical Device Table

Section ABC

ADC	\$0806+T6157	08	REQUEST STATUS	ARC05500
ADC	\$0806+T6159	08	REQUEST STATUS	ARC05600
NUM	0	09	DRIVER STATUS	ARC05700
NUM	0	10	CURRENT LOCATION	ARC05800
NUM	0	11	LAST LOCATION PLUS ONE	ARC05900
NUM	0	12	DEVICE STATUS	ARC06000
ADC	L17322	13	DRIVER LENGTH IF MASS MEMORY	ARC06100
ADC	S17322	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARC06200
NUM	0	15	RESERVED FOR FNR AND CMR	ARC06300
NUM	\$0448	16	UNIT, FUNCTION AND DENSITY CONTROL	ARC06400
ADC	U73220	17	DIAGNOSTIC LU	ARC06500
ADC	0	18	ERROR CODE	ARC06600
NUM	0	19	RECOVERY RETURN ADDRESS	ARC06700
NUM	0	20	RECORD CHECKSUM	ARC06800
NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ARC06900
NUM	0	22	TEMPORARY CHECKSUM	ARC07000
ADC	U73221	23	PHYSIC THREAD	ARC07100
NUM	0	24	RESERVED	ARC07200
NUM	0	25	RESERVED	ARC07300
ADC	DHSREC	24	MAX PHY RECORR SIZE (7 TRACK)	ARC07400
ADC	RF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARC07500
EJT				ARC07600

6 1 5 - 7 3 / 9 3 M A G T A P E , U N I T 1

EQL	U73221(0)			ARC07700
EQL	U73221(x73221-LOG1A)			ARC07800
SPC	1			ARC07900
P73221	ADC \$520A	00	SCHEDULER CALL	ARC08000
	ADC I17322	01	INITIATOR ADDRESS	ARC08100
	ADC C17322	02	CONTINUATOR ADDRESS	ARC08200
	ADC E17322	03	TIMEOUT ERROR ADDRESS	ARC08300
	NUM -1	04	DIAGNOSTIC CLOCK	ARC08400
	NUM 0	05	LOGICAL UNIT	ARC08500
	NUM 0	06	PARAMETER LOCATION	ARC08600
	NUM \$0381	07	CONVERTER, EQUIPMENT. STATION	ARC08700
	ADC \$0806+T6157	08	REQUEST STATUS	ARC08800
	ADC \$0806+T6159	08	REQUEST STATUS	ARC08900
	NUM 0	09	DRIVER STATUS	ARC09000
	NUM 0	10	CURRENT LOCATION	ARC09100
	NUM 0	11	LAST LOCATION PLUS ONE	ARC09200
	NUM 0	12	DEVICE STATUS	ARC09300
	ADC L17322	13	DRIVER LENGTH IF MASS MEMORY	ARC09400
	ADC S17322	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARC09500
	NUM 0	15	RESERVED FOR FNR AND CMR	ARC09600
	NUM \$04C8	16	UNIT, FUNCTION AND DENSITY CONTROL	ARC09700
	ADC U73221	17	DIAGNOSTIC LU	ARC09800
	ADC 0	18	ERROR CODE	ARC09900
	NUM 0	19	RECOVERY RETURN ADDRESS	ARC10000
	NUM 0	20	RECORD CHECKSUM	ARC10100
	NUM \$DCDC	21	FUNCTION DIRECTORY BITWORD	ARC10200
	NUM 0	22	TEMPORARY CHECKSUM	ARC10300
				ARC10400
				ARC10500

Figure 1-32. 1732-2/615-73/93 Magnetic Tape Physical Device Table
Section ABC {Continued}

ADC	P73222	23	PHYSIB THREAD	ARC10600
NUM	0	24	RESERVED	ARC10700
NUM	0	25	RESERVED	ARC10800
ADC	PHSREC	24	MAX PHY RECCRD SIZE (7 TRACK)	ARC10900
ADC	RF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARC11000
EJT				ARC11100
	6 1 5 - 7 3 / 9 3		M A G T A P E ,	ARC11200
			U N I T 2	ARC11300
EQU	U73222(0)			ARC11400
EQU	U73222(x73222-LOG1A)			ARC11500
SPC	1			ARC11600
P73222	ADC \$520A	00	SCHEDULER CALL	ARC11700
	ADC I17322	01	INITIATOR ADDRESS	ARC11800
	ADC C17322	02	CONTINUATOR ADDRESS	ARC11900
	ADC E17322	03	TIMEOUT ERROR ADDRESS	ARC12000
	NUM -1	04	DIAGNOSTIC CLOCK	ARC12100
	NUM 0	05	LOGICAL UNIT	ARC12200
	NUM 0	06	PARAMETER LOCATION	ARC12300
	NUM \$0381	07	CONVERTER, EQUIPMENT, STATION	ARC12400
	ADC \$0806+T6157	08	REQUEST STATUS	ARC12500
	ADC \$0806+T6159	08	REQUEST STATUS	ARC12600
	NUM 0	09	DRIVER STATUS	ARC12700
	NUM 0	10	CURRENT LOCATION	ARC12800
	NUM 0	11	LAST LOCATION PLUS ONE	ARC12900
	NUM 0	12	DEVICE STATUS	ARC13000
	ADC L17322	13	DRIVER LENGTH IF MASS MEMORY	ARC13100
	ADC S17322	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARC13200
	NUM 0	15	RESERVED FOR FNR AND CMP	ARC13300
	NUM \$0548	16	UNIT, FUNCTION AND DENSITY CONTROL	ARC13400
	ADC U73222	17	DIAGNOSTIC LU	ARC13500
	ADC 0	18	ERROR CODE	ARC13600
	NUM 0	19	RECOVERY RETURN ADDRESS	ARC13700
	NUM 0	20	RECCRD CHECKSUM	ARC13800
	NUM \$DCDC	21	FUNCTION DIRECTORY BITWORD	ARC13900
	NUM 0	22	TEMPORARY CHECKSUM	ARC14000
	ADC P73223	23	PHYSIB THREAD	ARC14100
	NUM 0	24	RESERVED	ARC14200
	NUM 0	25	RESERVED	ARC14300
	ADC PHSREC	24	MAX PHY RECCRD SIZE (7 TRACK)	ARC14400
	ADC RF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARC14500
	EJT			ARC14600

Figure 1-32. 1732-2/615-73/93 Magnetic Tape Physical Device Table, Section ABC {Continued}

6 1 5 - 7 3 / 9 3 M A G T A P E , U N I T 3

	EQL	073223(0)			ARC14700
	EQL	073223(x73423-LCG1A)			ARC14800
	SPC	1			ARC14900
P73223	ADC	4520A	00	SCHEDULER CALL	ARC15000
	ADC	117322	01	INITIATOR ADDRESS	ARC15100
	ADC	C17322	02	CONTINUATOR ADDRESS	ARC15200
	ADC	E17322	03	TIMEOUT ERROR ADDRESS	ARC15300
	NUM	-1	04	DIAGNOSTIC CLOCK	ARC15400
	NUM	0	05	LOGICAL UNIT	ARC15500
	NUM	0	06	PARAMETER LOCATION	ARC15600
	NUM	40381	07	CONVERTER, EQUIPMENT, STATION	ARC15700
	ADC	40806+T6157	08	REQUEST STATUS	ARC15800
	ADC	40806+T6159	08	REQUEST STATUS	ARC15900
	NUM	0	09	DRIVER STATUS	ARC16000
	NUM	0	10	CURRENT LOCATION	ARC16100
	NUM	0	11	LAST LOCATION PLUS ONE	ARC16200
	NUM	0	12	DEVICE STATUS	ARC16300
	ADC	L17322	13	DRIVER LENGTH IF MASS MEMORY	ARC16400
	ADC	S17322	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARC16500
	NUM	0	15	RESERVED FOR FNR AND CMP	ARC16600
	NUM	40509	16	UNIT, FUNCTION AND DENSITY CONTROL	ARC16700
	ADC	073223	17	DIAGNOSTIC LU	ARC16800
	ADC	0	18	ERROR CODE	ARC16900
	NUM	0	19	RECOVERY RETURN ADDRESS	ARC17000
	NUM	0	20	RECORD CHECKSUM	ARC17100
	NUM	4000C	21	FUNCTION DIRECTORY BITWORD	ARC17200
	NUM	0	22	TEMPORARY CHECKSUM	ARC17300
	ADC	D73220	23	PHYSTH THREAD	ARC17400
	NUM	0	24	RESERVED	ARC17500
	NUM	0	25	RESERVED	ARC17600
	ADC	DHSREC	24	MAX PHY RECCRD SIZE (7 TRACK)	ARC17700
	ADC	RF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARC17800
	EUT				ARC17900
					ARC18000
					ARC18100

Figure 1-32. 1732-2/615-73/93 Magnetic Tape Physical Device Table, Section ABC {Continued}

1.30 1732-1/608/609 Unbuffered Magnetic Tape Physical Device

Table, Section ABD

Up to eight unbuffered 1732-1/608/609 magnetic tape drives may be included in a system. Each has its own physical device table, as shown in Figure 1-33. In each table, the type code in word 8 specifies whether the unit is a 608 or a 609. Word 17 of each table is an index into the LOG1A table if there is a diagnostic logical unit associated with this drive. Word 17 is zero if there is no corresponding diagnostic logical unit.

There is one driver for all 608/609 magnetic tape drives in a system. The driver includes the packing/unpacking logic needed for a 608 unit, even if all units in the system are 609's.

The driver can be core resident or mass resident. If M1732U is zero, the driver is core resident, and words 1, 2, and 3 of each table are driver entries. If M1732U is one, the driver is mass resident, and words 1, 2, and 3 of each table point to jumps to MMEXEC.

The interrupt response routine is contained in lines *ABD0330 and *ABD0340 of Figure 1-33. It is in the form of a standard interrupt processor for MSOS. The common interrupt handler transfers control to the interrupt response routine which in turn puts the

address of the first physical device table in θ and jumps to the driver continuator entry. The driver then determines which of the tape drives caused the interrupt.

The physical device tables are threaded, the thread appearing in word 23 of each table. The thread word in the first table points to the second, the second to the third, etc. The thread word for the last unit points to the first. If there are n units in the system, where n is less than eight, the physical device tables in the system will correspond to units $0, 1, 2, \dots, n-2, 7$. For example, if there are five units, the units are numbered $0, 1, 2, 3, 7$. If there is only one unit in a system it is unit 0 and the thread word points to the physical device table for unit 0 .

* 6 0 8 / 9 M A G T A P E		
SPC	1	ARD00100
EQL	T608U(24*\$10) TYPE CODE - 608 MAG TAPE	ARD00200
EQL	T609U(25*\$10) TYPE CODE - 609 MAG TAPE	ARD00300
EQL	PHSREC(192) MAX. PHYSICAL RECORD SIZE FOR 7 TRACK	ARD00400
SPC	2	ABD00500
BF1F32	BZS RF1F32(PHSREC*4/3+2) PACK/UNPACK BUFFER (7 TRACK)	ARD00600
SPC	1	ARD00700
EQL	M1732U(0)	ARD00800
EQL	M1732U(1)	ARD00900
* IFA M1732U.EQ,0 CORE RESIDENT DRIVER		ARD01000
EXT	I1732U	ARD01100
EXT	C1732U	ARD01200
EXT	E1732U	ARD01300
EQL	L1732U(0)	ARD01400
EQL	S1732U(\$7FFF)	ARD01500
EIF		ARD01600
* IFA M1732U.EQ,1 MASS RESIDENT DRIVER		ARD01700
EXT	MASDRV	ARD01800
EXT	MASCON	ARD01900
EXT	MASERR	ARD02000
I1732U	JMP+ MASDRV INITIATE DRIVER	ARD02100
C1732U	JMP+ MASCON INTERRUPT RESPONSE	ARD02200
E1732U	JMP+ MASERR TIMEOUT ERROR	ARD02300
EXT	L1732U	ARD02400
EXT	S1732U	ARD02500
EIF		ARD02600
* SPC 2		ARD02700
R1732U	LDG =XP732U0 INTERRUPT RESPONSE FOR 608/9 MAG TAPE	ARD02800
JMP*	(P732U0+2)	ARD02900
EJT		ARD03000
* 6 0 8 / 9 M A G T A P E . U N I T 0		ARD03100
* EQL U732U0(0)		ARD03200
EQL U732U0(X732U0-LCG1A)		ARD03300
SPC	1	ARD03400
P732U0	ADC \$520E 00 SCHEDULER CALL	ARD03500
ADC	I1732U 01 INITIATOR ADDRESS	ARD03600
ADC	C1732U 02 CONTINUATOR ADDRESS	ARD03700
ADC	E1732U 03 TIMEOUT ERROR ADDRESS	ARD03800
NUM	-1 04 DIAGNOSTIC CLOCK	ARD03900
NUM	0 05 LOGICAL UNIT	ARD04000
NUM	0 06 PARAMETER LOCATION	ARD04100
NUM	\$0381 07 CONVERTER, EQUIPMENT, STATION	ARD04200
ADC	\$0806+T608U 08 REQUEST STATUS	ARD04300
ADC	\$0806+T609U 08 REQUEST STATUS	ARD04400
NUM	0 09 DRIVER STATUS	ARD04500
		ARD04600
		ARD04700
		ARD04800
		ARD04900
		ARD05000
		ARD05100

Figure 1-33. 1732-1/608/609 Unbuffered Magnetic Tape Physical Device
Table, Section ABD

NUM	0	10	CURRENT LOCATION	ARD05200
NUM	0	11	LAST LOCATION PLUS ONE	ARD05300
NUM	0	12	DEVICE STATUS	ARD05400
ADC	L1732U	13	DRIVER LENGTH IF MASS MEMORY	ARD05500
ADC	S1732U	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARD05600
NUM	0	15	RESERVED FOR FNR AND CMR	ARD05700
NUM	\$0440	16	FUNCTION AND UNIT CONTROL	ARD05800
ADC	U732U0	17	DIAGNOSTIC LU	ARD05900
ADC	0	18	ERROR CODE	ARD06000
NUM	0	19	RECOVERY RETURN ADDRESS	ARD06100
NUM	0	20	RECORD CHECKSUM	ARD06200
NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ARD06300
NUM	0	22	TEMPORARY CHECKSUM	ARD06400
ADC	0732U1	23	PHYSTB THREAD	ARD06500
ADC	PHSREC	24	MAX PHY RECCRD SIZE (7 TRACK)	ARD06600
ADC	RF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARD06700
NUM	0	26	END-OF-TAPE FLAG	ARD06800
EJT				ARD06900
* 6 0 8 / 9 M A G T A P E , U N I T 1				ARD07000
* EQU U732U1(0)				ARD07100
EQU U732U1(X732U1-LCG1A)				ARD07200
SPC 1				ARD07300
P732U1	ADC	\$520E	00 SCHEDULER CALL	ARD07400
	ADC	I1732U	01 INITIATOR ADDRESS	ARD07500
	ADC	C1732U	02 CONTINUATOR ADDRESS	ARD07600
	ADC	E1732U	03 TIMEOUT ERROR ADDRESS	ARD07700
	NUM	-1	04 DIAGNOSTIC CLOCK	ARD07800
	NUM	0	05 LOGICAL UNIT	ARD07900
	NUM	0	06 PARAMETER LOCATION	ARD08000
	NUM	\$0381	07 CONVERTER, EQUIPMENT, STATION	ARD08100
	ADC	\$0806+T608U	08 REQUEST STATUS	ARD08200
	ADC	\$0806+T609U	08 REQUEST STATUS	ARD08300
	NUM	0	09 DRIVER STATUS	ARD08400
	NUM	0	10 CURRENT LOCATION	ARD08500
	NUM	0	11 LAST LOCATION PLUS ONE	ARD08600
	NUM	0	12 DEVICE STATUS	ARD08700
	ADC	L1732U	13 DRIVER LENGTH IF MASS MEMORY	ARD08800
	ADC	S1732U	14 NAME ASSOCIATED WITH SECTOR NUMBER	ARD08900
	NUM	0	15 RESERVED FOR FNR AND CMR	ARD09000
	NUM	\$04C0	16 FUNCTION AND UNIT CONTROL	ARD09100
	ADC	U732U1	17 DIAGNOSTIC LU	ARD09200
	ADC	0	18 ERROR CODE	ARD09300
	NUM	0	19 RECOVERY RETURN ADDRESS	ARD09400
	NUM	0	20 RECCRD CHECKSUM	ARD09500
	NUM	\$DCDC	21 FUNCTION DIRECTORY BITWORD	ARD09600
	NUM	0	22 TEMPORARY CHECKSUM	ARD09700
	ADC	0732U2	23 PHYSTB THREAD	ARD09800
	ADC	PHSREC	24 MAX PHY RECCRD SIZE (7 TRACK)	ARD09900
	ADC	RF1F32	25 PACK/UNPACK BUFFER (7 TRACK)	ARD10000
	NUM	0	26 END-OF-TAPE FLAG	ARD10100
	EJT			ARD10200
				ARD10300

Figure 1-33. 1732-1/608/609 Unbuffered Magnetic Tape Physical Device
Table, Section ABD (Continued)

6 0 8 / 9 M A G T A P E , U N I T 2

EQU	U732U2(0)			ARD10400
EQU	U732U2(x732U2-LCG1A)			ARD10500
SPC	1			ARD10600
P732U2	ADC	\$520E	00 SCHEDULER CALL	ARD10700
	ADC	I1732U	01 INITIATOR ADDRESS	ARD10800
	ADC	C1732U	02 CONTINUATOR ADDRESS	ARD10900
	ADC	E1732U	03 TIMEOUT ERROR ADDRESS	ARD11000
	NUM	-1	04 DIAGNOSTIC CLOCK	ARD11100
	NUM	0	05 LOGICAL UNIT	ARD11200
	NUM	0	06 PARAMETER LOCATION	ARD11300
	NUM	\$0381	07 CONVERTER, EQUIPMENT, STATION	ARD11400
	ADC	\$0806+T608U	08 REQUEST STATUS	ARD11500
	ADC	\$0806+T609U	08 REQUEST STATUS	ARD11600
	NUM	0	09 DRIVER STATUS	ARD11700
	NUM	0	10 CURRENT LOCATION	ARD11800
	NUM	0	11 LAST LOCATION PLUS ONE	ARD11900
	NUM	0	12 DEVICE STATUS	ARD12000
	ADC	L1732U	13 DRIVER LENGTH IF MASS MEMORY	ARD12100
	ADC	S1732L	14 NAME ASSOCIATED WITH SECTOR NUMBER	ARD12200
	NUM	0	15 RESERVED FOR FNR AND CMP	ARD12300
	NUM	\$0540	16 FUNCTION AND UNIT CONTROL	ARD12400
	ADC	U732U2	17 DIAGNOSTIC LU	ARD12500
	ADC	0	18 ERROR CCDE	ARD12600
	NUM	0	19 RECOVERY RETURN ADDRESS	ARD12700
	NUM	0	20 RECCRD CHECKSUM	ARD12800
	NUM	\$DCDC	21 FUNCTION DIRECTORY BITWORD	ARD12900
	NUM	0	22 TEMPORARY CHECKSUM	ARD13000
	ADC	6732U3	23 PHYSTR THREAD	ARD13100
	ADC	PHSREC	24 MAX PHY RECCRD SIZE (7 TRACK)	ARD13200
	ADC	RF1F32	25 PACK/UNPACK BUFFER (7 TRACK)	ARD13300
	NUM	0	26 END-OF-TAPE FLAG	ARD13400
	EJT			ARD13500
				ARD13600
				ARD13700
				ARD13800
				ARD13900
				ARD14000

6 0 8 / 9 M A G T A P E , U N I T 3

EQU	U732U3(0)			ARD14100
EQU	U732U3(x732U3-LCG1A)			ARD14200
SPC	1			ARD14300
P732U3	ADC	\$520E	00 SCHEDULER CALL	ARD14400
	ADC	I1732U	01 INITIATOR ADDRESS	ARD14500
	ADC	C1732U	02 CONTINUATOR ADDRESS	ARD14600
	ADC	E1732U	03 TIMEOUT ERROR ADDRESS	ARD14700
	NUM	-1	04 DIAGNOSTIC CLOCK	ARD14800
	NUM	0	05 LOGICAL UNIT	ARD14900
	NUM	0	06 PARAMETER LOCATION	ARD15000
	NUM	\$0381	07 CONVERTER, EQUIPMENT, STATION	ARD15100
	ADC	\$0806+T608U	08 REQUEST STATUS	ARD15200
	ADC	\$0806+T609U	08 REQUEST STATUS	ARD15300
	NUM	0	09 DRIVER STATUS	ARD15400
	NUM	0	10 CURRENT LOCATION	ARD15500
	NUM	0	11 LAST LOCATION PLUS ONE	ARD15600
	NUM	0	12 DEVICE STATUS	ARD15700
	ADC	L1732U	13 DRIVER LENGTH IF MASS MEMORY	

Figure 1-33. 1732-1/608/609 Unbuffered Magnetic Tape Physical Device Table, Section ABD {Continued}

ADC	S1732U	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARD15800
NUM	0	15	RESERVED FOR FNR AND CMR	ARD15900
NUM	\$05C0	16	FUNCTION AND UNIT CONTROL	ARD16000
ADC	U732U3	17	DIAGNOSTIC LU	ARD16100
ADC	0	18	ERRCR CCDE	ARD16200
NUM	0	19	RECOVERY RETURN ADDRESS	ARD16300
NUM	0	20	RECCRD CHECKSUM	ARD16400
NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ARD16500
NUM	0	22	TEMPORARY CHECKSUM	ARD16600
ADC	0732U4	23	PHYSTB THREAD	ARD16700
ADC	PHSREC	24	MAX PHY RECCRD SIZE (7 TRACK)	ARD16800
ADC	RF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARD16900
NUM	0	26	END-OF-TAPE FLAG	ARD17000
EJT				ARD17100
* 6 0 8 / 9 M A G T A P E , U N I T 4 *				
EQU	U732U4 (0)			ARD17200
EQL	U732U4 (X732U4-LCG1A)			ARD17300
SPC	1			ARD17400
P732U4	ADC \$520E	00	SCHEDULER CALL	ARD17500
	ADC I1732U	01	INITIATOR ADDRESS	ARD17600
	ADC C1732U	02	CONTINUATOR ADDRESS	ARD17700
	ADC E1732U	03	TIMEOUT ERRCR ADDRESS	ARD17800
	NUM -1	04	DIAGNOSTIC CLOCK	ARD17900
	NUM 0	05	LOGICAL UNIT	ARD18000
	NUM 0	06	PARAMETER LOCATION	ARD18100
	NUM \$0381	07	CONVERTER, EQUIPMENT, STATION	ARD18200
	ADC \$0806+T608U	08	REQUEST STATUS	ARD18300
	ADC \$0806+T609U	08	REQUEST STATUS	ARD18400
	NUM 0	09	DRIVER STATUS	ARD18500
	NUM 0			ARD18600
	NUM 0	10	CURRENT LOCATION	ARD18700
	NUM 0	11	LAST LOCATION PLUS ONE	ARD18800
	NUM 0	12	DEVICE STATUS	ARD18900
	ADC L1732U	13	DRIVER LENGTH IF MASS MEMORY	ARD19000
	ADC S1732U	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARD19100
	NUM 0	15	RESERVED FOR FNR AND CMR	ARD19200
	NUM \$0640	16	FUNCTION AND UNIT CONTROL	ARD19300
	ADC U732U4	17	DIAGNOSTIC LU	ARD19400
	ADC 0	18	ERRCR CCDE	ARD19500
	NUM 0	19	RECOVERY RETURN ADDRESS	ARD19600
	NUM 0	20	RECCRD CHECKSUM	ARD19700
	NUM \$DCDC	21	FUNCTION DIRECTORY BITWORD	ARD19800
	NUM 0	22	TEMPORARY CHECKSUM	ARD19900
	ADC 0732U5	23	PHYSTB THREAD	ARD20000
	ADC PHSREC	24	MAX PHY RECCRD SIZE (7 TRACK)	ARD20100
	ADC RF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARD20200
	NUM 0	26	END-OF-TAPE FLAG	ARD20300
EJT				ARD20400
				ARD20500

Figure 1-33. 1732-1/608/609 Unbuffered Magnetic Tape Physical Device

Table, Section ABD (Continued)

6 0 8 / 9 M A G T A P E , U N I T 5

EQU	U732U5 (0)			ARD20600
EQU	U732U5 (X732U5-LOG1A)			ARD20700
SPC	1			ARD20800
P732U5	ADC \$520E	00	SCHEDULER CALL	ARD20900
	ADC I1732U	01	INITIATOR ADDRESS	ARD21000
	ADC C1732U	02	CONTINUATOR ADDRESS	ARD21100
	ADC E1732U	03	TIMEOUT ERROR ADDRESS	ARD21200
	NUM -1	04	DIAGNOSTIC CLOCK	ARD21300
	NUM 0	05	LOGICAL UNIT	ARD21400
	NUM 0	06	PARAMETER LOCATION	ARD21500
	NUM \$0381	07	CONVERTER, EQUIPMENT, STATION	ARD21600
	ADC \$0806+T608U	08	REQUEST STATUS	ARD21700
	ADC \$0806+T609U	08	REQUEST STATUS	ARD21800
	NUM 0	09	DRIVER STATUS	ARD21900
	NUM 0	10	CURRENT LOCATION	ARD22000
	NUM 0	11	LAST LOCATION PLUS ONE	ARD22100
	NUM 0	12	DEVICE STATUS	ARD22200
	ADC L1732U	13	DRIVER LENGTH IF MASS MEMORY	ARD22300
	ADC S1732U	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARD22400
	NUM 0	15	RESERVED FOR FNR AND CMR	ARD22500
	NUM \$06C0	16	FUNCTION AND UNIT CONTROL	ARD22600
	ADC U732U5	17	DIAGNOSTIC LU	ARD22700
	ADC 0	18	ERROR CODE	ARD22800
	NUM 0	19	RECOVERY RETURN ADDRESS	ARD22900
	NUM 0	20	RECORD CHECKSUM	ARD23000
	NUM \$DCDC	21	FUNCTION DIRECTORY BITWORD	ARD23100
	NUM 0	22	TEMPORARY CHECKSUM	ARD23200
	ADC P732U6	23	PHYSICAL THREAD	ARD23300
	ADC PHSREC	24	MAX PHYS RECORD SIZE (7 TRACK)	ARD23400
	ADC RF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARD23500
	NUM 0	26	END-OF-TAPE FLAG	ARD23600
EJT				ARD23700

6 0 8 / 9 M A G T A P E , U N I T 6

EQU	U732U6 (0)			ARD23800
EQU	U732U6 (X732U6-LOG1A)			ARD23900
SPC	1			ARD24000
P732U6	ADC \$520E	00	SCHEDULER CALL	ARD24100
	ADC I1732U	01	INITIATOR ADDRESS	ARD24200
	ADC C1732U	02	CONTINUATOR ADDRESS	ARD24300
	ADC E1732U	03	TIMEOUT ERROR ADDRESS	ARD24400
	NUM -1	04	DIAGNOSTIC CLOCK	ARD24500
	NUM 0	05	LOGICAL UNIT	ARD24600
	NUM 0	06	PARAMETER LOCATION	ARD24700
	NUM \$0381	07	CONVERTER, EQUIPMENT, STATION	ARD24800
	ADC \$0806+T608U	08	REQUEST STATUS	ARD24900
	ADC \$0806+T609U	08	REQUEST STATUS	ARD25000
	NUM 0	09	DRIVER STATUS	ARD25100
	NUM 0	10	CURRENT LOCATION	ARD25200
	NUM 0	11	LAST LOCATION PLUS ONE	ARD25300
	NUM 0	12	DEVICE STATUS	ARD25400
	ADC L1732U	13	DRIVER LENGTH IF MASS MEMORY	ARD25500
	ADC S1732U	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARD25600
	NUM 0	15	RESERVED FOR FNR AND CMR	ARD25700
	NUM \$0740	16	FUNCTION AND UNIT CONTROL	ARD25800

Figure 1-33. 1732-1/608/609 Unbuffered Magnetic Tape Physical Device Table, Section ABD {Continued}

ADC	U732U6	17	DIAGNOSTIC LU	ARD26300
ADC	0	18	ERRCR CODE	ARD26400
NUM	0	19	RECOVERY RETURN ADDRESS	ARD26500
NUM	0	20	RECORD CHECKSUM	ARD26600
NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ARD26700
NUM	0	22	TEMPORARY CHECKSUM	ARD26800
ADC	D732U7	23	PHYSTB THREAD	ARD26900
ADC	PHSREC	24	MAX PHY RECCRD SIZE (7 TRACK)	ARD27000
ADC	RF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARD27100
NUM	0	26	END-OF-TAPE FLAG	ARD27200
EJT				ARD27300
* 6 0 8 / 9 M A G T A P E , U N I T 7				
* 6 0 8 / 9 M A G T A P E , U N I T 7				
EQL	U732U7(0)			ARD27400
EQL	U732U7(x732U7-LOG1A)			ARD27500
SPC	1			ARD27600
P732U7	ADC \$520E	00	SCHEDULER CALL	ARD27700
	ADC I1732L	01	INITIATOR ADDRESS	ARD27800
	ADC C1732L	02	CONTINUATOR ADDRESS	ARD27900
	ADC E1732L	03	TIMEOUT ERRCR ADDRESS	ARD28000
	NUM -1	04	DIAGNOSTIC CLOCK	ARD28100
	NUM 0	05	LOGICAL UNIT	ARD28200
	NUM 0	06	PARAMETER LOCATION	ARD28300
	NUM \$0381	07	CONVERTER, EQUIPMENT, STATION	ARD28400
	ADC \$0806+T608U	08	REQUEST STATUS	ARD28500
	ADC \$0806+T609U	08	REQUEST STATUS	ARD28600
	NUM 0	09	DRIVER STATUS	ARD28700
	NUM 0	10	CURRENT LOCATION	ARD28800
	NUM 0	11	LAST LOCATION PLUS ONE	ARD28900
	NUM 0	12	DEVICE STATUS	ARD29000
	ADC L1732L	13	DRIVER LENGTH IF MASS MEMORY	ARD29100
	ADC S1732L	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARD29200
	NUM 0	15	RESERVED FOR FNR AND CMR	ARD29300
	NUM \$07C0	16	FUNCTION AND UNIT CONTROL	ARD29400
	ADC U732U7	17	DIAGNOSTIC LU	ARD29500
	ADC 0	18	ERRCR CODE	ARD29600
	NUM 0	19	RECOVERY RETURN ADDRESS	ARD29700
	NUM 0	20	RECORD CHECKSUM	ARD29800
	NUM \$DCDC	21	FUNCTION DIRECTORY BITWORD	ARD29900
	NUM 0	22	TEMPORARY CHECKSUM	ARD30000
	ADC D732U0	23	PHYSTB THREAD	ARD30100
	ADC PHSREC	24	MAX PHY RECCRD SIZE (7 TRACK)	ARD30200
	ADC RF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARD30300
	NUM 0	26	END-OF-TAPE FLAG	ARD30400
EJT				ARD30500
				ARD30600
				ARD30700

Figure 1-33. 1732-1/608/609 Unbuffered Magnetic Tape Physical Device
Table, Section ABD {Continued}

1.31 1732-1/608/609 Buffered Magnetic Tapes, Section ABE

Section ABE, Figure 1-34, contains the Data Base for the 1732-1/608/609 buffered magnetic tapes. One physical device table for each drive in the system will appear in a SYSDAT for a given system. The words in these tables have meanings analogous to the tables for 608/609 unbuffered units except that word 7 includes the converter code for the 1706 Buffered Data Channel. As for the unbuffered case, there can be up to eight 608/609 buffered magnetic tapes in a system.

Words 24 and 25 are the same for a 608 physical device as for a 615-73 physical device table. Both can use the same pack/unpack buffer since any given system can contain either 615-73 units or 608 units, but not both. This is true due to hardware requirements.

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* *	6 0 8 / 9 B U F F E R E D M A G T A P E			ARE00100
	SPC	1		ARE00200
	EQL	T608B(22*\$10) TYPE CODE - 608 MAG TAPE		ARE00300
	EQL	T609B(30*\$10) TYPE CODE - 609 MAG TAPE		ARE00400
	EQL	N7061()		ARE00500
	EQL	N17068(N7061*\$100)		ARE00600
	EQL	PHSREC(192) MAX. PHYSICAL RECORD SIZE FOR 7 TRACK		ARE00700
	SPC	2		ARE00800
BF1F32	BZS	BF1F32(PHSREC*4/3+2) PACK/UNPACK BUFFER (7 TRACK)		ARE00900
	SPC	1		ARE01000
	EQL	M1732B(0)		ARE01100
	EQL	M1732B(1)		ARE01200
* *	IFA	M1732B, EQ, 0 CORE RESIDENT DRIVER		ARE01300
	EXT	T1732B		ARE01400
	EXT	C1732B		ARE01500
	EXT	F1732B		ARE01600
	EQL	L1732B(0)		ARE01700
	EQL	S1732B(\$7FFF)		ARE01800
	EIF			ARE01900
* *	IFA	M1732B, EQ, 1 MASS RESIDENT DRIVER		ARE02000
	EXT	MASDRV		ARE02100
	EXT	MASCON		ARE02200
	EXT	MASERR		ARE02300
I1732B	JMF+	MASDRV INITIATE DRIVER		ARE02400
C1732B	JMF+	MASCON INTERRUPT RESPONSE		ARE02500
E1732B	JMF+	MASERR TIMEOUT ERROR		ARE02600
	EXT	L1732B		ARE02700
	EXT	S1732B		ARE02800
	EIF			ARE02900
* *	SPC	2		ARE03000
R1732B	LDG	=XP732B0 INTERRUPT RESPONSE FOR 608/9 BUFFERED TAPE		ARE03100
	JMF*	(P732B0+2)		ARE03200
	EJT			ARE03300
* *	6 0 8 / 9 B U F F E R E D T A P E , U N I T 0			ARE03400
	EQL	H732B0(0)		ARE03500
	EQL	H732B0(X732B0-LCG1A)		ARE03600
	SPC	1		ARE03700
P732B0	ADC	\$5204 00 SCHEDULER CALL		ARE03800
	ADC	T1732B 01 INITIATOR ADDRESS		ARE03900
	ADC	C1732B 02 CONTINUATOR ADDRESS		ARE04000
	ADC	F1732B 03 TIMEOUT ERROR ADDRESS		ARE04100
	NUM	-1 04 DIAGNOSTIC CLOCK		ARE04200
	NUM	0 05 LOGICAL UNIT		ARE04300
	NUM	0 06 PARAMETER LOCATION		ARE04400
	ADC	\$0381+N17068 07 CONVERTER, EQUIPMENT, STATION		ARE04500
	ADC	\$0806+T608B 08 REQUEST STATUS		ARE04600
	ADC	\$0806+T609B 08 REQUEST STATUS		ARE04700
	NUM	0 09 DRIVER STATUS		ARE04800
				ARE04900
				ARE05000
				ARE05100
				ARE05200
				ARE05300

Figure 1-34. 1732-1/608/609 Buffered Magnetic Tape Physical Device Table, Section ABE

NUM	0	10	CURRENT LOCATION	ARE05400
NUM	0	11	LAST LOCATION PLUS ONE	ARE05500
NUM	0	12	DEVICE STATUS	ARE05600
ADC	L1732B	13	DRIVER LENGTH IF MASS MEMORY	ARE05700
ADC	21732B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARE05800
NUM	0	15	RESERVED FOR FNR AND CMR	ARE05900
NUM	80440	14	FUNCTION AND UNIT CONTROL	ARE06000
ADC	U732B0	17	DIAGNOSTIC LU	ARE06100
ADC	0	18	ERROR CODE	ARE06200
NUM	0	19	RECOVERY RETURN ADDRESS	ARE06300
NUM	0	20	RECORD CHECKSUM	ARE06400
NUM	8DCDC	21	FUNCTION DIRECTORY BITWORD	ARE06500
NUM	0	22	TEMPORARY CHECKSUM	ARE06600
ADC	P732B1	23	PHYSTB THREAD	ARE06700
ADC	PFSREC	24	MAX PHY RECORD SIZE (7 TRACK)	ARE06800
ADC	BF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARE06900
NUM	0	26	END-OF-TAPE FLAG	ARE07000
EJT				ARE07100
* 6 0 8 / 9 B U F F E R E D T A P E , U N I T 1				ARE07200
*				ARE07300
EQL	U732B1(0)			ARE07400
EQL	U732B1(X732B1-LOG1A)			ARE07500
SPC	1			ARE07600
P732B1	ADC 8520A	00	SCHEDULER CALL	ARE07700
	ADC 11732B	01	INITIATOR ADDRESS	ARE07800
	ADC C1732B	02	CONTINUATOR ADDRESS	ARE07900
	ADC F1732B	03	TIMEOUT ERROR ADDRESS	ARE08000
	NUM -1	04	DIAGNOSTIC CLOCK	ARE08100
	NUM 0	05	LOGICAL UNIT	ARE08200
	NUM 0	06	PARAMETER LOCATION	ARE08300
	ADC 803B1+N1706B	07	CONVERTER, EQUIPMENT, STATION	ARE08400
	ADC 80806+T608B	08	REQUEST STATUS	ARE08500
	ADC 80806+T609B	08	REQUEST STATUS	ARE08600
	NUM 0	09	DRIVER STATUS	ARE08700
	NUM 0	10	CURRENT LOCATION	ARE08800
	NUM 0	11	LAST LOCATION PLUS ONE	ARE08900
	NUM 0	12	DEVICE STATUS	ARE09000
	ADC L1732B	13	DRIVER LENGTH IF MASS MEMORY	ARE09100
	ADC S1732B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARE09200
	NUM 0	15	RESERVED FOR FNR AND CMR	ARE09300
	NUM 804C0	14	FUNCTION AND UNIT CONTROL	ARE09400
	ADC U732B1	17	DIAGNOSTIC LU	ARE09500
	ADC 0	18	ERROR CODE	ARE09600
	NUM 0	19	RECOVERY RETURN ADDRESS	ARE09700
	NUM 0	20	RECORD CHECKSUM	ARE09800
	NUM 8DCDC	21	FUNCTION DIRECTORY BITWORD	ARE09900
	NUM 0	22	TEMPORARY CHECKSUM	ARE10000
	ADC P732B2	23	PHYSTB THREAD	ARE10100
	ADC PFSREC	24	MAX PHY RECORD SIZE (7 TRACK)	ARE10200
	ADC BF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARE10300
	NUM 0	26	END-OF-TAPE FLAG	ARE10400
EJT				ARE10500

Figure 1-34. 1732-1/608/609 Buffered Magnetic Tape Physical Device Table, Section ABE (Continued)

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		6 0 8 / 9 B U F F E R E D T A P E , U N I T 2			
	EQU	U732B2 (0)			ABE10600
	EQU	U732B2 (X732B2-LOG1A)			ABE10700
	SPC	1			ABE10800
P732B2	ADC	\$520A	00	SCHEDULER CALL	ABE10900
	ADC	T1732B	01	INITIATOR ADDRESS	ABE11000
	ADC	C1732B	02	CONTINUATOR ADDRESS	ABE11100
	ADC	E1732B	03	TIMEOUT ERROR ADDRESS	ABE11200
	NUM	-1	04	DIAGNOSTIC CLOCK	ABE11300
	NUM	0	05	LOGICAL UNIT	ABE11400
	NUM	0	06	PARAMETER LOCATION	ABE11500
	ADC	\$0381+N1706B	07	CONVERTER, EQUIPMENT, STATION	ABE11600
	ADC	\$0806+T408B	08	REQUEST STATUS	ABE11700
	ADC	\$0806+T409B	08	REQUEST STATUS	ABE11800
	NUM	0	09	DRIVER STATUS	ABE11900
	NUM	0	10	CURRENT LOCATION	ABE12000
	NUM	0	11	LAST LOCATION PLUS ONE	ABE12100
	NUM	0	12	DEVICE STATUS	ABE12200
	ADC	L1732B	13	DRIVER LENGTH IF MASS MEMORY	ABE12300
	ADC	C1732B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABE12400
	NUM	0	15	RESERVED FOR FNR AND CMR	ABE12500
	NUM	\$0540	16	FUNCTION AND UNIT CONTROL	ABE12600
	ADC	U732B2	17	DIAGNOSTIC LU	ABE12700
	ADC	0	18	ERROR CODE	ABE12800
	NUM	0	19	RECOVERY RETURN ADDRESS	ABE12900
	NUM	0	20	RECORD CHECKSUM	ABE13000
	NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ABE13100
	NUM	0	22	TEMPORARY CHECKSUM	ABE13200
	ADC	P732B3	23	PHYSIC THREAD	ABE13300
	ADC	PHSREC	24	MAX PHY RECORD SIZE (7 TRACK)	ABE13400
	ADC	RF1F32	25	PACK/LNPACK BUFFER (7 TRACK)	ABE13500
	NUM	0	26	END-OF-TAPE FLAG	ABE13600
	EUT				ABE13700

		6 0 8 / 9 B U F F E R E D T A P E , U N I T 3			
	EQU	U732B3 (0)			ABE13800
	EQU	U732B3 (X732B3-LOG1A)			ABE13900
	SPC	1			ABE14000
P732B3	ADC	\$520A	00	SCHEDULER CALL	ABE14100
	ADC	T1732B	01	INITIATOR ADDRESS	ABE14200
	ADC	C1732B	02	CONTINUATOR ADDRESS	ABE14300
	ADC	E1732B	03	TIMEOUT ERROR ADDRESS	ABE14400
	NUM	-1	04	DIAGNOSTIC CLOCK	ABE14500
	NUM	0	05	LOGICAL UNIT	ABE14600
	NUM	0	06	PARAMETER LOCATION	ABE14700
	ADC	\$0381+N1706B	07	CONVERTER, EQUIPMENT, STATION	ABE14800
	ADC	\$0806+T408B	08	REQUEST STATUS	ABE14900
	ADC	\$0806+T409B	08	REQUEST STATUS	ABE15000
	NUM	0	09	DRIVER STATUS	ABE15100
	NUM	0	10	CURRENT LOCATION	ABE15200
	NUM	0	11	LAST LOCATION PLUS ONE	ABE15300
	NUM	0	12	DEVICE STATUS	ABE15400
	ADC	L1732B	13	DRIVER LENGTH IF MASS MEMORY	ABE15500
					ABE15600
					ABE15700
					ABE15800
					ABE15900

Figure 1-34. 1732-1/608/609 Buffered Magnetic Tape Physical Device Table, Section ABE {Continued}

ADC	S1732B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABE16000
NUM	0	15	RESERVED FOR FNR AND CMR	ABE16100
NUM	\$05C0	16	FUNCTION AND UNIT CONTROL	ABE16200
ADC	U732B3	17	DIAGNOSTIC LU	ABE16300
ADC	0	18	ERROR CODE	ABE16400
NUM	0	19	RECOVERY RETURN ADDRESS	ABE16500
NUM	0	20	RECORD CHECKSUM	ABE16600
NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ABE16700
NUM	0	22	TEMPORARY CHECKSUM	ABE16800
ADC	P732B4	23	PHYSTB THREAD	ABE16900
ADC	PHSREC	24	MAX PHY RECCRD SIZE (7 TRACK)	ABE17000
ADC	BF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ABE17100
NUM	0	26	END-OF-TAPE FLAG	ABE17200
EJT				ABE17300
* A 0 8 / 9 B U F F E R E D T A P E , U N I T 4				ABE17400
* EQU U732B4(0)				ABE17500
EQU U732B4(X732B4-LCG1A)				ABE17600
SPC 1				ABE17700
P732B4	ADC \$520A	00	SCHEDULER CALL	ABE17800
	ADC I1732B	01	INITIATOR ADDRESS	ABE17900
	ADC C1732B	02	CONTINUATOR ADDRESS	ABE18000
	ADC F1732B	03	TIMEOUT ERROR ADDRESS	ABE18100
	NUM -1	04	DIAGNOSTIC CLOCK	ABE18200
	NUM 0	05	LOGICAL UNIT	ABE18300
	NUM 0	06	PARAMETER LOCATION	ABE18400
	ADC \$0381+N17068	07	CONVERTER, EQUIPMENT, STATION	ABE18500
	ADC \$0806+T608B	08	REQUEST STATUS	ABE18600
	ADC \$0806+T609B	09	REQUEST STATUS	ABE18700
	NUM 0	10	DRIVER STATUS	ABE18800
	NUM 0	11	CURRENT LOCATION	ABE18900
	NUM 0	12	LAST LOCATION PLUS ONE	ABE19000
	NUM 0	13	DEVICE STATUS	ABE19100
	ADC L1732B	14	DRIVER LENGTH IF MASS MEMORY	ABE19200
	ADC S1732B	15	NAME ASSOCIATED WITH SECTOR NUMBER	ABE19300
	NUM 0	16	RESERVED FOR FNR AND CMR	ABE19400
	NUM \$0640	17	FUNCTION AND UNIT CONTROL	ABE19500
	ADC U732B4	18	DIAGNOSTIC LU	ABE19600
	ADC 0	19	ERROR CODE	ABE19700
	NUM 0	20	RECOVERY RETURN ADDRESS	ABE19800
	NUM 0	21	RECORD CHECKSUM	ABE19900
	NUM \$DCDC	22	FUNCTION DIRECTORY BITWORD	ABE20000
	NUM 0	23	TEMPORARY CHECKSUM	ABE20100
	ADC P732B5	24	PHYSTB THREAD	ABE20200
	ADC PHSREC	25	MAX PHY RECCRD SIZE (7 TRACK)	ABE20300
	ADC BF1F32	26	PACK/UNPACK BUFFER (7 TRACK)	ABE20400
	NUM 0		END-OF-TAPE FLAG	ABE20500
	EJT			ABE20600
				ABE20700

Figure 1-34. 1732-1/608/609 Buffered Magnetic Tape Physical Device Table, Section ABE {Continued}

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		6 0 8 / 9 B U F F E R E D T A P E , U N I T 5			
	EQL	U73285 (n)			ABE20800
	EQL	U73285 (x73285-LOG1A)			ABE20900
	SPC	1			ABE21000
P73285	ADC	\$520A	00	SCHEDULER CALL	ABE21200
	ADC	Y1732B	01	INITIATOR ADDRESS	ABE21300
	ADC	C1732B	02	CONTINUATOR ADDRESS	ABE21400
	ADC	F1732B	03	TIMEOUT ERROR ADDRESS	ABE21500
	NUM	-1	04	DIAGNOSTIC CLOCK	ABE21600
	NUM	0	05	LOGICAL UNIT	ABE21700
	NUM	0	06	PARAMETER LOCATION	ABE21800
	ADC	\$0381+N1700B	07	CONVERTER, EQUIPMENT, STATION	ABE21900
	ADC	\$0806+T608B	08	REQUEST STATUS	ABE22000
	ADC	\$0806+T609B	08	REQUEST STATUS	ABE22100
	NUM	0	09	DRIVER STATUS	ABE22200
	NUM	0	10	CURRENT LOCATION	ABE22300
	NUM	0	11	LAST LOCATION PLUS ONE	ABE22400
	NUM	0	12	DEVICE STATUS	ABE22500
	ADC	L1732B	13	DRIVER LENGTH IF MASS MEMORY	ABE22600
	ADC	S1732B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABE22700
	NUM	0	15	RESERVED FOR FNR AND CMR	ABE22800
	NUM	\$06C0	16	FUNCTION AND UNIT CONTROL	ABE22900
	ADC	U73285	17	DIAGNOSTIC LOG	ABE23000
	ADC	0	18	ERROR CODE	ABE23100
	NUM	0	19	RECOVERY RETURN ADDRESS	ABE23200
	NUM	0	20	RECORD CHECKSUM	ABE23300
	NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ABE23400
	NUM	0	22	TEMPORARY CHECKSUM	ABE23500
	ADC	P73286	23	PHYSTB THREAD	ABE23600
	ADC	PHSREC	24	MAX PHY RECORDED SIZE (7 TRACK)	ABE23700
	ADC	BF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ABE23800
	NUM	0	26	END-OF-TAPE FLAG	ABE23900
	EJT				ABE24000
		6 0 8 / 9 B U F F E R E D T A P E , U N I T 6			
	EQL	U73286 (n)			ABE24100
	EQL	U73286 (x73286-LOG1A)			ABE24200
	SPC	1			ABE24300
P73286	ADC	\$520A	00	SCHEDULER CALL	ABE24400
	ADC	Y1732B	01	INITIATOR ADDRESS	ABE24500
	ADC	C1732B	02	CONTINUATOR ADDRESS	ABE24600
	ADC	F1732B	03	TIMEOUT ERROR ADDRESS	ABE24700
	NUM	-1	04	DIAGNOSTIC CLOCK	ABE24800
	NUM	0	05	LOGICAL UNIT	ABE24900
	NUM	0	06	PARAMETER LOCATION	ABE25000
	ADC	\$0381+N1700B	07	CONVERTER, EQUIPMENT, STATION	ABE25100
	ADC	\$0806+T608B	08	REQUEST STATUS	ABE25200
	ADC	\$0806+T609B	08	REQUEST STATUS	ABE25300
	NUM	0	09	DRIVER STATUS	ABE25400
					ABE25500
					ABE25600
					ABE25700

Figure 1-34. 1732-1/608/609 Buffered Magnetic Tape Physical Device
Table, Section ABE {CONTINUED}

NUM	0	10	CURRENT LOCATION	ARE25800
NUM	0	11	LAST LOCATION PLUS ONE	ARE25900
NUM	0	12	DEVICE STATUS	ARE26000
ADC	L1732B	13	DRIVER LENGTH IF MASS MEMORY	ARE26100
ADC	S1732B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARE26200
NUM	0	15	RESERVED FOR FNR AND CMR	ARE26300
NUM	\$0740	16	FUNCTION AND UNIT CONTROL	ARE26400
ADC	U732B6	17	DIAGNOSTIC LU	ARE26500
ADC	0	18	ERROR CODE	ARE26600
NUM	0	19	RECOVERY RETURN ADDRESS	ARE26700
NUM	0	20	RECORD CHECKSUM	ARE26800
NUM	\$0C0C	21	FUNCTION DIRECTORY BITWORD	ARE26900
NUM	0	22	TEMPORARY CHECKSUM	ARE27000
ADC	P732B7	23	PHYSR THREAD	ARE27100
ADC	PHSREC	24	MAX PHY RECCRD SIZE (7 TRACK)	ARE27200
ADC	RF1F32	25	PACK/UNPACK BUFFER (7 TRACK)	ARE27300
NUM	0	26	END-OF-TAPE FLAG	ARE27400
EJT				ARE27500
* A 0 8 / 9 B U F F E R E D T A P E U N I T 7				ARE27600
* EQC U732B7 (G)				ARE27700
EQC U732B7 (X732B7-LOG1A)				ARE27800
SPC 1				ARE27900
P732B7	ADC	\$520A	00 SCHEDULER CALL	ARE28000
	ADC	T1732B	01 INITIATOR ADDRESS	ARE28100
	ADC	C1732B	02 CONTINUATOR ADDRESS	ARE28200
	ADC	F1732B	03 TIMEOUT ERROR ADDRESS	ARE28300
	NUM	-1	04 DIAGNOSTIC CLOCK	ARE28400
	NUM	0	05 LOGICAL UNIT	ARE28500
	NUM	0	06 PARAMETER LOCATION	ARE28600
	ADC	\$0381+M1700B	07 CONVERTER, EQUIPMENT, STATION	ARE28700
	ADC	\$0806+T608B	08 REQUEST STATUS	ARE28800
	ADC	\$0806+T609B	08 REQUEST STATUS	ARE28900
	NUM	0	09 DRIVER STATUS	ARE29000
	NUM	0	10 CURRENT LOCATION	ARE29100
	NUM	0	11 LAST LOCATION PLUS ONE	ARE29200
	NUM	0	12 DEVICE STATUS	ARE29300
	ADC	L1732B	13 DRIVER LENGTH IF MASS MEMORY	ARE29400
	ADC	S1732B	14 NAME ASSOCIATED WITH SECTOR NUMBER	ARE29500
	NUM	0	15 RESERVED FOR FNR AND CMR	ARE29600
	NUM	\$07C0	16 FUNCTION AND UNIT CONTROL	ARE29700
	ADC	U732B7	17 DIAGNOSTIC LU	ARE29800
	ADC	0	18 ERROR CODE	ARE29900
	NUM	0	19 RECOVERY RETURN ADDRESS	ARE30000
	NUM	0	20 RECCRD CHECKSUM	ARE30100
	NUM	\$0C0C	21 FUNCTION DIRECTORY BITWORD	ARE30200
	NUM	0	22 TEMPORARY CHECKSUM	ARE30300
	ADC	P732B7	23 PHYSR THREAD	ARE30400
	ADC	PHSREC	24 MAX PHY RECCRD SIZE (7 TRACK)	ARE30500
	ADC	RF1F32	25 PACK/UNPACK BUFFER (7 TRACK)	ARE30600
	NUM	0	26 END-OF-TAPE FLAG	ARE30700
	EJT			ARE30800
				ARE30900

Figure 1-34. 1732-1/608/609 Buffered Magnetic Tape Physical Device Table, Section ABE {CONTINUED}

1.32 1731-601 Unbuffered Magnetic Tape Physical Device Tables,
Section ABF

Up to eight 1731-601 unbuffered units may be included in a system. Each unit must have its own physical device table. A 1731-601 unbuffered magnetic tape physical device table is similar to a table for a 608/609 unbuffered magnetic tape unit, as discussed in Section 1.30. The following differences exist:

- {1} The maximum physical record size and the pack/unpack buffer word are not specified in a physical device table for a 1731-601 magnetic tape unit, since these parameters are contained in the driver.
- {2} Words 23 and 26 of a physical device table for a 608/609 unbuffered unit correspond to words 24 and 25, respectively, of a table for a 601 unbuffered unit. {Compare Figure 1-33 with Figure 1-35.}
- {3} Word 7 is the same for all 601 units since the number of tracks is seven for all 601 units.

		1 7 3 1 - 0 0 1 M A G T A P E		ARF00100
				ARF00200
	SPC	1		ARF00300
	EQL	M1731U(0)		ARF00400
	EQL	M1731U(1)		ARF00500
				ARF00600
	IFA	M1731U.EQ.0	CORE RESIDENT DRIVER	ARF00700
	EXT	T1731U		ARF00800
	EXT	C1731U		ARF00900
	EXT	F1731U		ARF01000
	EQL	L1731U(0)		ARF01100
	EQL	S1731U(*7FFF)		ARF01200
	EIF			ARF01300
				ARF01400
	IFA	M1731U.EQ.1	MASS RESIDENT DRIVER	ARF01500
	EXT	MASDRV		ARF01600
	EXT	MASCON		ARF01700
	EXT	MASERR		ARF01800
I1731U	JMF+	MASDRV	INITIATE DRIVER	ARF01900
C1731U	JMF+	MASCON	INTERRUPT RESPONSE	ARF02000
E1731U	JMF+	MASERR	TIMEOUT ERROR	ARF02100
	EXT	L1731U		ARF02200
	EXT	S1731U		ARF02300
	EIF			ARF02400
				ARF02500
	SPC	2		ARF02600
R1731U	LDG	=XP731U0	INTERRUPT RESPONSE FOR THE 601 MAG TAPE	ARF02700
	JMF+	(P731U0+2)		ARF02800
	EUT			ARF02900
				ARF03000
		1 7 3 1 - 0 0 1 M A G T A P E , U N I T 0		ARF03100
				ARF03200
	EQL	U731U0(0)		ARF03300
	EQL	U731U0(X731U0-LCG1A)		ARF03400
	SPC	1		ARF03500
P731U0	ADC	*520F	00 SCHEDULER CALL	ARF03600
	ADC	T1731U	01 INITIATOR ADDRESS	ARF03700
	ADC	C1731U	02 CONTINUATOR ADDRESS	ARF03800
	ADC	F1731U	03 TIMEOUT ERROR ADDRESS	ARF03900
	NUM	-1	04 DIAGNOSTIC CLOCK	ARF04000
	NUM	0	05 LOGICAL UNIT	ARF04100
	NUM	0	06 PARAMETER LOCATION	ARF04200
	NUM	*0381	07 CONVERTER, EQUIPMENT, STATION	ARF04300
	NUM	\$0896	08 REQUEST STATUS	ARF04400
	NUM	0	09 DRIVER STATUS	ARF04500
	NUM	0	10 CURRENT LOCATION	ARF04600
	NUM	0	11 LAST LOCATION PLUS ONE	ARF04700
	NUM	0	12 DEVICE STATUS	ARF04800
	ADC	L1731U	13 DRIVER LENGTH IF MASS MEMORY	ARF04900
	ADC	S1731U	14 NAME ASSOCIATED WITH SECTOR NUMBER	ARF05000
	NUM	0	15 RESERVED FOR FNR AND CMR	ARF05100
	NUM	*0414	16 FUNCTION AND UNIT CONTROL	ARF05200
	ADC	U731U0	17 DIAGNOSTIC LU	ARF05300
	NUM	0	18 ERROR CODE	ARF05400
	NUM	0	19 RECOVERY RETURN ADDRESS	ARF05500
	NUM	0	20 RECORD CHECKSUM	

Figure 1-35. 1731-601 Unbuffered Magnetic Tape Physical Device Table, Section ABF

NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ABF05600
NUM	0	22	TEMPORARY CHECKSUM	ABF05700
NUM	0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ABF05800
ADC	P73101	24	PHYSIB THREAD	ABF05900
NUM	0	25	END-OF-TAPE FLAG	ABF06000

1 7 3 1 - 0 0 1 M A G T A P E , U N I T 1

EQL	U73101(0)			ABF06400
EQL	U73101(x73101-LCG1A)			ABF06500
SPC	1			ABF06600
P73101	ADC	\$520E	00 SCHEDULER CALL	ABF06700
	ADC	T1731L	01 INITIATOR ADDRESS	ABF06800
	ADC	C1731L	02 CONTINUATOR ADDRESS	ABF06900
	ADC	E1731L	03 TIMEOUT ERROR ADDRESS	ABF07000
	NUM	-1	04 DIAGNOSTIC CLOCK	ABF07100
	NUM	0	05 LOGICAL UNIT	ABF07200
	NUM	0	06 PARAMETER LOCATION	ABF07300
	NUM	\$0381	07 CONVERTER, EQUIPMENT, STATION	ABF07400
	NUM	\$0896	08 REQUEST STATUS	ABF07500
	NUM	0	09 DRIVER STATUS	ABF07600
	NUM	0	10 CURRENT LOCATION	ABF07700
	NUM	0	11 LAST LOCATION PLUS ONE	ABF07800
	NUM	0	12 DEVICE STATUS	ABF07900
	ADC	L1731L	13 DRIVER LENGTH IF MASS MEMORY	ABF08000
	ADC	\$1731L	14 NAME ASSOCIATED WITH SECTOR NUMBER	ABF08100
	NUM	0	15 RESERVED FOR FNR AND CMR	ABF08200
	NUM	\$0494	16 FUNCTION AND UNIT CONTROL	ABF08300
	ADC	U73101	17 DIAGNOSTIC LU	ABF08400
	NUM	0	18 ERROR CODE	ABF08500
	NUM	0	19 RECOVERY RETURN ADDRESS	ABF08600
	NUM	0	20 RECORD CHECKSUM	ABF08700
	NUM	\$DCDC	21 FUNCTION DIRECTORY BITWORD	ABF08800
	NUM	0	22 TEMPORARY CHECKSUM	ABF08900
	NUM	0	23 READ/WRITE PROCESSOR RETURN ADDRESS	ABF09000
	ADC	P73102	24 PHYSIB THREAD	ABF09100
	NUM	0	25 END-OF-TAPE FLAG	ABF09200

1 7 3 1 - 0 0 1 M A G T A P E , U N I T 2

EQL	U73102(0)			ABF09300
EQL	U73102(x73102-LCG1A)			ABF09400
SPC	1			ABF09500
P73102	ADC	\$520E	00 SCHEDULER CALL	ABF09600
	ADC	T1731L	01 INITIATOR ADDRESS	ABF09700
	ADC	C1731L	02 CONTINUATOR ADDRESS	ABF09800
	ADC	E1731L	03 TIMEOUT ERROR ADDRESS	ABF09900
	NUM	-1	04 DIAGNOSTIC CLOCK	ABF10000
	NUM	0	05 LOGICAL UNIT	ABF10100
	NUM	0	06 PARAMETER LOCATION	ABF10200
	NUM	\$0381	07 CONVERTER, EQUIPMENT, STATION	ABF10300
	NUM	\$0896	08 REQUEST STATUS	ABF10400
	NUM	0	09 DRIVER STATUS	ABF10500
	NUM	0	10 CURRENT LOCATION	ABF10600
	NUM	0	11 LAST LOCATION PLUS ONE	ABF10700
	NUM	0	12 DEVICE STATUS	ABF10800
	NUM	0	13 DRIVER LENGTH IF MASS MEMORY	ABF10900
	ADC	L1731L	14 NAME ASSOCIATED WITH SECTOR NUMBER	ABF11000
	NUM	0	15 RESERVED FOR FNR AND CMR	ABF11100
	NUM	\$0494	16 FUNCTION AND UNIT CONTROL	ABF11200

Figure 1-35. 1731-601 Unbuffered Magnetic Tape Physical Device Table Section ABF (Continued)

ADC	S1731U	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABF11300
NUM	0	15	RETURN AND TEMP STORAGE	ABF11400
NUM	\$0514	14	FUNCTION AND UNIT CONTROL	ABF11500
ADC	U731U2	17	DIAGNOSTIC LU	ABF11600
NUM	0	18	ERROR CODE	ABF11700
NUM	0	19	RECOVERY RETURN ADDRESS	ABF11800
NUM	0	20	RECORD CHECKSUM	ABF11900
NUM	\$0000	21	FUNCTION DIRECTORY BITWORD	ABF12000
NUM	0	22	TEMPORARY CHECKSUM	ABF12100
NUM	0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ABF12200
ADC	P731U3	24	PHYSIC THREAD	ABF12300
NUM	0	25	END-OF-TAPE FLAG	ABF12400
EUT				ABF12500
* 1 7 3 1 - 0 0 1 M A G T A P E , U N I T 3				
* EQU U731U3(0)				
EQU U731U3(X731U3-LOG1A)				
SPC 1				
P731U3	ADC \$520F	00	SCHEDULER CALL	ABF13000
	ADC T1731U	01	INITIATOR ADDRESS	ABF13100
	ADC C1731U	02	CONTINUATOR ADDRESS	ABF13200
	ADC E1731U	03	TIMEOUT ERROR ADDRESS	ABF13300
	NUM -1	04	DIAGNOSTIC CLOCK	ABF13400
	NUM 0	05	LOGICAL UNIT	ABF13500
	NUM 0	06	PARAMETER LOCATION	ABF13600
	NUM \$0381	07	CONVERTER, EQUIPMENT, STATION	ABF13700
	NUM \$0896	08	REQUEST STATUS	ABF13800
	NUM 0	09	DRIVER STATUS	ABF13900
	NUM 0	10	CURRENT LOCATION	ABF14000
	NUM 0	11	LAST LOCATION PLUS ONE	ABF14100
	NUM 0	12	DEVICE STATUS	ABF14200
	ADC L1731U	13	DRIVER LENGTH IF MASS MEMORY	ABF14300
	ADC S1731U	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABF14400
	NUM 0	15	RESERVED FOR FNR AND CMR	ABF14500
	NUM \$0594	14	FUNCTION AND UNIT CONTROL	ABF14600
	ADC U731U3	17	DIAGNOSTIC LU	ABF14700
	NUM 0	18	ERROR CODE	ABF14800
	NUM 0	19	RECOVERY RETURN ADDRESS	ABF14900
	NUM 0	20	RECORD CHECKSUM	ABF15000
	NUM \$0000	21	FUNCTION DIRECTORY BITWORD	ABF15100
	NUM 0	22	TEMPORARY CHECKSUM	ABF15200
	NUM 0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ABF15300
	ADC P731U4	24	PHYSIC THREAD	ABF15400
	NUM 0	25	END-OF-TAPE FLAG	ABF15500
EUT				ABF15600
* 1 7 3 1 - 0 0 1 M A G T A P E , U N I T 4				
* EQU U731U4(0)				
EQU U731U4(X731U4-LOG1A)				
SPC 1				
P731U4	ADC \$520F	00	SCHEDULER CALL	ABF15700
	ADC T1731U	01	INITIATOR ADDRESS	ABF15800
	ADC C1731U	02	CONTINUATOR ADDRESS	ABF15900
	ADC E1731U	03	TIMEOUT ERROR ADDRESS	ABF16000
	NUM -1	04	DIAGNOSTIC CLOCK	ABF16100
	NUM 0	05	LOGICAL UNIT	ABF16200
	NUM 0	06	PARAMETER LOCATION	ABF16300
				ABF16400
				ABF16500
				ABF16600
				ABF16700
				ABF16800
				ABF16900

Figure 1-35. 1731-601 Unbuffered Magnetic Tape Physical Device Table, Section ABF {Continued}

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NUM	\$0381	07	CONVERTER, EQUIPMENT, STATION	ABF17000
NUM	\$0896	08	REQUEST STATUS	ABF17100
NUM	0	09	DRIVER STATUS	ABF17200
NUM	0	10	CURRENT LOCATION	ABF17300
NUM	0	11	LAST LOCATION PLUS ONE	ABF17400
NUM	0	12	DEVICE STATUS	ABF17500
ADC	L1731L	13	DRIVER LENGTH IF MASS MEMORY	ABF17600
ADC	S1731L	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABF17700
NUM	0	15	RESERVED FOR FNR AND CMR	ABF17800
NUM	\$0614	16	FUNCTION AND UNIT CONTROL	ABF17900
ADC	U731U4	17	DIAGNOSTIC LU	ABF18000
NUM	0	18	ERROR CODE	ABF18100
NUM	0	19	RECOVERY RETURN ADDRESS	ABF18200
NUM	0	20	RECORD CHECKSUM	ABF18300
NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ABF18400
NUM	0	22	TEMPORARY CHECKSUM	ABF18500
NUM	0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ABF18600
ADC	P731U5	24	PHYSIB THREAD	ABF18700
NUM	0	25	END-OF-TAPE FLAG	ABF18800

1 7 3 1 - 0 0 1 M A G T A P E . U N I T 5

EQL	U731U5(0)			ABF18900	
EQL	U731U5(X731U5-LOG1A)			ABF19000	
SPC	1			ABF19100	
P731U5	ADC	\$520F	00	SCHEDULER CALL	ABF19200
	ADC	T1731L	01	INITIATOR ADDRESS	ABF19300
	ADC	C1731L	02	CONTINUATOR ADDRESS	ABF19400
	ADC	E1731L	03	TIMEOUT ERROR ADDRESS	ABF19500
	NUM	-1	04	DIAGNOSTIC CLOCK	ABF19600
	NUM	0	05	LOGICAL UNIT	ABF19700
	NUM	0	06	PARAMETER LOCATION	ABF19800
	NUM	\$0381	07	CONVERTER, EQUIPMENT, STATION	ABF19900
	NUM	\$0896	08	REQUEST STATUS	ABF20000
	NUM	0	09	DRIVER STATUS	ABF20100
	NUM	0	10	CURRENT LOCATION	ABF20200
	NUM	0	11	LAST LOCATION PLUS ONE	ABF20300
	NUM	0	12	DEVICE STATUS	ABF20400
	ADC	L1731L	13	DRIVER LENGTH IF MASS MEMORY	ABF20500
	ADC	S1731L	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABF20600
	NUM	0	15	RESERVED FOR FNR AND CMR	ABF20700
	NUM	\$0694	16	FUNCTION AND UNIT CONTROL	ABF20800
	ADC	U731U5	17	DIAGNOSTIC LU	ABF20900
	NUM	0	18	ERROR CODE	ABF21000
	NUM	0	19	RECOVERY RETURN ADDRESS	ABF21100
	NUM	0	20	RECORD CHECKSUM	ABF21200
	NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ABF21300
	NUM	0	22	TEMPORARY CHECKSUM	ABF21400
	NUM	0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ABF21500
	ADC	P731U6	24	PHYSIB THREAD	ABF21600
	NUM	0	25	END-OF-TAPE FLAG	ABF21700

1 7 3 1 - 0 0 1 M A G T A P E . U N I T 6

EQL	U731U6(0)			ABF21800
EQL	U731U6(X731U6-LOG1A)			ABF21900
SPC	1			ABF22000
				ABF22100
				ABF22200
				ABF22300
				ABF22400
				ABF22500
				ABF22600

Figure 1-35. 1731-601 Unbuffered Magnetic Tape Physical Device Table Section ABF (Continued)

P73106	ADC	\$520F	00	SCHEDULER CALL	ARF22700
	ADC	T1731U	01	INITIATOR ADDRESS	ARF22800
	ADC	C1731U	02	CONTINUATOR ADDRESS	ARF22900
	ADC	E1731U	03	TIMEOUT ERROR ADDRESS	ARF23000
	NUM	-1	04	DIAGNOSTIC CLOCK	ARF23100
	NUM	0	05	LOGICAL UNIT	ARF23200
	NUM	0	06	PARAMETER LOCATION	ARF23300
	NUM	\$0381	07	CONVERTER, EQUIPMENT, STATION	ARF23400
	NUM	\$0896	08	REQUEST STATUS	ARF23500
	NUM	0	09	DRIVER STATUS	ARF23600
	NUM	0	10	CURRENT LOCATION	ARF23700
	NUM	0	11	LAST LOCATION PLUS ONE	ARF23800
	NUM	0	12	DEVICE STATUS	ARF23900
	ADC	L1731U	13	DRIVER LENGTH IF MASS MEMORY	ARF24000
	ADC	S1731U	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARF24100
	NUM	0	15	RESERVED FOR FNR AND CMP	ARF24200
	NUM	\$0714	16	FUNCTION AND UNIT CONTROL	ARF24300
	ADC	U731U6	17	DIAGNOSTIC LU	ARF24400
	NUM	0	18	ERROR CODE	ARF24500
	NUM	0	19	RECOVERY RETURN ADDRESS	ARF24600
	NUM	0	20	RECORD CHECKSUM	ARF24700
	NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ARF24800
	NUM	0	22	TEMPORARY CHECKSUM	ARF24900
	NUM	0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ARF25000
	ADC	P731U7	24	PHYSIB THREAD	ARF25100
	NUM	0	25	END-OF-TAPE FLAG	ARF25200
	EJT				ARF25300
		1 7 3 1 - 6 0 1		M A G T A P E . U N I T 7	ARF25400
	EQU	U731U7(0)			ARF25500
	EQU	U731U7(X731U7-LC61A)			ARF25600
	SPC	1			ARF25700
P731U7	ADC	\$520E	00	SCHEDULER CALL	ARF25800
	ADC	T1731U	01	INITIATOR ADDRESS	ARF25900
	ADC	C1731U	02	CONTINUATOR ADDRESS	ARF26000
	ADC	E1731U	03	TIMEOUT ERROR ADDRESS	ARF26100
	NUM	-1	04	DIAGNOSTIC CLOCK	ARF26200
	NUM	0	05	LOGICAL UNIT	ARF26300
	NUM	0	06	PARAMETER LOCATION	ARF26400
	NUM	\$0381	07	CONVERTER, EQUIPMENT, STATION	ARF26500
	NUM	\$0896	08	REQUEST STATUS	ARF26600
	NUM	0	09	DRIVER STATUS	ARF26700
	NUM	0	10	CURRENT LOCATION	ARF26800
	NUM	0	11	LAST LOCATION PLUS ONE	ARF26900
	NUM	0	12	DEVICE STATUS	ARF27000
	ADC	L1731U	13	DRIVER LENGTH IF MASS MEMORY	ARF27100
	ADC	S1731U	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARF27200
	NUM	0	15	RESERVED FOR FNR AND CMP	ARF27300
	NUM	\$0794	16	FUNCTION AND UNIT CONTROL	ARF27400
	ADC	U731U7	17	DIAGNOSTIC LU	ARF27500
	NUM	0	18	ERROR CODE	ARF27600
	NUM	0	19	RECOVERY RETURN ADDRESS	ARF27700
	NUM	0	20	RECORD CHECKSUM	ARF27800
	NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ARF27900
	NUM	0	22	TEMPORARY CHECKSUM	ARF28000
	NUM	0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ARF28100
	ADC	P731U0	24	PHYSIB THREAD	ARF28200
	NUM	0	25	END-OF-TAPE FLAG	ARF28300
	EJT				ARF28400
					ARF28500

Figure 1-35. 1731-601 Unbuffered Magnetic Tape Physical Device Table, Section ABF {Continued}

1.33 1731-601 Buffered Magnetic Tape Physical Device Table,
Section ABG

A maximum of eight 1731-601 buffered magnetic tape units may be included in a system. Each must have its own physical device table. The words of this table are defined in the same way as in a physical device table for a 1731-601 unbuffered unit except that word 7 also contains the converter code for the 1706 Buffered Data Channel {Figure 1-36}.

		1 7 3 1 - 0 0 1 B U F F E R E D T A P E		ARG00100
				ARG00200
	SPC	1		ARG00300
	EQL	N7062(1)		ARG00400
	EQL	N17061(N7062*+100)		ARG00500
	EQL	M1731B(0)		ARG00600
	EQL	M1731B(1)		ARG00700
				ARG00800
	IFA	M1731B.FQ.0 CORE RESIDENT DRIVER		ARG00900
	EXT	T1731B		ARG01000
	EXT	C1731B		ARG01100
	EXT	E1731B		ARG01200
	EQL	S1731B(\$7FFF)		ARG01300
	EIF			ARG01400
				ARG01500
	IFA	M1731B.FQ.1 MASS RESIDENT DRIVER		ARG01600
	EXT	MASDRV		ARG01700
	EXT	MASCON		ARG01800
	EXT	MASERR		ARG01900
				ARG02000
I1731B	JMP+	MASDRV	INITIATE DRIVER	ARG02000
C1731B	JMP+	MASCON	INTERRUPT RESPONSE	ARG02100
E1731B	JMP+	MASERR	TIMEOUT ERROR	ARG02200
	EXT	L1731B		ARG02300
	EXT	S1731B		ARG02400
	EIF			ARG02500
				ARG02600
				ARG02700
				ARG02800
				ARG02900
				ARG03000
				ARG03100
				ARG03200
				ARG03300
				ARG03400
				ARG03500
				ARG03600
				ARG03700
				ARG03800
				ARG03900
				ARG04000
				ARG04100
				ARG04200
				ARG04300
				ARG04400
				ARG04500
				ARG04600
				ARG04700
				ARG04800
				ARG04900
				ARG05000
				ARG05100
				ARG05200
				ARG05300
				ARG05400
				ARG05500
				ARG05600
				ARG05700
				ARG05800
				ARG05900
				ARG06000
				ARG06100
				ARG06200
				ARG06300
				ARG06400
				ARG06500
				ARG06600
				ARG06700
				ARG06800
				ARG06900
				ARG07000
				ARG07100
				ARG07200
				ARG07300
				ARG07400
				ARG07500
				ARG07600
				ARG07700
				ARG07800
				ARG07900
				ARG08000
				ARG08100
				ARG08200
				ARG08300
				ARG08400
				ARG08500
				ARG08600
				ARG08700
				ARG08800
				ARG08900
				ARG09000
				ARG09100
				ARG09200
				ARG09300
				ARG09400
				ARG09500
				ARG09600
				ARG09700
				ARG09800
				ARG09900
				ARG10000

Figure 1-36. 1731-601 Buffered Magnetic Tape Physical Device Table, Section ABG

NUM	0	22	TEMPORARY CHECKSUM	ARG05700
NUM	0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ARG05800
ADC	P731B1	24	PHYSTB THREAD	ARG05900
NUM	0	25	END-OF-TAPE FLAG	ARG06000
EJT				ARG06100

1 7 3 1 - 0 0 1 B U F F E R E D T A P E , U N I T 1

EQL	U731B1(0)			ARG06200
EQL	U731B1(X731B1-LOG1A)			ARG06300
SPC	1			ARG06400
				ARG06500
				ARG06600

P731B1	ADC	\$520A	00	SCHEDULER CALL	ARG06700
	ADC	I1731B	01	INITIATOR ADDRESS	ARG06800
	ADC	C1731B	02	CONTINUATOR ADDRESS	ARG06900
	ADC	E1731B	03	TIMEOUT ERROR ADDRESS	ARG07000
	NUM	-1	04	DIAGNOSTIC CLOCK	ARG07100
	NUM	0	05	LOGICAL UNIT	ARG07200
	NUM	0	06	PARAMETER LOCATION	ARG07300
	ADC	\$0381+N17001	07	CONVERTER, EQUIPMENT, STATION	ARG07400
	NUM	\$0A86	08	REQUEST STATUS	ARG07500
	NUM	0	09	DRIVER STATUS	ARG07600
	NUM	0	10	CURRENT LOCATION	ARG07700
	NUM	0	11	LAST LOCATION PLUS ONE	ARG07800
	NUM	0	12	DEVICE STATUS	ARG07900
	ADC	L1731B	13	DRIVER LENGTH IF MASS MEMORY	ARG08000
	ADC	S1731B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARG08100
	NUM	0	15	RESERVED FOR FNR AND CMR	ARG08200
	NUM	\$0494	16	FUNCTION AND UNIT CONTROL	ARG08300
	ADC	U731B1	17	DIAGNOSTIC LU	ARG08400
	NUM	0	18	ERROR CODE	ARG08500
	NUM	0	19	RECOVERY RETURN ADDRESS	ARG08600
	NUM	0	20	RECORD CHECKSUM	ARG08700
	NUM	\$0C0C	21	FUNCTION DIRECTORY BITWORD	ARG08800
	NUM	0	22	TEMPORARY CHECKSUM	ARG08900
	NUM	0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ARG09000
	ADC	P731B2	24	PHYSTB THREAD	ARG09100
	NUM	0	25	END-OF-TAPE FLAG	ARG09200
EJT				ARG09300	

1 7 3 1 - 0 0 1 B U F F E R E D T A P E , U N I T 2

EQL	U731B2(0)			ARG09400
EQL	U731B2(X731B2-LOG1A)			ARG09500
SPC	1			ARG09600
				ARG09700
				ARG09800
				ARG09900

P731B2	ADC	\$520A	00	SCHEDULER CALL	ARG10000
	ADC	I1731B	01	INITIATOR ADDRESS	ARG10100
	ADC	C1731B	02	CONTINUATOR ADDRESS	ARG10200
	ADC	E1731B	03	TIMEOUT ERROR ADDRESS	ARG10300
	NUM	-1	04	DIAGNOSTIC CLOCK	ARG10400
	NUM	0	05	LOGICAL UNIT	ARG10500
	NUM	0	06	PARAMETER LOCATION	ARG10600
	ADC	\$0381+N17001	07	CONVERTER, EQUIPMENT, STATION	ARG10700
	NUM	\$0A86	08	REQUEST STATUS	ARG10800
	NUM	0	09	DRIVER STATUS	ARG10900
	NUM	0	10	CURRENT LOCATION	ARG11000
	NUM	0	11	LAST LOCATION PLUS ONE	ARG11100
	NUM	0	12	DEVICE STATUS	ARG11200
	ADC	L1731B	13	DRIVER LENGTH IF MASS MEMORY	ARG11300
	ADC	S1731B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARG11400
	NUM	0	15	RESERVED FOR FNR AND CMR	ARG11500
	NUM	\$0514	16	FUNCTION AND UNIT CONTROL	ARG11600
	ADC	U731B2	17	DIAGNOSTIC LU	

Figure 1-36. 1731-601 Buffered Magnetic Tape Physical Device Table

NUM	0	18	ERRCR CCDF	ARG11700
NUM	0	19	RECCVRY RETURN ADDRESS	ARG11800
NUM	0	20	RECCRD CHECKSUM	ARG11900
NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ARG12000
NUM	0	22	TEMPORARY CHECKSUM	ARG12100
NUM	0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ARG12200
ADC	P731B3	24	PHYSTB THREAD	ARG12300
NUM	0	25	END-OF-TAPE FLAG	ARG12400
EJT				ARG12500
* 1 7 3 1 - 0 0 1 B U F F E R E D T A P E , U N I T 3				ARG12600
EQL	U731B3(0)			ARG12700
EQL	U731B3(X731B3-LOG1A)			ARG12800
SPC	1			ARG12900
P731B3	ADC	\$520A	00 SCHEDULER CALL	ARG13000
	ADC	T1731F	01 INITIATOR ADDRESS	ARG13100
	ADC	C1731B	02 CONTINUATOR ADDRESS	ARG13200
	ADC	F1731F	03 TIMEOUT ERROR ADDRESS	ARG13300
	NUM	-1	04 DIAGNOSTIC CLOCK	ARG13400
	NUM	0	05 LOGICAL UNIT	ARG13500
	NUM	0	06 PARAMETER LOCATION	ARG13600
	ADC	\$0381+N17001	07 CONVERTER, EQUIPMENT, STATION	ARG13700
	NUM	\$0A86	08 REQUEST STATUS	ARG13800
	NUM	0	09 DRIVER STATUS	ARG13900
	NUM	0	10 CURRENT LOCATION	ARG14000
	NUM	0	11 LAST LOCATION PLUS ONE	ARG14100
	NUM	0	12 DEVICE STATUS	ARG14200
	ADC	L1731F	13 DRIVER LENGTH IF MASS MEMORY	ARG14300
	ADC	S1731B	14 NAME ASSOCIATED WITH SECTOR NUMBER	ARG14400
	NUM	0	15 RESERVED FOR FNR AND QMR	ARG14500
	NUM	\$0594	16 FUNCTION AND UNIT CONTROL	ARG14600
	ADC	U731B3	17 DIAGNOSTIC LU	ARG14700
	NUM	0	18 ERRCR CCDF	ARG14800
	NUM	0	19 RECCVRY RETURN ADDRESS	ARG14900
	NUM	0	20 RECCRD CHECKSUM	ARG15000
	NUM	\$DCDC	21 FUNCTION DIRECTORY BITWORD	ARG15100
	NUM	0	22 TEMPORARY CHECKSUM	ARG15200
	NUM	0	23 READ/WRITE PROCESSOR RETURN ADDRESS	ARG15300
	ADC	P731B4	24 PHYSTB THREAD	ARG15400
	NUM	0	25 END-OF-TAPE FLAG	ARG15500
EJT				ARG15600
* 1 7 3 1 - 0 0 1 B U F F E R E D T A P E , U N I T 4				ARG15700
EQL	U731B4(0)			ARG15800
EQL	U731B4(X731B4-LOG1A)			ARG15900
SPC	1			ARG16000
P731B4	ADC	\$520A	00 SCHEDULER CALL	ARG16100
	ADC	T1731F	01 INITIATOR ADDRESS	ARG16200
	ADC	C1731B	02 CONTINUATOR ADDRESS	ARG16300
	ADC	F1731F	03 TIMEOUT ERROR ADDRESS	ARG16400
	NUM	-1	04 DIAGNOSTIC CLOCK	ARG16500
	NUM	0	05 LOGICAL UNIT	ARG16600
	NUM	0	06 PARAMETER LOCATION	ARG16700
	ADC	\$0381+N17001	07 CONVERTER, EQUIPMENT, STATION	ARG16800
	NUM	\$0A86	08 REQUEST STATUS	ARG16900
	NUM	0	09 DRIVER STATUS	ARG17000
	NUM	0	10 CURRENT LOCATION	ARG17100
				ARG17200
				ARG17300

Figure 1-3b. 1731-601 Buffered Magnetic Tape Physical Device Table - Section ABG {Continued}

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NUM 0	11	LAST LOCATION PLUS ONE	ARG17400
NUM 0	12	DEVICE STATUS	ARG17500
ADC L1731B	13	DRIVER LENGTH IF MASS MEMORY	ARG17600
ADC S1731B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARG17700
NUM 0	15	RESERVED FOR FNR AND CMR	ARG17800
NUM \$0614	16	FUNCTION AND UNIT CONTROL	ARG17900
ADC U731B4	17	DIAGNOSTIC LU	ARG18000
NUM 0	18	ERROR CODE	ARG18100
NUM 0	19	RECOVERY RETURN ADDRESS	ARG18200
NUM 0	20	RECORD CHECKSUM	ARG18300
NUM \$DCDC	21	FUNCTION DIRECTORY BITWORD	ARG18400
NUM 0	22	TEMPORARY CHECKSUM	ARG18500
NUM 0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ARG18600
ADC P731B5	24	PHYSIB THREAD	ARG18700
NUM 0	25	END-OF-TAPE FLAG	ARG18800
EJT			ARG18900

* * 1 7 3 1 - 0 0 1 B U F F E R E D T A P E , U N I T 5

EQL U731B5(0)			ARG19000
EQL U731B5(X731B5-LOG1A)			ARG19100
SPC 1			ARG19200
P731B5 ADC \$520A	00	SCHEDULER CALL	ARG19300
ADC Y1731B	01	INITIATOR ADDRESS	ARG19400
ADC C1731B	02	CONTINUATOR ADDRESS	ARG19500
ADC F1731B	03	TIMEOUT ERROR ADDRESS	ARG19600
NUM -1	04	DIAGNOSTIC CLOCK	ARG19700
NUM 0	05	LOGICAL UNIT	ARG19800
NUM 0	06	PARAMETER LOCATION	ARG19900
ADC \$0381+N170B1	07	CONVERTER, EQUIPMENT, STATION	ARG20000
NUM \$0A86	08	REQUEST STATUS	ARG20100
NUM 0	09	DRIVER STATUS	ARG20200
NUM 0	10	CURRENT LOCATION	ARG20300
NUM 0	11	LAST LOCATION PLUS ONE	ARG20400
NUM 0	12	DEVICE STATUS	ARG20500
ADC L1731B	13	DRIVER LENGTH IF MASS MEMORY	ARG20600
ADC S1731B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARG20700
NUM 0	15	RESERVED FOR FNR AND CMR	ARG20800
NUM \$0694	16	FUNCTION AND UNIT CONTROL	ARG20900
ADC U731B5	17	DIAGNOSTIC LU	ARG21000
NUM 0	18	ERROR CODE	ARG21100
NUM 0	19	RECOVERY RETURN ADDRESS	ARG21200
NUM 0	20	RECORD CHECKSUM	ARG21300
NUM \$DCDC	21	FUNCTION DIRECTORY BITWORD	ARG21400
NUM 0	22	TEMPORARY CHECKSUM	ARG21500
NUM 0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ARG21600
ADC P731B6	24	PHYSIB THREAD	ARG21700
NUM 0	25	END-OF-TAPE FLAG	ARG21800
EJT			ARG21900

* * 1 7 3 1 - 0 0 1 B U F F E R E D T A P E , U N I T 6

EQL U731B6(0)			ARG22000
EQL U731B6(X731B6-LOG1A)			ARG22100
SPC 1			ARG22200
P731B6 ADC \$520A	00	SCHEDULER CALL	ARG22300
ADC Y1731B	01	INITIATOR ADDRESS	ARG22400
ADC C1731B	02	CONTINUATOR ADDRESS	ARG22500
ADC F1731B	03	TIMEOUT ERROR ADDRESS	ARG22600
NUM -1	04	DIAGNOSTIC CLOCK	ARG22700
NUM 0	05	LOGICAL UNIT	ARG22800
NUM 0	06	PARAMETER LOCATION	ARG22900

Figure 1-36. 1731-601 Buffered Magnetic Tape Physical Device Table Section ABG {Continued}

ADC	\$0381+N17001	07	CONVERTER, EQUIPMENT, STATION	ARG23400	
NUM	\$0A86	08	REQUEST STATUS	ARG23500	
NUM	0	09	DRIVER STATUS	ARG23600	
NUM	0	10	CURRENT LOCATION	ARG23700	
NUM	0	11	LAST LOCATION PLUS ONE	ARG23800	
NUM	0	12	DEVICE STATUS	ARG23900	
ADC	L1731B	13	DRIVER LENGTH IF MASS MEMORY	ARG24000	
ADC	S1731B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARG24100	
NUM	0	15	RESERVED FOR FNR AND CNR	ARG24200	
NUM	\$0714	16	FUNCTION AND UNIT CONTROL	ARG24300	
ADC	U731B6	17	DIAGNOSTIC LU	ARG24400	
NUM	0	18	ERROR CODE	ARG24500	
NUM	0	19	RECOVERY RETURN ADDRESS	ARG24600	
NUM	0	20	RECORD CHECKSUM	ARG24700	
NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ARG24800	
NUM	0	22	TEMPORARY CHECKSUM	ARG24900	
NUM	0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ARG25000	
ADC	P731B7	24	PHYSIB THREAD	ARG25100	
NUM	0	25	END-OF-TAPE FLAG	ARG25200	
EJT				ARG25300	
* 1 7 3 1 - 0 0 1 B U F F E R E D T A P E , U N I T 7				ARG25400	
* 1 7 3 1 - 0 0 1 B U F F E R E D T A P E , U N I T 7				ARG25500	
EQL	U731B7(0)			ARG25600	
EQL	U731B7(*731B7-LCG1A)			ARG25700	
SPC	1			ARG25800	
P731B7	ADC	\$520A	00	SCHEDULER CALL	ARG25900
	ADC	T1731B	01	INITIATOR ADDRESS	ARG26000
	ADC	C1731B	02	CONTINUATOR ADDRESS	ARG26100
	ADC	F1731B	03	TIMEOUT ERROR ADDRESS	ARG26200
	NUM	-1	04	DIAGNOSTIC CLOCK	ARG26300
	NUM	2	05	LOGICAL UNIT	ARG26400
	NUM	0	06	PARAMETER LOCATION	ARG26500
	ADC	\$0381+N17001	07	CONVERTER, EQUIPMENT, STATION	ARG26600
	NUM	\$0A86	08	REQUEST STATUS	ARG26700
	NUM	0	09	DRIVER STATUS	ARG26800
	NUM	0	10	CURRENT LOCATION	ARG26900
	NUM	0	11	LAST LOCATION PLUS ONE	ARG27000
	NUM	0	12	DEVICE STATUS	ARG27100
	ADC	L1731B	13	DRIVER LENGTH IF MASS MEMORY	ARG27200
	ADC	S1731B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ARG27300
	NUM	0	15	RESERVED FOR FNR AND CNR	ARG27400
	NUM	\$0794	16	FUNCTION AND UNIT CONTROL	ARG27500
	ADC	U731B7	17	DIAGNOSTIC LU	ARG27600
	NUM	0	18	ERROR CODE	ARG27700
	NUM	0	19	RECOVERY RETURN ADDRESS	ARG27800
	NUM	0	20	RECORD CHECKSUM	ARG27900
	NUM	\$DCDC	21	FUNCTION DIRECTORY BITWORD	ARG28000
	NUM	0	22	TEMPORARY CHECKSUM	ARG28100
	NUM	0	23	READ/WRITE PROCESSOR RETURN ADDRESS	ARG28200
	ADC	P731B7	24	PHYSIB THREAD	ARG28300
	NUM	0	25	END-OF-TAPE FLAG	ARG28400
	EJT			ARG28500	

Figure 1-36. 1731-601 Buffered Magnetic Tape Physical Device Table, Section ABG {Continued}

1.34 Pseudo Tape Physical Device Tables, Section ABH

The pseudo tape driver may be core resident or mass resident. The residency of the driver is indicated by the parameter PSTPDD {refer to Figure 1-37}. If PSTPDD is zero, the driver is core resident, and word 1 of each table is a driver entry. If PSTPDD is one, the driver is mass resident, and word 1 of each table is linked to a jump to an MMEEXEC entry.

Since the pseudo tape driver is a software driver, physical device table words 2, 3, and 4, associated with hardware operation, are not used. For the same reason, diagnostic logical unit capability is not available.

The initial value of word 12, device status, is dependent on whether the pseudo device is to be used by the background or whether it is to be accessed by the foreground. For a pseudo tape device used by the background, only bit 0 of word 12 is set initially. Bit 0 indicates the device is ready. No other bits are set since a background user is required to use certain Job Processor control statements before accessing a pseudo tape. These control statements will initialize other bits of the device status word as necessary. For a pseudo tape device to be accessed by the foreground, no Job Processor control statements will be used to initialize the device status. The initial value

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of word 12 for such a device is C401_{1b}. The initially-set bits are as follows:

<u>Bit</u>	<u>Meaning</u>
0	Ready
10	Load point
14	Mode {0 for read, 1 for write}
15	Write enabled

Word 16 contains the file number. This is zero for a physical device table for a background pseudo tape; non-zero for a foreground pseudo tape. Each foreground pseudo tape corresponds to one Job Processor file. Pseudo tape unit 0 is Job Processor file number 7FF8_{1b}, unit 1 is file number 7FF9_{1b}, etc. Up to eight pseudo tapes may be included in a system. If eight are included, the last pseudo tape is Job Processor file number 7FFF_{1b}.

Beginning at word 19 is a request buffer of 12 words. This buffer is used for making monitor requests when pseudo tapes are read or written.

Beginning at word 32 is a pointer block of 30 words. This block consists of the following:

1} a two-word pointer to the last block of records stored in the file.

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2) twenty-eight two-word pointers, each pointing to a record stored within this last block.

Each two-word pointer consists of a sector and a word within that sector. Pseudo tape records are written using File Manager store sequential requests. After twenty-eight sequential records have been written, the pointer block is also transferred to mass memory. By using these pointer blocks, pseudo tape file records may be read by using a Monitor read request instead of a File Manager retrieve request, thus saving time.

Although any number of pseudo tapes, up to a maximum of eight, are permissible in a system, care should be exercised in determining the number, in that each pseudo tape physical device table requires 76 words of core.

* P S E U D O T A P E				
	SPC	1		ABH00100
	EQL	PSTPDC(0)		ABH00200
	EQL	PSTPDC(1)		ABH00300
	EQL	PSTPDC(1)		ABH00400
	EQL	PSTPDC(1)		ABH00500
	IFA	PSTPDC, EQ, 0	CORE RESIDENT DRIVER	ABH00600
	EXT	TFSUDC		ABH00700
	EQL	LPSUDC(0)		ABH00800
	EQL	SPSUDC(\$7FFF)		ABH00900
	EIF			ABH01000
	EIF			ABH01100
	EIF			ABH01200
	IFA	PSTPDC, EQ, 1	MASS RESIDENT DRIVER	ABH01300
	EXT	MASDRV		ABH01400
IPSUDO	JMP	MASDRV	INITIATE DRIVER	ABH01500
	EXT	LPSUDC		ABH01600
	EXT	SPSUDC		ABH01700
	EIF			ABH01800
	EIF			ABH01900
	EJT			ABH02000
	EJT			ABH02100
	EJT			ABH02200
	EJT			ABH02300
	EJT			ABH02400
	EJT			ABH02500
	EJT			ABH02600
	EJT			ABH02700
	EJT			ABH02800
	EJT			ABH02900
	EJT			ABH03000
	EJT			ABH03100
	EJT			ABH03200
	EJT			ABH03300
	EJT			ABH03400
	EJT			ABH03500
	EJT			ABH03600
	EJT			ABH03700
	EJT			ABH03800
	EJT			ABH03900
	EJT			ABH04000
	EJT			ABH04100
	EJT			ABH04200
	EJT			ABH04300
	EJT			ABH04400
	EJT			ABH04500
	EJT			ABH04600
	EJT			ABH04700
	EJT			ABH04800
	EJT			ABH04900
	EJT			ABH05000
	EJT			ABH05100
	EJT			ABH05200
	EJT			ABH05300
	EJT			ABH05400
	EJT			ABH05500
	EJT			ABH05600

Figure 1-37. Pseudo Tape Physical Device Table, Section ABH

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* P S E U D O T A P E , U N I T 1

SPC	1				ABH05700
PSUD01	ADC \$5208	00	SCHEDULER CALL		ABH05800
	ADC TSPUDC	01	INITIATOR ADDRESS		ABH05900
	ADC 0	02	CONTINUATOR ADDRESS - NOT USED		ABH06000
	ADC 0	03	TIMEOUT ERROR ADDRESS - NOT USED		ABH06100
	NUM -1	04	DIAGNOSTIC CLOCK - NOT USED		ABH06200
	NUM 0	05	LOGICAL UNIT		ABH06300
	NUM 0	06	PARAMETER LOCATION		ABH06400
PWES1	NUM 0	07	CONVERTER, EQUIPMENT, STATION - NONE		ABH06500
	NUM \$0A46	08	REQUEST STATUS		ABH06600
	NUM 0	09	DRIVER STATUS		ABH06700
	NUM 0	10	CURRENT LOCATION		ABH06800
	NUM 0	11	LAST LOCATION PLUS ONE		ABH06900
	NUM 1	12	DEVICE STATUS		ABH07000
	NUM \$C401	12	DEVICE STATUS		ABH07100
	ADC LPSUDC	13	DRIVER LENGTH IF MASS MEMORY		ABH07200
	ADC SPSUDC	14	NAME ASSOCIATED WITH SECTOR NUMBER		ABH07300
	NUM 0	15	RESERVED FOR FNR AND CMR		ABH07400
	NUM 0	16	FILE NUMBER		ABH07500
	NUM \$7FF9	16	FILE NUMBER		ABH07600
	NUM 0	17	TEMP FOR MOTION REQ PROCESSOR		ABH07700
	NUM 0	18	TEMP FOR MOTION REQ PROCESSOR		ABH07800
	BZS (12)	19	REQUEST BUFFER - REQRUF		ABH07900
	NUM 0	31	BLOCK POINTER - BLKPTR		ABH08000
	BZS (30)	32	POINTER BLOCK - PTRBLK		ABH08100
PSDR01	NUM 0	62	*		ABH08200
	NUM \$5400	63	*		ABH08300
	NUM 0	64	* AREA TO BE STUFFED WITH		ABH08400
	ADC PWES1	65	* FILE MANAGER AND DISK REQUESTS		ABH08500
	BZS (6)	66	*		ABH08600
	NUM \$1400	72	*		ABH08700
	RTJ* (PSDRG1)	73	*		ABH08800
	ADC 0	74	INPUT BUFFER ADDRESS		ABH08900
	ADC PSUD02	75	PHYSIB THREAD		ABH09000
EJT					ABH09100
					ABH09200
					ABH09300
					ABH09400
					ABH09500
					ABH09600

* P S E U D O T A P E , U N I T 2

SPC	1				ABH09700
PSUD02	ADC \$5208	00	SCHEDULER CALL		ABH09800
	ADC TSPUDC	01	INITIATOR ADDRESS		ABH09900
	ADC 0	02	CONTINUATOR ADDRESS - NOT USED		ABH10000
	ADC 0	03	TIMEOUT ERROR ADDRESS - NOT USED		ABH10100
	NUM -1	04	DIAGNOSTIC CLOCK - NOT USED		ABH10200
	NUM 0	05	LOGICAL UNIT		ABH10300
	NUM 0	06	PARAMETER LOCATION		ABH10400
PWES2	NUM 0	07	CONVERTER, EQUIPMENT, STATION - NONE		ABH10500
	NUM \$0A46	08	REQUEST STATUS		ABH10600
	NUM 0	09	DRIVER STATUS		ABH10700
	NUM 0	10	CURRENT LOCATION		ABH10800
	NUM 0	11	LAST LOCATION PLUS ONE		ABH10900
	NUM 1	12	DEVICE STATUS		ABH11000
	NUM \$C401	12	DEVICE STATUS		ABH11100
	ADC LPSUDC	13	DRIVER LENGTH IF MASS MEMORY		ABH11200
	ADC SPSUDC	14	NAME ASSOCIATED WITH SECTOR NUMBER		ABH11300
	NUM 0	15	RESERVED FOR FNR AND CMR		ABH11400
	NUM 0	16	FILE NUMBER		ABH11500
	NUM \$7FFA	16	FILE NUMBER		ABH11500

Figure 1-37. Pseudo Tape Physical Device Table, Section ABH (Conti.

Label	Field	Value	Description	Address
	NUM	0	17 TEMP FOR MOTION REQ PROCESSOR	ABH11600
	NUM	0	18 TEMP FOR MOTION REQ PROCESSOR	ABH11700
	EZS	(12)	19 REQUEST BUFFER - REQBUF	ABH11800
	NUM	0	31 BLOCK POINTER - BLKPTR	ABH11900
	EZS	(30)	32 POINTER BLOCK - PTRBLK	ABH12000
PSDR02	NUM	0	62 *	ABH12100
	NUM	\$5400	63 *	ABH12200
	NUM	0	64 * AREA TO BE STUFFED WITH	ABH12300
	ADC	PWES2	65 * FILE MANAGER AND DISK REQUESTS	ABH12400
	EZS	(6)	66 *	ABH12500
	NUM	\$1400	72 *	ABH12600
	RTU*	(PSDR02)	73 *	ABH12700
	ADC	0	74 INPUT BUFFER ADDRESS	ABH12800
	ADC	PSUD03	75 PHYSIC THREAD	ABH12900
	EJT			ABH13000
*		P S E U D O	T A P E ,	U N I T
*				3
	SPC	1		ABH13100
PSUD03	ADC	\$5208	00 SCHEDULER CALL	ABH13200
	ADC	PSUD03	01 INITIATOR ADDRESS	ABH13300
	ADC	0	02 CONTINUATOR ADDRESS - NOT USED	ABH13400
	ADC	0	03 TIMEOUT ERROR ADDRESS - NOT USED	ABH13500
	NUM	-1	04 DIAGNOSTIC CLOCK - NOT USED	ABH13600
	NUM	0	05 LOGICAL UNIT	ABH13700
	NUM	0	06 PARAMETER LOCATION	ABH13800
PWES3	NUM	0	07 CONVERTER EQUIPMENT, STATION - NONE	ABH13900
	NUM	\$0A46	08 REQUEST STATUS	ABH14000
	NUM	0	09 DRIVER STATUS	ABH14100
	NUM	0	10 CURRENT LOCATION	ABH14200
	NUM	0	11 LAST LOCATION PLUS ONE	ABH14300
	NUM	1	12 DEVICE STATUS	ABH14400
	NUM	\$C401	13 DEVICE STATUS	ABH14500
	ADC	PSUD03	14 DRIVER LENGTH IF MASS MEMORY	ABH14600
	ADC	PSUD03	15 NAME ASSOCIATED WITH SECTOR NUMBER	ABH14700
	NUM	0	16 RESERVED FOR ENR AND CMR	ABH14800
	NUM	0	17 FILE NUMBER	ABH14900
	NUM	\$7FFB	18 FILE NUMBER	ABH15000
	NUM	0	19 TEMP FOR MOTION REQ PROCESSOR	ABH15100
	NUM	0	18 TEMP FOR MOTION REQ PROCESSOR	ABH15200
	EZS	(12)	19 REQUEST BUFFER - REQBUF	ABH15300
	NUM	0	31 BLOCK POINTER - BLKPTR	ABH15400
	EZS	(30)	32 POINTER BLOCK - PTRBLK	ABH15500
PSDR03	NUM	0	62 *	ABH15600
	NUM	\$5400	63 *	ABH15700
	NUM	0	64 * AREA TO BE STUFFED WITH	ABH15800
	ADC	PWES3	65 * FILE MANAGER AND DISK REQUESTS	ABH15900
	EZS	(6)	66 *	ABH16000
	NUM	\$1400	72 *	ABH16100
	RTU*	(PSDR03)	73 *	ABH16200
	ADC	0	74 INPUT BUFFER ADDRESS	ABH16300
	ADC	PSUD04	75 PHYSIC THREAD	ABH16400
	EJT			ABH16500
				ABH16600
				ABH16700

Figure 1-37. Pseudo Tape Physical Device Table, Section ABH {Continued}

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		P S E U D O T A P E , U N I T 4			
	SPC	1			ABH16800
PSUD04	ADC	\$5208	00	SCHEDULER CALL	ABH16900
	ADC	TPSUDC	01	INITIATOR ADDRESS	ABH17000
	ADC	0	02	CONTINUATOR ADDRESS - NOT USED	ABH17100
	ADC	0	03	TIMEOUT ERROR ADDRESS - NOT USED	ABH17200
	NUM	-1	04	DIAGNOSTIC CLOCK - NOT USED	ABH17300
	NUM	0	05	LOGICAL UNIT	ABH17400
	NUM	0	06	PARAMETER LOCATION	ABH17500
PWES4	NUM	0	07	CONVERTER, EQUIPMENT, STATION - NONE	ABH17600
	NUM	\$0A46	08	REQUEST STATUS	ABH17700
	NUM	0	09	DRIVER STATUS	ABH17800
	NUM	0	10	CURRENT LOCATION	ABH17900
	NUM	0	11	LAST LOCATION PLUS ONE	ABH18000
	NUM	1	12	DEVICE STATUS	ABH18100
	NUM	\$C401	12	DEVICE STATUS	ABH18200
	ADC	LPSUDC	13	DRIVER LENGTH IF MASS MEMORY	ABH18300
	ADC	SFSUDC	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABH18400
	NUM	0	15	RESERVED FOR ENR AND CMR	ABH18500
	NUM	0	16	FILE NUMBER	ABH18600
	NUM	\$7FFC	16	FILE NUMBER	ABH18700
	NUM	0	17	TEMP FOR MOTION REQ PROCESSOR	ABH18800
	NUM	0	18	TEMP FOR MOTION REQ PROCESSOR	ABH18900
	EZS	(12)	19	REQUEST BUFFER - REQUIF	ABH19000
	NUM	0	31	BLOCK POINTER - BLKPTR	ABH19100
	EZS	(30)	32	POINTER BLOCK - PTRBLK	ABH19200
PSDR04	NUM	0	62	*	ABH19300
	NUM	\$5400	62	*	ABH19400
	NUM	0	64	* AREA TO BE STUFFED WITH	ABH19500
	ADC	PWES4	65	* FILE MANAGER AND DISK REQUESTS	ABH19600
	EZS	(6)	66	*	ABH19700
	NUM	\$1400	72	*	ABH19800
	RTJ*	(PSDRG4)	73	*	ABH19900
	ADC	0	74	INPUT BUFFER ADDRESS	ABH20000
	ADC	PSUD05	75	PHYSIB THREAD	ABH20100
	EJT				ABH20200
					ABH20300
					ABH20400
					ABH20500

		P S E U D O T A P E , U N I T 5			
	SPC	1			ABH20600
PSUD05	ADC	\$5208	00	SCHEDULER CALL	ABH20700
	ADC	TPSUDC	01	INITIATOR ADDRESS	ABH20800
	ADC	0	02	CONTINUATOR ADDRESS - NOT USED	ABH20900
	ADC	0	03	TIMEOUT ERROR ADDRESS - NOT USED	ABH21000
	NUM	-1	04	DIAGNOSTIC CLOCK - NOT USED	ABH21100
	NUM	0	05	LOGICAL UNIT	ABH21200
	NUM	0	06	PARAMETER LOCATION	ABH21300
PWES5	NUM	0	07	CONVERTER, EQUIPMENT, STATION - NONE	ABH21400
	NUM	\$0A46	08	REQUEST STATUS	ABH21500
	NUM	0	09	DRIVER STATUS	ABH21600
	NUM	0	10	CURRENT LOCATION	ABH21700
	NUM	0	11	LAST LOCATION PLUS ONE	ABH21800
	NUM	1	12	DEVICE STATUS	ABH21900
	NUM	\$C401	12	DEVICE STATUS	ABH22000
	ADC	LPSUDC	13	DRIVER LENGTH IF MASS MEMORY	ABH22100
	ADC	SFSUDC	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABH22200
	NUM	0	15	RESERVED FOR ENR AND CMR	ABH22300
	NUM	0	16	FILE NUMBER	ABH22400
	NUM	\$7FFD	16	FILE NUMBER	ABH22500
	NUM	0	17	TEMP FOR MOTION REQ PROCESSOR	ABH22600
					ABH22700

Figure 1-37. Pseudo Tape Physical Device Table, Section ABH (Contir

	NUM	0	18	TEMP FOR MOTION REQ PROCESSOR	ABH22800
	BZS	(12)	19	REQUEST BUFFER - REQBUF	ABH22900
	NUM	0	31	BLOCK POINTER - BLKPTR	ABH23000
PSDR05	BZS	(30)	32	POINTER BLOCK - PTRBLK	ABH23100
	NUM	0	62	*	ABH23200
	NUM	\$5400	63	*	ABH23300
	NUM	0	64	* AREA TO BE STUFFED WITH	ABH23400
	ADC	PWES5	65	* FILE MANAGER AND DISK REQUESTS	ABH23500
	BZS	(6)	66	*	ABH23600
	NUM	\$1400	72	*	ABH23700
	RTU*	(PSDR05)	73	*	ABH23800
	ADC	0	74	INPUT BUFFER ADDRESS	ABH23900
	ADC	PSUD06	75	PHYSIC THREAD	ABH24000
	EJT				ABH24100
*		P S E U D O T A P E , U N I T 6			ABH24200
*					ABH24300
PSUD06	SPC	1	00	SCHEDULER CALL	ABH24400
	ADC	\$5208	01	INITIATOR ADDRESS	ABH24500
	ADC	1PSUDC	02	CONTINUATOR ADDRESS - NOT USED	ABH24600
	ADC	0	03	TIMEOUT ERROR ADDRESS - NOT USED	ABH24700
	ADC	0	03	TIMEOUT ERROR ADDRESS - NOT USED	ABH24800
	NUM	-1	04	DIAGNOSTIC CLOCK - NOT USED	ABH24900
	NUM	0	05	LOGICAL UNIT	ABH25000
	NUM	0	06	PARAMETER LOCATION	ABH25100
PWES6	NUM	0	07	CONVERTER, EQUIPMENT, STATION - NONE	ABH25200
	NUM	\$0A46	08	REQUEST STATUS	ABH25300
	NUM	0	09	DRIVER STATUS	ABH25400
	NUM	0	10	CURRENT LOCATION	ABH25500
	NUM	0	11	LAST LOCATION PLUS ONE	ABH25600
	NUM	1	12	DEVICE STATUS	ABH25700
	NUM	\$C401	12	DEVICE STATUS	ABH25800
	ADC	LPSUDC	13	DRIVER LENGTH IF MASS MEMORY	ABH25900
	ADC	0PSUDC	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABH26000
	NUM	0	15	RESERVED FOR FNR AND CMR	ABH26100
	NUM	0	16	FILE NUMBER	ABH26200
	NUM	\$7FFF	16	FILE NUMBER	ABH26300
	NUM	0	17	TEMP FOR MOTION REQ PROCESSOR	ABH26400
	NUM	0	18	TEMP FOR MOTION REQ PROCESSOR	ABH26500
	BZS	(12)	19	REQUEST BUFFER - REQBUF	ABH26600
	NUM	0	31	BLOCK POINTER - BLKPTR	ABH26700
PSDR06	BZS	(30)	32	POINTER BLOCK - PTRBLK	ABH26800
	NUM	0	62	*	ABH26900
	NUM	\$5400	63	*	ABH27000
	NUM	0	64	* AREA TO BE STUFFED WITH	ABH27100
	ADC	PWES6	65	* FILE MANAGER AND DISK REQUESTS	ABH27200
	BZS	(6)	66	*	ABH27300
	NUM	\$1400	72	*	ABH27400
	RTU*	(PSDR06)	73	*	ABH27500
	ADC	0	74	INPUT BUFFER ADDRESS	ABH27600
	ADC	PSUD07	75	PHYSIC THREAD	ABH27700
	EJT				ABH27800

Figure 1-37. Pseudo Tape Physical Device Table, Section ABH (Continued)

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* P S E U D O T A P E . U N I T 7				
	SPC	1		ARR27900
	ADC	\$5208	00 SCHEDULER CALL	ARR28000
PSUD07	ADC	TPSUDC	01 INITIATOR ADDRESS	ARR28100
	ADC	0	02 CONTINUATOR ADDRESS - NOT USED	ARR28200
	ADC	0	03 TIMEOUT ERROR ADDRESS - NOT USED	ARR28300
	NUM	-1	04 DIAGNOSTIC CLOCK - NOT USED	ARR28400
	NUM	0	05 LOGICAL UNIT	ARR28500
	NUM	0	06 PARAMETER LOCATION	ARR28600
PWES7	NUM	0	07 CONVERTER. EQUIPMENT. STATION - NONE	ARR28700
	NUM	\$0A46	08 REQUEST STATUS	ARR28800
	NUM	0	09 DRIVER STATUS	ARR28900
	NUM	0	10 CURRENT LOCATION	ARR29000
	NUM	0	11 LAST LOCATION PLUS ONE	ARR29100
	NUM	1	12 DEVICE STATUS	ARR29200
	NUM	\$C401	12 DEVICE STATUS	ARR29300
	ADC	LPSUDC	13 DRIVER LENGTH IF MASS MEMORY	ARR29400
	ADC	SPSUDC	14 NAME ASSOCIATED WITH SECTOR NUMBER	ARR29500
	NUM	0	15 RESERVED FOR FNR AND CMR	ARR29600
	NUM	0	16 FILE NUMBER	ARR29700
	NUM	\$7FFF	16 FILE NUMBER	ARR29800
	NUM	0	17 TEMP FOR MOTION REQ PROCESSOR	ARR29900
	NUM	0	18 TEMP FOR MOTION REQ PROCESSOR	ARR30000
	EZS	(12)	19 REQUEST BUFFER - REQBUF	ARR30100
	NUM	0	31 BLOCK POINTER - BLKPTR	ARR30200
	EZS	(30)	32 POINTER BLOCK - PTRBLK	ARR30300
PSDR07	NUM	0	62 *	ARR30400
	NUM	\$5400	63 *	ARR30500
	NUM	0	64 * AREA TO BE STUFFED WITH	ARR30600
	ADC	PWES7	65 * FILE MANAGER AND DISK REQUESTS	ARR30700
	EZS	(6)	66 *	ARR30800
	NUM	\$1400	72 *	ARR30900
	RTL*	(PSDR07)	73 *	ARR31000
	ADC	0	74 INPUT BUFFER ADDRESS	ARR31100
	ADC	PSUD00	75 PHYSTB THREAD	ARR31200
	EJT			ARR31300
				ARR31400
				ARR31500

Figure 1-37. Pseudo Tape Physical Device Table, Section ABH (Continued)

1.35 1733-2/856 Disk Physical Device Tables, Section ABI

Section ABI, Figure 1-38, contains the physical device tables for any 1733-2/856 disks in a system. In a given SYSDAT word 8 will contain the type code, T8562 for an 856-2; or T8564, for an 856-4. Up to 4 856 drives may be included in a system. There is one physical device table for each drive. These are threaded with word 22 of each table as the thread word.

This set of physical device tables has a standard interrupt response routine located at R17332. The address of the first physical device table is loaded into Q, and control is transferred to the driver continuator entry.

In each table, bit 15 of word 16 indicates whether the hardware is to be operated in the compare or no compare mode. If bit 15 is one, no compare will be performed. If bit 15 is zero, the compare feature will be used. Use of the compare feature slows down mass memory operation, but it will detect any data compare problems during a mass memory operation.

Note that the 1733-2/856 disk does not have diagnostic logical unit capability.

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1 7 3 3 - 2 / 8 5 6 D I S K

SPC 1
 ENT P332D0
 EXT 17332,C17332,E17332
 EQL T8562(15*\$10) TYPE CODE - 1733-2 856-2
 EQL T8564(16*\$10) TYPE CODE - 1733-2 856-4
 SPC 2
 R17332 LDC =XP73320 INTERRUPT RESPONSE FOR 1733-2 DISK
 JMP* (P73320+2)
 EJT

1 7 3 3 - 2 / 8 5 6 D I S K - U N I T 0

P73320 SPC 1
 EQL P332D0(*)
 ADC \$5209 00 SCHEDULER CALL
 ADC 17332 01 INITIATOR ADDRESS
 ADC C17332 02 CONTINUATOR ADDRESS
 ADC E17332 03 TIMEOUT ERROR ADDRESS
 NUM -1 04 DIAGNOSTIC CLOCK
 NUM 2 05 LOGICAL UNIT
 NUM 0 06 PARAMETER LOCATION
 NUM \$0181 07 CONVERTER, EQUIPMENT, STATION
 ADC \$1006+T8562 08 REQUEST STATUS
 ADC \$1006+T8564 08 REQUEST STATUS
 NUM \$0200 09 DRIVER STATUS
 NUM 0 10 CURRENT LOCATION
 NUM 0 11 LAST LOCATION PLUS ONE
 NUM 0 12 DEVICE STATUS
 NUM 0 13 ERROR COUNTER
 NUM 0 14 DATA TRANSFER FUNCTION
 NUM 0 15 SECTOR NUMBER OR FNR RETURN
 NUM \$8100 16 NO COMPARE FLAG / DIRECTOR FUNCTION
 NUM 0 17 TEMSEC - USED BY WORD ADDRESSING
 NUM 0 18 OVERLAY AREA (SCHEDULER CALL)
 NUM 0 19 OVERLAY AREA (COMPLETION ADDRESS)
 NUM 0 20 OVERLAY AREA (THREAD)
 NUM 0 21 OVERLAY AREA (LOGICAL UNIT)
 ADC P73321 22 PHYSTB THREAD
 NUM 0 23 RETURN ADDRESS FOR DATA TRANSFER
 NUM 0 24 FIRST SECTOR ADDRESS ON DISK 1
 NUM 0 25 LAST DATA TRANSFER FUNCTION
 NUM 0 26 BUFFER SIZE FOR SPLIT TRANSFERS
 NUM 0 27 CYLINDER ADDRESS FOR TRANSFER
 NUM 0 28 MASK FOR THIS UNITS SEEK COMPLETE BIT
 ADC BF332A 29 ADDRESS OF 96 WORD BUFFER
 NUM 0 30 TEMPORARY FOR WORD ADDRESSING
 NUM 0 31 TEMPORARY FOR WORD ADDRESSING
 NUM 0 32 TEMPORARY FOR WORD ADDRESSING
 NUM 0 33 REQUEST CODE
 NUM 0 34 REQUEST PRIORITY
 NUM 0 35 STARTING SECTOR FOR COMPARE OR RETRY
 NUM 0 36 FWA OF TRANSFER FOR COMPARE OR RETRY
 NUM 0 37 ERROR COUNTER
 NUM 0 38 DATA TRANSFER FUNCTION CODE
 NUM 0 39 SECTOR NUMBER CURRENTLY IN BUFFER
 NUM 0 40 LAST VALUE OF CYLINDER ADDRESS STATUS
 NUM 0 41 LAST VALUE OF C W A STATUS
 NUM 0 42 LAST VALUE OF CHECKWORD STATUS
 NUM 0 43 LAST VALUE OF DRIVE CYLINDER STATUS

ABI00100
 ABI00200
 ABI00300
 ABI00400
 ABI00500
 ABI00600
 ABI00700
 ABI00800
 ABI00900
 ABI01000
 ABI01100
 ABI01200
 ABI01300
 ABI01400
 ABI01500
 ABI01600
 ABI01700
 ABI01800
 ABI01900
 ABI02000
 ABI02100
 ABI02200
 ABI02300
 ABI02400
 ABI02500
 ABI02600
 ABI02700
 ABI02800
 ABI02900
 ABI03000
 ABI03100
 ABI03200
 ABI03300
 ABI03400
 ABI03500
 ABI03600
 ABI03700
 ABI03800
 ABI03900
 ABI04000
 ABI04100
 ABI04200
 ABI04300
 ABI04400
 ABI04500
 ABI04600
 ABI04700
 ABI04800
 ABI04900
 ABI05000
 ABI05100
 ABI05200
 ABI05300
 ABI05400
 ABI05500
 ABI05600
 ABI05700
 ABI05800
 ABI05900
 ABI06000

Figure 1-38. 1733-2/856 Disk Physical Device Table, Section ABI

EZS	BF332A(96)	44	BUFFER FOR WORD ADDRESSING	ABI06100
		139	BUFFER FOR WORD ADDRESSING	ABI06200
EJT				ABI06300
	1 7 3 3 - 2 / 8 5 6		D I S K - U N I T 1	ABI06400
				ABI06500
				ABI06600
				ABI06700
P73321	SPC 1			ABI06800
	ADC 5209	00	SCHEDULER CALL	ABI06900
	ADC T17332	01	INITIATOR ADDRESS	ABI07000
	ADC C17332	02	CONTINUATOR ADDRESS	ABI07100
	ADC E17332	03	TIMEOUT ERROR ADDRESS	ABI07200
	NUM -1	04	DIAGNOSTIC CLOCK	ABI07300
	NUM 0	05	LOGICAL UNIT	ABI07400
	NUM 0	06	PARAMETER LOCATION	ABI07500
	NUM \$0181	07	CONVERTER, EQUIPMENT, STATION	ABI07600
	ADC \$1006+T8562	08	REQUEST STATUS	ABI07700
	ADC \$1006+T8564	08	REQUEST STATUS	ABI07800
	NUM \$0200	09	DRIVER STATUS	ABI07900
	NUM 0	10	CURRENT LOCATION	ABI08000
	NUM 0	11	LAST LOCATION PLUS ONE	ABI08100
	NUM 0	12	DEVICE STATUS	ABI08200
	NUM 0	13	ERROR COUNTER	ABI08300
	NUM 0	14	DATA TRANSFER FUNCTION	ABI08400
	NUM 0	15	SECTOR NUMBER OR FNR RETURN	ABI08500
	NUM \$8300	16	NO COMPARE FLAG / DIRECTOR FUNCTION	ABI08600
	NUM 0	17	TEMSEC - USED BY WORD ADDRESSING	ABI08700
	NUM 0	18	OVERLAY AREA (SCHEDULER CALL)	ABI08800
	NUM 0	19	OVERLAY AREA (COMPLETION ADDRESS)	ABI08900
	NUM 0	20	OVERLAY AREA (THREAD)	ABI09000
	NUM 0	21	OVERLAY AREA (LOGICAL UNIT)	ABI09100
	ADC P73322	22	PHYSIB THREAD	ABI09200
	NUM 0	23	RETURN ADDRESS FOR DATA TRANSFER	ABI09300
	NUM 0	24	FIRST SECTOR ADDRESS ON DISK 1	ABI09400
	NUM 0	25	LAST DATA TRANSFER FUNCTION	ABI09500
	NUM 0	26	BUFFER SIZE FOR SPLIT TRANSFERS	ABI09600
	NUM 0	27	CYLINDER ADDRESS FOR TRANSFER	ABI09700
	NUM 0	28	MASK FOR THIS UNITS SEEK COMPLETE BIT	ABI09800
	ADC BF332B	29	ADDRESS OF 96 WORD BUFFER	ABI09900
	NUM 0	30	TEMPORARY FOR WORD ADDRESSING	ABI10000
	NUM 0	31	TEMPORARY FOR WORD ADDRESSING	ABI10100
	NUM 0	32	TEMPORARY FOR WORD ADDRESSING	ABI10200
	NUM 0	33	REQUEST CODE	ABI10300
	NUM 0	34	REQUEST PRIORITY	ABI10400
	NUM 0	35	STARTING SECTOR FOR COMPARE OR RETRY	ABI10500
	NUM 0	36	FWA OF TRANSFER FOR COMPARE OR RETRY	ABI10600
	NUM 0	37	ERROR COUNTER	ABI10700
	NUM 0	38	DATA TRANSFER FUNCTION CODE	ABI10800
	NUM 0	39	SECTOR NUMBER CURRENTLY IN BUFFER	ABI10900
	NUM 0	40	LAST VALUE OF CYLINDER ADDRESS STATUS	ABI11000
	NUM 0	41	LAST VALUE OF C W A STATUS	ABI11100
	NUM 0	42	LAST VALUE OF CHECKWORD STATUS	ABI11200
	NUM 0	43	LAST VALUE OF DRIVE CYLINDER STATUS	ABI11300
EZS	BF332B(96)	44	BUFFER FOR WORD ADDRESSING	ABI11400
		139	BUFFER FOR WORD ADDRESSING	ABI11500
EJT				ABI11600

Figure 1-38. 1733-2/856 Disk Physical Device Table, Section ABI (Co

* 1 7 3 3 - 2 / 8 5 6 D I S K - U N I T 2			
	SPC 1		ABI11700
P73322	ADC \$5209	00 SCHEDULER CALL	ABI11800
	ADC T17332	01 INITIATOR ADDRESS	ABI11900
	ADC C17332	02 CONTINUATOR ADDRESS	ABI12000
	ADC F17332	03 TIMEOUT ERROR ADDRESS	ABI12100
	NUM -1	04 DIAGNOSTIC CLOCK	ABI12200
	NUM 0	05 LOGICAL UNIT	ABI12300
	NUM 0	06 PARAMETER LOCATION	ABI12400
	NUM \$0181	07 CONVERTER, EQUIPMENT, STATION	ABI12500
	ADC \$1006+T8562	08 REQUEST STATUS	ABI12600
	ADC \$1006+T8564	09 REQUEST STATUS	ABI12700
	NUM \$0200	09 DRIVER STATUS	ABI12800
	NUM 0	10 CURRENT LOCATION	ABI12900
	NUM 0	11 LAST LOCATION PLUS ONE	ABI13000
	NUM 0	12 DEVICE STATUS	ABI13100
	NUM 0	13 ERROR COUNTER	ABI13200
	NUM 0	14 DATA TRANSFER FUNCTION	ABI13300
	NUM 0	15 SECTOR NUMBER OR FNR RETURN	ABI13400
	NUM \$8500	16 NO COMPARE FLAG / DIRECTOR FUNCTION	ABI13500
	NUM 0	17 TEMSEC - USED BY WORD ADDRESSING	ABI13600
	NUM 0	18 OVERLAY AREA (SCHEDULER CALL)	ABI13700
	NUM 0	19 OVERLAY AREA (COMPLETION ADDRESS)	ABI13800
	NUM 0	20 OVERLAY AREA (THREAD)	ABI13900
	NUM 0	21 OVERLAY AREA (LOGICAL UNIT)	ABI14000
	ADC P73323	22 PHYSTE THREAD	ABI14100
	NUM 0	23 RETURN ADDRESS FOR DATA TRANSFER	ABI14200
	NUM 0	24 FIRST SECTOR ADDRESS ON DISK 1	ABI14300
	NUM 0	25 LAST DATA TRANSFER FUNCTION	ABI14400
	NUM 0	26 BUFFER SIZE FOR SPLIT TRANSFERS	ABI14500
	NUM 0	27 CYLINDER ADDRESS FOR TRANSFER	ABI14600
	NUM 0	28 MASK FOR THIS UNITS SEEK COMPLETE BIT	ABI14700
	ADC BF332C	29 ADDRESS OF 96 WORD BUFFER	ABI14800
	NUM 0	30 TEMPORARY FOR WORD ADDRESSING	ABI14900
	NUM 0	31 TEMPORARY FOR WORD ADDRESSING	ABI15000
	NUM 0	32 TEMPORARY FOR WORD ADDRESSING	ABI15100
	NUM 0	33 REQUEST CODE	ABI15200
	NUM 0	34 REQUEST PRIORITY	ABI15300
	NUM 0	35 STARTING SECTOR FOR COMPARE OR RETRY	ABI15400
	NUM 0	36 FWA OF TRANSFER FOR COMPARE OR RETRY	ABI15500
	NUM 0	37 ERROR COUNTER	ABI15600
	NUM 0	38 DATA TRANSFER FUNCTION CODE	ABI15700
	NUM 0	39 SECTOR NUMBER CURRENTLY IN BUFFER	ABI15800
	NUM 0	40 LAST VALUE OF CYLINDER ADDRESS STATUS	ABI15900
	NUM 0	41 LAST VALUE OF C W A STATUS	ABI16000
	NUM 0	42 LAST VALUE OF CHECKWORD STATUS	ABI16100
	NUM 0	43 LAST VALUE OF DRIVE CYLINDER STATUS	ABI16200
			ABI16300
			ABI16400
			ABI16500
EZS	BF332C(96)	44 BUFFER FOR WORD ADDRESSING	ABI16600
		139 BUFFER FOR WORD ADDRESSING	ABI16700
EJT			ABI16800

Figure 1-38. 1733-2/856 Disk Physical Device Table, Section ABI
{Continued}

1 7 3 3 - 2 / 8 5 6 D I S K - U N I T 3

P73323

SPC	1				ABI16900
ADC	\$5209	00	SCHEDULER CALL		ABI17000
ADC	117332	01	INITIATOR ADDRESS		ABI17100
ADC	C17332	02	CONTINUATOR ADDRESS		ABI17200
ADC	E17332	03	TIMEOUT ERROR ADDRESS		ABI17300
NUM	-1	04	DIAGNOSTIC CLOCK		ABI17400
NUM	0	05	LOGICAL UNIT		ABI17500
NUM	0	06	PARAMETER LOCATION		ABI17600
NUM	\$0181	07	CONVERTER, EQUIPMENT, STATION		ABI17700
ADC	\$1006+T8562	08	REQUEST STATUS		ABI17800
ADC	\$1006+T8564	09	REQUEST STATUS		ABI17900
NUM	\$0200	09	DRIVER STATUS		ABI18000
NUM	0	10	CURRENT LOCATION		ABI18100
NUM	0	11	LAST LOCATION PLUS ONE		ABI18200
NUM	0	12	DEVICE STATUS		ABI18300
NUM	0	13	ERROR COUNTER		ABI18400
NUM	0	14	DATA TRANSFER FUNCTION		ABI18500
NUM	0	15	SECTOR NUMBER OR FNR RETURN		ABI18600
NUM	\$8700	16	NO COMPARE FLAG / DIRECTOR FUNCTION		ABI18700
NUM	0	17	TEMSEC - USED BY WORD ADDRESSING		ABI18800
NUM	0	18	OVERLAY AREA (SCHEDULER CALL)		ABI18900
NUM	0	19	OVERLAY AREA (COMPLETION ADDRESS)		ABI19000
NUM	0	20	OVERLAY AREA (THREAD)		ABI19100
NUM	0	21	OVERLAY AREA (LOGICAL UNIT)		ABI19200
ADC	P73320	22	PHYSIC THREAD		ABI19300
NUM	0	23	RETURN ADDRESS FOR DATA TRANSFER		ABI19400
NUM	0	24	FIRST SECTOR ADDRESS ON DISK 1		ABI19500
NUM	0	25	LAST DATA TRANSFER FUNCTION		ABI19600
NUM	0	26	BUFFER SIZE FOR SPLIT TRANSFERS		ABI19700
NUM	0	27	CYLINDER ADDRESS FOR TRANSFER		ABI19800
NUM	0	28	MASK FOR THIS UNITS SEEK COMPLETE BIT		ABI19900
ADC	BF3320	29	ADDRESS OF 56 WORD BUFFER		ABI20000
NUM	0	30	TEMPORARY FOR WORD ADDRESSING		ABI20100
NUM	0	31	TEMPORARY FOR WORD ADDRESSING		ABI20200
NUM	0	32	TEMPORARY FOR WORD ADDRESSING		ABI20300
NUM	0	33	REQUEST CODE		ABI20400
NUM	0	34	REQUEST PRIORITY		ABI20500
NUM	0	35	STARTING SECTOR FOR COMPARE OR RETRY		ABI20600
NUM	0	36	FWA OF TRANSFER FOR COMPARE OR RETRY		ABI20700
NUM	0	37	ERROR COUNTER		ABI20800
NUM	0	38	DATA TRANSFER FUNCTION CODE		ABI20900
NUM	0	39	SECTOR NUMBER CURRENTLY IN BUFFER		ABI21000
NUM	0	40	LAST VALUE OF CYLINDER ADDRESS STATUS		ABI21100
NUM	0	41	LAST VALUE OF C W A STATUS		ABI21200
NUM	0	42	LAST VALUE OF CHECKWORD STATUS		ABI21300
NUM	0	43	LAST VALUE OF DRIVE CYLINDER STATUS		ABI21400
EZS	BF3320(96)	44	BUFFER FOR WORD ADDRESSING		ABI21500
EJ1		139	BUFFER FOR WORD ADDRESSING		ABI21600
					ABI21700
					ABI21800
					ABI21900
					ABI22000

Figure 1-38. 1733-2/856 Disk Physical Device Table, Section ABI (Continued)

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1.36 1738-853/854 Disk Physical Device Tables, Section ABJ

Section ABJ, Figure 1-39, contains physical device tables for up to two disk drives. The type code in word 8 of each table specifies whether the corresponding drive is an 853 or an 854. If a drive has a corresponding diagnostic logical unit word 22 contains an index into the logical unit numbers in the LOG1A table. Word 23 is the thread word used to thread the physical device tables in this section.

The interrupt response routine for this section is located at R1738. The interrupt response is the MSOS standard interrupt response routine.

Word 16 of each table determines whether or not the hardware compare feature is to be used. If bit 15 of word 16 is one, no compare will be performed. If bit 15 is zero, the compare feature will be used. Use of the compare feature slows down mass memory operation, but it will detect any data compare problems during a mass memory operation.

1 7 3 8 8 5 3 / 4 D I S K , U N I T 0			
SPC	1		ARJ00100
EXT	I1738,C1738,E1738		ARJ00200
EQL	T853(05*F10) TYPE CODE - 853 DISK		ARJ00300
EQL	T854(08*F10) TYPE CODE - 854 DISK		ARJ00400
EQL	U17380(0)		ARJ00500
EQL	U17380(X17380-LOG1A)		ARJ00600
SPC	2		ARJ00700
R1738	LDG =XP17380	INTERRUPT RESPONSE FOR 853/4 DISK	ARJ00800
	JMP* (P17380+2)		ARJ00900
SPC	2		ARJ01000
P17380	ADC \$5209	00 SCHEDULER CALL	ARJ01100
	ADC T1738	01 INITIATOR ADDRESS	ARJ01200
	ADC C1738	02 CONTINUATOR ADDRESS	ARJ01300
	ADC E1738	03 TIMEOUT ERROR ADDRESS	ARJ01400
	NUM -1	04 DIAGNOSTIC CLOCK	ARJ01500
	NUM 0	05 LOGICAL UNIT	ARJ01600
	NUM 0	06 PARAMETER LOCATION	ARJ01700
	NUM \$0181	07 CONVERTER, EQUIPMENT, STATION	ARJ01800
	ADC \$1006+T853	08 REQUEST STATUS	ARJ01900
	ADC \$1006+T854	09 REQUEST STATUS	ARJ02000
	NUM \$0200	10 DRIVER STATUS	ARJ02100
	NUM 0	11 CURRENT LOCATION	ARJ02200
	NUM 0	12 LAST LOCATION PLUS ONE	ARJ02300
	NUM 0	13 DEVICE STATUS	ARJ02400
	NUM 0	14 ERROR COUNTER	ARJ02500
	NUM 0	15 DATA TRANSFER FUNCTION	ARJ02600
	NUM 0	16 SECTOR NUMBER	ARJ02700
	NUM \$811A	17 NO COMPARE FLAG / DIRECTOR FUNCTION	ARJ02800
	NUM 0	18 SPARE	ARJ02900
	NUM 0	19 OVERLAY AREA (SCHEDULER CALL)	ARJ03000
	NUM 0	20 OVERLAY AREA (COMPLETION ADDRESS)	ARJ03100
	NUM 0	21 OVERLAY AREA (THREAD)	ARJ03200
	NUM 0	22 OVERLAY AREA (LOGICAL UNIT)	ARJ03300
	ADC U17380	23 DIAGNOSTIC LU	ARJ03400
	ADC P17381	PHYSIB THREAD	ARJ03500
EJT			ARJ03600

1 7 3 8 8 5 3 / 4 D I S K , U N I T 1			
SPC	1		ARJ03700
EQL	U17381(0)		ARJ03800
EQL	U17381(X17381-LOG1A)		ARJ03900
SPC	1		ARJ04000
P17381	ADC \$5209	00 SCHEDULER CALL	ARJ04100
	ADC T1738	01 INITIATOR ADDRESS	ARJ04200
	ADC C1738	02 CONTINUATOR ADDRESS	ARJ04300
	ADC E1738	03 TIMEOUT ERROR ADDRESS	ARJ04400
	NUM -1	04 DIAGNOSTIC CLOCK	ARJ04500
	NUM 0	05 LOGICAL UNIT	ARJ04600
	NUM 0	06 PARAMETER LOCATION	ARJ04700
	NUM \$0181	07 CONVERTER, EQUIPMENT, STATION	ARJ04800
	ADC \$1006+T853	08 REQUEST STATUS	ARJ04900
	ADC \$1006+T854	09 REQUEST STATUS	ARJ05000
	NUM \$0200	10 DRIVER STATUS	ARJ05100
	NUM 0	11 CURRENT LOCATION	ARJ05200
	NUM 0	12 LAST LOCATION PLUS ONE	ARJ05300
	NUM 0	13 DEVICE STATUS	ARJ05400
	NUM 0	14 ERROR COUNTER	ARJ05500
	NUM 0	15 DATA TRANSFER FUNCTION	ARJ05600
	NUM 0	16 SECTOR NUMBER	ARJ05700
	NUM \$831A	17 NO COMPARE FLAG / DIRECTOR FUNCTION	ARJ05800
			ARJ05900
			ARJ06000
			ARJ06100

Figure 1-39. 1738-853/854 Disk Physical Device Table, Section ABJ

NUM	0	17	SPARE	ABJ06200
NUM	0	18	OVERLAY AREA (SCHEDULER CALL)	ABJ06300
NUM	0	19	OVERLAY AREA (COMPLETION ADDRESS)	ABJ06400
NUM	0	20	OVERLAY AREA (THREAD)	ABJ06500
NUM	0	21	OVERLAY AREA (LOGICAL UNIT)	ABJ06600
ADC	U17381	22	DIAGNOSTIC LU	ABJ06700
ADC	P17380	23	PHYSIC THREAD	ABJ06800
EJT				ABJ06900

1.37 1733-1/853/854 Disk Physical Device Tables, Section ABK

This section is similar to Section ABJ, except that up to eight 853/854 Disk drives may be used with a 1733-1 Controller.

Each drive controlled by a 1733-1 must have a physical device table in this section. The data base for Section ABK is shown in Figure 1-40. A standard interrupt response routine is located at R17331.

In each table word 8 contains a type code depending on whether the drive is an 853 or an 854. Word 22 is the thread word, connecting all the tables in this section. Bit 15 of word 16 is set if the hardware compare feature is not to be used. Diagnostic logical unit capability is not available for 1733-1/853/854 Disk drives.

Words 17 - 21 and words 23-127 are used by the driver to handle word addressing requests, to provide an overlay area for requests which would read over request parameters, and to accomplish overlap seek capability. All of these words must be initially zero and may not be modified by the user.

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1 7 3 3 - 1 8 5 3 / 4 D I S K , U N I T 0

```

SPC 1
EXT I17331,C17331,E17331
EQL T33853(39*310) TYPE CODE - 1733-853
EQL T33854(14*310) TYPE CODE - 1733-854
SPC 2
R17331 LDG =XP73310 INTERRUPT RESPONSE FOR 1733-1 DISK
JMP* (P73310+2)
SPC 2
P73310 ADC $5209 00 SCHEDULER CALL
ADC T17331 01 INITIATOR ADDRESS
ADC C17331 02 CONTINUATOR ADDRESS
ADC E17331 03 TIMEOUT ERROR ADDRESS
NUM -1 04 DIAGNOSTIC CLOCK
NUM 0 05 LOGICAL UNIT
NUM 0 06 PARAMETER LOCATION
NUM $0181 07 CONVERTER, EQUIPMENT, STATION
ADC $1006+T33853 08 REQUEST STATUS
ADC $1006+T33854 08 REQUEST STATUS
NUM $0200 09 DRIVER STATUS
NUM 0 10 CURRENT LOCATION
NUM 0 11 LAST LOCATION PLUS ONE
NUM 0 12 DEVICE STATUS
NUM 0 13 ERROR COUNTER
NUM 0 14 DATA TRANSFER FUNCTION
NUM 0 15 FILE ADDRESS / SECTOR NUMBER
NUM $8100 16 NO COMPARE FLAG / DIRECTOR FUNCTION
NUM 0 17 LAST SECTOR READ INTO BUFFER
NUM 0 18 OVERLAY AREA (SCHEDULER CALL)
NUM 0 19 OVERLAY AREA (COMPLETION ADDRESS)
NUM 0 20 OVERLAY AREA (THREAD)
NUM 0 21 OVERLAY AREA (LOGICAL UNIT)
ADC P73311 22 PHYSTB THREAD
NUM 0 23 COMPARE FLAG
NUM 0 24 WORD NUMBER WITHIN SECTOR
NUM 0 25 TEMPORARY FWA
NUM 0 26 TEMPORARY LWA + 1
NUM 0 27 TEMPORARY SECTOR
NUM 0 28 TEMPORARY REQUEST PRIORITY
NUM 0 29 TEMPORARY REQUEST
NUM 0 30 TEMPORARY STORAGE
NUM 0 31 RETURN ADDRESS WITHIN DRIVER

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```

ABK00100
ABK00200
ABK00300
ABK00400
ABK00500
ABK00600
ABK00700
ABK00800
ABK00900
ABK01000
ABK01100
ABK01200
ABK01300
ABK01400
ABK01500
ABK01600
ABK01700
ABK01800
ABK01900
ABK02000
ABK02100
ABK02200
ABK02300
ABK02400
ABK02500
ABK02600
ABK02700
ABK02800
ABK02900
ABK03000
ABK03100
ABK03200
ABK03300
ABK03400
ABK03500
ABK03600
ABK03700
ABK03800
ABK03900
ABK04000
ABK04100
ABK04200
ABK04300

```

```

EVS (96) 32 BUFFER FOR WORD ADDRESSING
EVT 127 BUFFER FOR WORD ADDRESSING

```

1 7 3 3 - 1 8 5 3 / 4 D I S K , U N I T 1

```

SPC 1
P73311 ADC $5209 00 SCHEDULER CALL
ADC T17331 01 INITIATOR ADDRESS
ADC C17331 02 CONTINUATOR ADDRESS
ADC E17331 03 TIMEOUT ERROR ADDRESS
NUM -1 04 DIAGNOSTIC CLOCK
NUM 0 05 LOGICAL UNIT
NUM 0 06 PARAMETER LOCATION
NUM $0181 07 CONVERTER, EQUIPMENT, STATION
ADC $1006+T33853 08 REQUEST STATUS

```

```

ABK04400
ABK04500
ABK04600
ABK04700
ABK04800
ABK04900
ABK05000
ABK05100
ABK05200
ABK05300
ABK05400
ABK05500
ABK05600
ABK05700
ABK05800
ABK05900

```

Figure 1-40. 1733-1/853/854 Disk Physical Device Tables, Section ABK

ADC	\$1006+T33854	08	REQUEST STATUS	ARK06000	
NUM	\$0200	09	DRIVER STATUS	ARK06100	
NUM	0	10	CURRENT LOCATION	ARK06200	
NUM	0	11	LAST LOCATION PLUS ONE	ARK06300	
NUM	0	12	DEVICE STATUS	ARK06400	
NUM	0	13	ERRCR COUNTER	ARK06500	
NUM	0	14	DATA TRANSFER FUNCTION	ARK06600	
NUM	0	15	FILE ADDRESS / SECTOR NUMBER	ARK06700	
NUM	\$8300	16	NO COMPARE FLAG / DIRECTOR FUNCTION	ARK06800	
NUM	0	17	LAST SECTOR READ INTO BUFFER	ARK06900	
NUM	0	18	OVERLAY AREA (SCHEDULER CALL)	ARK07000	
NUM	0	19	OVERLAY AREA (COMPLETION ADDRESS)	ARK07100	
NUM	0	20	OVERLAY AREA (THREAD)	ARK07200	
NUM	0	21	OVERLAY AREA (LOGICAL UNIT)	ARK07300	
ADC	P73312	22	PHYSIC THREAD	ARK07400	
NUM	0	23	COMPARE FLAG	ARK07500	
NUM	0	24	WORD NUMBER WITHIN SECTOR	ARK07600	
NUM	0	25	TEMPORARY FWA	ARK07700	
NUM	0	26	TEMPORARY LWA + 1	ARK07800	
NUM	0	27	TEMPORARY SECTOR	ARK07900	
NUM	0	28	TEMPORARY REQUEST PRIORITY	ARK08000	
NUM	0	29	TEMPORARY REQUEST	ARK08100	
NUM	0	30	TEMPORARY STORAGE	ARK08200	
NUM	0	31	RETURN ADDRESS WITHIN DRIVER	ARK08300	
EZS	(96)	32	BUFFER FOR WORD ADDRESSING	ARK08400	
EJT		127	BUFFER FOR WORD ADDRESSING	ARK08500	
				ARK08600	
				ARK08700	
				ARK08800	
				ARK08900	
				ARK09000	
SPC	1			ARK09100	
P73312	ADC	\$5209	00	SCHEDULER CALL	ARK09200
	ADC	T17331	01	INITIATOR ADDRESS	ARK09300
	ADC	C17331	02	CONTINUATOR ADDRESS	ARK09400
	ADC	E17331	03	TIMEOUT ERROR ADDRESS	ARK09500
	NUM	-1	04	DIAGNOSTIC CLOCK	ARK09600
	NUM	0	05	LOGICAL UNIT	ARK09700
	NUM	0	06	PARAMETER LOCATION	ARK09800
	NUM	\$0181	07	CONVERTER, EQUIPMENT, STATION	ARK09900
	ADC	\$1006+T33853	08	REQUEST STATUS	ARK10000
	ADC	\$1006+T33854	08	REQUEST STATUS	ARK10000
	NUM	\$0200	09	DRIVER STATUS	ARK10100
	NUM	0	10	CURRENT LOCATION	ARK10200
	NUM	0	11	LAST LOCATION PLUS ONE	ARK10300
	NUM	0	12	DEVICE STATUS	ARK10400
	NUM	0	13	ERRCR COUNTER	ARK10500
	NUM	0	14	DATA TRANSFER FUNCTION	ARK10600
	NUM	0	15	FILE ADDRESS / SECTOR NUMBER	ARK10700
	NUM	\$8500	16	NO COMPARE FLAG / DIRECTOR FUNCTION	ARK10800
	NUM	0	17	LAST SECTOR READ INTO BUFFER	ARK10900
	NUM	0	18	OVERLAY AREA (SCHEDULER CALL)	ARK11000
	NUM	0	19	OVERLAY AREA (COMPLETION ADDRESS)	ARK11100
	NUM	0	20	OVERLAY AREA (THREAD)	ARK11200
	NUM	0	21	OVERLAY AREA (LOGICAL UNIT)	ARK11300
	ADC	P73313	22	PHYSIC THREAD	ARK11400
	NUM	0	23	COMPARE FLAG	ARK11500
	NUM	0	24	WORD NUMBER WITHIN SECTOR	ARK11600
	NUM	0	25	TEMPORARY FWA	ARK11700
	NUM	0	26	TEMPORARY LWA + 1	ARK11800

Figure 1-40. 1733-1/853/854 Disk Physical Device Tables, Section A1 (Continued)

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NUM	0	27	TEMPORARY SECTOR	ARK11900
NUM	0	28	TEMPORARY REQUEST PRIORITY	ARK12000
NUM	0	29	TEMPORARY REQUEST	ARK12100
NUM	0	30	TEMPORARY STORAGE	ARK12200
NUM	0	31	RETURN ADDRESS WITHIN DRIVER	ARK12300
* * * * *				
EZS	(96)	32	BUFFER FOR WORD ADDRESSING	ARK12400
		127	BUFFER FOR WORD ADDRESSING	ARK12500
EJ1				ARK12600
* * * * *				
	1 7 3 3 - 1	8 5 3 / 4	D I S K . U N I T 3	ARK12700
				ARK12800
				ARK12900
				ARK13000
SPC	1			ARK13100
P73313	ADC	\$5209	00 SCHEDULER CALL	ARK13200
	ADC	T17331	01 INITIATOR ADDRESS	ARK13300
	ADC	C17331	02 CONTINUATOR ADDRESS	ARK13400
	ADC	F17331	03 TIMEOUT ERROR ADDRESS	ARK13500
	NUM	-1	04 DIAGNOSTIC CLOCK	ARK13600
	NUM	0	05 LOGICAL UNIT	ARK13700
	NUM	0	06 PARAMETER LOCATION	ARK13800
	NUM	\$0181	07 CONVERTER, EQUIPMENT, STATION	ARK13900
	ADC	\$1006+T33R53	08 REQUEST STATUS	ARK14000
	ADC	\$1006+T33R54	09 REQUEST STATUS	ARK14100
	NUM	\$0200	10 DRIVER STATUS	ARK14200
	NUM	0	11 CURRENT LOCATION	ARK14300
	NUM	0	12 LAST LOCATION PLUS ONE	ARK14400
	NUM	0	13 DEVICE STATUS	ARK14500
	NUM	0	14 ERROR COUNTER	ARK14600
	NUM	0	15 DATA TRANSFER FUNCTION	ARK14700
	NUM	0	16 FILE ADDRESS / SECTOR NUMBER	ARK14800
	NUM	\$E700	17 NO COMPARE FLAG / DIRECTOR FUNCTION	ARK14900
	NUM	0	18 LAST SECTOR READ INTO BUFFER	ARK15000
	NUM	0	19 OVERLAY AREA (SCHEDULER CALL)	ARK15100
	NUM	0	20 OVERLAY AREA (COMPLETION ADDRESS)	ARK15200
	NUM	0	21 OVERLAY AREA (THREAD)	ARK15300
	NUM	0	22 OVERLAY AREA (LOGICAL UNIT)	ARK15400
	ADC	P73314	23 PHYSIC THREAD	ARK15500
	NUM	0	24 COMPARE FLAG	ARK15600
	NUM	0	25 WORD NUMBER WITHIN SECTOR	ARK15700
	NUM	0	26 TEMPORARY FWA	ARK15800
	NUM	0	27 TEMPORARY LWA + 1	ARK15900
	NUM	0	28 TEMPORARY SECTOR	ARK16000
	NUM	0	29 TEMPORARY REQUEST PRIORITY	ARK16100
	NUM	0	30 TEMPORARY REQUEST	ARK16200
	NUM	0	31 TEMPORARY STORAGE	ARK16300
	NUM	0	32 RETURN ADDRESS WITHIN DRIVER	ARK16400
* * * * *				
EZS	(96)	32	BUFFER FOR WORD ADDRESSING	ARK16500
		127	BUFFER FOR WORD ADDRESSING	ARK16600
EJ1				ARK16700
* * * * *				
	1 7 3 3 - 1	8 5 3 / 4	D I S K . U N I T 4	ARK16800
				ARK16900
				ARK17000
SPC	1			ARK17100
P73314	ADC	\$5209	00 SCHEDULER CALL	ARK17200
	ADC	T17331	01 INITIATOR ADDRESS	ARK17300
	ADC	C17331	02 CONTINUATOR ADDRESS	ARK17400
	ADC	F17331	03 TIMEOUT ERROR ADDRESS	ARK17500
	NUM	-1	04 DIAGNOSTIC CLOCK	ARK17600
	NUM	0	05 LOGICAL UNIT	ARK17700
	NUM	0	06 PARAMETER LOCATION	ARK17800
	NUM	\$0181	07 CONVERTER, EQUIPMENT, STATION	ARK17900
	ADC	\$1006+T33R53	08 REQUEST STATUS	ARK18000
	ADC	\$1006+T33R54	09 REQUEST STATUS	

Figure 1-40. 1733-1/853/854 Disk Physical Device Tables, Section ABK (Continued)

NUM	\$0200	09	DRIVER STATUS	ARK18100
NUM	0	10	CURRENT LOCATION	ARK18200
NUM	0	11	LAST LOCATION PLUS ONE	ARK18300
NUM	0	12	DEVICE STATUS	ARK18400
NUM	0	13	ERRCR COUNTER	ARK18500
NUM	0	14	DATA TRANSFER FUNCTION	ARK18600
NUM	0	15	FILE ADDRESS / SECTOR NUMBER	ARK18700
NUM	\$8900	16	NO COMPARE FLAG / DIRECTOR FUNCTION	ARK18800
NUM	0	17	LAST SECTOR READ INTO BUFFER	ARK18900
NUM	0	18	OVERLAY AREA (SCHEDULER CALL)	ARK19000
NUM	0	19	OVERLAY AREA (COMPLETION ADDRESS)	ARK19100
NUM	0	20	OVERLAY AREA (THREAD)	ARK19200
NUM	0	21	OVERLAY AREA (LOGICAL UNIT)	ARK19300
ADC	P73315	22	PHYSIC THREAD	ARK19400
NUM	0	23	COMPARE FLAG	ARK19500
NUM	0	24	WORD NUMBER WITHIN SECTOR	ARK19600
NUM	0	25	TEMPORARY FWA	ARK19700
NUM	0	26	TEMPORARY LWA + 1	ARK19800
NUM	0	27	TEMPORARY SECTOR	ARK19900
NUM	0	28	TEMPORARY REQUEST PRIORITY	ARK20000
NUM	0	29	TEMPORARY REQUEST	ARK20100
NUM	0	30	TEMPORARY STORAGE	ARK20200
NUM	0	31	RETURN ADDRESS WITHIN DRIVER	ARK20300
BZS	(96)	32	BUFFER FOR WORD ADDRESSING	ARK20400
EJT		127	BUFFER FOR WORD ADDRESSING	ARK20500
				ARK20600
				ARK20700
				ARK20800
				ARK20900
				ARK21000
SPC	1	00	SCHEDULER CALL	ARK21100
ADC	\$5209	01	INITIATOR ADDRESS	ARK21200
ADC	T17331	02	CONTINUATOR ADDRESS	ARK21300
ADC	C17331	03	TIMEOUT ERROR ADDRESS	ARK21400
ADC	F17331	04	DIAGNOSTIC CLOCK	ARK21500
NUM	0	05	LOGICAL UNIT	ARK21600
NUM	0	06	PARAMETER LOCATION	ARK21700
NUM	\$0181	07	CONVERTER, EQUIPMENT, STATION	ARK21800
ADC	\$1006+T33853	08	REQUEST STATUS	ARK21900
ADC	\$1006+T33854	08	REQUEST STATUS	ARK22000
NUM	\$0200	09	DRIVER STATUS	ARK22100
NUM	0	10	CURRENT LOCATION	ARK22200
NUM	0	11	LAST LOCATION PLUS ONE	ARK22300
NUM	0	12	DEVICE STATUS	ARK22400
NUM	0	13	ERRCR COUNTER	ARK22500
NUM	0	14	DATA TRANSFER FUNCTION	ARK22600
NUM	0	15	FILE ADDRESS / SECTOR NUMBER	ARK22700
NUM	\$8800	16	NO COMPARE FLAG / DIRECTOR FUNCTION	ARK22800
NUM	0	17	LAST SECTOR READ INTO BUFFER	ARK22900
NUM	0	18	OVERLAY AREA (SCHEDULER CALL)	ARK23000
NUM	0	19	OVERLAY AREA (COMPLETION ADDRESS)	ARK23100
NUM	0	20	OVERLAY AREA (THREAD)	ARK23200
NUM	0	21	OVERLAY AREA (LOGICAL UNIT)	ARK23300
ADC	P73316	22	PHYSIC THREAD	ARK23400
NUM	0	23	COMPARE FLAG	ARK23500
NUM	0	24	WORD NUMBER WITHIN SECTOR	ARK23600
NUM	0	25	TEMPORARY FWA	ARK23700
NUM	0	26	TEMPORARY LWA + 1	ARK23800

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NUM	0	27	TEMPORARY SECTOR	ARK23900
NUM	0	28	TEMPORARY REQUEST PRIORITY	ARK24000
NUM	0	29	TEMPORARY REQUEST	ARK24100
NUM	0	30	TEMPORARY STORAGE	ARK24200
NUM	0	31	RETURN ADDRESS WITHIN DRIVER	ARK24300
BZS	(96)	32	BUFFER FOR WORD ADDRESSING	ARK24400
EJ1		127	BUFFER FOR WORD ADDRESSING	ARK24500
	1 7 3 3 - 1	8 5 3 / 4	D I S K , U N I T 6	ARK24600
				ARK24700
				ARK24800
				ARK24900
SPC	1			ARK25000
P73316	ADC	\$5209	00 SCHEDULER CALL	ARK25100
	ADC	I17331	01 INITIATOR ADDRESS	ARK25200
	ADC	C17331	02 CONTINUATOR ADDRESS	ARK25300
	ADC	F17331	03 TIMEOUT ERROR ADDRESS	ARK25400
	NUM	-1	04 DIAGNOSTIC CLOCK	ARK25500
	NUM	0	05 LOGICAL UNIT	ARK25600
	NUM	0	06 PARAMETER LOCATION	ARK25700
	NUM	\$0181	07 CONVERTER, EQUIPMENT, STATION	ARK25800
	ADC	\$1006+T33853	08 REQUEST STATUS	ARK25900
	ADC	\$1006+T33854	08 REQUEST STATUS	ARK26000
	NUM	\$0200	09 DRIVER STATUS	ARK26100
	NUM	0	10 CURRENT LOCATION	ARK26200
	NUM	0	11 LAST LOCATION PLUS ONE	ARK26300
	NUM	0	12 DEVICE STATUS	ARK26400
	NUM	0	13 ERROR COUNTER	ARK26500
	NUM	0	14 DATA TRANSFER FUNCTION	ARK26600
	NUM	0	15 FILE ADDRESS / SECTOR NUMBER	ARK26700
	NUM	\$8D00	16 NO COMPARE FLAG / DIRECTOR FUNCTION	ARK26800
	NUM	0	17 LAST SECTOR READ INTO BUFFER	ARK26900
	NUM	0	18 OVERLAY AREA (SCHEDULER CALL)	ARK27000
	NUM	0	19 OVERLAY AREA (COMPLETION ADDRESS)	ARK27100
	NUM	0	20 OVERLAY AREA (THREAD)	ARK27200
	NUM	0	21 OVERLAY AREA (LOGICAL UNIT)	ARK27300
	ADC	P73317	22 PHYSTB THREAD	ARK27400
	NUM	0	23 COMPARE FLAG	ARK27500
	NUM	0	24 WORD NUMBER WITHIN SECTOR	ARK27600
	NUM	0	25 TEMPORARY FWA	ARK27700
	NUM	0	26 TEMPORARY LWA + 1	ARK27800
	NUM	0	27 TEMPORARY SECTOR	ARK27900
	NUM	0	28 TEMPORARY REQUEST PRIORITY	ARK28000
	NUM	0	29 TEMPORARY REQUEST	ARK28100
	NUM	0	30 TEMPORARY STORAGE	ARK28200
	NUM	0	31 RETURN ADDRESS WITHIN DRIVER	ARK28300
BZS	(96)	32	BUFFER FOR WORD ADDRESSING	ARK28400
EJ1		127	BUFFER FOR WORD ADDRESSING	ARK28500
	1 7 3 3 - 1	8 5 3 / 4	D I S K , U N I T 7	ARK28600
				ARK28700
				ARK28800
				ARK28900
				ARK29000

				ARK29100
				ARK29200
				ARK29300
				ARK29400
				ARK29500
				ARK29600
				ARK29700
				ARK29800
SPC	1			
P73317	ADC	\$5209	00 SCHEDULER CALL	
	ADC	I17331	01 INITIATOR ADDRESS	
	ADC	C17331	02 CONTINUATOR ADDRESS	
	ADC	F17331	03 TIMEOUT ERROR ADDRESS	
	NUM	-1	04 DIAGNOSTIC CLOCK	
	NUM	0	05 LOGICAL UNIT	
	NUM	0	06 PARAMETER LOCATION	
	NUM	\$0181	07 CONVERTER, EQUIPMENT, STATION	

Figure 1-40. 1733-1/853/854 Disk Physical Device Tables, Section ABK {Continued}

ADC	\$1006+T33853	08	REQUEST STATUS	ARK29900
ADC	\$1006+T33854	08	REQUEST STATUS	ARK30000
NUM	\$0200	09	DRIVER STATUS	ARK30100
NUM	0	10	CURRENT LOCATION	ARK30200
NUM	0	11	LAST LOCATION PLUS ONE	ARK30300
NUM	0	12	DEVICE STATUS	ARK30400
NUM	0	13	ERRCR COUNTER	ARK30500
NUM	0	14	DATA TRANSFER FUNCTION	ARK30600
NUM	0	15	FILE ADDRESS / SECTOR NUMBER	ARK30700
NUM	\$8F00	16	NO COMPARE FLAG / DIRECTOR FUNCTION	ARK30800
NUM	0	17	LAST SECTOR READ INTO BUFFER	ARK30900
NUM	0	18	OVERLAY AREA (SCHEDULER CALL)	ARK31000
NUM	0	19	OVERLAY AREA (COMPLETION ADDRESS)	ARK31100
NUM	0	20	OVERLAY AREA (THREAD)	ARK31200
NUM	0	21	OVERLAY AREA (LOGICAL UNIT)	ARK31300
ADC	P73310	22	PHYSIC THREAD	ARK31400
NUM	0	23	COMPARE FLAG	ARK31500
NUM	0	24	WORD NUMBER WITHIN SECTOR	ARK31600
NUM	0	25	TEMPORARY FWA	ARK31700
NUM	0	26	TEMPORARY LWA + 1	ARK31800
NUM	0	27	TEMPORARY SECTOR	ARK31900
NUM	0	28	TEMPORARY REQUEST PRIORITY	ARK32000
NUM	0	29	TEMPORARY REQUEST	ARK32100
NUM	0	30	TEMPORARY STORAGE	ARK32200
NUM	0	31	RETURN ADDRESS WITHIN DRIVER	ARK32300
*				ARK32400
EZS	(96)	32	BUFFER FOR WORD ADDRESSING	ARK32500
*		127	BUFFER FOR WORD ADDRESSING	ARK32600
EJ1				ARK32700
*				ARK32800
	1 7 3 9 - 1		C A R T R I D G E D I S K	ARK32900
SPC	1			ARK33000
EXT	I17391,C17391,E17391			ARK33100
SPC	2			ARK33200
R17391	LDG =XP17391		INTERRUPT RESPONSE FOR 1739-1 DISK	ARK33300
	JMP* (P17391+2)			ARK33400
	EJ1			ARK33500

Figure 1-40. 1733-1/853/854 Disk Physical Device Tables, Section ABK {Continued}

1.38 1739-1 Cartridge Disk Physical Device Tables, Section ABL

Section ABL, Figure 1-41, contains a standard interrupt response routine, located at R17391. Bit 15 of word 1b is the no compare flag. If set, the hardware compare feature will not be used. Diagnostic logical unit capability is not available for this device.

Words 17-13b are used by the driver for error analysis for an overlay area, and for word addressing. Word 29 is a pointer to the 9b-word buffer. Words 17-28 and words 30-13b are all zero initially. Words 17-13b may not be modified by the user.

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		1 7 3 9 - 1	C A R T R I D G E	D I S K	
	SPC	1			ABL00100
P17391	ADC	45209	00	SCHEDULER CALL	ABL00200
	ADC	117391	01	INITIATOR ADDRESS	ABL00300
	ADC	017391	02	CONTINUATOR ADDRESS	ABL00400
	ADC	F17391	03	TIMEOUT ERROR ADDRESS	ABL00500
	NUM	-1	04	DIAGNOSTIC CLOCK	ABL00600
	NUM	0	05	LOGICAL UNIT	ABL00700
	NUM	0	06	PARAMETER LOCATION	ABL00800
	NUM	80181	07	CONVERTER, EQUIPMENT, STATION	ABL00900
	NUM	81076	08	REQUEST STATUS	ABL01000
	NUM	80200	09	DRIVER STATUS	ABL01100
	NUM	0	10	CURRENT LOCATION	ABL01200
	NUM	0	11	LAST LOCATION PLUS ONE	ABL01300
	NUM	0	12	DEVICE STATUS	ABL01400
	NUM	0	13	DRIVER LENGTH IF MASS MEMORY	ABL01500
	NUM	87FFF	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABL01600
	NUM	0	15	SECTOR NUMBER OR FNR RETURN	ABL01700
	NUM	88100	16	NO COMPARE FLAG / DIRECTOR FUNCTION	ABL01800
	NUM	0	17	TEMSEC - USED BY WORD ADDRESSING	ABL01900
	NUM	0	18	OVERLAY AREA (SCHEDULER CALL)	ABL02000
	NUM	0	19	OVERLAY AREA (COMPLETION ADDRESS)	ABL02100
	NUM	0	20	OVERLAY AREA (THREAD)	ABL02200
	NUM	0	21	OVERLAY AREA (LOGICAL UNIT)	ABL02300
	NUM	0	22	SECTOR NUMBER CURRENTLY IN BUFFER	ABL02400
	NUM	0	23	RETURN ADDRESS FOR DATA TRANSFER	ABL02500
	NUM	0	24	ERROR COUNTER	ABL02600
	NUM	0	25	LAST DATA TRANSFER FUNCTION	ABL02700
	NUM	0	26	BUFFER SIZE FOR SPLIT TRANSFERS	ABL02800
	NUM	0	27	CYLINDER ADDRESS FOR TRANSFER	ABL02900
	NUM	0	28	DATA TRANSFER FUNCTION CODE	ABL03000
	ADC	BF7931	29	ADDRESS OF 96 WORD BUFFER	ABL03100
	NUM	0	30	TEMPORARY FOR WORD ADDRESSING	ABL03200
	NUM	0	31	TEMPORARY FOR WORD ADDRESSING	ABL03300
	NUM	0	32	TEMPORARY FOR WORD ADDRESSING	ABL03400
	NUM	0	33	REQUEST CODE	ABL03500
	NUM	0	34	REQUEST PRIORITY	ABL03600
	NUM	0	35	STARTING SECTOR FOR COMPARE OR RETRY	ABL03700
	NUM	0	36	FWA OF TRANSFER FOR COMPARE OR RETRY	ABL03800
	NUM	0	37	LAST VALUE OF CYLINDER ADDRESS STATUS	ABL03900
	NUM	0	38	LAST VALUE OF C W A STATUS	ABL04000
	NUM	0	39	LAST VALUE OF CHECKWORD STATUS	ABL04100
	NUM	0	40	LAST VALUE OF DRIVE CYLINDER STATUS	ABL04200
	EZS	BF7931(96)	41	BUFFER FOR WORD ADDRESSING	ABL04300
			136	BUFFER FOR WORD ADDRESSING	ABL04400
	EJ1				ABL04500
					ABL04600
					ABL04700
					ABL04800

Figure 1-41. 1739-1 Cartridge Disk Physical Device Tables.
Section ABL

1.39 1752 Drum Physical Device Tables, Section ABM

The standard interrupt response routine for the 1752 drum is located at R1752, Figure 1-42. Diagnostic logical unit capability is not available. Words 16-30 are used by the driver for an overlay area for requests that would read over request parameters, and for error analysis. These words are initially zero. They must not be modified by the user.

* *	1 7 5 2	U P L M		ARM00100
	EXT	Y1752,C1754,E1752		ARM00200
	SPC	2		ARM00300
R1752	LDG	XP1752	INTERRUPT RESPONSE FOR 1752 DRUM	ARM00400
	JMP*	(P1752+2)		ARM00500
	EJT			ARM00600
* *	1 7 5 2	U P L M		ARM00700
	SPC	1		ARM00800
P1752	ADC	\$5219	00 SCHEDULER CALL	ARM00900
	ADC	Y1752	01 INITIATOR ADDRESS	ARM01000
	ADC	C1752	02 CONTINUATOR ADDRESS	ARM01100
	ADC	F1752	03 TIMEOUT ERROR ADDRESS	ARM01200
	NUM	-1	04 DIAGNOSTIC CLOCK	ARM01300
	NUM	0	05 LOGICAL UNIT	ARM01400
	NUM	0	06 PARAMETER LOCATION	ARM01500
	NUM	\$0101	07 CONVERTER, EQUIPMENT, STATION	ARM01600
	NUM	\$1036	08 REQUEST STATUS	ARM01700
	NUM	0	09 DRIVER STATUS	ARM01800
	NUM	0	10 CURRENT LOCATION	ARM01900
	NUM	0	11 LAST LOCATION PLUS ONE	ARM02000
	NUM	0	12 DEVICE STATUS	ARM02100
	NUM	0	13 RESERVED	ARM02200
	NUM	\$7FFF	14 RESERVED	ARM02300
	NUM	0	15 RESERVED FOR FNR AND CMR	ARM02400
	NUM	0	16 SECTOR NUMBER	ARM02500
	NUM	0	17 DATA TRANSFER FUNCTION	ARM02600
	NUM	0	18 COUNTER	ARM02700
	NUM	0	19 FULL SECTOR COUNTER	ARM02800
	NUM	0	20 SAVE ECCOR	ARM02900
	NUM	0	21 SAVE ELSTWD	ARM03000
	NUM	0	22 OVERLAY AREA (SCHEDULER CALL)	ARM03100
	NUM	0	23 OVERLAY AREA (COMPLETION ADDRESS)	ARM03200
	NUM	0	24 OVERLAY AREA (THREAD)	ARM03300
	NUM	0	25 OVERLAY AREA (LOGICAL UNIT)	ARM03400
	NUM	0	26 UNSUCCESSFUL I/O ATTEMPT COUNTER	ARM03500
	NUM	0	27 EQUIPMENT STATUS (ON LAST ERROR)	ARM03600
	NUM	0	28 CORE STATUS (ON LAST ERROR)	ARM03700
	NUM	0	29 SECTOR STATUS (ON LAST ERROR)	ARM03800
	NUM	0	30 DATA STATUS (ON LAST ERROR)	ARM03900
	EJT			ARM04000
				ARM04100
				ARM04200

Figure 1-42. 1752 Drum Physical Device Tables, Section ABM

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1.40 1751 Drum Physical Device Tables, Section ABN

The standard interrupt response routine for the 1751 Drum is located at R1751, Figure 1-43. The 1751 Drum does not have diagnostic logical unit capability.

Words 13-18 are used by the driver as an overlay area for requests that would read over request parameters in the user's program. These words are initially zero and are not to be modified by the user.

Words 19-30 comprise 6 pairs of words used to send commands to the 1751 Drum to initiate I/O transfer. The first word of each pair is the data word which is placed in the A-register. The second word of each pair is the director function which is placed in Q. In word 19, bits 0-7 are the eight most significant bits of the track number. In word 21, bits 11-14 are the four least significant bits of the track number, and bits 0-10 are the drum sector number. A drum sector is one word only and should not be confused with a disk sector of 96 words. A drum sector number has a value between 0 and 2047. Word 23 contains the initial core address for the transfer. Word 25 contains the final core address. Word 27 contains a one in bit position 3 to request an end-of-operation interrupt from the hardware.

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Word 29 will contain either the contents of physical device table word 36, for a drum write, or physical device table word 37, for a drum read. Word 32 contains a negative value to indicate to the driver that the preceding pair is the end of the string of pairs to output to the drum.

Bits 7-10 of words 20, 22, 24, 26, 28, 36, and 37 contain the equipment code for the 1751 drum. If the equipment code in bits 7-10 of word 7 is changed, the equipment code in bits 7-10 of words 20, 22, 24, 26, 28, 36, and 37 must be changed to match the revised code.

For system directory calls, a monitor FREAD request will be performed by the 1751 driver. The request code, 4, corresponding to an FREAD request is therefore stored in word 33 of the physical device table for use by the driver.

Word 34 is used by the driver for temporary storage of the current request code. Word 35 is used by the driver to store one less than the number of words to be transferred.

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		1 7 5 1	U R L M		
	EXT	Y1751	C1751	F1751	ARN00100
	SPC	1			ARN00200
R1751	LDG	=XP1751		INTERRUPT RESPONSE FOR 1751 DRUM	ARN00300
	JMP#	(F1751+2)			ARN00400
	SPC	2			ARN00500
P1751	ADC	\$5209	00	SCHEDULER CALL	ARN00800
	ADC	Y1751	01	INITIATOR ADDRESS	ARN00900
	ADC	C1751	02	CONTINUATOR ADDRESS	ARN01000
	ADC	F1751	03	TIMEOUT ERROR ADDRESS	ARN01100
	NUM	-1	04	DIAGNOSTIC CLOCK	ARN01200
	NUM	0	05	LOGICAL UNIT	ARN01300
	NUM	0	06	PARAMETER LOCATION	ARN01400
	NUM	\$0101	07	CONVERTER, EQUIPMENT, STATION	ARN01500
	NUM	\$1066	08	REQUEST STATUS	ARN01600
	NUM	0	09	DRIVER STATUS	ARN01700
	NUM	0	10	CURRENT LOCATION	ARN01800
	NUM	0	11	LAST LOCATION PLUS ONE	ARN01900
	NUM	0	12	DEVICE STATUS	ARN02000
	NUM	0	13	OVERLAY AREA (SCHEDULER CALL)	ARN02100
	NUM	0	14	OVERLAY AREA (COMPLETION ADDRESS)	ARN02200
	NUM	0	15	OVERLAY AREA (THREAD)	ARN02300
	NUM	0	16	OVERLAY AREA (LOGICAL UNIT)	ARN02400
	NUM	0	17	OVERLAY AREA (SPARE)	ARN02500
	NUM	0	18	OVERLAY AREA (SPARE)	ARN02600
	NUM	0	19	TRACK ADDRESS FUNCTION	ARN02700
	NUM	\$0109	20	TRACK ADDRESS FUNCTION	ARN02800
	NUM	0	21	SECTOR ADDRESS FUNCTION	ARN02900
	NUM	\$010A	22	SECTOR ADDRESS FUNCTION	ARN03000
	NUM	0	23	S REGISTER FUNCTION	ARN03100
	NUM	\$010C	24	S REGISTER FUNCTION	ARN03200
	NUM	0	25	CORE ADDRESS FUNCTION	ARN03300
	NUM	\$010E	26	CORE ADDRESS FUNCTION	ARN03400
	NUM	\$0008	27		ARN03500
	NUM	\$0101	28	REQUEST STATUS	ARN03600
	NUM	0	29	READ / WRITE CONTROL	ARN03700
	NUM	0	30	READ / WRITE CONTROL	ARN03800
	NUM	0	31	READ / WRITE CONTROL	ARN03900
	NUM	-1	32	READ / WRITE CONTROL	ARN04000
	NUM	4	33	READSD	ARN04100
	NUM	0	34	READSD	ARN04200
	NUM	0	35	DN	ARN04300
	NUM	\$0100	36	WRITE CONTROL FUNCTION	ARN04400
	NUM	\$0104	37	READ CONTROL FUNCTION	ARN04500
	EJT				ARN04600

Figure 1-43. 1751 Drum Physical Device Tables, Section ABN

1.41 1742-30/120 Line Printer Physical Device Table,
Section AB0

Section AB0, Figure 1-44, defines the physical device table for either a 1742-30 line printer or a 1742-120 line printer. The type code in word 8 is dependent on the type of printer. The standard interrupt response routine is located at R42312. The driver may be either core resident or mass memory resident. If core resident, words 1, 2, and 3 are linked to the driver. If mass resident, words 1, 2, and 3 are linked to jumps to entries in MMEXEC.

If there is a corresponding FORTRAN logical unit for this device, word 19 will contain an index into the LOG1A table of logical unit numbers. The FORTRAN logical unit is the one that is used when it is desired to have the first character of the output buffer interpreted as a FORTRAN carriage control character.

Word 21, maximum number of lines per page, is 60 in a standard system. This may be changed if desired. If word 21 is negative, lines are printed consecutively with no spaces skipped between pages. Word 22, number of characters per line, is 136 in a standard system. This may be changed if the user desires.

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Word 23 is an index into the LOG1A table if there is a corresponding diagnostic logical unit. If there is no corresponding diagnostic logical unit, word 23 is zero.

The train image module with entry, TRAIN, is necessary for a 1742-120 printer, since the driver must have a set of codes corresponding, in order, to the characters on the hardware train within the printer. The train image module is not necessary for the 1742-30 printer, and thus cards *AB00680 and *AB00690 are included in a physical device table for a 1742-30 printer. In such a system the train image module is not present. By equating the entry TRAIN with 7FFF₁₆, TRAIN does not appear as an unpatched external at the time of system installation.

(175)

* 1 7 4 2 - 3 0 / 1 2 0 L I N E P R I N T E R			AR000100
* SPC 1			AR000200
EQL	T4230(17*\$10)	TYPE CODE - 1742-30	AR000300
EQL	T4212(1R*\$10)	TYPE CODE - 1742-120	AR000400
EQL	U42312(0)		AR000500
EQL	U42312(X42-12-LCG1A)		AR000600
EQL	F42312(FTN742-LCG1A)		AR000700
SPC	1		AR000800
EQL	M42312(0)		AR000900
EQL	M42312(1)		AR001000
* IFA M42312.F0.0 CORE RESIDENT DRIVER			AR001100
EXT	T42312		AR001200
EXT	C42312		AR001300
EXT	F42312		AR001400
EQL	L42312(0)		AR001500
EQL	S42312(\$7FFF)		AR001600
EIF			AR001700
* IFA M42312.F0.1 MASS RESIDENT DRIVER			AR001800
EXT	MASDRV		AR001900
EXT	MASCON		AR002000
EXT	MASERR		AR002100
I42312	JMP+ MASDRV	INITIATE DRIVER	AR002200
C42312	JMP+ MASCON	INTERUPT RESPONSE	AR002300
E42312	JMP+ MASERR	TIMEOUT ERROR	AR002400
EXT	L42312		AR002500
EXT	S42312		AR002600
EIF			AR002700
* SPC 2			AR002800
R42312	LDG =XP42312	INTERUPT RESPONSE FOR 1742-30/120 PRINTER	AR002900
JMP*	(F42312+2)		AR003000
EJ1			AR003100
* 1 7 4 2 - 3 0 / 1 2 0 L I N E P R I N T E R			AR003200
* SPC 1			AR003300
P42312	ADC \$520A	00 SCHEDULER CALL	AR003400
ADC	T42312	01 INITIATOR ADDRESS	AR003500
ADC	C42312	02 CONTINUATOR ADDRESS	AR003600
ADC	F42312	03 TIMEOUT ERROR ADDRESS	AR003700
NUM	-1	04 DIAGNOSTIC CLOCK	AR003800
NUM	0	05 LOGICAL UNIT	AR003900
NUM	0	06 PARAMETER LOCATION	AR004000
NUM	\$0201	07 CONVERTER, EQUIPMENT, STATION	AR004100
ADC	\$2804+T4230	08 REQUEST STATUS	AR004200
ADC	\$2804+T4212	09 REQUEST STATUS	AR004300
NUM	0	09 DRIVER STATUS	AR004400
NUM	0	10 CURRENT LOCATION	AR004500
NUM	0	11 LAST LOCATION PLUS ONE	AR004600
NUM	0	12 DEVICE STATUS	AR004700
ADC	L42312	13 DRIVER LENGTH IF MASS MEMORY	AR004800
ADC	S42312	14 NAME ASSOCIATED WITH SECTOR NUMBER	AR004900
NUM	0	15 BLANK DETECTION INDICATOR	AR005000
NUM	+	16 LINE COUNT	AR005100
NUM	0	17 TEMPORARY STORAGE FOR CONTROL FUNC.	AR005200
NUM	0	18 CHARACTER COUNT	AR005300
			AR005400
			AR005500
			AR005600
			AR005700
			AR005800

Figure 1-44. 1742-30/120 Line Printer Physical Device Table, Section A30

ADC	F42312	10	FORTRAN LOGICAL UNIT	AB005900
NUM	0	20	NUMBER OF BLANKS TO BE SENT	AB006000
NUM	40	21	MAXIMUM NUMBER OF LINES PER PAGE	AB006100
NUM	136	22	NUMBER OF CHARACTERS PER LINE	AB006200
ADC	042312	23	DIAGNOSTIC LU	AB006300
SPC	?			AB006400
ENT	TRAIN		LINK UNSELECTED 1742-120 TRAIN IMAGE	AB006500
EQU	TRAIN(\$7FFF)			AB006600
EJT				AB006700
				AB006800
			F O R T R A N L I N E P R I N T E R	AB006900
SPC	?			AB007000
			PHYSTH SHARED WITH 1742-30/120 LINE PRINTER	AB007100
EJT				AB007200

Figure 1-44. 1742-30/120 Line Printer Physical Device Table, Section AB0 (Continued)

1.42 1740-501/1742-1 Line Printer Physical Device Table,
Section ABP

Section ABP, Figure 1-45, is the set of data for a 1740-501 line printer or a 1742-1 line printer physical device table. The type code in bits 4-10 of word B is dependent on which of these printers is in the system. The standard interrupt response routine is located at R40421. The driver is core resident if M40421 is zero, mass resident if M40421 is one. The remainder of the lines in this section have the same meanings as the lines of Section AB0 as described in Article 1.41, except that no train image module is needed for either the 1740-501 or the 1742-1 printer.

* 1740 - 501 / 1742 LINE PRINTER			ARP00100
* SPC 1			ARP00200
EQL	T501(19*510)	TYPE CODE - 1740-501	ARP00300
EQL	T742(34*510)	TYPE CODE - 1742-1	ARP00400
EQL	U40421(0)		ARP00500
EQL	U40421(x40421-LCG1A)		ARP00600
EQL	F40421(FTN740-LCG1A)		ARP00700
SPC	1		ARP00800
EQL	M40421(0)		ARP00900
EQL	M40421(1)		ARP01000
* IFA M40421, FQ, 0 CORE RESIDENT DRIVER			ARP01100
EXT	T40421		ARP01200
EXT	C40421		ARP01300
EXT	E40421		ARP01400
EQL	L40421(0)		ARP01500
EQL	S40421(\$7FF)		ARP01600
EIF			ARP01700
* IFA M40421, FQ, 1 MASS RESIDENT DRIVER			ARP01800
EXT	MASDRV		ARP01900
EXT	MASCON		ARP02000
EXT	MASERR		ARP02100
I40421	JMF+	MASDRV INITIATE DRIVER	ARP02200
C40421	JMF+	MASCON INTERRUPT RESPONSE	ARP02300
E40421	JMF+	MASERR TIMEOUT ERROR	ARP02400
EXT	L40421		ARP02500
EXT	S40421		ARP02600
EIF			ARP02700
* SPC 2			ARP02800
R40421	LDG	=XP40421 INTERRUPT RESPONSE FOR 501/1742 LINE PRINTER	ARP02900
	JMF*	(P40421+2)	ARP03000
	EJT		ARP03100
* 1740 - 501 / 1742 LINE PRINTER			ARP03200
* SPC 1			ARP03300
P40421	ADC	\$520A 00 SCHEDULER CALL	ARP03400
	ADC	T40421 01 INITIATOR ADDRESS	ARP03500
	ADC	C40421 02 CONTINUATOR ADDRESS	ARP03600
	ADC	E40421 03 TIMEOUT ERROR ADDRESS	ARP03700
	NUM	-1 04 DIAGNOSTIC CLOCK	ARP03800
	NUM	0 05 LOGICAL UNIT	ARP03900
	NUM	0 06 PARAMETER LOCATION	ARP04000
	NUM	\$0201 07 CONVERTER, EQUIPMENT, STATION	ARP04100
	ADC	\$2804+T742 08 REQUEST STATUS	ARP04200
	ADC	\$2804+T501 08 REQUEST STATUS	ARP04300
	NUM	0 09 DRIVER STATUS	ARP04400
	NUM	0 10 CURRENT LOCATION	ARP04500
	NUM	0 11 LAST LOCATION PLUS ONE	ARP04600
	NUM	0 12 DEVICE STATUS	ARP04700
	ADC	L40421 13 DRIVER LENGTH IF MASS MEMORY	ARP04800
	ADC	S40421 14 NAME ASSOCIATED WITH SECTOR NUMBER	ARP04900
	NUM	0 15 ODD CHARACTER AND ERROR CODE	ARP05000
			ARP05100
			ARP05200
			ARP05300
			ARP05400
			ARP05500

Figure 1-45. 1740-501/1742-1 Line Printer Physical Device Table, Section ABP

NUM	0	16	SPARE	ARP05600
NUM	0	17	TEMPORARY	ARP05700
NUM	0	18	LINE COUNT	ARP05800
ADC	F44421	19	FORTRAN LINE PRINTER LOGICAL UNIT	ARP05900
NUM	0	20	NUMBER OF BLANKS TO BE SENT	ARP06000
NUM	60	21	MAXIMUM NUMBER OF LINES PER PAGE	ARP06100
NUM	136	22	NUMBER OF CHARACTERS PER LINE	ARP06200
ADC	040421	23	DIAGNOSTIC LU	ARP06300
EJT				ARP06400
*			F O R T R A N L I N E P R I N T E R	ARP06500
SPC	2			ARP06600
*			PHYSIC SHARED WITH 1740-501/1742 LINE PRINTER	ARP06700
EJT				ARP06800

Figure 1-45. 1740-501/1742-1 Line Printer Physical Device Table, Section ABP {Continued}

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1.43 1729-3 Card Reader Physical Device Table, Section AB0

The driver for the 1729-3 card reader is optionally core resident or mass resident, depending on the value of M17293. {Refer to Figure 1-46.} The standard interrupt response routine is located at R17293.

Word 16 contains the end-of-file code. The hexadecimal value, F, in bits 0-3 corresponds to a 6, 7, 8, and 9 punched in column one of a card.

Word 27 is an index into the LOG1A table if there is a corresponding diagnostic logical unit. Otherwise word 27 is zero.

Words 28-107 are used by the driver as a temporary storage area for input from the card reader.

* 1 7 2 9 - 3 C A R D R E A D E R		ARQ00100
* SPC 1		ARQ00200
EQL	U17293(0)	ARQ00300
EQL	U17293(X17293-LCG1A)	ARQ00400
SPC	1	ARQ00500
EQL	M17293(0)	ARQ00600
EQL	M17293(1)	ARQ00700
* IFA M17293.EQ.0 CORE RESIDENT DRIVER		ARQ00800
EXT	T17293	ARQ00900
EXT	C17293	ARQ01000
EXT	E17293	ARQ01100
EQL	L17293(0)	ARQ01200
EQL	S17293(*7FFF)	ARQ01300
EIF		ARQ01400
* IFA M17293.EQ.1 MASS RESIDENT DRIVER		ARQ01500
EXT	MASDRV	ARQ01600
EXT	MASCON	ARQ01700
EXT	MASERR	ARQ01800
I17293 JMF+	MASDRV	ARQ01900
C17293 JMF+	MASCON	ARQ02000
E17293 JMF+	MASERR	ARQ02100
EXT	L17293	ARQ02200
EXT	S17293	ARQ02300
EIF		ARQ02400
* SPC 2		ARQ02500
R17293 LDG	=XP17293	ARQ02600
JMF*	(P17293+2)	ARQ02700
EJT		ARQ02800
* 1 7 2 9 - 3 C A R D R E A D E R		ARQ02900
* SPC 1		ARQ03000
P17293 ADC	*520E	ARQ03100
ADC	T17293	ARQ03200
ADC	C17293	ARQ03300
ADC	E17293	ARQ03400
NUM	-1	ARQ03500
NUM	0	ARQ03600
NUM	0	ARQ03700
NUM	05A1	ARQ03800
NUM	1A62	ARQ03900
NUM	0	ARQ04000
NUM	0	ARQ04100
NUM	0	ARQ04200
NUM	0	ARQ04300
NUM	0	ARQ04400
NUM	0	ARQ04500
NUM	0	ARQ04600
NUM	0	ARQ04700
NUM	0	ARQ04800
ADC	L17293	ARQ04900
ADC	S17293	ARQ05000
NUM	0	ARQ05100
NUM	000F	ARQ05200
ADC	BFR293	ARQ05300
NUM	0	ARQ05400
NUM	0	ARQ05500
NUM	0	ARQ05600

Figure 1-4b. 1729-3 Card Reader Physical Device Table, Section ABQ

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NUM	0	21	RECORD LENGTH	ARQ05700
NUM	0	22	CHECKSUM ACCUMULATOR	ARQ05800
NUM	0	23	TEMPORARY STORAGE	ARQ05900
NUM	0	24	OUTPUT OFFSET SWITCH	ARQ06000
NUM	0	25	TEMPORARY STORAGE	ARQ06100
NUM	0	26	HOLLERITH ERROR FLAG	ARQ06200
ADC	U17293	27	DIAGNOSTIC LOGICAL UNIT	ARQ06300
				ARQ06400
EZS	BFR293(80)	28	INPUT BUFFER	ARQ06500
		107	INPUT BLFFER	ARQ06600
EJT				ARQ06700

Figure 1-46. 1729-B Card Reader Physical Device Table, Section ABQ
{Continued}

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1.44 1728/430 Card Reader/Punch Physical Device Table.

Section ABR

Data for the 1728/430 Card Reader/Punch physical device table is contained in Section ABR, Figure 1-47. The driver is optionally core resident or mass resident depending on the value of M1728. The standard interrupt response routine is located at R1728. Word 16 contains the end-of-file code, F₁₆, in bits 0-3. This corresponds to a 6, 7, 8, and 9 punched in column one of a card. Bit 15 of word 16 is used to designate whether the mode change from read to punch and vice versa is to cause the message "Error 11" to be printed by the Alternate Device Handler. If Bit 15 is one, a mode change causes an Error 11 message. If bit 15 is zero, a mode change does not cause an error message to be printed.

Word 27 is an index into the LOG1A table if there is a corresponding diagnostic logical unit. Otherwise, word 27 is zero.

1 7 2 8 / 4 3 0 C A R D R E A D E R / P U N C H			ARR00100
	SPC	1	ARR00200
	EQL	U1728(0)	ARR00300
	EQL	U1728(X1728-LOG1A)	ARR00400
	SPC	1	ARR00500
	EQL	M1728(0)	ARR00600
	EQL	M1728(1)	ARR00700
			ARR00800
			ARR00900
	IFA	M1728, EQ.0 CORE RESIDENT DRIVER	ARR01000
	EXT	T1728	ARR01100
	EXT	C1728	ARR01200
	EXT	E1728	ARR01300
	EQL	L1728(0)	ARR01400
	EQL	S1728(\$7FFF)	ARR01500
	EIF		ARR01600
			ARR01700
	IFA	M1728, EQ.1 MASS RESIDENT DRIVER	ARR01800
	EXT	MASDRV	ARR01900
	EXT	MASCON	ARR02000
	EXT	MASERR	ARR02100
I1728	JMF+	MASDRV INITIATE DRIVER	ARR02200
C1728	JMF+	MASCON INTERRUPT RESPONSE	ARR02300
E1728	JMF+	MASERR TIMEOUT ERROR	ARR02400
	EXT	L1728	ARR02500
	EXT	S1728	ARR02600
	EIF		ARR02700
			ARR02800
	SPC	2	ARR02900
R1728	LDG	=XP1728 INTERRUPT RESPONSE FOR 1728-430 READ/PUNCH	ARR03000
	JMF*	(F1728+2)	ARR03100
	EUT		ARR03200
			ARR03300
			ARR03400
			ARR03500
			ARR03600
P1728	SPC	1	ARR03700
	ADC	\$520E 00 SCHEDULER CALL	ARR03800
	ADC	T1728 01 INITIATOR ADDRESS	ARR03900
	ADC	C1728 02 CONTINUATOR ADDRESS	ARR04000
	ADC	E1728 03 TIMEOUT ERROR ADDRESS	ARR04100
	NUM	-1 04 DIAGNOSTIC CLOCK	ARR04200
	NUM	0 05 LOGICAL UNIT	ARR04300
	NUM	0 06 PARAMETER LOCATION	ARR04400
	NUM	\$05A1 07 CONVERTER, EQUIPMENT, STATION	ARR04500
	NUM	\$1806 08 REQUEST STATUS	ARR04600
	NUM	0 09 DRIVER STATUS	ARR04700
	NUM	0 10 CURRENT LOCATION	ARR04800
	NUM	0 11 LAST LOCATION PLUS ONE	ARR04900
	NUM	0 12 DEVICE STATUS	ARR05000
	ADC	L1728 13 DRIVER LENGTH IF MASS MEMORY	ARR05100
	ADC	S1728 14 NAME ASSOCIATED WITH SECTOR NUMBER	ARR05200
	NUM	0 15 PACKING CYCLE ADDRESS STORAGE	ARR05300
	NUM	\$800F 16 READ/PUNCH SWITCH, EOF FORMAT (6789)	ARR05400
	ADC	R1728 17 FIRST LOCATION OF 80 WORD I / O BUFFER	ARR05500
	NUM	0 18 CURRENT CARD BUFFER LOCATION	ARR05600
	NUM	0 19 SUBROUTINE RETURN ADDRESS	ARR05700
	NUM	0 20 CARD SEQUENCE NUMBER	ARR05800
	NUM	0 21 RECORD LENGTH	
	NUM	0 22 CHECKSUM ACCUMULATOR	

Figure 1-47. 1728/430 Card Reader/Punch Physical Device Table, Section ABR

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NUM	0	23	TEMPORARY STORAGE	ARR05900
NUM	0	24	OUTPUT OFFSET SWITCH	ARR06000
NUM	0	25	ERROR RETURN	ARR06100
NUM	0	26	HOLLERITH ERROR FLAG	ARR06200
AUC	111728	27	DIAGNOSTIC LU	ARR06300
				ARR06400
BZS	RUF28(84)	28	INPLT / OUTPUT BUFFER	ARR06500
		107	INPUT / OUTPUT BUFFER	ARR06600
EJ1				ARR06700

Figure 1-47. 1728/430 Card Reader/Punch Physical Device Table.
Section ABR (Continued)

1.45 1729-2 Card Reader Physical Device Table, Section ABS

The driver for the 1729-2 card reader is optionally core resident or mass resident depending on the value of M17292. If core resident, words 1, 2, and 3 of the table are linked to driver entries. If mass resident, words 1, 2, and 3 of the table refer to jumps to the MMEXEC module. {Refer to Figure 1-48.} The standard interrupt response routine is located at R17292. Word 16 contains the end-of-file code, F_{16} , corresponding to a 6, 7, 8, and 9 punch in column one of a card. Word 27 contains an index into the LOG1A table if there is a corresponding diagnostic logical unit. Otherwise, word 27 is zero.

* 1 7 2 9 - 2 C A R D R E A D E R			ARS00100
	SPC	1	ARS00200
	EQL	U17292(0)	ARS00300
	EQL	U17292(X17292-LOG1A)	ARS00400
	SPC	1	ARS00500
	EQL	M17292(0)	ARS00600
	EQL	M17292(1)	ARS00700
			ARS00800
			ARS00900
	IFA	M17292.EQ.0 CORE RESIDENT DRIVER	ARS01000
	EXT	T17292	ARS01100
	EXT	C17292	ARS01200
	EXT	F17292	ARS01300
	EQL	L17292(0)	ARS01400
	EQL	S17292(\$7FFF)	ARS01500
	EIF		ARS01600
			ARS01700
	IFA	M17292.EQ.1 MASS RESIDENT DRIVER	ARS01800
	EXT	MASDRV	ARS01900
	EXT	MASCON	ARS02000
	EXT	MASERR	ARS02100
I17292	JMP+	MASDRV INITIATE DRIVER	ARS02200
C17292	JMP+	MASCON INTERRUPT RESPONSE	ARS02300
E17292	JMP+	MASERR TIMEOUT ERROR	ARS02400
	EXT	L17292	ARS02500
	EXT	S17292	ARS02600
	EIF		ARS02700
			ARS02800
	SPC	2	ARS02900
R17292	LDG	=XP17292 INTERRUPT RESPONSE FOR 1729-2 CARD READER	ARS03000
	JMP*	(P17292+2)	ARS03100
	EJT		ARS03200
			ARS03300
			ARS03400
			ARS03500
			ARS03600
			ARS03700
			ARS03800
			ARS03900
			ARS04000
			ARS04100
			ARS04200
			ARS04300
			ARS04400
			ARS04500
			ARS04600
			ARS04700
			ARS04800
			ARS04900
			ARS05000
			ARS05100
			ARS05200
			ARS05300
			ARS05400
			ARS05500
			ARS05600
			ARS05700
			ARS05800
			ARS05900
			ARS06000

Figure 1-48. 1729-2 Card Reader Physical Device Table, Section ABS

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NUM	0	25	TEMPORARY STORAGE
NUM	0	26	MOLLERITH ERROR FLAG
ADC	U17292	27	DIAGNOSTIC LOGICAL UNIT
BZS	BFR292 (R0)	28	INPUT BUFFER
EJT		107	INPUT BUFFER

ABS06100
ABS06200
ABS06300
ABS06400
ABS06500
ABS06600
ABS06700

1.4b 1726-405 Card Reader Physical Device Table, Section ABT

The 1726-405 Card Reader driver is optionally core resident or mass resident depending on the value of M1726U. The standard interrupt response routine is located at R1726U. {Refer to Figure 1-49.}

Word 1b contains the hardware conversion indicator in bit 15. If bit 15 is one, the 1726 controller will perform conversion from Hollerith punched codes to ASCII. {EBCDIC to ASCII hardware conversion is not available.} If bit 15 is zero, software conversion will be used. Software conversion may be performed either to convert Hollerith punched codes to ASCII or to convert EBCDIC punched codes to ASCII. If bit 15 is zero, the particular software conversion table to use is indicated in the MSOS skeleton. {Refer to Chapter 2.}

If a corresponding diagnostic logical unit exists, word 2b will be an index into the LOG1A table of logical unit numbers. Otherwise, word 2b will be zero.

1 7 2 6 - 4 0 5 C A R D R E A D E R			ABT00100
SPC	1		ABT00200
EQL	U1726U(0)		ABT00300
EQU	U1726U(X1726U-LCG1A)		ABT00400
SPC	1		ABT00500
EQL	M1726U(0)		ABT00600
EQL	M1726U(1)		ABT00700
IFA	M1726U,EO,0	CORE RESIDENT DRIVER	ABT00800
EXT	T1726L		ABT00900
EXT	C1726U		ABT01000
EXT	F1726L		ABT01100
EQL	L1726L(0)		ABT01200
EQL	S1726L(\$7FFF)		ABT01300
EIF			ABT01400
			ABT01500
			ABT01600
			ABT01700
IFA	M1726U,EO,1	MASS RESIDENT DRIVER	ABT01800
EXT	MASDRV		ABT01900
EXT	MASCON		ABT02000
EXT	MASERR		ABT02100
I1726U JMF+	MASDRV	INITIATE DRIVER	ABT02200
C1726U JMF+	MASCON	INTERRUPT RESPONSE	ABT02300
E1726U JMF+	MASERR	TIMEOUT ERROR	ABT02400
EXT	L1726U		ABT02500
EXT	S1726U		ABT02600
EIF			ABT02700
			ABT02800
			ABT02900
SPC	2		ABT03000
R1726U LDG	=XP1726U	INTERUPT RESPONSE FOR 1726-405 CARD READER	ABT03100
JMF+	(F1726U+2)		ABT03200
EJT			ABT03300
			ABT03400
			ABT03500
			ABT03600
1 7 2 6 - 4 0 5 C A R D R E A D E R			ABT03700
			ABT03800
			ABT03900
SPC	1		ABT04000
P1726U ADC	\$520A	00 SCHEDULER CALL	ABT04100
ADC	T1726U	01 INITIATOR ADDRESS	ABT04200
ADC	C1726U	02 CONTINUATOR ADDRESS	ABT04300
ADC	E1726U	03 TIMEOUT ERROR ADDRESS	ABT04400
NUM	01	04 DIAGNOSTIC CLOCK	ABT04500
NUM	02	05 LOGICAL UNIT	ABT04600
NUM	03	06 PARAMETER LOCATION	ABT04700
NUM	\$0581	07 CONVERTER, EQUIPMENT, STATION	ABT04800
NUM	\$1972	08 REQUEST STATUS	ABT04900
NUM	0	09 DRIVER STATUS	ABT05000
NUM	0	10 CURRENT LOCATION	ABT05100
NUM	0	11 LAST LOCATION PLUS ONE	ABT05200
NUM	0	12 DEVICE STATUS	ABT05300
ADC	L1726L	13 DRIVER LENGTH IF MASS MEMORY	
ADC	S1726L	14 NAME ASSOCIATED WITH SECTOR NUMBER	
NUM	0	15 PACKING CYCLE ADDRESS STORAGE	
NUM	0	16 S/W CONVERSION INDICATOR	
NUM	\$8000	16 H/W CONVERSION INDICATOR	

Figure 1-49. 1726-405 Card Reader Physical Device Table, Section ABT

ADC	BF726L	17	FIRST ADDRESS OF 80 WORD INPUT BUFFER	ART05400
NUM	0	18	CURRENT CARD BUFFER LOCATION	ART05500
NUM	0	19	SUBROUTINE RETURN ADDRESS	ART05600
NUM	0	20	CARD SEQUENCE NUMBER	ART05700
NUM	0	21	RECORD LENGTH	ART05800
NUM	0	22	CHECKSUM ACCUMULATOR	ART05900
NUM	0	23	TEMPORARY STORAGE	ART06000
NUM	0	24	OUTPUT OFFSET SWITCH	ART06100
NUM	0	25	SPARE	ART06200
ADC	U1726L	26	DIAGNOSTIC LU	ART06300
				ART06400
				ART06500
BZS	BF726L (R0)	27	INPUT BUFFER	ART06600
		106	INPUT BUFFER	ART06700
EJT				

Figure 1-49. 1726-405 Card Reader Physical Device Table, Section ABT
{Continued}

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1.47 1726-405 Buffered Card Reader Physical Device Table,

Section ABU

The 1726-405 buffered card reader driver is optionally core resident or mass resident depending on the value of M1726B. The standard interrupt response routine is located at R1726B. {Refer to Figure 1-50.} The converter code for the 1706 Buffered Data Channel is contained in word 7. Words 16 and 26 have the same definitions as in Section ABT, as described in Article 1.46.

		1 7 2 6 - 4 0 5	B U F F E R E D	C A R D	R E A D E R	ABU00100
						ABU00200
	SPC	1				ABU00300
	EQL	M1726B(0)				ABU00400
	EQL	U1726B(X1726B-LCG1A)				ABU00500
	EQL	N7063(1)				ABU00600
	EQL	N1706C(N7063*100)				ABU00700
	SPC	1				ABU00800
	EQL	M1726B(0)				ABU00900
	EQL	M1726B(1)				ABU01000
						ABU01100
	IFA	M1726B.F0.0	CORE	RESIDENT	DRIVER	ABU01200
	EXT	T1726B				ABU01300
	EXT	C1726B				ABU01400
	EXT	F1726B				ABU01500
	EQL	L1726B(0)				ABU01600
	EQL	S1726B(*7FFF)				ABU01700
	EIF					ABU01800
						ABU01900
	IFA	M1726B.F0.1	MASS	RESIDENT	DRIVER	ABU02000
	EXT	MASDRV				ABU02100
	EXT	MASCON				ABU02200
	EXT	MASERR				ABU02300
I1726B	JMP+	MASDRV	INITIATE	DRIVER		ABU02400
C1726B	JMP+	MASCON	INTERRUPT	RESPONSE		ABU02500
E1726B	JMP+	MASERR	TIMEOUT	ERRCR		ABU02600
	EXT	L1726B				ABU02700
	EXT	S1726B				ABU02800
	EIF					ABU02900
						ABU03000
	SPC	2				ABU03100
R1726B	LDC	XP1726B	INTERRUPT	RESPONSE	FOR 1726-405 CARD READER	ABU03200
	JMP*	(P1726B+2)				ABU03300
	EJT					ABU03400
						ABU03500
		1 7 2 6 - 4 0 5	B U F F E R E D	C A R D	R E A D E R	ABU03600
						ABU03700
P1726B	SPC	1				ABU03800
	ADC	\$520A	00	SCHEDULER	CALL	ABU03900
	ADC	T1726B	01	INITIATOR	ADDRESS	ABU04000
	ADC	C1726B	02	CONTINUATOR	ADDRESS	ABU04100
	ADC	E1726B	03	TIMEOUT	ERRCR ADDRESS	ABU04200
	NUM	-1	04	DIAGNOSTIC	CLOCK	ABU04300
	NUM	0	05	LOGICAL	UNIT	ABU04400
	NUM	0	06	PARAMETER	LOCATION	ABU04500
	ADC	\$0581+N1706C	07	CONVERTER,	EQUIPMENT, STATION	ABU04600
	NUM	\$1A92	08	REQUEST	STATUS	

Figure 1-50. 1726-405 Buffered Card Reader Physical Device Table, Section ABU

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NUM	0	09	DRIVER STATUS	ABU04700
NUM	0	10	CURRENT LOCATION	ABU04800
NUM	0	11	LAST LOCATION PLUS ONE	ABU04900
NUM	0	12	DEVICE STATUS	ABU05000
ADC	L1726B	13	DRIVER LENGTH IF MASS MEMORY	ABU05100
ADC	S1726B	14	NAME ASSOCIATED WITH SECTOR NUMBER	ABU05200
NUM	0	15	PACKING CYCLE ADDRESS STORAGE	ABU05300
NUM	0	16	S/W CONVERSION INDICATOR	ABU05400
NUM	#8900	16	H/W CONVERSION INDICATOR	ABU05500
ADC	BF726B	17	FIRST ADDRESS OF 80 WORD INPUT BUFFER	ABU05600
NUM	0	18	CURRENT CARD BUFFER LOCATION	ABU05700
NUM	0	19	SUBROUTINE RETURN ADDRESS	ABU05800
NUM	0	20	CARD SEQUENCE NUMBER	ABU05900
NUM	0	21	RECORD LENGTH	ABU06000
NUM	0	22	CHECKSUM ACCUMULATOR	ABU06100
NUM	0	23	TEMPORARY STORAGE	ABU06200
NUM	0	24	OUTPUT OFFSET SWITCH	ABU06300
NUM	0	25	SPARE	ABU06400
ADC	U1726B	26	DIAGNOSTIC LU	ABU06500
EZS	BF726B(80)	27	INPUT BLFFEH	ABU06600
EJ1		106	INPUT BLFFEH	ABU06700
				ABU06800
				ABU06900

Figure 1-50. 1726-405 Buffered Card Reader Physical Device Table, Section ABU {Continued}

1.48 1728/430 Punch Software Buffered Physical Device Table,
Section ABV

Section ABV, Figure 1-51, contains the macro instruction BUFFER and a list of parameters for the macro expansion. For an explanation of each parameter, refer to Article 1.27.

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* 1 7 2 8 - 4 3 0 P U N C H S / W B U F F E R E D
* P172AS BUFFER \$0000,\$0000,\$0000,00,\$0000.0097
EJT

ABV00100
ABV00200
ABV00300
ABV00400

Figure 1-51. 1728-430 Punch Software Buffered Physical Device Table, Section ABV

1.49 1777/1721 Paper Tape Reader Physical Device Table, Section ABW

This section defines the physical device table for either the 1777 Paper Tape Reader or the 1721 Paper Tape Reader. The type code in word 8 is dependent on which device is in the system. {Refer to Figure 1-52.} The driver is optionally core resident or mass resident depending on the value of M1777R. The interrupt is processed by the Line 1 Interrupt Handler.

Word 22 is an index into the LOG1A table if there is a no-parity paper tape reader logical unit in the system. If there is a no-parity paper tape reader logical unit, it uses the physical device table defined in this section and when it is used there is no parity check when data is read.

Word 23 is an index into the LOG1A table of logical unit numbers if there is a corresponding diagnostic logical unit. Otherwise word 23 is zero.

1 7 7 7 / 1 7 2 1 P A P E R T A P E R E A D E R

SPC	1		ARW00100
EQL	T177R(35*\$10)	TYPE CODE - 1777 PAPER TAPE READER	ARW00200
EQL	T1721(01*\$10)	TYPE CODE - 1721 PAPER TAPE READER	ARW00300
EQL	SKPAR(PTRONP-LCG1A)		ARW00400
EQL	U1777R(0)		ARW00500
EQL	U1777R(X1777R-LCG1A)		ARW00600
SPC	1		ARW00700
EQL	M1777R(0)		ARW00800
EQL	M1777R(1)		ARW00900

IFA	M1777R.EQ.0	CORE RESIDENT DRIVER	ARW01000
EXT	T1777R		ARW01100
EXT	C1777R		ARW01200
EXT	F1777R		ARW01300
EQL	L1777R(0)		ARW01400
EQL	S1777R(\$7FFF)		ARW01500
EIF			ARW01600

IFA	M1777R.EQ.1	MASS RESIDENT DRIVER	ARW01700
EXT	MASDRV		ARW01800
EXT	MASCON		ARW01900
EXT	MASERR		ARW02000
I1777R JMF+	MASDRV	INITIATE DRIVER	ARW02100
C1777R JMF+	MASCON	INTERRUPT RESPONSE	ARW02200
E1777R JMF+	MASERR	TIMEOUT ERROR	ARW02300
EXT	L1777R		ARW02400
EXT	S1777R		ARW02500
EIF			ARW02600

EJT			ARW02700
			ARW02800
			ARW02900
			ARW03000
			ARW03100
			ARW03200
			ARW03300
			ARW03400

1 7 7 7 / 1 7 2 1 P A P E R T A P E R E A D E R

SPC	1		ARW03500
P1777R ADC	\$520E	00 SCHEDULER CALL	ARW03600
ADC	T1777R	01 INITIATOR ADDRESS	ARW03700
ADC	C1777R	02 CONTINUATOR ADDRESS	ARW03800
ADC	E1777R	03 TIMEOUT ERROR ADDRESS	ARW03900
NUM	-1	04 DIAGNOSTIC CLOCK	ARW04000
NUM	0	05 LOGICAL UNIT	ARW04100
NUM	+	06 PARAMETER LOCATION	ARW04200
NUM	\$00A1	07 CONVERTER, EQUIPMENT, STATION	ARW04300
ADC	\$2002+T1721	08 REQUEST STATUS	ARW04400
ADC	\$2002+T177R	08 REQUEST STATUS	ARW04500
NUM	0	09 DRIVER STATUS	ARW04600
NUM	0	10 CURRENT LOCATION	ARW04700
NUM	0	11 LAST LOCATION PLUS ONE	ARW04800
NUM	0	12 DEVICE STATUS	ARW04900
ADC	L1777R	13 DRIVER LENGTH IF MASS MEMORY	ARW05000
ADC	S1777R	14 NAME ASSOCIATED WITH SECTOR NUMBER	ARW05100
NUM	0	15 CHECKSUM AND ERROR CODE	ARW05200
NUM	0	16 LENGTH	ARW05300
NUM	0	17 RETURN ADDRESS	ARW05400
NUM	0	18 TEMPORARY ADDRESS	ARW05500
NUM	0	19 ERROR CODE	ARW05600
NUM	+	20 TEMPORARY	ARW05700
NUM	0	21 LOST DATA	ARW05800
ADC	SKPAR	22 LU OF NON-PARITY PAPER TAPE READER	ARW05900
ADC	U1777R	23 DIAGNOSTIC LOGICAL UNIT	ARW06000
EJT			ARW06100

Figure 1-52. 1777/1721 Paper Tape Reader Physical Device Table, Section ABW

SPC	1 7 7 7 / 1 7 2 1 P A P E R T A P E R E A D E R	ΔRW06200
2		ΔRW06300
	NO PARITY CHECKS ON ASCII REACS	ΔRW06400
		ΔRW06500
EJT	PHYSIC SHARED WITH 1777/1721 READER	ΔRW06600
		ΔRW06700

Figure 1-52. 1777/1721 Paper Tape Reader Physical Device Table, Section ABJ (Continued)

1.50 1777/1723 Paper Tape Punch Physical Device Table, Section ABX

Section ABX defines the physical device table for a 1777 paper tape punch or a 1723 paper tape punch. The type code in word 8 is dependent on which device is in the system. {Refer to Figure 1-53.} The driver is optionally core or mass resident depending on the value of M1777P. The interrupt is processed by the Line 1 Interrupt Handler. Word 22 contains an index into the LOG1A table if there is a corresponding diagnostic logical unit. Otherwise word 22 is zero.

1 7 7 7 / 1 7 2 3 P A P E R T A P E P U N C H

SPC 1
 EGL T1723(02*010) TYPE CODE - 1723 PAPER TAPE PUNCH
 EGL T177P(37*010) TYPE CODE - 1777 PAPER TAPE PUNCH
 EGL U1777P(0)
 EGL U1777P(X1777P-LCG1A)
 SPC 1
 EGL M1777P(0)
 EGL M1777P(1)
 IFA M1777P.F0.0 CORE RESIDENT DRIVER
 EXT T1777P
 EXT C1777P
 EXT F1777P
 EGL L1777P(0)
 EGL S1777P(57FFF)
 EIF

IFA M1777P.F0.1 MASS RESIDENT DRIVER
 EXT MASDRV
 EXT MASCON
 EXT MASERR
 I1777P JMP. MASDRV INITIATE DRIVER
 C1777P JMP. MASCON INTERRUPT RESPONSE
 E1777P JMP. MASERR TIMEOUT ERROR
 EXT L1777P
 EXT C1777P
 EIF

1 7 7 7 / 1 7 2 3 P A P E R T A P E P U N C H

P1777P SPC 1
 ADC 0520E 00 SCHEDULER CALL
 ADC T1777P 01 INITIATOR ADDRESS
 ADC C1777P 02 CONTINUATOR ADDRESS
 ADC F1777P 03 TIMEOUT ERROR ADDRESS
 NUP 01 04 DIAGNOSTIC CLOCK
 NUP 0 05 LOGICAL UNIT
 NUP 0 06 PARAMETER LOCATION
 NUP 000C1 07 CONVERTER, EQUIPMENT, STATION
 ADC 02004+T1723 08 REQUEST STATUS
 ADC 02004+T177P 09 REQUEST STATUS
 NUP 0 10 DRIVER STATUS
 NUP 0 11 CURRENT LOCATION
 NUP 0 12 LAST LOCATION PLUS ONE
 NUP 0 13 DEVICE STATUS
 ADC L1777P 14 DRIVER LENGTH IF MASS MEMORY
 ADC S1777P 15 NAME ASSOCIATED WITH SECTOR NUMBER
 NUP 0 16 CHECKSUM AND ERROR CODE
 NUP 0 17 TEMPORARY
 NUP 0 18 RETURN ADDRESS
 NUP 0 19 TEMPORARY ADDRESS
 NUP 0 20 ERROR CODE
 NUP 0 21 TEMPORARY
 NUP 0 22 LCST DATA
 ADC U1777P
 EJT

ARX00100
 ARX00200
 ARX00300
 ARX00400
 ARX00500
 ARX00600
 ARX00700
 ARX00800
 ARX00900
 ARX01000
 ARX01100
 ARX01200
 ARX01300
 ARX01400
 ARX01500
 ARX01600
 ARX01700
 ARX01800
 ARX01900
 ARX02000
 ARX02100
 ARX02200
 ARX02300
 ARX02400
 ARX02500
 ARX02600
 ARX02700
 ARX02800
 ARX02900
 ARX03000
 ARX03100
 ARX03200
 ARX03300
 ARX03400
 ARX03500
 ARX03600
 ARX03700
 ARX03800
 ARX03900
 ARX04000
 ARX04100
 ARX04200
 ARX04300
 ARX04400
 ARX04500
 ARX04600
 ARX04700
 ARX04800
 ARX04900
 ARX05000
 ARX05100
 ARX05200
 ARX05300
 ARX05400
 ARX05500
 ARX05600
 ARX05700
 ARX05800
 ARX05900

Figure 1-53. 1777/1723 Paper Tape Punch Physical Device Table, Section ABX

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1.51 1777/1723 Paper Tape Punch Software Buffered Physical
Device Table, Section ABY

Section ABY, Figure 1-54, consists of the macro instruction
BUFFER and a list of parameters for the macro expansion.

Refer to Article 1.27 for an explanation of the macro expansion.

•	1 7 7 7	T A P E	P U N C H	S / W	B U F F E R R E D	ABY00100
•						ABY00200
P777PS	EUFFER	\$0000.	\$0000.	\$0000,00,	\$000.0097	ABY00300
	EJT					ABY00400

Figure 1-54. 1777/1723 Paper Tape Punch Software Buffered Physical Device Table, Section ABY

1.52 364-4 Communications Multiplexor Physical Device

Table, Section ABZ

Up to 16 364-4 Communications Multiplexor units may be included in a system. Each unit has its own physical device table defined in Section ABZ, Figure 1-55. The interrupt response routine at R3644 is standard. In each table, the type code in word 8 specifies whether a 361-1 or a 361-4 communications adaptor is used.

Word 16 of each table contains the codes for the two characters which will precede every FWRITE request. In a standard system word 16 contains DADD₁₆ corresponding to a line feed and carriage return. It is unlikely that the user would wish to use different characters to precede each FWRITE, but he may do so if he wishes. Word 17 contains any special input termination character codes. If TIMESHARE is in the system, these codes are 1B₁₆ which represents the ESCAPE character and 21₁₆ which represents an exclamation point {!}. Input of either of these characters is a signal to TIMESHARE that the current operation is to be terminated. If TIMESHARE is not in the system, word 17 is zero in a released system. The user may store data in word 17 if he supplies his own input data handler. {Refer to the description of word 21 below.}

Word 20 contains the following parameters:

<u>Bits</u>	<u>Meaning</u>
15	Echoplex mode flag
4-14	Lapsed time allowed before timeout, specified in seconds.
0-3	Priority level of input handler

If bit 15 is set to one each terminal connected to the communications adapter is operated in echoplex mode, that is, the driver automatically outputs all input characters as they are received. If bit 15 is zero echoing will not be performed. The echoplex mode flag is set to one in a standard released system. Bits 4-15 of word 20 contain the amount of time which may elapse before a timeout error condition is indicated. Lapsed time before timeout is 300 seconds in a standard system. This may be changed if desired. If bits 4-14 of word 20 are all zero, the timeout error feature will not be used. If timeout error detection is desired, a lapsed time value of from 1 second to 2047 seconds may be specified. The priority level in bits 0-3 refers to the entry point address in word 21. Bits 0-3 contain the value 4 in a standard system without TIMESHARE. In a system including TIMESHARE bits 0-3 contain the value 8, since the TIME-SHARE executive program must run at level 8.

Word 21 is the address of the entry to the program which will process input from the terminals connected to the communications adapter. In a system which includes TIMESHARE, this is the address of EXECRD, entry to the TIMESHARE executive program. In a system without TIMESHARE, the user may provide his own input handler, specifying the entry point in word 21 and the corresponding priority level in bits 0-3 of word 20. The actual handler may be mass resident, but a core location must still be specified in word 21. This may be the location of a program

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to schedule the mass resident input handler.

Word 65 of each table is the thread word. The thread words link all the communications adapters in the system so that the driver can examine the status of each adapter when an interrupt occurs, performing input and output as required.

* 3 6 4 - 4 C O M M . M U X , U N I T 0			
SPC	1		ARZ00100
EXT	I3644,C3644,E3644		ARZ00200
ENT	CARF00		ARZ00300
EQU	T3611(32*\$10) TYPE CODE = 361-1 COMM. ADAPTER		ARZ00400
EQU	T3614(33*\$10) TYPE CODE = 361-4 COMM. ADAPTER		ARZ00500
SPC	2		ARZ00600
R3644	LDG =XPCOM00	INTERRUPT RESPONSE FOR 364-4 COMM. MUX.	ARZ00700
JMF*	(PCOM00+2)		ARZ00800
SPC	2		ARZ00900
PCOM00			ARZ01000
ADC	\$520C	00 SCHEDULER CALL	ARZ01100
ADC	T3644	01 INITIATOR ADDRESS	ARZ01200
ADC	C3644	02 CONTINUATOR ADDRESS	ARZ01300
ADC	E3644	03 TIMEOUT ERROR ADDRESS	ARZ01400
NUM	-1	04 DIAGNOSTIC CLOCK	ARZ01500
NUM	0	05 LOGICAL UNIT	ARZ01600
NUM	0	06 PARAMETER LOCATION	ARZ01700
NUM	0	07 CONVERTER, EQUIPMENT, STATION	ARZ01800
ADC	\$3004+T3614	08 REQUEST STATUS	ARZ01900
ADC	\$3004+T3611	08 REQUEST STATUS	ARZ02000
NUM	0	09 DRIVER STATUS	ARZ02100
NUM	0	10 CURRENT LOCATION	ARZ02200
NUM	0	11 LAST LOCATION PLUS ONE	ARZ02300
NUM	0	12 DEVICE STATUS	ARZ02400
NUM	0	13 RESERVED	ARZ02500
NUM	\$7FFF	14 RESERVED	ARZ02600
NUM	0	15 RESERVED FOR FNR AND CMR	ARZ02700
NUM	\$0A0D	16 CONTROL WORD TO PRECEED FWRITE	ARZ02800
NUM	\$0000	17 SPECIAL INPUT TERMINATION CHARACTERS	ARZ02900
NUM	\$1B21	17 SPECIAL INPUT TERMINATION CHARACTERS	ARZ03000
NUM	0	18 CURRENT DATA CHARACTER	ARZ03100
NUM	0	19 FLAGWORD FOR DRIVER	ARZ03200
ADC	(60*16+04)	20 INPUT TIMEOUT + LEVEL OF INPUT HANDLER	ARZ03300
ADC	0	21 ENTRY POINT OF INPUT HANDLER	ARZ03400
NUM	-1	22 INPUT DIAGNOSTIC CLOCK	ARZ03500
NUM	0	23 INDEX TO INPUT BUFFER	ARZ03600
BZS	CARF00(40)	24 INPUT BUFFER	ARZ03700
NUM	0	63 INPUT BUFFER	ARZ03800
ADC	PCOM01	64 WORD COUNT OF INPUT DATA	ARZ03900
EJT		65 PHYSTB THREAD	ARZ04000
* 3 6 4 - 4 C O M M . M U X , U N I T 1			ARZ04100
ENT	CARF01		ARZ04200
SPC	1		ARZ04300
PCOM01			ARZ04400
ADC	\$520C	00 SCHEDULER CALL	ARZ04500
ADC	T3644	01 INITIATOR ADDRESS	ARZ04600
ADC	C3644	02 CONTINUATOR ADDRESS	ARZ04700
ADC	E3644	03 TIMEOUT ERROR ADDRESS	ARZ04800
NUM	-1	04 DIAGNOSTIC CLOCK	ARZ04900
NUM	0	05 LOGICAL UNIT	ARZ05000
NUM	0	06 PARAMETER LOCATION	ARZ05100
NUM	0	07 CONVERTER, EQUIPMENT, STATION	ARZ05200
ADC	\$3004+T3614	08 REQUEST STATUS	ARZ05300
ADC	\$3004+T3611	08 REQUEST STATUS	ARZ05400
NUM	0	09 DRIVER STATUS	ARZ05500
NUM	0	10 CURRENT LOCATION	ARZ05600
NUM	0	11 LAST LOCATION PLUS ONE	ARZ05700
			ARZ05800
			ARZ05900

Figure 1-55. 364-4 Communications Multiplexor Physical Device Table, Section ABZ

NUM	0	12	DEVICE STATUS	ABZ06000
NUM	0	13	RESERVED	ABZ06100
NUM	\$7FFF	14	RESERVED	ABZ06200
NUM	0	15	RESERVED FOR FNR AND CMR	ABZ06300
NUM	\$0A00	16	CONTROL WORD TO PRECEED FWRITE	ABZ06400
NUM	\$0000	17	SPECIAL INPLT TERMINATION CHARACTERS	ABZ06500
NUM	\$1B21	17	SPECIAL INPLT TERMINATION CHARACTERS	ABZ06600
NUM	0	18	CURRENT DATA CHARACTER	ABZ06700
NUM	0	19	FLAGWORD FOR DRIVER	ABZ06800
ADC	(60*16+04)	20	INPLT TIMEOUT + LEVEL OF INPUT HANDLER	ABZ06900
ADC	0	21	ENTRY POINT OF INPUT HANDLER	ABZ07000
NUM	-1	22	INPUT DIAGNOSTIC CLOCK	ABZ07100
NUM	0	23	INDEX TO INPUT BUFFER	ABZ07200
EZS	CARF01(40)	24	INPUT BUFFER	ABZ07300
		63	INPUT BUFFER	ABZ07400
NUM	0	64	WORD COUNT OF INPLT DATA	ABZ07500
ADC	PCOM02	65	PHYSTR THREAD	ABZ07600
EJT				ABZ07700

3 6 4 - 4 C C M M . M U X , U N I T 2

ENT	CARF02			ABZ07800	
SPC	1			ABZ07900	
PCOM02	ADC	\$520C	00	SCHEDULER CALL	ABZ08000
	ADC	T3644	01	INITIATOR ADDRESS	ABZ08100
	ADC	C3644	02	CONTINUATOR ADDRESS	ABZ08200
	ADC	E3644	03	TIMEOUT ERROR ADDRESS	ABZ08300
	NUM	-1	04	DIAGNOSTIC CLOCK	ABZ08400
	NUM	0	05	LOGICAL UNIT	ABZ08500
	NUM	0	06	PARAMETER LOCATION	ABZ08600
	NUM	0	07	CONVERTER, EQUIPMENT, STATION	ABZ08700
	ADC	\$3004+T361+	08	REQUEST STATUS	ABZ08800
	ADC	\$3004+T3611	08	REQUEST STATUS	ABZ08900
	NUM	0	09	DRIVER STATUS	ABZ09000
	NUM	0	10	CURRENT LOCATION	ABZ09100
	NUM	0	11	LAST LOCATION PLUS ONE	ABZ09200
	NUM	0	12	DEVICE STATUS	ABZ09300
	NUM	0	13	RESERVED	ABZ09400
	NUM	\$7FFF	14	RESERVED	ABZ09500
	NUM	0	15	RESERVED FOR FNR AND CMR	ABZ09600
	NUM	\$0A00	16	CONTROL WORD TO PRECEED FWRITE	ABZ09700
	NUM	\$0000	17	SPECIAL INPLT TERMINATION CHARACTERS	ABZ09800
	NUM	\$1B21	17	SPECIAL INPLT TERMINATION CHARACTERS	ABZ09900
	NUM	0	18	CURRENT DATA CHARACTER	ABZ10000
	NUM	0	19	FLAGWORD FOR DRIVER	ABZ10100
	ADC	(60*16+04)	20	INPLT TIMEOUT + LEVEL OF INPUT HANDLER	ABZ10200
	ADC	0	21	ENTRY POINT OF INPUT HANDLER	ABZ10300
	NUM	-1	22	INPUT DIAGNOSTIC CLOCK	ABZ10400
	NUM	0	23	INDEX TO INPUT BUFFER	ABZ10500
	EZS	CARF02(40)	24	INPUT BUFFER	ABZ10600
			63	INPUT BUFFER	ABZ10700
	NUM	0	64	WORD COUNT OF INPLT DATA	ABZ10800
	ADC	PCOM03	65	PHYSTR THREAD	ABZ10900
	EJT				ABZ11000
					ABZ11100
					ABZ11200

Figure 1-55. 364-4 Communications Multiplexor Physical Device Table, Section ABZ (Continued)

3 6 4 - 4 C O M M . M U X . U N I T 3

ENT	CABF03			ARZ11300	
SPC	1			ARZ11400	
PCOM03	ADC	\$520C	00	SCHEDULER CALL	ARZ11500
	ADC	T3644	01	INITIATOR ADDRESS	ARZ11600
	ADC	C3644	02	CONTINUATOR ADDRESS	ARZ11700
	ADC	E3644	03	TIMEOUT ERROR ADDRESS	ARZ11800
	NUM	-1	04	DIAGNOSTIC CLOCK	ARZ11900
	NUM	0	05	LOGICAL UNIT	ARZ12000
	NUM	0	06	PARAMETER LOCATION	ARZ12100
	NUM	0	07	CONVERTER, EQUIPMENT, STATION	ARZ12200
	ADC	\$3004+T3614	08	REQUEST STATUS	ARZ12300
	ADC	\$3004+T3611	09	REQUEST STATUS	ARZ12400
	NUM	0	10	DRIVER STATUS	ARZ12500
	NUM	0	11	CURRENT LOCATION	ARZ12600
	NUM	0	12	LAST LOCATION PLUS ONE	ARZ12700
	NUM	0	13	DEVICE STATUS	ARZ12800
	NUM	0	14	RESERVED	ARZ12900
	NUM	\$7FFF	15	RESERVED	ARZ13000
	NUM	0	16	RESERVED FOR FNR AND CMR	ARZ13100
	NUM	\$0A0D	17	CONTROL WORD TO PRECEED FWRITE	ARZ13200
	NUM	\$0000	18	SPECIAL INPLT TERMINATION CHARACTERS	ARZ13300
	NUM	\$1B21	19	SPECIAL INPLT TERMINATION CHARACTERS	ARZ13400
	NUM	0	20	CURRENT DATA CHARACTER	ARZ13500
	NUM	0	21	FLAGWORD FOR DRIVER	ARZ13600
	ADC	(60*16+04)	22	INPUT TIMEOUT + LEVEL OF INPUT HANDLER	ARZ13700
	ADC	0	23	ENTRY POINT OF INPUT HANDLER	ARZ13800
	NUM	-1	24	INPUT DIAGNOSTIC CLOCK	ARZ13900
	NUM	0	25	INDEX TO INPUT BUFFER	ARZ14000
	BZS	CABF03(40)	26	INPUT BUFFER	ARZ14100
			27	INPUT BUFFER	ARZ14200
	NUM	0	28	WORD COUNT OF INPUT DATA	ARZ14300
	ADC	PCOM04	29	PHYSTB THREAD	ARZ14400
	EJT				ARZ14500

3 6 4 - 4 C O M M . M U X . U N I T 4

ENT	CABF04			ARZ14700	
SPC	1			ARZ14800	
PCOM04	ADC	\$520C	00	SCHEDULER CALL	ARZ14900
	ADC	T3644	01	INITIATOR ADDRESS	ARZ15000
	ADC	C3644	02	CONTINUATOR ADDRESS	ARZ15100
	ADC	E3644	03	TIMEOUT ERROR ADDRESS	ARZ15200
	NUM	-1	04	DIAGNOSTIC CLOCK	ARZ15300
	NUM	0	05	LOGICAL UNIT	ARZ15400
	NUM	0	06	PARAMETER LOCATION	ARZ15500
	NUM	0	07	CONVERTER, EQUIPMENT, STATION	ARZ15600
	ADC	\$3004+T3614	08	REQUEST STATUS	ARZ15700
	ADC	\$3004+T3611	09	REQUEST STATUS	ARZ15800
	NUM	0	10	DRIVER STATUS	ARZ15900
	NUM	0	11	CURRENT LOCATION	ARZ16000
	NUM	0	12	LAST LOCATION PLUS ONE	ARZ16100
	NUM	0	13	DEVICE STATUS	ARZ16200
	NUM	0	14	RESERVED	ARZ16300
	NUM	\$7FFF	15	RESERVED	ARZ16400
					ARZ16500
					ARZ16600
					ARZ16700

Figure 1-55. 364-4 Communications Multiplexor Physical Device Table, Section ABZ {Continued}

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NUM	0	15	RESERVED FOR FNR AND CMR	ARZ16800	
NUM	\$0A00	16	CONTROL WORD TO PRECEED FWRITE	ARZ16900	
NUM	\$0000	17	SPECIAL INPLT TERMINATION CHARACTERS	ARZ17000	
NUM	\$1R21	17	SPECIAL INPLT TERMINATION CHARACTERS	ABZ17100	
NUM	0	18	CURRENT DATA CHARACTER	ARZ17200	
NUM	0	19	FLAGWORD FOR DRIVER	ABZ17300	
ADC	(60*16+04)	20	INPUT TIMEOLT + LEVEL OF INPUT HANDLER	ARZ17400	
ADC	0	21	ENTRY PCINT OF INPUT HANDLER	ARZ17500	
NUM	-1	22	INPUT DIAGNOSTIC CLOCK	ARZ17600	
NUM	0	23	INDEX TO INPUT BUFFER	ABZ17700	
BZS	CABF04(40)	24	INPUT BUFFFF	ABZ17800	
		63	INPUT BUFFER	ABZ17900	
NUM	0	64	WORD COUNT OF INPUT DATA	ABZ18000	
ADC	PCOM05	65	PHYSTB THREAD	ABZ18100	
EJT				ABZ18200	
3 6 4 - 4 C O M M . M U X . U N I T 5					
ENT	CABF05			ABZ18300	
SPL	1			ABZ18400	
PCOM05	ADC	\$5200	00	SCHEDULER CALL	ABZ18500
	ADC	T3644	01	INITIATOR ADDRESS	ABZ18600
	ADC	C3644	02	CONTINUATOR ADDRESS	ABZ18700
	ADC	E3644	03	TIMEOUT ERROR ADDRESS	ABZ18800
	NUM	-1	04	DIAGNOSTIC CLOCK	ABZ18900
	NUM	0	05	LOGICAL UNIT	ABZ19000
	NUM	0	06	PARAMETER LOCATION	ABZ19100
	NUM	0	07	CONVERTER, EQUIPMENT, STATION	ABZ19200
	ADC	\$3004+T3614	08	REQUEST STATUS	ABZ19300
	ADC	\$3004+T3611	08	REQUEST STATUS	ABZ19400
	NUM	0	09	DRIVER STATUS	ABZ19500
	NUM	0	10	CURRENT LOCATION	ABZ19600
	NUM	0	11	LAST LOCATION PLUS ONE	ABZ19700
	NUM	0	12	DEVICE STATUS	ABZ19800
	NUM	0	13	RESERVED	ABZ19900
	NUM	\$7FFF	14	RESERVED	ABZ20000
	NUM	0	15	RESERVED FOR FNR AND CMR	ABZ20100
	NUM	\$0A00	16	CONTROL WORD TO PRECEED FWRITE	ABZ20200
	NUM	\$0000	17	SPECIAL INPLT TERMINATION CHARACTERS	ABZ20300
	NUM	\$1R21	17	SPECIAL INPLT TERMINATION CHARACTERS	ABZ20400
	NUM	0	18	CURRENT DATA CHARACTER	ABZ20500
	NUM	0	19	FLAGWORD FOR DRIVER	ABZ20600
	ADC	(60*16+04)	20	INPUT TIMEOLT + LEVEL OF INPUT HANDLER	ABZ20700
	ADC	0	21	ENTRY PCINT OF INPUT HANDLER	ABZ20800
	NUM	-1	22	INPUT DIAGNOSTIC CLOCK	ABZ20900
	NUM	0	23	INDEX TO INPUT BUFFER	ABZ21000
	BZS	CABF05(40)	24	INPUT BUFFER	ABZ21100
			63	INPUT BUFFER	ABZ21200
	NUM	0	64	WORD COUNT OF INPUT DATA	ABZ21300
	ADC	PCOM06	65	PHYSTB THREAD	ABZ21400
	EJT				ABZ21500
					ABZ21600
					ABZ21700

Figure 1-55. 364-4 Communications Multiplexor Physical Device Table, Section ABZ {Continued}

3 6 4 - 4 C O M M . M U X , U N I T 6

ENT	CABF06				ARZ21800
SPC	1				ARZ21900
PCOM06	ADC	\$520C	00	SCHEDULER CALL	ARZ22000
	ADC	T3644	01	INITIATOR ADDRESS	ARZ22100
	ADC	C3644	02	CONTINUATOR ADDRESS	ARZ22200
	ADC	E3644	03	TIMEOUT ERROR ADDRESS	ARZ22300
	NUM	-1	04	DIAGNOSTIC CLOCK	ARZ22400
	NUM	0	05	LOGICAL UNIT	ARZ22500
	NUM	0	06	PARAMETER LOCATION	ARZ22600
	NUM	0	07	CONVERTER, EQUIPMENT, STATION	ARZ22700
	ADC	\$3004+T3614	08	REQUEST STATUS	ARZ22800
	ADC	\$3004+T3611	08	REQUEST STATUS	ARZ22900
	NUM	0	09	DRIVER STATUS	ARZ23000
	NUM	0	10	CURRENT LOCATION	ARZ23100
	NUM	0	11	LAST LOCATION PLUS ONE	ARZ23200
	NUM	0	12	DEVICE STATUS	ARZ23300
	NUM	0	13	RESERVED	ARZ23400
	NUM	\$7FFF	14	RESERVED	ARZ23500
	NUM	0	15	RESERVED FOR FNR AND CMR	ARZ23600
	NUM	\$0A0D	16	CONTROL WORD TO PRECEED FWRITE	ARZ23700
	NUM	\$0000	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ23800
	NUM	\$1B21	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ23900
	NUM	0	18	CURRENT DATA CHARACTER	ARZ24000
	NUM	0	19	FLAGWORD FOR DRIVER	ARZ24100
	ADC	(60*16+04)	20	INPUT TIMEOUT + LEVEL OF INPUT HANDLER	ARZ24200
	ADC	0	21	ENTRY POINT OF INPUT HANDLER	ARZ24300
	NUM	-1	22	INPUT DIAGNOSTIC CLOCK	ARZ24400
	NUM	0	23	INDEX TO INPUT BUFFER	ARZ24500
	BZS	CABF06(40)	24	INPUT BUFFER	ARZ24600
			63	INPUT BUFFER	ARZ24700
	NUM	0	64	WORD COUNT OF INPUT DATA	ARZ24800
	ADC	PCOM07	65	PHYSIB THREAD	ARZ24900
EJT					ARZ25000

3 6 4 - 4 C O M M . M U X , U N I T 7

ENT	CABF07				ARZ25100
SPC	1				ARZ25200
PCOM07	ADC	\$520C	00	SCHEDULER CALL	ARZ25300
	ADC	T3644	01	INITIATOR ADDRESS	ARZ25400
	ADC	C3644	02	CONTINUATOR ADDRESS	ARZ25500
	ADC	E3644	03	TIMEOUT ERROR ADDRESS	ARZ25600
	NUM	-1	04	DIAGNOSTIC CLOCK	ARZ25700
	NUM	0	05	LOGICAL UNIT	ARZ25800
	NUM	0	06	PARAMETER LOCATION	ARZ25900
	NUM	0	07	CONVERTER, EQUIPMENT, STATION	ARZ26000
	ADC	\$3004+T3614	08	REQUEST STATUS	ARZ26100
	ADC	\$3004+T3611	08	REQUEST STATUS	ARZ26200
	NUM	0	09	DRIVER STATUS	ARZ26300
	NUM	0	10	CURRENT LOCATION	ARZ26400
	NUM	0	11	LAST LOCATION PLUS ONE	ARZ26500
	NUM	0	12	DEVICE STATUS	ARZ26600
	NUM	0	13	RESERVED	ARZ26700
	NUM	\$7FFF	14	RESERVED	ARZ26800
	NUM	0	15	RESERVED FOR FNR AND CMR	ARZ26900
	NUM	\$0A0D	16	CONTROL WORD TO PRECEED FWRITE	ARZ27000
	NUM	\$0000	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ27100
	NUM	\$1B21	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ27200
	NUM	0	18	CURRENT DATA CHARACTER	ARZ27300
	NUM	0	19	FLAGWORD FOR DRIVER	ARZ27400
					ARZ27500
					ARZ27600
					ARZ27700
					ARZ27800

Figure 1-55. 364-4 Communications Multiplexor Physical Device Table, Section ABZ {Continued}

ADC	(60*16+04)	20	INPUT TIMEOUT + LEVEL OF INPUT HANDLER	AR227900
ADC	0	21	ENTRY POINT OF INPUT HANDLER	AR228000
NUM	-1	22	INPUT DIAGNOSTIC CLOCK	AR228100
NUM	0	23	INDEX TO INPUT BUFFER	AR228200
BZS	CABF07(40)	24	INPUT BUFFER	AR228300
		63	INPUT BUFFER	AR228400
NUM	0	64	WORD COUNT OF INPUT DATA	AR228500
ADC	PCOM08	65	PHYSICAL THREAD	AR228600
EJT				AR228700
	3 6 4 - 4		C O M M . M U X , U N I T 8	AR228800
				AR228900
ENT	CABF08			AR229000
SPC	1			AR229100
PCOM08	ADC \$520C	00	SCHEDULER CALL	AR229200
	ADC T3644	01	INITIATOR ADDRESS	AR229300
	ADC C3644	02	CONTINUATOR ADDRESS	AR229400
	ADC E3644	03	TIMEOUT ERROR ADDRESS	AR229500
	NUM -1	04	DIAGNOSTIC CLOCK	AR229600
	NUM 0	05	LOGICAL UNIT	AR229700
	NUM 0	06	PARAMETER LOCATION	AR229800
	NUM 0	07	CONVERTER, EQUIPMENT, STATION	AR229900
	ADC \$3004+T3614	08	REQUEST STATUS	AR230000
	ADC \$3004+T3611	09	REQUEST STATUS	AR230100
	NUM 0	09	DRIVER STATUS	AR230200
	NUM 0	10	CURRENT LOCATION	AR230300
	NUM 0	11	LAST LOCATION PLUS ONE	AR230400
	NUM 0	12	DEVICE STATUS	AR230500
	NUM 0	13	RESERVED	AR230600
	NUM \$7FFF	14	RESERVED	AR230700
	NUM 0	15	RESERVED FOR FNR AND CMR	AR230800
	NUM \$0A0D	16	CONTROL WORD TO PRECEED FWRITE	AR230900
	NUM \$0000	17	SPECIAL INPUT TERMINATION CHARACTERS	AR231000
	NUM \$1B21	17	SPECIAL INPUT TERMINATION CHARACTERS	AR231100
	NUM 0	18	CURRENT DATA CHARACTER	AR231200
	NUM 0	19	FLAGWORD FOR DRIVER	AR231300
	ADC (60*16+04)	20	INPUT TIMEOUT + LEVEL OF INPUT HANDLER	AR231400
	ADC 0	21	ENTRY POINT OF INPUT HANDLER	AR231500
	NUM -1	22	INPUT DIAGNOSTIC CLOCK	AR231600
	NUM 0	23	INDEX TO INPUT BUFFER	AR231700
	BZS CABF08(40)	24	INPUT BUFFER	AR231800
		63	INPUT BUFFER	AR231900
	NUM 0	64	WORD COUNT OF INPUT DATA	AR232000
	ADC PCOM09	65	PHYSICAL THREAD	AR232100
	EJT			AR232200
	3 6 4 - 4		C O M M . M U X , U N I T 9	AR232300
				AR232400
				AR232500
PCOM09	ENT CABF09			AR232600
	SPC 1			AR232700
	ADC \$520C	00	SCHEDULER CALL	AR232800
	ADC T3644	01	INITIATOR ADDRESS	AR232900
	ADC C3644	02	CONTINUATOR ADDRESS	AR233000
	ADC E3644	03	TIMEOUT ERROR ADDRESS	AR233100
	NUM -1	04	DIAGNOSTIC CLOCK	AR233200
	NUM 0	05	LOGICAL UNIT	AR233300
	NUM 0	06	PARAMETER LOCATION	AR233400
	NUM 0	07	CONVERTER, EQUIPMENT, STATION	AR233500
	ADC \$3004+T3614	08	REQUEST STATUS	AR233600

Figure 1-55. 364-4 Communications Multiplexor Physical Device Table, Section ABZ (Continued)

ADC	\$3004+T3611	08	REQUEST STATUS	ARZ33600
NUM	0	09	DRIVER STATUS	ARZ33700
NUM	0	10	CURRENT LOCATION	ARZ33800
NUM	0	11	LAST LOCATION PLUS ONE	ARZ33900
NUM	0	12	DEVICE STATUS	ARZ34000
NUM	0	13	RESERVED	ARZ34100
NUM	\$7FFF	14	RESERVED	ARZ34200
NUM	0	15	RESERVED FOR FNR AND CMR	ARZ34300
NUM	\$0A0D	16	CONTROL WORD TO PRECEED FWRITE	ARZ34400
NUM	\$0000	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ34500
NUM	\$1B21	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ34600
NUM	0	18	CURRENT DATA CHARACTER	ARZ34700
NUM	0	19	FLAGWORD FOR DRIVER	ARZ34800
ADC	(60*16+04)	20	INPUT TIMEOUT + LEVEL OF INPUT HANDLER	ARZ34900
ADC	0	21	ENTRY POINT OF INPUT HANDLER	ARZ35000
NUM	-1	22	INPUT DIAGNOSTIC CLOCK	ARZ35100
NUM	0	23	INDEX TO INPUT BUFFER	ARZ35200
EZS	CABF09(40)	24	INPUT BUFFER	ARZ35300
		63	INPUT BUFFER	ARZ35400
NUM	0	64	WORD COUNT OF INPUT DATA	ARZ35500
ADC	PCOM10	65	PHYSIB THREAD	ARZ35600
EJ1				ARZ35700
	3 6 4 - 4		C O M M . M U X . U N I T 1 0	ARZ35800
				ARZ35900
ENT	CABF10			ARZ36000
SPC	1			ARZ36100
PCOM10	ADC \$520C	00	SCHEDULER CALL	ARZ36200
	ADC T3644	01	INITIATOR ADDRESS	ARZ36300
	ADC C3644	02	CONTINUATOR ADDRESS	ARZ36400
	ADC E3644	03	TIMEOUT ERROR ADDRESS	ARZ36500
	NUM -1	04	DIAGNOSTIC CLOCK	ARZ36600
	NUM 0	05	LOGICAL UNIT	ARZ36700
	NUM 0	06	PARAMETER LOCATION	ARZ36800
	NUM 0	07	CONVERTER, EQUIPMENT, STATION	ARZ36900
	ADC \$3004+T3611	08	REQUEST STATUS	ARZ37000
	ADC \$3004+T3611	08	REQUEST STATUS	ARZ37100
	NUM 0	09	DRIVER STATUS	ARZ37200
	NUM 0	10	CURRENT LOCATION	ARZ37300
	NUM 0	11	LAST LOCATION PLUS ONE	ARZ37400
	NUM 0	12	DEVICE STATUS	ARZ37500
	NUM 0	13	RESERVED	ARZ37600
	NUM \$7FFF	14	RESERVED	ARZ37700
	NUM 0	15	RESERVED FOR FNR AND CMR	ARZ37800
	NUM \$0A0D	16	CONTROL WORD TO PRECEED FWRITE	ARZ37900
	NUM \$0000	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ38000
	NUM \$1B21	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ38100
	NUM 0	18	CURRENT DATA CHARACTER	ARZ38200
	NUM 0	19	FLAGWORD FOR DRIVER	ARZ38300
	ADC (60*16+04)	20	INPUT TIMEOUT + LEVEL OF INPUT HANDLER	ARZ38400
	ADC 0	21	ENTRY POINT OF INPUT HANDLER	ARZ38500
	NUM -1	22	INPUT DIAGNOSTIC CLOCK	ARZ38600
	NUM 0	23	INDEX TO INPUT BUFFER	ARZ38700
	EZS CABF10(40)	24	INPUT BUFFER	ARZ38800
		63	INPUT BUFFER	ARZ38900
	NUM 0	64	WORD COUNT OF INPUT DATA	ARZ39000
	ADC PCOM11	65	PHYSIB THREAD	ARZ39100
	EJ1			ARZ39200

Figure 1-55. 364-4 Communications Multiplexor Physical Device Table, Section ABZ {Continued}

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3 6 4 - 4 C O M M . M U X . U N I T 1 1

ENT	CARF11			ARZ39300
SPC	1			ARZ39400
PCOM11	ADC	\$520C	00 SCHEDULER CALL	ARZ39500
	ADC	T3644	01 INITIATOR ADDRESS	ARZ39600
	ADC	C3644	02 CONTINUATOR ADDRESS	ARZ39700
	ADC	F3644	03 TIMEOUT ERROR ADDRESS	ARZ39800
	NUM	-1	04 DIAGNOSTIC CLOCK	ARZ39900
	NUM	0	05 LOGICAL UNIT	ARZ40000
	NUM	0	06 PARAMETER LOCATION	ARZ40100
	NUM	0	07 CONVERTER, EQUIPMENT, STATION	ARZ40200
	ADC	\$3004+T3614	08 REQUEST STATUS	ARZ40300
	ADC	\$3004+T3611	08 REQUEST STATUS	ARZ40400
	NUM	0	09 DRIVER STATUS	ARZ40500
	NUM	0	10 CURRENT LOCATION	ARZ40600
	NUM	0	11 LAST LOCATION PLUS ONE	ARZ40700
	NUM	0	12 DEVICE STATUS	ARZ40800
	NUM	0	13 RESERVED	ARZ40900
	NUM	\$7FFF	14 RESERVED	ARZ41000
	NUM	0	15 RESERVED FOR FNR AND CMR	ARZ41100
	NUM	\$0A00	16 CONTROL WORD TO PRECEED FWRITE	ARZ41200
	NUM	\$0000	17 SPECIAL INPUT TERMINATION CHARACTERS	ARZ41300
	NUM	\$1B21	17 SPECIAL INPUT TERMINATION CHARACTERS	ARZ41400
	NUM	0	18 CURRENT DATA CHARACTER	ARZ41500
	NUM	0	19 FLAGWORD FOR DRIVER	ARZ41600
	ADC	(60*16+04)	20 INPUT TIMEOUT + LEVEL OF INPUT HANDLER	ARZ41700
	ADC	0	21 ENTRY POINT OF INPUT HANDLER	ARZ41800
	NUM	-1	22 INPUT DIAGNOSTIC CLOCK	ARZ41900
	NUM	0	23 INDEX TO INPUT BUFFER	ARZ42000
	EZS	CARF11(40)	24 INPUT BUFFER	ARZ42100
			63 INPUT BUFFER	ARZ42200
	NUM	0	64 WORD COUNT OF INPUT DATA	ARZ42300
	ADC	PCOM12	65 PHYSTB THREAD	ARZ42400
	EJT			ARZ42500

3 6 4 - 4 C O M M . M U X . U N I T 1 2

ENT	CARF12			ARZ42600
SPC	1			ARZ42700
PCOM12	ADC	\$520C	00 SCHEDULER CALL	ARZ42800
	ADC	T3644	01 INITIATOR ADDRESS	ARZ42900
	ADC	C3644	02 CONTINUATOR ADDRESS	ARZ43000
	ADC	F3644	03 TIMEOUT ERROR ADDRESS	ARZ43100
	NUM	-1	04 DIAGNOSTIC CLOCK	ARZ43200
	NUM	0	05 LOGICAL UNIT	ARZ43300
	NUM	0	06 PARAMETER LOCATION	ARZ43400
	NUM	0	07 CONVERTER, EQUIPMENT, STATION	ARZ43500
	ADC	\$3004+T3614	08 REQUEST STATUS	ARZ43600
	ADC	\$3004+T3611	08 REQUEST STATUS	ARZ43700
	NUM	0	09 DRIVER STATUS	ARZ43800
	NUM	0	10 CURRENT LOCATION	ARZ43900
	NUM	0	11 LAST LOCATION PLUS ONE	ARZ44000
	NUM	0	12 DEVICE STATUS	ARZ44100
	NUM	0	13 RESERVED	ARZ44200
	NUM	\$7FFF	14 RESERVED	ARZ44300
	NUM	0	15 RESERVED FOR FNR AND CMR	ARZ44400
	NUM	\$0A00	16 CONTROL WORD TO PRECEED FWRITE	ARZ44500
	NUM	\$0000	17 SPECIAL INPUT TERMINATION CHARACTERS	ARZ44600
	NUM	\$1B21	17 SPECIAL INPUT TERMINATION CHARACTERS	ARZ44700
	NUM	0	18 CURRENT DATA CHARACTER	ARZ44800

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	NUM	0	19	FLAGWORD FOR DRIVER	ARZ45300
	ADC	(6B*16+04)	20	INPUT TIMEOUT + LEVEL OF INPUT HANDLER	ARZ45400
	ADC	0	21	ENTRY POINT OF INPUT HANDLER	ARZ45500
	NUM	-1	22	INPUT DIAGNOSTIC CLOCK	ARZ45600
	NUM	0	23	INDEX TO INPUT BUFFER	ARZ45700
	BZS	CARF12(40)	24	INPUT BUFFER	ARZ45800
			63	INPUT BUFFER	ARZ45900
	NUM	0	64	WORD COUNT OF INPUT DATA	ARZ46000
	ADC	PCOM13	65	PHYSIB THREAD	ARZ46100
	EJT				ARZ46200
		3 6 4 - 4		C O M M . M U X . U N I T 1 3	ARZ46300
	ENT	CARF13			ARZ46400
	SPC	1			ARZ46500
PCOM13	ADC	\$520C	00	SCHEDULER CALL	ARZ46600
	ADC	T3644	01	INITIATOR ADDRESS	ARZ46700
	ADC	C3644	02	CONTINUATOR ADDRESS	ARZ46800
	ADC	E3644	03	TIMEOUT ERROR ADDRESS	ARZ46900
	NUM	-1	04	DIAGNOSTIC CLOCK	ARZ47000
	NUM	0	05	LOGICAL UNIT	ARZ47100
	NUM	0	06	PARAMETER LOCATION	ARZ47200
	NUM	0	07	CONVERTER, EQUIPMENT, STATION	ARZ47300
	ADC	\$3004+T3611	08	REQUEST STATUS	ARZ47400
	ADC	\$3004+T3611	09	REQUEST STATUS	ARZ47500
	NUM	0	09	DRIVER STATUS	ARZ47600
	NUM	0	10	CURRENT LOCATION	ARZ47700
	NUM	0	11	LAST LOCATION PLUS ONE	ARZ47800
	NUM	0	12	DEVICE STATUS	ARZ47900
	NUM	0	13	RESERVED	ARZ48000
	NUM	\$7FFF	14	RESERVED	ARZ48100
	NUM	0	15	RESERVED FOR FNP AND CMR	ARZ48200
	NUM	\$0A0D	16	CONTROL WORD TO PRECEED FWRITE	ARZ48300
	NUM	\$0000	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ48400
	NUM	\$1B21	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ48500
	NUM	0	18	CURRENT DATA CHARACTER	ARZ48600
	NUM	0	19	FLAGWORD FOR DRIVER	ARZ48700
	ADC	(6B*16+04)	20	INPUT TIMEOUT + LEVEL OF INPUT HANDLER	ARZ48800
	ADC	0	21	ENTRY POINT OF INPUT HANDLER	ARZ48900
	NUM	-1	22	INPUT DIAGNOSTIC CLOCK	ARZ49000
	NUM	0	23	INDEX TO INPUT BUFFER	ARZ49100
	BZS	CARF13(40)	24	INPUT BUFFER	ARZ49200
			63	INPUT BUFFER	ARZ49300
	NUM	0	64	WORD COUNT OF INPUT DATA	ARZ49400
	ADC	PCOM14	65	PHYSIB THREAD	ARZ49500
	EJT				ARZ49600
		3 6 4 - 4		C O M M . M U X . U N I T 1 4	ARZ49700
	ENT	CARF14			ARZ49800
	SPC	1			ARZ49900
PCOM14	ADC	\$520C	00	SCHEDULER CALL	ARZ50000
	ADC	T3644	01	INITIATOR ADDRESS	ARZ50100
	ADC	C3644	02	CONTINUATOR ADDRESS	ARZ50200
	ADC	E3644	03	TIMEOUT ERROR ADDRESS	ARZ50300
	NUM	-1	04	DIAGNOSTIC CLOCK	ARZ50400
	NUM	0	05	LOGICAL UNIT	ARZ50500
	NUM	0	06	PARAMETER LOCATION	ARZ50600
	NUM	0	07	CONVERTER, EQUIPMENT, STATION	ARZ50700
	ADC	\$3004+T3614	08	REQUEST STATUS	ARZ50800
	ADC	\$3004+T3611	09	REQUEST STATUS	ARZ50900
	NUM	0	09	DRIVER STATUS	ARZ51000
	NUM	0	10	CURRENT LOCATION	ARZ51100
	NUM	0	11	LAST LOCATION PLUS ONE	ARZ51200
	NUM	0	12	DEVICE STATUS	ARZ51300
					ARZ51400
					ARZ51500

Figure 1-55. 364-4 Communications Multiplexor Physical Device Table, Sect. ABZ (ct

NUM	0	13	RESERVED	ARZ51600
NUM	\$7FFF	14	RESERVED	ARZ51700
NUM	0	15	RESERVED FOR FNR AND CMR	ARZ51800
NUM	\$0A0D	16	CONTROL WORD TO PRECEED FWRITE	ARZ51900
NUM	\$0000	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ52000
NUM	\$1B21	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ52100
NUM	0	18	CURRENT DATA CHARACTER	ARZ52200
NUM	0	19	FLAGWORD FOR DRIVER	ARZ52300
ADL	(60*16+04)	20	INPUT TIMEOUT + LEVEL OF INPUT HANDLER	ARZ52400
ADC	0	21	ENTRY PCINT OF INPUT HANDLER	ARZ52500
NUM	-1	22	INPUT DIAGNOSTIC CLOCK	ARZ52600
NUM	0	23	INDEX TO INPUT BUFFER	ARZ52700
BZS	CARF14(40)	24	INPUT BUFFER	ARZ52800
		63	INPUT BUFFER	ARZ52900
NUM	0	64	WORD COUNT OF INPUT DATA	ARZ53000
ADC	PCOM15	65	PHYSTB THREAD	ARZ53100
EJT				ARZ53200
	3 6 4 - 4		C O M M . M U X , U N I T 1 5	ARZ53300
				ARZ53400
ENT	CARF15			ARZ53500
SPC	1			ARZ53600
PCOM15	ADC \$520C	00	SCHEDULER CALL	ARZ53700
	ADC E3644	03	TIMEOUT ERROR ADDRESS	ARZ53800
	ADC I3644	01	INITIATOR ADDRESS	ARZ53900
	ADC C3644	02	CONTINUATOR ADDRESS	ARZ54000
	NUM -1	04	DIAGNOSTIC CLOCK	ARZ54100
	NUM 0	05	LOGICAL UNIT	ARZ54200
	NUM 0	06	PARAMETER LOCATION	ARZ54300
	NUM 0	07	CONVERTER, EQUIPMENT, STATION	ARZ54400
	ADC \$3004+T3614	08	REQUEST STATUS	ARZ54500
	ADC \$3004+T3611	08	REQUEST STATUS	ARZ54600
	NUM 0	09	DRIVER STATUS	ARZ54700
	NUM 0	10	CURRENT LOCATION	ARZ54800
	NUM 0	11	LAST LOCATION PLUS ONE	ARZ54900
	NUM 0	12	DEVICE STATUS	ARZ55000
	NUM 0	13	RESERVED	ARZ55100
	NUM \$7FFF	14	RESERVED	ARZ55200
	NUM 0	15	RESERVED FOR FNR AND CMR	ARZ55300
	NUM \$0A0D	16	CONTROL WORD TO PRECEED FWRITE	ARZ55400
	NUM \$0000	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ55500
	NUM \$1B21	17	SPECIAL INPUT TERMINATION CHARACTERS	ARZ55600
	NUM 0	18	CURRENT DATA CHARACTER	ARZ55700
	NUM 0	19	FLAGWORD FOR DRIVER	ARZ55800
	ADC (60*16+04)	20	INPUT TIMEOUT + LEVEL OF INPUT HANDLER	ARZ55900
	ADC 0	21	ENTRY POINT OF INPUT HANDLER	ARZ56000
	NUM -1	22	INPUT DIAGNOSTIC CLOCK	ARZ56100
	NUM 0	23	INDEX TO INPUT BUFFER	ARZ56200
BZS	CARF15(40)	24	INPUT BUFFER	ARZ56300
		63	INPUT BUFFER	ARZ56400
NUM	0	64	WORD COUNT OF INPUT DATA	ARZ56500
NUM	\$FFFF	65	PHYSTB THREAD	ARZ56600
EJT				ARZ56700

Figure 1-55. 364-4 Communications Multiplexor Physical Device Table, Sect.

1.53 ЭБ4-4 Communications Multiplexor Software Buffered Physical Device Table, Section ACA

Section ACA, Figure 1-5b, contains the buffer macro instruction together with a set of parameters to be expanded into a physical device table. The general form of the buffer macro expansion is described in Article 1.27.

* 3 6 4 - 4 S / W B U F F E R E D , U N I T 0	218	ACA00100
* PCMS00 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA00200
EJT		ACA00300
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 1		ACA00400
* PCMS01 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA00500
EJT		ACA00600
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 2		ACA00700
* PCMS02 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA00800
EJT		ACA00900
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 3		ACA01000
* PCMS03 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA01100
EJT		ACA01200
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 4		ACA01300
* PCMS04 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA01400
EJT		ACA01500
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 5		ACA01600
* PCMS05 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA01700
EJT		ACA01800
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 6		ACA01900
* PCMS06 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA02000
EJT		ACA02100
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 7		ACA02200
* PCMS07 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA02300
EJT		ACA02400
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 8		ACA02500
* PCMS08 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA02600
EJT		ACA02700
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 9		ACA02800
* PCMS09 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA02900
EJT		ACA03000
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 10		ACA03100
* PCMS10 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA03200
EJT		ACA03300
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 11		ACA03400
* PCMS11 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA03500
EJT		ACA03600
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 12		ACA03700
* PCMS12 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA03800
EJT		ACA03900
* 3 6 4 - 4 S / W B U F F E R E D , U N I T 13		ACA04000
* PCMS13 BUFFER \$0000.\$0000.\$0000.00,\$000.0041		ACA04100
EJT		ACA04200
		ACA04300
		ACA04400
		ACA04500
		ACA04600
		ACA04700
		ACA04800
		ACA04900
		ACA05000
		ACA05100
		ACA05200
		ACA05300
		ACA05400
		ACA05500
		ACA05600

Figure 1-5b. 364-4 Communications Multiplexor Software Buffered Physical Device Table, Section ACA

*	3 6 4 - 4	S / W	B U F F E R E D ,	U N I T	1 4	ACA05700
*						ACA05800
	PCMS14	BUFFER	\$0000.\$0000.\$0000.00,\$000.0041			ACA05900
		EJT				ACA06000
*	3 6 4 - 4	S / W	B U F F E R E D ,	U N I T	1 5	ACA06100
*						ACA06200
	PCMS15	BUFFER	\$0000.\$0000.\$0000.00,\$000.0041			ACA06300
		EJT				ACA06400

Figure 1-5b. 364-4 Communications Multiplexor Software Buffered Physical Device Table, Section ACA {Continued}

1.54 1747 Data Set Controller Physical Device Table,
Section ACB

This section is shown in Figure 1-57. A standard interrupt response routine is located at R1747. Bits 7-10 of word 7 contain the converter code for the 1706 Buffered Data Channel, since the 1706 is required for this device. Words 18-29 contain a monitor request to print a hardware failure message. Section ACB does not contain any options.

1747 DATA SET CONTROLLER			ACB00100
	SPC 1		ACB00200
	EXT I1747.C1747.F1747		ACB00300
	EQL N7065()		ACB00400
	EQL N17067(N7005*4100)		ACB00500
	SPC 2		ACB00600
R1747	LUG =XPI747	INTERRUPT RESPONSE FOR THE 1747	ACB00700
	JMP+ (F1747+2)		ACB00800
	SPC 2		ACB00900
P1747	ADC \$520A	00 SCHEDULER CALL	ACB01000
	ADC T1747	01 INITIATOR ADDRESS	ACB01100
	ADC C1747	02 CONTINUATOR ADDRESS	ACB01200
	ADC F1747	03 TIMEOUT ERROR ADDRESS	ACB01300
	NUM 1	04 DIAGNOSTIC CLOCK	ACB01400
	NUM 0	05 LOGICAL UNIT	ACB01500
	NUM 0	06 PARAMETER LOCATION	ACB01600
	ADC \$0501+N17007	07 CONVERTER, EQUIPMENT, STATION	ACB01700
	NUM \$02A1	08 REQUEST STATUS	ACB01800
	NUM 0	09 DRIVER STATUS	ACB01900
	NUM 0	10 CURRENT LOCATION	ACB02000
	NUM 0	11 LAST LOCATION PLUS ONE	ACB02100
	NUM 0	12 DEVICE STATUS	ACB02200
	NUM 0	13 RESERVED	ACB02300
	NUM \$7FFF	14 RESERVED	ACB02400
	NUM 0	15 RESERVED FOR FNR AND CMR	ACB02500
	NUM 0	16 TEMPORARY STORAGE	ACB02600
ISAVE	NUM 0	17 TEMPORARY STORAGE OF 1	ACB02700
	RTU- (AMONI)	18 TELETYPE MONITOR REQUEST	ACB02800
	ADC \$4C88	19 FORMATTED WRITE	ACB02900
	ADC TTYCMP	20 COMPLETION	ACB03000
	ADC 0	21 THREAD	ACB03100
	ADC 4	22 LOGICAL UNIT	ACB03200
	ADC 0	23 MESSAGE LENGTH	ACB03300
	ADC 0	24 MESSAGE ADDRESS	ACB03400
	JMP- (ADISP)	25 EXIT	ACB03500
TTYCMP	LDA# ISAVE	26 REQUEST COMPLETION	ACB03600
	STA- T	27	ACB03700
	JMP+ 0	28 RETURN	ACB03800
		29 RETURN	ACB03900
	EJT		ACB04000
			ACB04100

Figure 1-57. 1747 Data Set Controller Physical Device Table, Section ACB

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1.55 1744/274 Digigraphics Console Physical Device Tables,
Section ACC

The standard interrupt response routine for the 1744/274 Digigraphic Consoles is located at R17441. {Refer to Figure 1-58.} In a standard system there are two consoles, however the driver will support more consoles. If more than two consoles are to be used in a system the user must supply a physical device table for each additional console.

Word 7 of each table contains the converter code for the 1706 Buffered Data Channel. Word 17 is the thread word used to connect the physical device tables for all consoles in the system. Word 23 is the console number as shown in the table heading, i.e., the console number if the consoles are numbered starting at zero. If the consoles are numbered beginning with one, then word 23 is the console number minus one. There are no options in this section.

		1 7 4 4 D I G I G R A P H I C S C O N S O L E 0		
	EXT	T1744,C1744,E1744		ACC00100
	EWL	N17064()		ACC00200
	EQL	N17064(N7004**\$100)		ACC00300
	SPC	2		ACC00400
R17441	LDG	=XP17440	INTERRUPT RESPONSE FOR 1744 DIGIGRAPHICS	ACC00500
	UMF*	(F17440+2)		ACC00600
	SPC	2		ACC00700
P17440	ADC	\$520B	00 SCHEDULER CALL	ACC00800
	ADC	T1744	01 INITIATOR ADDRESS	ACC00900
	ADC	C1744	02 CONTINUATOR ADDRESS	ACC01000
	ADC	F1744	03 TIMEOUT ERROR ADDRESS	ACC01100
	NUM	-1	04 DIAGNOSTIC CLOCK	ACC01200
	NUM	0	05 LOGICAL UNIT	ACC01300
	NUM	0	06 PARAMETER LOCATION	ACC01400
	ADC	\$0600+N17064	07 CONVERTER, EQUIPMENT, STATION	ACC01500
	NUM	\$0286	08 REQUEST STATUS	ACC01600
	NUM	0	09 DRIVER STATUS	ACC01700
	NUM	0	10 CURRENT LOCATION	ACC01800
	NUM	0	11 LAST LOCATION PLUS ONE	ACC01900
	NUM	0	12 DEVICE STATUS	ACC02000
	NUM	0	13 RESERVED	ACC02100
	NUM	\$7FFF	14 RESERVED	ACC02200
	NUM	0	15 RESERVED FOR FNR AND CMR	ACC02300
	ADC	0	16	ACC02400
	ADC	P17441	17 PHYSTB THREAD	ACC02500
	ADC	0	18	ACC02600
	ADC	0	19	ACC02700
	NUM	0	20	ACC02800
	NUM	0	21	ACC02900
	NUM	0	22	ACC03000
	NUM	0	23 CONSOLE NUMBER MINUS 1	ACC03100
	EJT			ACC03200
				ACC03300
				ACC03400
				ACC03500
				ACC03600
				ACC03700
				ACC03800
				ACC03900
				ACC04000
				ACC04100
				ACC04200
				ACC04300
				ACC04400
				ACC04500
				ACC04600
				ACC04700
				ACC04800
				ACC04900
				ACC05000
				ACC05100
				ACC05200
				ACC05300
				ACC05400
				ACC05500
				ACC05600
				ACC05700
				ACC05800
				ACC05900

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Figure 1-58. 1744/274 Digigraphics Consoles Physical Device Tables Section ACC

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ADC
NUM
NUM
NUM
NUM
EJT

1
0
0
0
1

19
20
21
22
23

CONSOLE NUMBER MINUS 1

ACC06000
ACC06100
ACC06200
ACC06300
ACC06400
ACC06500

Figure 1-58. 1744/274 Digigraphics Consoles Physical Device Tables
Section ACC {Continued}

1.5b 1525-3/1501-10/1501-11 High Speed Analog Input
Multiplexor Controller Physical Device Table, Section ACD

This section defines a physical device table used without interrupts. Thus, words 2 and 3 of the table are not used. {Refer to Figure 1-59.} No options are available for this section.

		HIGH - SPEED A/D MULTIPLEXOR		
	SPC	1		ACD00100
	EXT	I1501		ACD00200
	SPC	1		ACD00300
P1501	ADC	5209	00 SCHEDULER CALL	ACD00400
	ADC	I1501	01 INITIATOR ADDRESS	ACD00500
	ADC	0	02 CONTINUATOR ADDRESS - NOT USED	ACD00600
	ADC	0	03 TIMEOUT ERROR ADDRESS - NOT USED	ACD00700
	NUM	-1	04 DIAGNOSTIC CLOCK	ACD00800
	NUM	0	05 LOGICAL UNIT	ACD00900
	NUM	0	06 PARAMETER LOCATION	ACD01000
	NUM	0	07 CONVERTER, EQUIPMENT, STATION - NONE	ACD01100
	NUM	502D1	08 REQUEST STATUS	ACD01200
	NUM	0	09 DRIVER STATUS	ACD01300
	NUM	0	10 CURRENT LOCATION	ACD01400
	NUM	0	11 LAST LOCATION PLUS ONE	ACD01500
	NUM	0	12 DEVICE STATUS	ACD01600
	NUM	0	13 RESERVED	ACD01700
	NUM	57FFF	14 RESERVED	ACD01800
	NUM	0	15 RESERVED FOR FNR AND CMR	ACD01900
	EJT			ACD02000
				ACD02100
				ACD02200

Figure 1-59. 1501-10 High Speed Analog Input Multiplexor Controller Physical Device Table, Section ACD

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1.57 1525-3/1536-2/1502-80 Relay Multiplexor Controller
Physical Device Table, Section ACE

The interrupt response routine at R1536, Figure 1-60,
is standard. {This section to be completed pending driver
design by Chuck Wollaston.}

		1 5 3 6 R E L A Y M U L T I P L E X O R			
	SPC	1			ACE00100
	EXT	I1536,C1536,E1536			ACE00200
	ENT	ARSAIV			ACE00300
	EQL	ARSAIV(%0F44) RIGHT JUSTIFY DATA SHIFT (11-RIT ADC)			ACE00400
	SPC	2			ACE00500
R1536	LDG	=XP1536	INTERRUPT RESPONSE FOR 1536 RELAY MUX		ACE00600
	JMP*	(P1536+2)			ACE00700
	SPC	2			ACE00800
P1536	NUM	\$5209	00 SCHEDULER CALL		ACE00900
	ADC	I1536	01 INITIATOR ADDRESS		ACE01000
	ADC	C1536	02 CONTINUATOR ADDRESS		ACE01100
	ADC	E1536	03 TIMEOUT ERROR ADDRESS		ACE01200
	NUM	-1	04 DIAGNOSTIC CLOCK		ACE01300
	NUM	0	05 LOGICAL UNIT		ACE01400
	NUM	0	06 PARAMETER LOCATION		ACE01500
	ADC	EG5362	07 CONVERTER, EQUIPMENT, STATION		ACE01600
	NUM	\$02C1	08 REQUEST STATUS		ACE01700
	NUM	0	09 DRIVER STATUS		ACE01800
	NUM	0	10 CURRENT LOCATION		ACE01900
	NUM	0	11 LAST LOCATION PLUS ONE		ACE02000
	NUM	0	12 DEVICE STATUS		ACE02100
	NUM	0	13 RESERVED		ACE02200
	NUM	\$7FFF	14 RESERVED		ACE02300
	NUM	0	15 RESERVED FOR FNR AND CMR		ACE02400
	NUM	0	16 STATUS OF ADC		ACE02500
	ADC	EG5253	17 CONVERTER, EQUIPMENT, STATION (A D C)		ACE02600
	NUM	-1	18 CURRENT INPLT ADDRESS		ACE02700
	NUM	0	19 CONVERT TO MV. FLAG		ACE02800
	NUM	0	20 ERROR FLAG		ACE02900
	NUM	10240	21 GAIN 1 = X1		ACE03000
	NUM	10240	22 GAIN 2 = X10		ACE03100
	NUM	10240	23 GAIN 3 = X100		ACE03200
	NUM	10240	24 GAIN 4 = X1000		ACE03300
	NUM	0	25 GAIN 5 NOT USED		ACE03400
	NUM	0	26 GAIN 6 NOT USED		ACE03500
	NUM	0	27 GAIN 7 NOT USED		ACE03600
	NUM	0	28 GAIN 8 NOT USED		ACE03700
	NUM	0	29 OFFSET 1		ACE03800
	NUM	0	30 OFFSET 2		ACE03900
	NUM	0	31 OFFSET 3		ACE04000
	NUM	0	32 OFFSET 4		ACE04100
	NUM	0	33 OFFSET 5 NOT USED		ACE04200
	NUM	0	34 OFFSET 6 NOT USED		ACE04300
	NUM	0	35 OFFSET 7 NOT USED		ACE04400
	NUM	0	36 OFFSET 8 NOT USED		ACE04500
	EJT				ACE04600
					ACE04700
					ACE04800

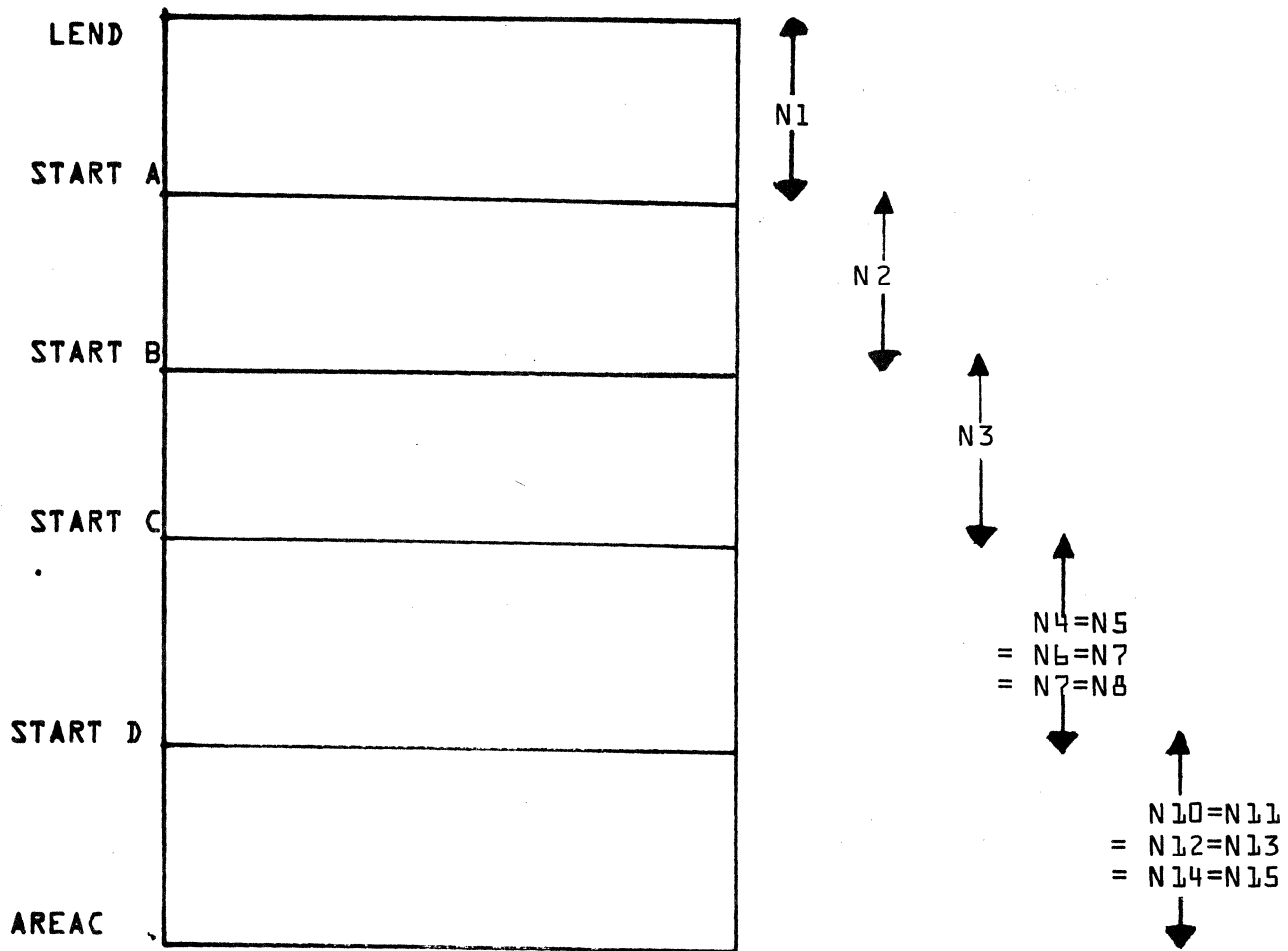
Figure 1-60. 1536-2 Relay Multiplexor Controller Physical Device Table, Section ACE

1.58 Core Allocation Data, Section ACF

Section ACF, Figure 1-61, contains the parameters which define the areas within allocatable core. The location CALTHD is used dynamically by the operating system to contain the current number of words available in allocatable core.

Beginning at location LVLSTR is a table of addresses. Each address is the starting location of the allocatable core area available for a particular priority level. In a SYSDAT listing, the LVLSTR table contains only dummy entries. A core dump of the LVLSTR table from an installed system will show the actual starting addresses of the areas within allocatable core. These starting addresses are inserted by the restart portion of the SPACE program. The starting addresses are dependent on the area lengths. Each area ends at the end of allocatable core. The lengths are given by the parameters N1, N2, ... , N15. Length of the area available to priority level 1 is the value N1, length of the area available to level 2 is N2, etc. In a standard system N1, N2, N3 and N4 are dependent on MSOS requirements, and N5 ... N15 are dependent on information supplied by the user in the 1700 MSOS Ordering Form. The following diagram illustrates a typical layout of allocatable core in a standard released system.

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The preceding diagram corresponds to the following table of addresses beginning at location LVLSTR:

<u>Location</u>	<u>Address</u>	<u>Definition</u>
LVLSTR	AREAC	START OF ALLOCATABLE CORE FOR LEVEL 0
	STARTA	START OF ALLOCATABLE CORE FOR LEVEL 1
	STARTB	START OF ALLOCATABLE CORE FOR LEVEL 2
	STARTC	START OF ALLOCATABLE CORE FOR LEVEL 3
	STARTD	START OF ALLOCATABLE CORE FOR LEVEL 4
	STARTD	START OF ALLOCATABLE CORE FOR LEVEL 5
	STARTD	START OF ALLOCATABLE CORE FOR LEVEL 6
	STARTD	START OF ALLOCATABLE CORE FOR LEVEL 7

<u>Location</u>	<u>Address</u>	<u>Definition</u>
	STARTD	START OF ALLOCATABLE CORE FOR LEVEL 8
	LEND	START OF ALLOCATABLE CORE FOR LEVEL 9
	AREAC	START OF ALLOCATABLE CORE FOR LEVEL 10
	AREAC	START OF ALLOCATABLE CORE FOR LEVEL 11
	AREAC	START OF ALLOCATABLE CORE FOR LEVEL 12
	AREAC	START OF ALLOCATABLE CORE FOR LEVEL 13
	AREAC	START OF ALLOCATABLE CORE FOR LEVEL 14
	AREAC	START OF ALLOCATABLE CORE FOR LEVEL 15

In the above example a program with core request priority 9 could not run within the allocatable area. It would require a core swap to run.

The user may change the starting locations of the areas within allocatable core, thus changing the lengths of the areas. He must observe the following restrictions when revising the lengths and starting addresses:

1. The start of allocatable core for level 0 must be the first location of allocatable core. This is true because level 0 is used in initializing the system and at that time the entire allocatable area must be available. The system library programs LIBEDT and the Job Processor are used during initialization before their core request priorities have been set to non-zero values.

2. The end of allocatable core, LEND, must not be changed unless the system is rebuilt.

3. If no space is allowed within allocatable core for a given priority level, then this level must exceed 2 so that a program at that level can force a core swap.

4. The area available to level 1 must be large enough to accommodate the following Job Processor modules:

JOBENT

T11

T7

T5

T3

At the time of system installation the number of words available to level 1 is set by executing the following system initializer control statement after loading these modules:

*S-N1-P

{The above modules have core request priority 1 in the system directory.}

5. The area available to level 2 must be large enough to accommodate the following Job Processor modules:

JOBPRO

ONE

TWO

THREE

The value of N2 is set during system installation by executing a *S system initializer control statement. After loading the modules JOBPRO, ONE, TWO and THREE, the control statement

```
*S,N2,P
```

is executed to set the value of N2. {The above modules have core request priority 2 in the system directory.}

6. The value of N3 is determined by the overlay size of the Protect Processor, since the Protect Processor has core request priority 3 in the System Directory.

7. The value of N4 must be large enough so that the largest of the following programs can run in the area available at level 4:

a} Any System Library Programs which have a core request priority level of four. In a typical system this includes

◊DEBUG

System Checkout Package

MIPRO

File Manager

TDFUNC

Engineering File Modules

SCMM

Verification Programs

- b) Any user programs which are to use this area.

If the File Manager is to be used by the background, N4 must be large enough so that even if the Job Processor and the Protect Processor occupy all of the areas available to levels 1-3, there will be enough area left for the File Manager to run. If the unbuffered protect processor, UPR0TP, is in the system and a File Manager request is made from the background, a core swap cannot be made since background input/output would be in progress. Even if the buffered protect processor, BPR0TP, is in the system only a maximum of 96 words could be swapped. The minimum allocatable core area needed by the File Manager

is
$$P + I \cdot 96 + 118$$

where P = the size of the largest File Manager request processor {store sequential, store direct, etc.}

$I = 0$, if there are no indexed files in the system
 $I = 1$, if there are indexed files in the system and the expected number of key values declared when a file is defined is less than 8465, for all files in the system.

= 2, if there is at least one indexed file in the system for which the expected number of key values is declared at the time of file definition to be greater than or equal to 8465.

{The constant value 118 is the sum of 96 words for the file information segment {FIS} directory, 16 words for one file information segment {FIS} and 6 words for the header appended to a FIS when a FIS is in core. The value $I \cdot 96$ is the size of the largest key information segment {KIS} directory for any file in the system.} Thus, for background File Manager requests to be possible we must have

$$N4 \geq P + I \cdot 96 + 118$$

Note that this is a minimum. If more core is available in area 4, the File Manager may be able to have more than one processor as well as more than one KIS directory and/or FIS in core at once, thus increasing throughput. If the value of $N4$ is small, it may be wise to adjust the File Manager timeout parameter so that core areas allocated for the File Manager are released soon after they have been used. Refer to Article 1.79, File Manager Data. In a standard released system $N4$ is made as large as possible. In a system which allows File Manager requests by background programs, a

released system allows at least 1500 words as the value of N4.

After modifying this section of SYSDAT, the system can be rebuilt or the references to the lengths and starting addresses can be changed in the restart portion of the SPACE program and the system can be autoloaded.


```

*          R E S I D E N T   C O R E   D A T A
*
ENT  LSTLOC
EXT  RGNMON
SPC  1
LSTLOC  ADC  RGNMON      BEGINNING LOCATION OF CORE RESIDENT SYSTEM
SPC  2
*          C O R E   A L L O C A T I O N   D A T A
*
ENT  CALTHD      CORE ALLOCATOR THREAD
ENT  LVLSTH      LEVEL START ALLOCATION TABLE
EXT  AREAC       START OF ALLOCATABLE AREA
EXT  LEND        END OF ALLOCATABLE AREA
*
CALTHD  ADC  C      TOTAL AVAILABLE ALLOCATABLE CORE
      ADC  AREAC     START OF ALLOCATABLE AREA
*
LVLSTH  ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL  0
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL  1
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL  2
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL  3
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL  4
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL  5
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL  6
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL  7
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL  8
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL  9
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL 10
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL 11
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL 12
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL 13
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL 14
      ADC  AREAC   START OF ALLOCATABLE CORE FOR LEVEL 15
      ADC  LEND    END OF ALLOCATABLE CORE
*
ENT  N5,N6,N7,N8,N9,N10,N11,N12,N13,N14,N15
SPC  1
*  NOTE - THE SIZE OF AREAS 1, 2, 3, AND 4 ARE SPECIFIED
*         DURING SYSTEM INITIALIZATION
*
N5  EGL  N5()      NUMBER OF CORE LOCATIONS FOR AREA 5
N6  EGL  N6()      NUMBER OF CORE LOCATIONS FOR AREA 6
N7  EGL  N7()      NUMBER OF CORE LOCATIONS FOR AREA 7
N8  EGL  N8()      NUMBER OF CORE LOCATIONS FOR AREA 8
N9  EGL  N9()      NUMBER OF CORE LOCATIONS FOR AREA 9
N10 EGL  N10()     NUMBER OF CORE LOCATIONS FOR AREA 10
N11 EGL  N11()     NUMBER OF CORE LOCATIONS FOR AREA 11
N12 EGL  N12()     NUMBER OF CORE LOCATIONS FOR AREA 12
N13 EGL  N13()     NUMBER OF CORE LOCATIONS FOR AREA 13
N14 EGL  N14()     NUMBER OF CORE LOCATIONS FOR AREA 14
N15 EGL  N15()     NUMBER OF CORE LOCATIONS FOR AREA 15
EJT

```

Figure 1-61. Core Allocation Data, Section ACF

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1.59

Partitioned Core Data, Section ACG

Section ACG, Figure 1-b2, contains the parameters defining partitioned core. The first part of this section is a set of equivalences which are included in SYSDAT only if partitioned core is not in the system. These equivalences do two things. They set those indicators checked by the monitor to indicate there is no partitioned core in the system. Also, they patch the unused partitioned core driver entries with $7FFF_{16}$ so that these entries will not be listed as unpatched externals at the time of system installation. That part of Section ACG following the equivalences is included in SYSDAT only if the system includes partitioned core.

In a released system the starting addresses of the partitions are determined by the information supplied by the user in the 1700 MSOS Ordering Form. An unused partition is indicated by an entry of $FFFF_{16}$ as the starting address. The number of the last partition in the system is equivalenced to LSTPRT.

Following the starting addresses is a table of top-of-threads, one for each partition. When the system is operating, the top of thread for partition n points to the first of a set of

programs waiting to be executed with a starting location at the start of partition n if there is a waiting list for the partition. If there is no waiting list, the thread is zero unless the partition is not in the system in which case it is FFFE₁₆. Initially the thread for a partition is zero if the partition is in the system. Otherwise, it is FFFE₁₆.

At line *ACG0550 is a table starting with location USE. This table contains 16 entries, one for each protected partition, i.e., partitions 0 through 15. For a given partition, the corresponding word of the USE table is zero unless there is currently a program which has its starting location at the beginning of this partition. In this case the corresponding word in the USE table has a bit set for each partition occupied by that program. For example, if a system contains protected partitions 0-7, and currently there is a program in partitions 0-2, a program in partition 4, and a program in partitions 6-7, the USE table would contain the following values:

<u>Location</u>	<u>Contents in hexadecimal</u>
USE +0	0007
+1	0000
+2	0000
+3	0000
+4	0010
+5	0000

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<u>Location</u>	<u>Contents in hexadecimal</u>
USE +6	00C0
+7	0000
+8	0000
+9	0000
+10	0000
+11	0000
+12	0000
+13	0000
+14	0000
+15	0000

This table must contain all zeroes initially.

The partition busy indicator word is labeled BUSY. Within this word bit n indicates the status of partition n. If the bit is a one, the corresponding partition is currently being used by a program or the partition is not included in the system. If the bit is zero the partition is available. In the above example, the value of BUSY corresponding to the USE table values would be $FFD7_{16}$. Initially the word BUSY must have those bits set which correspond to partitions not included in the system. All other bits must be zero initially.

The partition core driver active indicator, DIP, is initially $FFFF_{16}$.

The words in Section ACG may be modified by the user to re-define partitioned core. In doing this, the following requirements must be met:

1. The number of partitions in a system may be from 1 to 17. These must be numbered starting with partition 0. Partition 16 must be included to contain unprotected core, if unprotected core is in Part 1.
2. Any partitions with numbers 0 through 15 must be contiguous. {Partition 16 need not be contiguous to the other partitions.}
3. Partitioned core must start at the beginning of Part 1. It must begin at a location $\leq 8000_{16}$.

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PARTITION CORE DATA

THESE ENTRIES ALLOW PROPER SYSTEM LINKAGE IF PARTITIONED CORE IS NOT SELECTED.

SPC 2
ENT PARTBL,LSTPRT,PRTCDR,BUSY
ENT OUTPV4,RDPTV4,SPCEV4,PTNALC
ENT PTNREL,K65T10,K65T12,K65CCR
SPC 2
EQL PARTBL(\$7FFF),LSTPRT(\$0000),PRTCDR(\$7FFF),BUSY(\$7FFF)
EQL OUTPV4(\$7FFF),RDPTV4(\$7FFF),SPCEV4(\$7FFF),PTNALC(\$7FFF)
EQL PTNREL(\$7FFF),K65T10(\$7FFF),K65T12(\$7FFF),K65CCR(\$7FFF)
ENT PARTBL,BUSY,DIP,LSTPRT
SPC 1
EQL LSTPRT() LAST PARTITION IN SYSTEM

ACG00100
ACG00200
ACG00300
ACG00400
ACG00500
ACG00600
ACG00700
ACG00800
ACG00900
ACG01000
ACG01100
ACG01200
ACG01300
ACG01400
ACG01500
ACG01600
ACG01700
ACG01800
ACG01900
ACG02000
ACG02100
ACG02200
ACG02300
ACG02400
ACG02500
ACG02600
ACG02700
ACG02800
ACG02900
ACG03000
ACG03100
ACG03200
ACG03300
ACG03400
ACG03500
ACG03600
ACG03700
ACG03800
ACG03900
ACG04000
ACG04100
ACG04200
ACG04300
ACG04400
ACG04500
ACG04600
ACG04700
ACG04800
ACG04900
ACG05000
ACG05100
ACG05200
ACG05300
ACG05400
ACG05500
ACG05600
ACG05700
ACG05800
ACG05900

PARTBL NUM 0 STARTING ADDRESS OF PARTITION 0
NUM 0 STARTING ADDRESS OF PARTITION 1
NUM 0 STARTING ADDRESS OF PARTITION 2
NUM 0 STARTING ADDRESS OF PARTITION 3
NUM 0 STARTING ADDRESS OF PARTITION 4
NUM 0 STARTING ADDRESS OF PARTITION 5
NUM 0 STARTING ADDRESS OF PARTITION 6
NUM 0 STARTING ADDRESS OF PARTITION 7
NUM 0 STARTING ADDRESS OF PARTITION 8
NUM 0 STARTING ADDRESS OF PARTITION 9
NUM 0 STARTING ADDRESS OF PARTITION 10
NUM 0 STARTING ADDRESS OF PARTITION 11
NUM 0 STARTING ADDRESS OF PARTITION 12
NUM 0 STARTING ADDRESS OF PARTITION 13
NUM 0 STARTING ADDRESS OF PARTITION 14
NUM 0 STARTING ADDRESS OF PARTITION 15
NUM 0 STARTING ADDRESS OF PARTITION 16 - SWAP AREA
THDS SPC 2
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 0
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 1
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 2
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 3
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 4
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 5
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 6
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 7
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 8
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 9
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 10
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 11
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 12
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 13
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 14
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 15
NUM 0 TCP OF REQUEST THREAD FOR PARTITION 16 - SWAP
USE SPC 2
BZS USE(16) PARTITION-IN-USE INDICATORS
SPC 2
BUSY NUM 0 BUSY INDICATOR - BIT 0 = PARTITION 0
DIP NUM -0 PARTITION CORE DRIVER ACTIVE INDICATOR
EJT

Figure 1-62. Partitioned Core Data, Section ACG

1.60 System Common Declaration, Section ACH

Section ACH, Figure 1-63, is contained in SYSDAT only if blank common is to be available in the system. If it is available, blank common resides in Part D following allocatable core. Blank common may include up to 2710₁₆ words. The size of blank common is declared in SYSDAT since SYSDAT is always the first program in the system to be loaded. The size is specified by the user in the MSOS 4 Ordering Form. Blank common may not be preset with data during system initialization.

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```
*
*
*
*
SYSTEM COMMON DECLARATION
THIS ENTRY SPECIFIES THE AMOUNT OF SYSTEM (BLANK) COMMON
SPC 4
EQU NCOM()
SPC 1
COMMON COM COMMON(NCOM)
EUT
```

ACH00100
ACH00200
ACH00300
ACH00400
ACH00500
ACH00600
ACH00700
ACH00800

Figure 1-63. System Common Declaration, Section ACH

1.61 System Idle Loop, Section ACI

Location IDLE, Figure 1-64, is entered after an autoload and whenever there is no activity in the system. Immediately after an autoload, location STRTUP contains zero. Thus, at this time an entry at IDLE causes the code at STRTUP to be executed. If the system library request priorities have not yet been set, the standard input logical unit is set to the logical unit of the installation device. This idle loop is then executed until the operator enters a manual interrupt to start the procedure of building the system library and program library. {Refer to the 1700 MSOS 4 Installation Handbook, Publication Number 39520900.} If, on entry to STRTUP, the libraries have already been built, control is transferred to STRTP1 where a core swap is forced. This is the path taken after all autoloads except the autoload which occurs during system installation just prior to the building of the libraries. If other functions are to be performed for such autoloads they should be inserted at STRTP1 prior to the core swap. The reason for a core swap after an autoload is that the unprotected area as well as allocatable core are needed by the Restart routine to perform initialization tasks.

The loop beginning at IDLER is included in this section only if there is a hardware timing device in the system. The counter in this loop is used by the system in determining the percentage of system operation time that is idle time. If there is no hardware device in the system, the IDLER loop contained in SYSDAT will be the one found in Section AC0, Figure 1-70.

The IDLER loop {either the one from this section or the one from Section AC0} is entered in two situations:

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1} There is no system activity and the current priority level is -1.

2} A program in allocatable core requires more core and none is available except by a core swap. In this case, the protect processor checks to see that the following requirements are satisfied:

a} If the unbuffered protect processor, UPR0TK, is in the system, any background input/output in progress must be completed.

b} If the buffered protect processor, BPR0TK, is in the system, background input/output must be completed except for input/output involving less than 96 words per record.

In this case BPR0TK retains the input/output operation and the record buffer within itself.

c} Any incompleted background timer requests must be completed.

When the above requirements are satisfied, the unprotected area is written to mass memory and the core locations in the unprotected area are protected. The idle loop is scheduled at level 2. Control is returned to the program in allocatable core which made the space request. The idle loop is entered at level 2 during input/output operations performed by this program. When the program releases the space the unprotected area is read back in from mass memory and the area is again unprotected.

*	M I S C E L L A N E O U S P R O G R A M S	ACI00100
*		ACI00200
*	S Y S T E M I D L E L O O P	ACI00300
	SPC 2	ACI00400
	ENT IDLE BASIC SYSTEM IDLE LOOP	ACI00500
	ENT IDLER SYSTEM IDLE SUBROUTINE	ACI00600
	ENT IDLCNTR IDLE LOOP COUNTER	ACI00700
	SPC 2	ACI00800
IDLE	LDA# STRTUP IS THIS THE INITIAL IDLE ENTRY	ACI00900
	SAN IDLE1 NO	ACI01000
IDLE1	RTU# STRTUP YES, PERFORM STARTUP FUNCTIONS	ACI01100
	RTU# IDLER	ACI01200
	JMP# IDLE	ACI01300
IDLER	NUM 0	ACI01400
	IIN 0 USED AT LEVEL -1 OR LEVEL 2	ACI01500
	RAC# IDLCNTR	ACI01600
	EIN 0	ACI01700
	JMP# (IDLER)	ACI01800
IDLCNTR	SPC 1	ACI01900
	NUM 0 IDLE LOOP COUNTER	ACI02000
	SPC 2	ACI02100
	EXT LIBEDT LIBRARY EDIT	ACI02200
	EXT RELFLF SYSTEM CORE SWAP ROUTINE	ACI02300
	EXT INPTV4 JOB PROCESSOR STANDARD INPUT DEVICE	ACI02400
	SPC 1	ACI02500
STRTUP	NUM 0	ACI02600
	LDC- RFR	ACI02700
	ADG =XLIBEDT OBTAIN THE DIRECTORY ADDRESS OF LIBEDT	ACI02800
	LDA- (ZERO),0 HAVE THE REQUEST PRIORITIES BEEN SET UP	ACI02900
	SAN STRTUP YES	ACI03000
	ENA 0	ACI03100
	STA+ INPTV4 SET THE STD. INPUT TO THE INSTALLATION L.U.	ACI03200
	JMP# (STRTUP) AND EXIT	ACI03300
	SPC 1	ACI03400
STRTUP	RTU- (AMONI) SCHEDULE RELFLF TO FORCE A SWAP	ACI03500
	ADC +5203	ACI03600
	ADC RELFLF	ACI03700
		ACI03800
*		ACI03900
*	NOTE - ANY ADDITIONAL SYSTEM STARTUP FUNCTIONS MAY BE	ACI04000
*	ADDED HERE	ACI04100
*		ACI04200
	JMP# (STRTUP)	ACI04300
	END	

Figure 1-64. System Idle Loop, Section ACI

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1.62 Interrupt Response for the 1750/1572 Sample Rate
Timer, Section ACJ

This section is included in SYSDAT only if a 1750/1572 Sample Rate Timer is to be used as the system timer. {Refer to Figure 1-65}. The location 01572 contains the quotient:

$$\frac{n}{60 \text{ cycles per second}}$$

where n is the basic clock rate of the system timer. In a released system, n is 200 kilocycles and the quotient is 3333. The user must change this value if the basic clock rate is other than 200 kilocycles. The other entries in this section are fixed.

At the end of Section ACJ the entry points to the other timer interrupt response routines are equated to $7FFF_{16}$. This is done so that these entries will not appear as unpatched externals at the time of system installation.

		M I S C E L L A N E O U S P R O G R A M S	ACJ00100
		I N T E R R U P T R E S P O N S E F O R T I M E R	ACJ00200
			ACJ00300
			ACJ00400
			ACJ00500
	ENT	TMRTYP.TMCODE TYPE OF SYSTEM TIME BASE	ACJ00600
	EXT	TIMEUP TMINT INTERRUPT ENTRY	ACJ00700
	EQL	X(\$7FFF) VALUE FOR UNSELECTED ENTRY POINTS	ACJ00800
	SPC	1	ACJ00900
	EQL	TMCODE(1) 1572 PROGRAMMABLE SAMPLE RATE UNIT	ACJ01000
TMRTYP	ADC	TMCODE TIME BASE CODE	ACJ01100
	SPC	2	ACJ01200
	ENT	E1572F FUNCTION CODE TO ENABLE 1572	ACJ01300
	ENT	E1572 1572 BASIC W,E,S WORD	ACJ01400
	ENT	O1572 1572 OSCILLATOR FREQ./CLOCK FREQ.	ACJ01500
	SPC	1	ACJ01600
E1572F	NUM	\$8900	ACJ01700
		\$8000 = ENABLE INTERRUPT	ACJ01800
		\$800 = SET SAMPLE RATE GEN MODE	ACJ01900
		\$100 = ENABLE INPUT SOURCE 2	ACJ02000
E1572	NUM	\$0403	ACJ02100
O1572	NUM	1667	ACJ02200
	SPC	1	ACJ02300
	SPC	100 KC/60 CFS	ACJ02400
TIMINT	LDG*	E1572 FUNCTION OUTPUT	ACJ02500
	ENA	1 ACKNOWLEDGE INTERRUPT	ACJ02600
	CUT	TIMREJ-* RE-ENABLE TIMER	ACJ02700
	LDA*	E1572F DATA OUTPUT	ACJ02800
	CUT	TIMREJ-* COUNTDOWN VALUE	ACJ02900
	ING	-1	ACJ03000
	LDA*	O1572	ACJ03100
	CUT	TIMREJ-* EXIT TO TMINT	ACJ03200
	JMP+	TIMEUP TIMER REJECT	ACJ03300
TIMREJ	NOF	0	ACJ03400
	JMP-	(ADISP) EXIT	ACJ03500
	SPC	4	ACJ03600
		LINK ALL UNSELECTED ENTRY POINTS	ACJ03700
			ACJ03800
	ENT	E1573,H15721,E15721,D15721,O15721,EQ3644	ACJ03900
	EQL	E1573(X),H15721(X),E15721(X),D15721(X),O15721(X),EQ3644(X)	

Figure 1-65. Interrupt Response for the 1750/1572 Sample Rate Timer, Section ACJ

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1.63 Interrupt Response for the 1750/1573 Line Synchronized
Timer, Section ACK

This section is included in SYSDAT only if a 1750/1573 Line Synchronized Timer is the system timing device. All entries are fixed. At the end of the section entries to interrupt response routines for other timing devices are equated to $7FFF_{16}$ so that they will not appear as unpatched externals during system installation. Section ACK is shown in Figure 1-66.

TMRTYP	EQU	TMCODE (2)	1573 LINE SYNC TIMING GENERATOR	ACK00100
	ADC	TMCODE	TIME BASE CODE	ACK00200
	SPC	2		ACK00300
	ENT	E1573	1573 BASIC W,E,S WORD	ACK00400
	SPC	1		ACK00500
E1573	NUM	\$0401		ACK00600
	SPC	1		ACK00700
TIMINT	LDG#	E1573	FUNCTION OUTPUT	ACK00800
	ENA	0		ACK00900
	CUT	TIMREJ=*		ACK01000
	JMP+	TIMEUP	EXIT TO TMINT	ACK01100
TIMREJ	NOF	0	TIMER REJECT	ACK01200
	JMP-	(ADISP)	EXIT	ACK01300
	SPC	4		ACK01400
*				ACK01500
*		LINK ALL UNSELECTED ENTRY POINTS		ACK01600
*				ACK01700
	ENT	E1572F,E1572,01572,H15721,E15721,D15721,015721,EQ3644		ACK01800
	EQU	E1572F(X),E1572(X),01572(X),H15721(X)		ACK01900
	EQU	F15721(X),015721(X),015721(X),EQ3644(X)		ACK02000

Figure 1-66. Interrupt Response for the 1750/1573 Line Synchronized Timer. Section ACK

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1.64 Interrupt Response Routine for the Line Synchronized
Timing Portion of the 1750-1/1572-1 Sample Timing
Unit, Section ACL

This section, Figure 1-67, is included in SYSDAT if the Line Synchronized Timing Portion of a 1750-1/1572-1 Sample Timing Unit is the system timer. If the sample rate generator portion of the 1572-1 is used by a user program, an exit is made to the user-supplied interrupt response routine SRG721 when it is determined that the sample rate generator portion of the 1572-1 caused the interrupt.

Entry points for other timer interrupt response routines are equated to $7FFF_{16}$ to avoid having these names printed as unpatched externals at the time of system installation.

TMRTYP	EQL	TMCODE (3)	1572-1 SAMPLE TIMING UNIT (LST PART)	ACL00100
	ADC	TMCODE	TIME BASE CCDE	ACL00200
	SPC	2		ACL00300
	ENT	H15721	1572-1 FUNCTION OUTPUT HISTORY WORD	ACL00400
	ENT	E15721	1572-1 BASIC W,E,S WORD - FUNCTION	ACL00500
	EXI	SRG721	SRG SECTION USER PROGRAM	ACL00600
	SPC	1		ACL00700
H15721	NUM	0		ACL00800
E15721	NUM	\$048F		ACL00900
E15721	ADC	EG5721		ACL01000
	SPC	1		ACL01100
TIMINT	LDC*	E15721	STATUS INPUT	ACL01200
	INF	TIMREJ--*		ACL01300
	SAP	LST	BIT 15 = SRG, BIT 14 = LST	ACL01400
SRG	LDA*	H15721		ACL01500
	INA	8		ACL01600
	CUT	TIMREJ--*	ACKNOWLEDGE SRG INTERRUPT	ACL01700
	LDA*	GCSRG+1	VERIFY USER ROUTINE PATCHED	ACL01800
	EOR-	LPMSK+15		ACL01900
	SAZ	NOSRG	SKIP IF NOT PATCHED	ACL02000
GOSRG	JMP+	SRG721	EXIT TO USER PROGRAM	ACL02100
NOSRG	ENA	7	AND MASK FOR LST FUNCTION BITS	ACL02200
	IIN	0		ACL02300
	AND*	H15721		ACL02400
	CUT	TIMREJ--*	TURN OFF THE SRG	ACL02500
	STA*	H15721	RESTORE 1572-1 HISTORY	ACL02600
	EIN	0		ACL02700
	JMP-	(ADISP)	EXIT	ACL02800
LST	ENA	\$38	AND MASK FOR SRG FUNCTION BITS	ACL02900
	IIN	0		ACL03000
	AND*	H15721		ACL03100
	INA	3	ADD LST INTERRUPT ENABLE + ACKNOWLEDGE	ACL03200
	CUT	TIMREJ--*	FUNCTION OUTPUT	ACL03300
	INA	-1	REMOVE ACKNOWLEDGE BIT	ACL03400
	STA*	H15721	RESTORE 1572-1 HISTORY	ACL03500
	EIN	0		ACL03600
	JMP+	TIMEUP	EXIT TO TIMINT	ACL03700
TIMREJ	NOP	0	TIMER REJECT	ACL03800
	JMP-	(ADISP)	EXIT	ACL03900
	EJT			ACL04000
*			I N T E R R U P T R E S P O N S E F O R T I M E R	ACL04100
*				ACL04200
	SPC	2		ACL04300
*				ACL04400
*			LINK ALL UNSELECTED ENTRY POINTS	ACL04500
				ACL04600
ENT		E1572F,E1572,01572,E1573,D15721,015721,EQ3644		ACL04700
EQL		E1572F(X),E1572(X),01572(X),E1573(X)		ACL04800
EQL		D15721(X),015721(X),EQ3644(X)		ACL04900

Figure 1-67. Interrupt Response Routine for the Line Synchronized Timing Portion of the 1750-1/1572-1 Sample Timing Unit Section ACL

1.65 Interrupt Response Routine for the Sample Rate Generator
Portion of the 1750-1/1572-1 Sample Timing Unit, Section ACM

This section, Figure 1-68, is included in SYSDAT only if the sample rate generator portion of a 1750-1/1572-1 Sample Timing Unit is the system timer. The parameter 01572 has the value

$$\frac{k}{60 \text{ cycles per second}}$$

where k is the clock rate of the sample rate generator. In a standard released system k is 100 kilocycles and 01572 has the value 1667. The user must change this value if the clock rate of the sample rate generator is other than 100 kilocycles. If the user supplies an interrupt response routine, LST721, for the line synchronized timing portion of the 1572-1, an exit is made to this routine whenever it is determined that the interrupt was caused by the line synchronized timing portion of the device.

Entry points for other timer interrupt response routines are equated to $7FFF_{16}$ to avoid having these names appear as unpatched externals at the time of system installation.

	EQL	TMCODE (4)	1572-1 SAMPLE TIMING UNIT (SRG PART)	ACM00100
TMRTYP	ADC	TMCODE	TIME BASE CODE	ACM00200
	ENT	H15721	1572-1 FUNCTION OUTPUT HISTORY WORD	ACM00300
	ENT	E15721	1572-1 BASIC W.E.S WORD - FUNCTION	ACM00400
	ENT	D15721	1572-1 BASIC W.E.S WORD - DATA	ACM00500
	ENT	O15721	SRG TIME BASE/CLOCK FREQ.	ACM00600
	EXT	LST721	LST SECTION USER PROGRAM	ACM00700
	SPC	1		ACM00800
H15721	NUM	0		ACM00900
O15721	NUM	1667	100 KC/ 60 CPS	ACM01000
E15721	NUM	\$048F		ACM01100
D15721	NUM	\$040F		ACM01200
E15721	ADC	EG5721		ACM01300
O15721	ADC	EG5721-\$80		ACM01400
	SPC	1		ACM01500
TIMINT	LDG*	E15721	STATUS INPUT	ACM01600
	INF	TIMREJ-*		ACM01700
	SAM	SRG	RIT 15 = SRG, RIT 14 = LST	ACM01800
	LDA*	+15721		ACM01900
	INA	1		ACM02000
	CUT	TIMREJ-*	ACKNOWLEDGE LST INTERRUPT	ACM02100
	LDA*	GOLST+1	VERIFY USER ROUTINE PATCHED	ACM02200
	FOR-	LPMSK+15		ACM02300
	SAZ	NOLST	SKIP IF NOT PATCHED	ACM02400
GOLST	JMP+	LST721	EXIT TO USER PROGRAM	ACM02500
NOLST	ENA	\$3P	AND MASK FOR SRG FUNCTION BITS	ACM02600
	IIN	0		ACM02700
	AND*	H15721		ACM02800
	CUT	TIMREJ-*	TURN OFF THE LST	ACM02900
	STA*	H15721	RESTORE 1572-1 HISTORY	ACM03000
	EIN	0		ACM03100
SRG	JMP-	(ADISP)	EXIT	ACM03200
	ENA	7	AND MASK FOR LST FUNCTION BITS	ACM03300
	IIN	0		ACM03400
	AND*	H15721		ACM03500
	INA	\$18	ADD SRG INTERRUPT ENABLE + ACKNOWLEDGE	ACM03600
	CUT	TIMREJ-*		ACM03700
	INA	-8	REMOVE ACKNOWLEDGE	ACM03800
	STA*	H15721	RESTORE 1572-1 HISTORY WORD	ACM03900
	EIN	0		ACM04000
	LDG*	D15721	DATA OUTPUT	ACM04100
	LDA*	O15721		ACM04200
	CUT	TIMREJ-*		ACM04300
	JMP+	TIMEUP	EXIT TO TIMINT	ACM04400
TIMREJ	NOF	0	TIMER REJECT	ACM04500
	JMP-	(ADISP)	EXIT	ACM04600
	EJT			ACM04700
*			I N T E R R U P T R E S P O N S E F O R T I M E R	ACM04800
*				ACM04900
	SPC	2		ACM05000
*				ACM05100
*			LINK ALL UNSELECTED ENTRY POINTS	ACM05200
*				ACM05300
ENT		E1572F,E1572,O1572,E1573,EQ3644		ACM05400
EQL		E1572F(X),E1572(X),O1572(X),E1573(X),EQ3644(X)		ACM05500

Figure 1-6B. Interrupt Response Routine for the Sample Rate Generator Portion of the 1750-1/1572-1 Sample Timing Unit, Section ACM

1.66 Interrupt Response Routine for the 364-4 Communications Multiplexor Timebase, Section ACN

This section is included in SYSDAT only if a 364-4 Communications Multiplexor is the system timer. Entry points for other timer interrupt response routines are equated to $7FFF_{16}$ to avoid having these names appear as unpatched externals at the time of system installation. {Figure 1-69.}

	EQL	TMCODE(5)	364-4 COMMUNICATIONS MULTIPLEXOR	ACN00100
TMRTYP	ADC	TMCODE	TIME BASE CCDE	ACN00200
	SPC	2		ACN00300
	EQL	RASCTR(2)	TIMER INTERRUPTS / DRIVER ENTRY	ACN00400
*				ACN00500
*	NOTE	-	THIS VALUE IS USED TO ESTABLISH BOTH THE BAUD RATE	ACN00600
*			RESPONSE OF THE COMMUNICATIONS EQUIPMENT, AND ALSO	ACN00700
*			THE SYSTEM TIMER ACCURACY. THE 364-4 SHOULD NOT	ACN00800
*			BE ADJUSTED SUCH THAT THE SYSTEM TIMER FREQUENCY	ACN00900
*			IS LESS THAN 60 HZ. IF IT IS ADJUSTED TO ALLOW A	ACN01000
*			FREQUENCY GREATER THAN 60 HZ, THE VALUE OF #TIMCPS#	ACN01100
*			MUST BE CHANGED ACCORDINGLY. #TIMCPS# IS LOCATED	ACN01200
*			IN THE TIMER PARAMETER REGION OF SYSDAT.	ACN01300
	ENT	EGR3644	364-4 BASIC W,E,S WORD - FUNCTION	ACN01400
	SPC	1		ACN01500
EQ3644	NUM	\$0281		ACN01600
CTR	NUM	0		ACN01700
	SPC	1		ACN01800
TIMINT	LDA#	CTR	IS THE COUNT EXHAUSTED	ACN01900
	SAN	DOWN	NO, DECREMENT IT	ACN02000
	LDA	=XBASCTR	YES, RE-INITIALIZE	ACN02100
DOWN	INA	-1		ACN02200
	STA#	CTR	IS IT TIME TO ENTER THE DRIVER	ACN02300
	SAZ	DRIVER	YES	ACN02400
	LDG#	EG3644	NO, JUST ACKNOWLEDGE THE INTERRUPT	ACN02500
	ENA	6		ACN02600
	CUT	TIMREFJ-#		ACN02700
TIME	JMP+	TIMEUP	EXIT TO TMINT	ACN02800
TIMREFJ	NOF	0		ACN02900
	JMP-	(ADISP)		ACN03000
	SPC	1		ACN03100
DRIVER	LDG	=XPCOM00	SET UP TO SCHEDULE THE DRIVER	ACN03200
	LDA-	(ZERO),0		ACN03300
	STA#	CALL		ACN03400
	LDA-	2,0		ACN03500
	STA#	CALL+1		ACN03600
	RTJ-	(AMONI)	SCHEDULE THE CONTINUATION ENTRY	ACN03700
CALL	ADC	0		ACN03800
	ADC	0		ACN03900
	JMP#	TIME	EXIT	ACN04000
	EJT			ACN04100
*			INTERRUPT RESPONSE FOR TIMER	ACN04200
*				ACN04300
	SPC	2		ACN04400
*				ACN04500
*			LINK ALL UNSELECTED ENTRY POINTS	ACN04600
*				ACN04700
	ENT	F1572F,E1572,01572,F1573,F15721,E15721,D15721,015721		ACN04800
	EQL	E1572F(X),E1572(X),01572(X),E1573(X)		ACN04900
	EQL	H15721(X),E15721(X),D15721(X),015721(X)		ACN05000
				ACN05100

Figure 1-69. Interrupt Response Routine for the 364-4 Communications Multiplexor Timebase, Section ACN

1.67 Software Pseudo Timer Routine, Section AC0

This routine, Figure 1-70, is included in SYSDAT for a system with no hardware timing device. It contains the IDLER loop for such a system. The pseudo timer is used to approximate elapsed time by counting the number of passes through the IDLER loop. When the count overflows the TMINT program is scheduled. This program will check the status of the hardware devices in the system to determine if an interrupt expected from any device is late. Thus the pseudo timer is used for hardware failure detection.

The value of BASCTR, line *AC00050, is set to approximate 60 cycles per second. The fastest cycle time for a 1700-series central processing unit was used in deriving the value of BASCTR. Since system overhead is the critical factor in pseudo timer elapsed time, there is no reason to make the BASCTR value dependent on the system cycle time. The pseudo timer may be used for system timer calls if the only requirement is that a minimum time period must have elapsed.

Entry points for hardware timer interrupt response routines are equated to $7FFF_{16}$ to avoid having these entries printed as unpatched externals at the time of system installation.

	EQU	TMCode(0)	SOFTWARE PSEUDO TIMER	AC000100
TMRTYP	ADC	6	TIME BASE CODE	AC000200
	SPC	2		AC000300
CTR	NUM	30967		AC000400
BASCTR	NUM	30967	APPROXIMATION TO 60 CPS	AC000500
	SPC	1		AC000600
IDLER	NUM	0		AC000700
	IIN	0	USED AT LEVEL -1 OR LEVEL 2	AC000800
	SOV	0		AC000900
	RAC*	CTR	PSEUDO SYSTEM TIMER	AC001000
	SOV	COUNT	SKIP IF 1800 LOCPS EXECUTED	AC001100
	EIN	0		AC001200
	JMF*	(IDLER)	BACK TO BASIC IDLE LOOP	AC001300
COUNT	LDA*	RASCTR	INITIALIZE LOOP COUNTER	AC001400
	STA*	CTR		AC001500
	LDG*	IDLER	SAVE RETURN FOR RE-ENTRANCY IN VOLATILE	AC001600
	RTU-	(AVOLA)	GET 3 WORDS VOLATILE STORAGE	AC001700
	NUM	3		AC001800
	RTU-	(AMONI)		AC001900
	ADC	\$520D	SCHEDULE TMINT AT LEVEL 13	AC002000
	ADC	TIMEUP		AC002100
	IIN	0		AC002200
	RTU-	(AVOLR)	RETURN VOLATILE	AC002300
	JMF-	(ZERO),Q	RETURN TO IDLE LOOP	AC002400
	SPC	4		AC002500
*				AC002600
*		LINK ALL UNSELECTED ENTRY POINTS		AC002700
*				AC002800
	ENT	E1572F,E1572,01572,E1573,H15721,E15721,015721,015721,EQ3644		AC002900
	EQU	E1572F(X),E1572(X),01572(X),E1573(X),H15721(X)		AC003000
	EQU	E15721(X),015721(X),015721(X),EQ3644(X)		AC003100
	EJT			AC003200

Figure 1-7D. Software Pseudo Timer Routine, Section AC0

1.68 1706 Buffered Data Channel Handler, Section ACP

A 1706 Buffered Data Channel accumulates a buffer of data from the A/Q channel. When the data has been collected in the 1706 buffer, it is transmitted by the 1706 to a connected output device. This is more efficient than transmitting each word directly from the A/Q channel to the device since in the latter case the driver must process an interrupt for each word transmitted. There may be from one to three 1706 Buffered Data Channels in a system. If there is at least one 1706 in the system, Section ACP will be included in SYSDAT. The number of 1706's is equated to the parameter NUM06. Refer to Figure 1-71. Each 1706 channel may be connected to up to eight devices. The number of devices connected to the first 1706 is NON1, the number connected to the second 1706 is NON2, and the number connected to the third is NON3. A 1706 can transfer data to only one device at a time. For this reason, if any one of the 1706 channels is to be used by more than one device, the buffered data channel allocator program, AL1706, must be included in the system to queue the drivers which share usage of a 1706. If there are no shared 1706's in the system the access entry, RQ1706, and the release entry, RL1706, are equated, thus creating a dummy routine in place of AL1706.

The program, AL1706, uses the wait stacks STACKA, STACKB, and STACKC. When a driver requests 1706 allocation, and the 1706 to which the corresponding device is connected is currently in use, AL1706 places the driver return address, I-register {physical device table address}, and priority level in the appropriate stack in this section. The entries to any unused stacks are equated to 7FFF₁₆ to avoid having these entries

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appear as unpatched externals at the time of system installation. Following the stacks are parameter arrays used by AL1706 to queue the 1706's in the system.

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```

*
* MISCELLANEOUS PROGRAMS
*
* 1706 BUFFERED DATA CHANNEL
*
* HANDLER
*
SPC 2
ENT RQ1706 ACCESS ENTRY FOR 1706
ENT RL1706 RELEASE ENTRY FOR 1706
SPC 1
RL1706 NUM 0 ENTRY USED IF NO 1706 UNITS SHARED
IIN 0
LDG- I TRANSFER PDI ADDRESS TO Q
EIN 0
CMP* (RL1706) RETURN
SPC 1
EQL RQ1706(RL1706) EQUATE BOTH ENTRIES
*
EQL NUM06() NUMBER OF 1706 UNITS IN SYSTEM
*
1706 DATA STACKS
*
ENT STACKA DATA STACK FOR 1706 NO. 1
ENT STACKB DATA STACK FOR 1706 NO. 2
ENT STACKC DATA STACK FOR 1706 NO. 3
EQL NON1() NUMBER OF DEVICES ON 1706 NO. 1
STACKA BZS STACKA(3*NON1-3)
EQL NON2() NUMBER OF DEVICES ON 1706 NO. 2
STACKB BZS STACKB(3*NON2-3)
EQL NON3() NUMBER OF DEVICES ON 1706 NO. 3
STACKC BZS STACKC(3*NON3-3)
EQL STACKA($7FFF)
EQL STACKB($7FFF)
EQL STACKC($7FFF)
*
1706 DATA STACK SIZES
*
ENT STKSIZ
STACKSIZ EQL STKSIZ(*)
ADC NON1*3-3
ADC NON2*3-3
ADC NON3*3-3
*
1706 BUSY FLAGS
*
ENT BUSY1 BUSY1
BZS BUSY1(NUM06)
EJT
*
* 1706 BUFFERED DATA CHANNEL
*
* HANDLER
*
ACP00100
ACP00200
ACP00300
ACP00400
ACP00500
ACP00600
ACP00700
ACP00800
ACP00900
ACP01000
ACP01100
ACP01200
ACP01300
ACP01400
ACP01500
ACP01600
ACP01700
ACP01800
ACP01900
ACP02000
ACP02100
ACP02200
ACP02300
ACP02400
ACP02500
ACP02600
ACP02700
ACP02800
ACP02900
ACP03000
ACP03100
ACP03200
ACP03300
ACP03400
ACP03500
ACP03600
ACP03700
ACP03800
ACP03900
ACP04000
ACP04100
ACP04200
ACP04300
ACP04400
ACP04500
ACP04600
ACP04700
ACP04800
ACP04900
ACP05000
ACP05100
ACP05200

```

Figure 1-71. 1706 Buffered Data Channel Handler, Section ACP

	SPC 2		ACP05300
*			ACP05400
	ENT TEMPI		ACP05500
	ENT ITEMP		ACP05600
TEMPI	BZS TEMPI(NUM06)		ACP05700
ITEMP	BZS ITEMP(NUM06)		ACP05800
*			ACP05900
*	STACK NEXT IN POINTERS		ACP06000
*			ACP06100
	ENT NXTIN		ACP06200
NXTIN	BZS NXTIN(NUM06)		ACP06300
*			ACP06400
*	STACK NEXT OUT POINTERS		ACP06500
*			ACP06600
	ENT NXTOUT		ACP06700
NXTOUT	BZS NXTOUT(NUM06)		ACP06800
*			ACP06900
*	PHYSICAL DEVICE TABLE POINTERS		ACP07000
*			ACP07100
	ENT ONPDT		ACP07200
ONPDT	BZS ONPDT(NUM06)		ACP07300
	EJT		ACP07400

Figure 1-71. 1706 Buffered Data Channel Handler, Section ACP (Continued)

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1.69 A/Q Channel Allocator Routine, Section ACQ

Devices which do not use a hardware buffered data channel receive data one word at a time from the A/Q channel. Certain of these devices require that a data interrupt be serviced within a specified amount of time to avoid lost data. These are devices in which the data must be transmitted during the time interval that the data medium is being transported through the reading or writing mechanism. Examples of data media for which this is true are cards, paper tape, and magnetic tape. Specific examples of devices which require A/Q channel allocation are listed in Figure 1-72, beginning at line *ACQ0180. The area at AQSTCK is used by the A/Q channel allocator as a wait stack for devices requesting A/Q channel allocation. This stack requires 3 words for each device which is queued. If fewer than two devices in the system require A/Q channel allocation lines *ACQ0060 through *ACQ0150 constitute this section. These lines equate the request entry and the release entry of the A/Q channel allocator and replace the A/Q channel allocator with a dummy routine.

```

*
*
*
*
MISCELLANEOUS PROGRAMS
A/Q CHANNEL ALLOCATION
ACQ00100
ACQ00200
ACQ00300
ACQ00400
ACQ00500
ACQ00600
ACQ00700
ACQ00800
ACQ00900
ACQ01000
ACQ01100
ACQ01200
ACQ01300
ACQ01400
ACQ01500
ACQ01600
ACQ01700
ACQ01800
ACQ01900
ACQ02000
ACQ02100
ACQ02200
ACQ02300
ACQ02400
ACQ02500
ACQ02600
ACQ02700
ACQ02800
ACQ02900
ACQ03000
ACQ03100
ACQ03200
ACQ03300
ACQ03400
ACQ03500
ACQ03600
ACQ03700
ACQ03800
ACQ03900

SPC 2
ENT RQAG REQUEST ENTRY FOR A/Q
ENT RLAG RELEASE ENTRY FOR A/Q
RQAG SPC 1
NUM 0 ENTRY USED IF NO A/Q ALLOCATION
IIN 0
LDG- I TRANSFER PDT ADDRESS TO Q
EIN 0
JMP* (RQAG) RETURN
SPC 1
EQU RLAG(RQAG) EQUATE BOTH ENTRIES
DEVICES REQUIRING ALLOCATION
1711/713 TELETYPE / CRT
1713 KEYBOARD
1713 PAPER TAPE READER
1713 PAPER TAPE PUNCH
1721 PAPER TAPE READER
1723 PAPER TAPE PUNCH
1777 PAPER TAPE READER
1777 PAPER TAPE PUNCH
1728/430 CARD READER/PUNCH
1729-2 CARD READER
1729-3 CARD READER
1731/601 UNBUFFERED MAG TAPE
1732-1/608/609 UNBUFFERED MAG TAPE
SPC 2
ENT AQSTCK STACK FOR REQUESTS
ENT AQSSIZ STACK SIZE
SPC 2
EQU NUMAQ() QUANTITY OF ALLOCATION DEVICES
SPC 2
AQSTCK BZS AQSTCK(3*NUMAQ-3)
AQSSIZ ADC NUMAQ*3-3
EJT

```

Figure 1-72. A/Q Channel Allocator Routine, Section ACQ

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1.70 Interface to Mass Memory Driver; Common Exit for Drivers in System Without Mass Resident Drivers, Section ACR

If no mass resident driver is required by the system, only lines *ACR0010 through *ACR0100 will be included in this section of SYSDAT. {Refer to Figure 1-73.} These lines supply a common exit for those drivers which are optionally core resident or mass resident. In a system with at least one mass resident driver MMEEXEC is in the system. This program then processes driver exits for optionally mass resident drivers. It checks the physical device table for a device to see if the driver is core resident or mass resident. It then releases core if necessary and jumps to the dispatcher. For a system with no mass memory driver, MMEEXEC is not in the system and the entry MAS300 in this section provides the common driver exit.

The buffer located at BUFF is included in SYSDAT if there is at least one mass resident driver in the system. This buffer is used to contain mass resident drivers while they are in core. It must be sized to at least contain the largest mass resident driver in the system. For dual operation of any two drivers, it must be sized to contain the largest two mass resident drivers in the system. A system which includes a mass resident COSY driver should have a buffer large enough to contain the COSY driver together with the largest mass resident driver used by the COSY driver. This is because the COSY driver, DCOSY, makes requests of other input/output drivers. A driver being used by DCOSY must be in core simultaneously with DCOSY until the driver completes the request for the COSY driver.

*		M I S C E L L A N E O U S I N F O R M A T I O N	ACR00100
*			ACR00200
*		C O M M O N E X I T F O R A L L D R I V E R S	ACR00300
	SPC	2	ACR00400
	ENT	MAS300	ACR00500
	SPC	2	ACR00600
MAS300	JMP-	(ADISP)	ACR00700
	SPC	2	ACR00800
	ENT	RELBYG LINK ENTRY POINT IN MMEXEC	ACR00900
	EQL	RELBYG(\$7FFF)	ACR01000
*		M A S S R E S I D E N T D R I V E R S B U F F E R	ACR01100
*			ACR01200
*		THIS BUFFER WILL CONTAIN THE MASS RESIDENT DRIVER(S)	ACR01300
*		WHEN THEY ARE IN CORE. THE SMALLEST ALLOWABLE SIZE IS	ACR01400
*		EQUAL TO THE LARGEST MASS RESIDENT DRIVER IN THE SYSTEM.	ACR01500
*		OPTIMUM THROUGHPUT REQUIRES SIZING EQUAL TO THE TWO	ACR01600
*		LARGEST MASS RESIDENT DRIVERS IN THE SYSTEM.	ACR01700
	SPC	2	ACR01800
	ENT	BUFF,BUFFE	ACR01900
	EQL	MBFSZ()	ACR02000
	SPC	2	ACR02100
BUFF	BZS	BUFF(MBFSZ)	ACR02200
	EQL	BUFFE(*)	ACR02300
	EJT		ACR02400

Figure 1-73. Interface to Mass Memory Drivers; Common Exit for Drivers in System Without Mass Memory Drivers; Section ACR

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1.71 Reentrant FORTRAN Information, Section ACS

Section ACS, Figure 1-74, provides information regarding reentrant FORTRAN. This section is included in SYSDAT only if the reentrant FORTRAN library is needed in the system. The bits of FMASK specify which priority levels are available to programs using the foreground FORTRAN library. If bit n is 1, priority level n is a foreground FORTRAN level. If only one FORTRAN level is available in the foreground, FORTRAN reentrancy is not needed in the system and Section ACS is omitted from SYSDAT.

The list of entry point addresses beginning at FLIST lists those entry points of the reentrant FORTRAN library which are included in the system. For a system with a single precision reentrant FORTRAN library lines *ACSD220 through *ACSD450 are included in the entry point list. Lines *ACSD530 through *ACSD670 are included in the list only if the double precision routines are to be included in the reentrant FORTRAN library.

The entry QBSTP is provided for compatibility with the compiler. A jump to QBSTP is added to the end of a FORTRAN program by the compiler if the last statement in the program is not a branching statement. Usually a reentrant FORTRAN program is terminated with a CALL RELESE statement. A jump to QBSTP is added by the compiler even though it is unnecessary. A FORTRAN reentrant program links to the QBSTP entry in this section in terminating the program. FORTRAN STOP and PAUSE statements also use QBSTP. These statements are illegal in the foreground. If they are used they will also link to the QBSTP entry in this section.

Lines *ACSD800 through *ACSD840 are included if double precision routines are not to be a part of the FORTRAN reentrant library.

	M I S C E L L A N E O U S	I N F O R M A T I O N	ACS00100
	F O R T R A N	R E E N T R A N T	I N F O R M A T I O N
	ENT	FMASK, FLIST	ACS00200
	EXT	QBQF2I, QBQI2F, QBQF2F, QSAVE, E4SAVE	ACS00300
	EXT	RETAD, KG47V4, KG48V4, KG49V4	ACS00400
	EXT	ABS, SGRT, SIGN, IFIX, FLCAT	ACS00500
	EXT	EXP, ALOG, TANH, SIN, CCS, ATAN, IFALT	ACS00600
	EXT	FLOT, ARGUN	ACS00700
	EXT	QBDFLT, QBQD2I, QBQD2F, QBGD2D	ACS00800
	EXT	DARS, DSQRT, DSTGN, SNGL, DFLT	ACS00900
	EXT	DEXP, DLOG, DSIN, DCCS, DATAN, DFLCT	ACS01000
	SPC	1	ACS01100
FMASK	NUM	\$XXXX	ACS01200
		FORTTRAN REENTRANT LEVELS (BIT 0 = LEVEL 0)	ACS01300
	SPC	1	ACS01400
		TABLE OF FORTRAN ENTRY POINTS SAVED TO MAINTAIN REENTRANCY	ACS01500
	SPC	1	ACS01600
	ENTRY POINT	PROGRAM	DESCRIPTION
	-----	-----	-----
	SPC	1	ACS01700
FLIST	ADC	FEND	ACS01800
	ADC	QBQF2I	QBEXPR
			FLCAT-TO-INTEGGER FUNCTION
	ADC	QBQI2F	QBEXPR
			INTEGER-TO-FLOAT FUNCTION
	ADC	QBQF2F	QBEXPR
			FLCAT-TO-FLOAT FUNCTION
	ADC	QSAVE	QBEXPR
			G-REGISTER STORAGE
	ADC	E4SAVE	QBEXPR
			LOCATION \$E4 STORAGE
	ADC	RETAD	QBEXPR
			RETURN ADDRESS STORAGE
	ADC	KG47V4	QBEXPR
			TEMPORARY STORAGE
	ADC	KG48V4	QBEXPR
			TEMPORARY STORAGE
	ADC	KG49V4	QBEXPR
			TEMPORARY STORAGE
	ADC	ABS	QBABR
			ABSOLUTE VALUE FUNCTION
	ADC	SGRT	SGRTER
			SQUARE ROOT FUNCTION
	ADC	SIGN	SIGNR
			INTRINSIC FUNCTION SIGN
	ADC	IFIX	FXFLR
			INTRINSIC FUNCTION IFIX
	ADC	FLOAT	FXFLR
			INTRINSIC FUNCTION FLOAT
	ADC	EXP	EXPRGR
			EXTERNAL FUNCTION EXP
	ADC	ALOG	LNPRGR
			EXTERNAL FUNCTION ALOG
	ADC	TANH	TANFR
			EXTERNAL FUNCTION TANH
	ADC	SIN	SNCSR
			EXTERNAL FUNCTION SIN
	ADC	CCS	SNCSR
			EXTERNAL FUNCTION COS
	ADC	ATAN	ARCPGR
			EXTERNAL FUNCTION ATAN
	ADC	IFALT	IFALTR
			EXTERNAL FUNCTION IFALT
	ADC	FLOT	FLOATR
			FLOATING POINT PROCESSOR
	ADC	ARGUN	QBQIO
			TEMPORARY STORAGE
	EJT		ACS04500
			ACS04600

Figure 1-74. Reentrant FORTRAN Information, Section ACS

* F O R T R A N R E E N T R A N T I N F O R M A T I O N				ACS04700
* S P C 1				ACS04800
ENTRY POINT	PROGRAM	DESCRIPTION		ACS04900
-----	-----	-----		ACS05000
SPC 1				ACS05100
ADC Q8DFLT	Q8DBLR	INTEGER-TO-DOUBLE FUNCTION		ACS05200
ADC Q8QD2I	DEXPR	DOUBLE-TO-INTEGFR FUNCTION		ACS05300
ADC Q8QD2F	DEXPR	DOUBLE-TO-SINGLE FUNCTION		ACS05400
ADC Q8QD2D	DEXPR	DOURLE-TO-DOURLE FUNCTION		ACS05500
ADC DABS	Q8DARR	DBL. PREC. ABSOLUTE VALUE FUNCTION		ACS05600
ADC DSQRT	DSQRTR	DBL. PREC. SQUARE ROOT FUNCTION		ACS05700
ADC DSIGN	DSIGNP	INTRINSIC FUNCTION DSIGN		ACS05800
ADC SNGL	SNGLR	INTRINSIC FUNCTION SNGL		ACS05900
ADC DFLT	Q8DFLR	INTRINSIC FUNCTION DFLT		ACS06000
ADC DEXP	DEXPR	DBL. PREC. EXTERNAL FUNCTION EXP		ACS06100
ADC DLOG	DLOGR	DBL. PREC. EXTERNAL FUNCTION ALOG		ACS06200
ADC DSIN	DSNCSR	DBL. PREC. EXTERNAL FUNCTION SIN		ACS06300
ADC DCOS	DSNCSR	DBL. PREC. EXTERNAL FUNCTION COS		ACS06400
ADC DATAN	DATANR	DBL. PREC. EXTERNAL FUNCTION ATAN		ACS06500
ADC DFLOT	DFLOTR	DBL. PREC. FLOATING POINT PROCESSOR		ACS06600
FEND	EQL FEND(*-FLIST-1)			ACS06700
EJT				ACS06800
* F O R T R A N R E E N T R A N T I N F O R M A T I O N				ACS06900
* S P C 4				ACS07000
* THIS ENTRY IS PROVIDED TO ALLOW COMPATIBILITY BETWEEN THE				ACS07100
* NON-REENTRANT (BACKGROUND) FORTRAN AND REENTRANT FORTRAN				ACS07200
* S P C 1				ACS07300
* ENT Q8STP				ACS07400
* S P C 1				ACS07500
Q8STP	NOP 0	FORTRAN STOP		ACS07600
* CMP- (ADISP)				ACS07700
* S P C 2				ACS07800
* LINK THE DOUBLE PRECISION ENTRY POINT REFERENCED BY *FORMTR*				ACS07900
* S P C 1				ACS08000
* ENT DCUT				ACS08100
* EQL DCUT(\$7FFF)				ACS08200
* EJT				ACS08300
				ACS08400
				ACS08500

Figure 1-74. Reentrant FORTRAN Information, Section ACS (Continued)

1.72 Time/Date Parameter Storage, Section ACT

This section is a table of current time and date parameter values. The parameters AYERT0, AMONT0, and ADAYT0 are in ASCII. The other parameters are integer values.

{Refer to Figure 1-75.}

MISCELLANEOUS INFORMATION

ACT00100

TIME / DATE PARAMETER STORAGE

ACT00200

ACT00300

ACT00400

ACT00500

ACT00600

ACT00700

ACT00800

ACT00900

ACT01000

ACT01100

ACT01200

ACT01300

ACT01400

ACT01500

ACT01600

ACT01700

ACT01800

ACT01900

ACT02000

ACT02100

ACT02200

ACT02300

ACT02400

ACT02500

ACT02600

ACT02700

ACT02800

ACT02900

ACT03000

ACT03100

```

SPC 3
ENT AYERTO CURRENT YEAR (ASCII)
ENT AMONTC CURRENT MONTH (ASCII)
ENT ADAYTC CURRENT DAY (ASCII)
ENT YERTO CURRENT YEAR (INTEGER)
ENT MONTO CURRENT MONTH (INTEGER)
ENT DAYTO CURRENT DAY (INTEGER)
ENT HORTO CURRENT HOUR (INTEGER)
ENT MINTO CLURRENT MINUTE (INTEGER)
ENT SECON CLURRENT SECCND (INTEGER)
ENT CONTA CLURRENT COUNT (INTEGER)
ENT HORMIN CURRENT 24-HOUR TIME
ENT TOTMIN CURRENT DAY ELAPSED MINUTES

SPC 3
AYERTO NUM 0 CURRENT YEAR (ASCII)
AMONTO NUM 0 CURRENT MONTH (ASCII)
ADAYTO NUM 0 CURRENT DAY (ASCII)
YERTO NUM 0 CURRENT YEAR (INTEGER)
MONTO NUM 0 CURRENT MONTH (INTEGER)
DAYTO NUM 0 CURRENT DAY (INTEGER)
HORTO NUM 0 CLURRENT HOUR (INTEGER)
MINTO NUM 0 CURRENT MINUTE (INTEGER)
SECON NUM 0 CURRENT SECCND (INTEGER)
CONTA NUM 0 CURRENT COUNT (INTEGER)
HORMIN NUM 0 CURRENT 24-HOUR TIME
TOTMIN NUM 0 CURRENT DAY ELAPSED MINUTES
EJT

```

Figure 1-75. Time/Date Parameter Storage, Section ACT

1.73 System Timer Parameters, Section ACU

This section {Figure 1-7b} contains system timer parameter values. TIMCPS is the line frequency of the system. TIMEC is defined as

$$\left\lfloor \frac{n}{10} - 1 \right\rfloor$$

where n is the number of timer cycles per second, and

$\lfloor x \rfloor$ is the greatest integer less than or equal to x. TODLVL is the first word in the parameter list for a monitor request to schedule the time-of-day program, TOD. The request code is B, meaning that scheduling is to be done after a time delay. The priority level is B, the d-bit is 1, and the x-bit is 0. Bits 4 through 7 are zero indicating the time delay is to be expressed in counts of the timing device. NSCHED specifies the maximum number of timer request completions which will be processed at the time of a timer interrupt. When a timer interrupt occurs the timer request processor determines the number of timer requests for which the time delay has elapsed since the last timer interrupt. If this number exceeds NSCHED, the first NSCHED requests are scheduled and the remaining requests are placed on the counts thread for expiration at the next timer interrupt. The value of NSCHED may be used to limit the number of scheduler requests made at one time if a large number of timer requests are used in the system.

		M I S C E L L A N E O U S I N F O R M A T I O N		ACU00100
		S Y S T E M T I M E R P A R A M E T E R S		ACU00200
	SPC	4		ACU00300
	ENT	TIMCPS	BASIC SYSTEM CLOCK FREQUENCY	ACU00400
	SPC	1		ACU00500
TIMCPS	EQU	TIMCPS(60)	TIMER CYCLES PER SECOND	ACU00600
	SPC	4		ACU00700
	ENT	TIMEC	TIMER CYCLES PER 1/10 SECOND MINUS 1	ACU00800
	SPC	1		ACU00900
TIMEC	EQU	TIMEC(TIMCPS/10-1)		ACU01000
	SPC	4		ACU01100
	ENT	TODLVL	TIME OF DAY(TOD) PROGRAM REQ. CODE + PRIORITY	ACU01200
	SPC	1		ACU01300
TODLVL	EQU	TODLVL(\$5006)	D-BIT = 1, REQUEST CODE 8, PRIORITY 6	ACU01400
	SPC	4		ACU01500
	ENT	NSCHED	MAX. NO. OF COMPLETIONS PER TIMER INTERVAL	ACU01600
	SPC	1		ACU01700
NSCHED	NUM	5	MAXIMUM 5 COMPLETIONS PER INTERVAL	ACU01800
	SPC	4		ACU01900
	ENT	TMRLVL	DIAGNOSTIC TIMER PRIORITY LEVEL	ACU02000
	SPC	1		ACU02100
TMRLVL	EQU	TMRLVL(13)	LEVEL 13	ACU02200
	EJT			ACU02300
				ACU02400

Figure 1-7b. System Timer Parameters, Section ACU

1.74 System Program Overlay Sizes, Section ACV

This section, Figure 1-77, contains the overlay size for the library edit program, LIBEDT, for the protect processor, BPR0TK or UPR0TK, and for the on-line debug package, 0DEBUG. If there is enough core space in the system, the buffered protect processor, BPR0TK, is used with the larger overlay size {line *ACV0100}. Otherwise, the unbuffered protect processor, UPR0TK is used {line *ACV0110}.

M I S C E L L A N E O U S I N F O R M A T I O N

S Y S T E M P R O G R A M O V E R L A Y S I Z E S

SPC	2					ACV00100
ENT	LSIZV4	INITIAL OVERLAY SIZE OF	LIREDT			ACV00200
ENT	PSIZV4	INITIAL OVERLAY SIZE OF	PROTEC			ACV00300
ENT	ODRSIZ	INITIAL OVERLAY SIZE OF	ODEBUG			ACV00400
SPC	4					ACV00500
EQU	LSIZV4(\$4A5)	INITIAL OVERLAY SIZE OF	LIREDT			ACV00600
EQU	PSIZV4(\$3C8)	INITIAL OVERLAY SIZE OF	PROTEC			ACV00700
EQU	PSIZV4(\$488)	INITIAL OVERLAY SIZE OF	PROTEC			ACV00800
EQU	ODRSIZ(\$210)	INITIAL OVERLAY SIZE OF	ODEBUG			ACV00900
EJT						ACV01000
						ACV01100
						ACV01200
						ACV01300

Figure 1-77. System Program Overlay Sizes, Section ACV

1.75 IOM Station Assignments, Section ACW

This section to be written later, pending definition by
C. Wollaston.

MISCELLANEOUS INFORMATION

I O M STATION ASSIGNMENTS

SPC 4
 ENT EG5761 STATUS ADDRESS CODE FOR 1576-1 STALL ALARM
 EQU ED(\$0400) BASIC DATA EQUIPMENT CODE FOR THE I O M
 EQU ES(\$0480) BASIC STATUS EQUIPMENT CODE FOR THE I O M

MODULE 0 (CIU) ASSIGNMENTS

EQU S01572() NC. OF STATIONS ASSIGNED TO THE 1572-1 TIMER
 EQU S01576() NC. OF STATIONS ASSIGNED TO THE 1576-1 STALL
 EQU S01525() NC. OF STATIONS ASSIGNED TO THE 1525 ANDC
 EQU S01536() NC. OF STATIONS ASSIGNED TO THE 1536 MUX
 EQU S05011() NC. OF STATIONS ASSIGNED TO THE 1501-10 MUX
 EQU S01595() NC. OF STATIONS ASSIGNED TO THE 1595 RS232
 EQU S01544() NC. OF STATIONS ASSIGNED TO THE 1544 DIG-IN
 EQU S01553() NC. OF STATIONS ASSIGNED TO THE 1553 DIG-OUT
 EQU S01555() NC. OF STATIONS ASSIGNED TO THE 1555 RLY-OUT

MODULE 1 (CIE 1) ASSIGNMENTS

EQU S11595() NC. OF STATIONS ASSIGNED TO THE 1595 RS232
 EQU S11544() NC. OF STATIONS ASSIGNED TO THE 1544 DIG-IN
 EQU S11553() NC. OF STATIONS ASSIGNED TO THE 1553 DIG-OUT
 EQU S11555() NC. OF STATIONS ASSIGNED TO THE 1555 RLY-OUT

MODULE 2 (CIE 2) ASSIGNMENTS

EQU S21595() NC. OF STATIONS ASSIGNED TO THE 1595 RS232
 EQU S21544() NC. OF STATIONS ASSIGNED TO THE 1544 DIG-IN
 EQU S21553() NC. OF STATIONS ASSIGNED TO THE 1553 DIG-OUT
 EQU S21555() NC. OF STATIONS ASSIGNED TO THE 1555 RLY-OUT
 EJT

I O M STATION ASSIGNMENTS

SPC 4

MODULE 3 (CIE 3) ASSIGNMENTS

EQU S31595() NC. OF STATIONS ASSIGNED TO THE 1595 RS232
 EQU S31544() NC. OF STATIONS ASSIGNED TO THE 1544 DIG-IN
 EQU S31553() NC. OF STATIONS ASSIGNED TO THE 1553 DIG-OUT
 EQU S31555() NC. OF STATIONS ASSIGNED TO THE 1555 RLY-OUT

MODULE 4 (CIE 4) ASSIGNMENTS

EQU S41595() NC. OF STATIONS ASSIGNED TO THE 1595 RS232
 EQU S41544() NC. OF STATIONS ASSIGNED TO THE 1544 DIG-IN
 EQU S41553() NC. OF STATIONS ASSIGNED TO THE 1553 DIG-OUT
 EQU S41555() NC. OF STATIONS ASSIGNED TO THE 1555 RLY-OUT

ACW00100
 ACW00200
 ACW00300
 ACW00400
 ACW00500
 ACW00600
 ACW00700
 ACW00800
 ACW00900
 ACW01000
 ACW01100
 ACW01200
 ACW01300
 ACW01400
 ACW01500
 ACW01600
 ACW01700
 ACW01800
 ACW01900
 ACW02000
 ACW02100
 ACW02200
 ACW02300
 ACW02400
 ACW02500
 ACW02600
 ACW02700
 ACW02800
 ACW02900
 ACW03000
 ACW03100
 ACW03200
 ACW03300
 ACW03400
 ACW03500
 ACW03600
 ACW03700
 ACW03800
 ACW03900
 ACW04000
 ACW04100
 ACW04200
 ACW04300
 ACW04400
 ACW04500
 ACW04600
 ACW04700
 ACW04800
 ACW04900
 ACW05000
 ACW05100
 ACW05200
 ACW05300

Figure 1-78. IOM Station Assignments, Section ACW

MODULE 5 (CIE 5) ASSIGNMENTS

EQL S51595()	NO. OF STATIONS ASSIGNED TO THE 1595 RS232	ACW05400
EQL S51544()	NO. OF STATIONS ASSIGNED TO THE 1544 DIG-IN	ACW05500
EQL S51553()	NO. OF STATIONS ASSIGNED TO THE 1553 DIG-CUT	ACW05600
EQL S51555()	NO. OF STATIONS ASSIGNED TO THE 1555 RLY-CUT	ACW05700
		ACW05800
		ACW05900
		ACW06000

MODULE 6 (CIE 6) ASSIGNMENTS

EQL S61595()	NO. OF STATIONS ASSIGNED TO THE 1595 RS232	ACW06100
EQL S61544()	NO. OF STATIONS ASSIGNED TO THE 1544 DIG-IN	ACW06200
EQL S61553()	NO. OF STATIONS ASSIGNED TO THE 1553 DIG-CUT	ACW06300
EQL S61555()	NO. OF STATIONS ASSIGNED TO THE 1555 RLY-CUT	ACW06400
		ACW06500
		ACW06600
		ACW06700

MODULE 7 (CIE 7) ASSIGNMENTS

EQL S71595()	NO. OF STATIONS ASSIGNED TO THE 1595 RS232	ACW06800
EQL S71544()	NO. OF STATIONS ASSIGNED TO THE 1544 DIG-IN	ACW06900
EQL S71553()	NO. OF STATIONS ASSIGNED TO THE 1553 DIG-CUT	ACW07000
EQL S71555()	NO. OF STATIONS ASSIGNED TO THE 1555 RLY-CUT	ACW07100
		ACW07200
		ACW07300
		ACW07400

EJT

I O M S T A T I O N A S S I G N M E N T S

SPC 4		ACW07500
EQL M0S0(0016-S01572)		ACW07600
EQL M0S1(M0S0-S01576)		ACW07700
EQL M0S2(M0S1-S01575)		ACW07800
		ACW07900
EQL M0S3(M0S2-S01536)		ACW08000
EQL M0S4(M0S3-S05011)		ACW08100
		ACW08200
EQL M0S5(M0S4-S01595)		ACW08300
EQL M0S6(M0S5-S01544)		ACW08400
EQL M0S7(M0S6-S01553)		ACW08500
EQL M0S8(M0S7-S01555)		ACW08600
EQL M1S0(0016-S11595)		ACW08700
EQL M1S1(M1S0-S11544)		ACW08800
EQL M1S2(M1S1-S11553)		ACW08900
EQL M1S3(M1S2-S11555)		ACW09000
		ACW09100
EQL M2S0(0016-S21595)		ACW09200
EQL M2S1(M2S0-S21544)		ACW09300
		ACW09400
EQL M2S2(M2S1-S21553)		ACW09500
EQL M2S3(M2S2-S21555)		ACW09600
		ACW09700
EQL M3S0(0016-S31595)		ACW09800
EQL M3S1(M3S0-S31544)		ACW09900
EQL M3S2(M3S1-S31553)		ACW10000
EQL M3S3(M3S2-S31555)		ACW10100
EQL M4S0(0016-S41595)		ACW10200
EQL M4S1(M4S0-S41544)		ACW10300
EQL M4S2(M4S1-S41553)		ACW10400
EQL M4S3(M4S2-S41555)		ACW10500
EQL M5S0(0016-S51595)		
EQL M5S1(M5S0-S51544)		

Figure 1-78. IOM Station Assignments, Section ACW (Continued)

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EQU	M5S2 (M5S1-S51553)		ACW10600
EQU	M5S3 (M5S2-S51555)		ACW10700
EQU	M6S0 (0016-S61595)		ACW10800
EQU	M6S1 (M6S0-S61544)		ACW10900
EQU	M6S2 (M6S1-S61553)		ACW11000
EQU	M6S3 (M6S2-S61555)		ACW11100
EQU	M7S0 (0016-S71595)		ACW11200
EQU	M7S1 (M7S0-S71544)		ACW11300
EQU	M7S2 (M7S1-S71553)		ACW11400
EQU	M7S3 (M7S2-S71555)		ACW11500
EJT			ACW11600
			ACW11700
			ACW11800
			ACW11900
SPC	4		ACW12000
			ACW12100
			ACW12200
			ACW12300
			ACW12400
			ACW12500
			ACW12600
			ACW12700
			ACW12800
			ACW12900
			ACW13000
			ACW13100
			ACW13200
			ACW13300
			ACW13400
			ACW13500
			ACW13600
			ACW13700
			ACW13800
			ACW13900
			ACW14000
			ACW14100
			ACW14200
			ACW14300
			ACW14400
			ACW14500
			ACW14600
			ACW14700
			ACW14800
			ACW14900
			ACW15000
			ACW15100
			ACW15200
			ACW15300
			ACW15400
			ACW15500
			ACW15600
			ACW15700
			ACW15800
			ACW15900

I O M ADDRESSING CODES

STATUS ADDRESSING CODES FOR PERTINENT IOM MODULES

SPC 4

EQU EQ5721 (M0S0+ES) 1572-1 LST/SRG TIMER

EQU EQ5761 (M0S1+ES) 1576-1 STALL ALARM

EQU EQ5253 (M0S2+ES) 1525-3 ANALCG/DIGITAL CONVERTER

EQU EQ5362 (M0S3+ES) 1536-2 RELAY MULTIPLEXOR

EQU EQ5011 (M0S4+ES) 1501-10 HIGH SPEED MULTIPLEXOR

BASE STATUS ADDRESS CODES FOR EACH RS232 MODULE

SPC 4

EQU EQ5950 (ES+\$00+M0S5)

EQU EQ5951 (ES+\$10+M1S0)

EQU EQ5952 (ES+\$20+M2S0)

EQU EQ5953 (ES+\$30+M3S0)

EQU EQ5954 (ES+\$40+M4S0)

EQU EQ5955 (ES+\$50+M5S0)

EQU EQ5956 (ES+\$60+M6S0)

EQU EQ5957 (ES+\$70+M7S0)

I O M DATA TABLES

DIGITAL INPUT BLOCK ASSIGNMENT TABLE

BLKT44

SPC 1

ENT RLKT44, LBKT44

SPC 2

EQU RLKT44 (*)

IFA S01544, NE, 0

ADC ED+\$00+M0S5-1

ADC S01544

EIF

IFA S11544, NE, 0

ADC ED+\$10+M1S0-1

ADC S11544

EIF

IFA S21544, NE, 0

ADC ED+\$20+M2S0-1

ADC S21544

EIF

IFA S31544, NE, 0

Figure 1-78. IOM Station Assignments, Section ACW {Continued}

ADC	ED+\$30+M3S0-1	ACW16000
ADC	S31544	ACW16100
EIF		ACW16200
IFA	S41544,AE,0	ACW16300
ADC	ED+\$40+M4S0-1	ACW16400
ADC	S41544	ACW16500
EIF		ACW16600
IFA	S51544,AE,0	ACW16700
ADC	ED+\$50+M5S0-1	ACW16800
ADC	S51544	ACW16900
EIF		ACW17000
IFA	S61544,AE,0	ACW17100
ADC	ED+\$60+M6S0-1	ACW17200
ADC	S61544	ACW17300
EIF		ACW17400
IFA	S71544,AE,0	ACW17500
ADC	ED+\$70+M7S0-1	ACW17600
ADC	S71544	ACW17700
EIF		ACW17800
SPC	2	ACW17900
EQU	L1544A(S01544+S11544+S21544+S31544)	ACW18000
EQU	L1544B(S41544+S51544+S61544+S71544)	ACW18100
EQU	LBKT44 (L1544A+L1544B)	ACW18200
EJT		ACW18300
* I O M DATA TABLES		ACW18400
* * * * *		ACW18500
SPC	4	ACW18600
DIGITAL OUTPUT BLOCK ASSIGNMENT TABLE		ACW18700
SPC	5	ACW18800
ENT	BLKT35,LRKT35	ACW18900
SPC	2	ACW19000
BLKT35 EQU	BLKT35(*)	ACW19100
IFA	S01553,AE,0	ACW19200
ADC	ED+\$00+M0S6-1	ACW19300
ADC	S01553	ACW19400
EIF		ACW19500
IFA	S01555,AE,0	ACW19600
ADC	ED+\$00+M0S7-1	ACW19700
ADC	S01555	ACW19800
EIF		ACW19900
IFA	S11553,AE,0	ACW20000
ADC	ED+\$10+M1S1-1	ACW20100
ADC	S11553	ACW20200
EIF		ACW20300
IFA	S11555,AE,0	ACW20400
ADC	ED+\$10+M1S2-1	ACW20500
ADC	S11555	ACW20600
EIF		ACW20700
IFA	S21553,AE,0	ACW20800
ADC	ED+\$20+M2S1-1	ACW20900
ADC	S21553	ACW21000
EIF		ACW21100
IFA	S21555,AE,0	ACW21200
ADC	ED+\$20+M2S2-1	ACW21300

Figure 1-78. IOM Station Assignments, Section ACW (Continued)

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ADC S21555
 EIF
 IFA S31553,AE,0
 ADC ED+S30+M3S1-1
 ADC S31553
 EIF
 IFA S31555,AE,0
 ADC ED+S30+M3S2-1
 ADC S31555
 EIF
 EJT

I O M DATA TABLES

SPC 2
 IFA S41553,AE,0
 ADC ED+S40+M4S1-1
 ADC S41553
 EIF
 IFA S41555,AE,0
 ADC ED+S40+M4S2-1
 ADC S41555
 EIF
 IFA S51553,AE,0
 ADC ED+S50+M5S1-1
 ADC S51553
 EIF
 IFA S51555,AE,0
 ADC ED+S50+M5S2-1
 ADC S51555
 EIF
 IFA S61553,AE,0
 ADC ED+S60+M6S1-1
 ADC S61553
 EIF
 IFA S61555,AE,0
 ADC ED+S60+M6S2-1
 ADC S61555
 EIF
 IFA S71553,AE,0
 ADC ED+S70+M7S1-1
 ADC S71553
 EIF
 IFA S71555,AE,0
 ADC ED+S70+M7S2-1
 ADC S71555

SPC 1
 DIGITAL OUTPUT HISTORY TABLE

HIST SPC 1
 ENT HIST
 SPC 2
 EQL HIST(*)
 BZS H1(S01553+S11553+S21553+S31553+S41553+S51553+S61553+S71553)
 BZS H2(S01555+S11555+S21555+S31555+S41555+S51555+S61555+S71555)
 SPC 1
 EQL LBKT35(*-HIST)
 EJT

ACW21400
 ACW21500
 ACW21600
 ACW21700
 ACW21800
 ACW21900
 ACW22000
 ACW22100
 ACW22200
 ACW22300
 ACW22400
 ACW22500
 ACW22600
 ACW22700
 ACW22800
 ACW22900
 ACW23000
 ACW23100
 ACW23200
 ACW23300
 ACW23400
 ACW23500
 ACW23600
 ACW23700
 ACW23800
 ACW23900
 ACW24000
 ACW24100
 ACW24200
 ACW24300
 ACW24400
 ACW24500
 ACW24600
 ACW24700
 ACW24800
 ACW24900
 ACW25000
 ACW25100
 ACW25200
 ACW25300
 ACW25400
 ACW25500
 ACW25600
 ACW25700
 ACW25800
 ACW25900
 ACW26000
 ACW26100
 ACW26200
 ACW26300
 ACW26400
 ACW26500
 ACW26600
 ACW26700
 ACW26800
 ACW26900
 ACW27000

Figure 1-78. IOM Station Assignments, Section ACW

1.7b In Core Flags Used by SCMM and ODEBUG, Section ACX

Lines *ACX0010 through *ACX0080, defining the SCMM in-core flag, are included only if SCMM is included in the system. Lines *ACX0090 through *ACX0160, defining the ODEBUG in-core flag, are included only if the on-line debug package is included in the system. {Refer to Figure 1-79.}

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* * *	M I S C E L L A N E O U S I N F O R M A T I O N	ACX00100
	S C M M I N C O R E F L A G	ACX00200
		ACX00300
		ACX00400
		ACX00500
		ACX00600
SCMMLC	ENT SCMMLC	ACX00700
	SPC 2	ACX00800
	NUM 0	ACX00900
	EJT 0 NON-ZERO IF S C M M R U N N I N G	ACX01000
* * *	M I S C E L L A N E O U S I N F O R M A T I O N	ACX01100
	O N L I N E D E B U G I N C O R E F L A G	ACX01200
		ACX01300
		ACX01400
		ACX01500
		ACX01600
CHRSFG	ENT CHRSFG	
	SPC 2	
	NUM 0	
	EJT 0 NON-ZERO IF ODEBEG R U N N I N G	

Figure 1-79. In Core Flags Used by SCMM and ODEBEG, Section ACX

1.77 COBOPS Starting Sector, Patches for Unneeded FORTRAN Entries,
Section ACY

This section, Figure 1-80, will contain the starting sector for the failed core image as used by the system checkout package, SYSCOP, if SYSCOP is in the system. In addition, lines *ACY0150 through *ACY0210 will be included in this section if reentrant FORTRAN is not in the system. These lines equate the entry points FMASK and FLIST with $7FFF_{16}$ so that they will not be listed as unpatched externals at the time of system installation. If reentrant FORTRAN is in the system, the entry points FLIST and FMASK will be defined in Section ACS, Reentrant FORTRAN information. In this case lines *ACY0210 through *ACY0280 are included in this section. In a system with reentrant FORTRAN, the scheduler and dispatcher module, NDISP, is not needed. The entry NDISP is equated to $7FFF_{16}$ so that it will not appear as an unpatched external at the time of system installation.

```

*           M I S C E L L A N E O U S   I N F O R M A T I O N
*
*           S Y S T E M   C H E C K O U T   P A R A M E T E R S
*
* SPC 4
* THE STARTING SECTOR OF THE FAILED CORE IMAGE IS SPECIFIED BY
* THE NAME COBOPS. THIS AREA MUST BE SIZED TO ACCOMODATE A
* FAILED IMAGE OF THE SIZE SPECIFIED BY NAME MSIZV4. THE FAILED
* IMAGE MUST RESIDE ON THE LIBRARY MASS MEMORY UNIT. IF THE
* MASS MEMORY LIBRARY UNIT IS A CARTRIDGE DISK, THE IMAGE AREA
* CANNOT OVERLAP FROM ONE FLATTER TO THE OTHER.
* SPC 2
* ENT COBOPS
COBOPS EQU COBOPS( )          START SECTOR OF FAILED IMAGE
* SPC 4
* THIS ENTRY IS PROVIDED TO LINK THE FORTRAN REENTRANCY DATA
* ENTRY POINTS
* SPC 1
* ENT FMASK,FLIST
* SPC 1
* EQU FMASK($7FFF),FLIST($7FFF)
* SPC 4
* THIS ENTRY IS PROVIDED TO LINK THE NO-FORTRAN DISPATCHER
* ENTRY POINT
* SPC 1
* ENT NDTSP
* SPC 1
* EQU NDISP($7FFF)
* ENT

```

ACV00100
ACV00200
ACV00300
ACV00400
ACV00500
ACV00600
ACV00700
ACV00800
ACV00900
ACV01000
ACV01100
ACV01200
ACV01300
ACV01400
ACV01500
ACV01600
ACV01700
ACV01800
ACV01900
ACV02000
ACV02100
ACV02200
ACV02300
ACV02400
ACV02500
ACV02600
ACV02700
ACV02800

Figure 1-8D. SYSCOP Parameters, Section ACY

1.78 High Speed IMPORT Information, Section ACZ

Section ACZ is included only if High Speed IMPORT is in the system. In that case, the entries in this section are as shown in Figure 1-81. Word 1, UNDEF, is the address of the physical device table of the dummy logical unit. This address is used by IMPORT as the address inserted in the LOG1A table for each device that will be used for an IMPORT data stream. The dummy logical unit is so referenced in the LOG1A table until the corresponding physical device table has been set up for IMPORT.

Word 2 of this section, the priority level of IMPORT, is fixed at level 6. Word 3 must be the priority level at which D1747 will run. Word 4, transmission buffer size, is 325 in a released system. It may be reduced to 165 to save core if desired. One of the two values, 165 or 325, must be used as this entry. Words 5 and 6 should be zero for Hollerith/ASCII {026} card conversion, non-zero for EBCDIC/ASCII {029} card conversion. Words 7, 8, and 9 are fixed. The values of words ten and eleven must be negative. The magnitude of each of these entries is in counts of the system timing device. The magnitude of each may be increased if transmission is to occur over a longer than standard distance. The number of counts allows time for the 1700/6000 connecting lines to perform the transmission and to change from receive mode to send mode or vice versa. The last word of this section, JOBMAX, is the maximum number of jobs allowed to be transmitted from this 1700 to the 6000 at one time. This number may be adjusted depending on the number of 1700's connected to

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the 6000 and on the desires of the user.

For further information, refer to the 1700 MSOS IMPORT
High Speed Reference Manual, 1700 Version 1, Publica-
tion No. 60305700.

* * *	M I S C E L L A N E O U S	I N F O R M A T I O N		ACZ00100	
	H I G H	S P E E D	I M P O R T	I N F O R M A T I O N	ACZ00200
	SPC	4		ACZ00300	
	ENT	IMPSPD,IMPTE,IMPCT,IMSTR1,IMLAST		ACZ00400	
	ENT	IPRI,DSCPRI,DSCLU,IMCCM,IMRFSZ		ACZ00500	
	ENT	IMTID,UNDEF,P2629,P2629		ACZ00600	
	ENT	SYNCLI,RETLIM,JOBRMAX		ACZ00700	
	SPC	4		ACZ00800	
				ACZ00900	
UNDEF	ADC	PDUMMY	01 UNDEFINED UNIT	ACZ01000	
IPRI	ADC	6	02 PRIORITY LEVEL OF IMPORT	ACZ01100	
DSCPRI	ADC	10	03 PRIORITY LEVEL OF DATA SET DRIVER (D1747)	ACZ01200	
IMBFSZ	ADC	325	04 SIZE OF TRANSMISSION BUFFER	ACZ01300	
P2629	ADC	0	05 026/029 CARD OUTPLT FORMAT (0 = 026)	ACZ01400	
R2629	ADC	0	06 026/029 CARD INPUT FORMAT (0 = 026)	ACZ01500	
IMCOM	ADC	4	07 IMPORT COMMENT DEVICE LOGICAL UNIT	ACZ01600	
DSCLU	ADC	LU1747-LOG1A	08 DATA SET CONTROLLER LOGICAL UNIT	ACZ01700	
IMTID	ALF	1,ME	09 DEFAULT IMPORT TERMINAL I.D.	ACZ01800	
SYNCLI	NUM	-10	10 MAXIMUM ALLOWABLE SYNCHRONIZATION ERROR	ACZ01900	
RETLIM	NUM	-10	11 MAXIMUM ALLOWABLE RE-TRANSMISSION ERROR	ACZ02000	
JOBRMAX	NUM	25	12 MAXIMUM ALLOWABLE STACKED JOBS	ACZ02100	
EJT				ACZ02200	

Figure 1-81. High Speed IMPORT Information, Section ACZ

1.79 File Manager Data, Section ADA

Lines *ADAD050 through *ADAD110 are included in this section only if there is no File Manager in the system. {Refer to Figure 1-82.} These lines equate certain entries used by the File Manager with 7FFF₁₆ so that these entries will not be listed as unpatched externals at the time of system installation.

If there is a File Manager in the system, the first line in Section ADA after the heading is line *ADAD120. Beginning at this line are the system parameter values needed by the File Manager. FISLU is the library unit in a standard system. It may be changed to a different unit but in that case the system must be rebuilt so that all references to FISLU will be correctly linked. MAXMMA, the number of mass memory read/write attempts made when a mass memory error is detected, is one in a standard released system. A value of one means that once a mass memory error is detected the mass memory read or write request will be repeated once by the File Manager. The user should leave this value at one.

The parameter RPTPER is the time in hundredths of a second that a given request processor will remain in core after completion of a request. If there is enough space in allocatable core for several File Manager request processors to be in core simultaneously, then RPTPER may be increased to a value greater than the standard value of 10 which corresponds to a tenth of a second. Before a request processor releases its core it may be requested again. In that case, it would not have to be re-read from mass memory, thus saving system overhead time. If a minimum amount of allocatable core is available for File Manager request processors, then RPTPER should be set to the minimum value of one so that if one File Manager request processor is in core and a different request processor is requested, the

second request will not have to wait unnecessarily during the timeout before release of core by the first processor. The value of RPTPER cannot be set to zero since the timeout value for a request processor is initialized to RPTPER and the timeout value is decremented by one before being tested for zero. {Refer to Article 1.5b for a discussion of allocatable core area available for File Manager request processors.}

The parameter FDTPER is defined similarly to RPTPER except that it refers to the timeout period before core is released for a file information segment {FIS}. It is also the timeout period before core is released for the FIS Directory. Criteria similar to those discussed in conjunction with the parameter RPTPER should be used if the user wishes to change the value of FDTPER.

The parameters FIDSEC, FIBLSA, and FIBNIX are used dynamically by the File Manager. They must be zero initially. When the first define file request is encountered by the File Manager, non-zero values are defined for each of these parameters.

The parameter ADRFMS is the starting sector of the File Manager space on the FISLU logical unit. Its value is dependent on the particular device which is used as the library unit. The external BEGFMS, which is linked by a *S statement at the time of system installation, defines ADRFMS if one of the following is used as the library unit:

1738-853/854 Disk

1733-853/854 Disk

1751 Drum

1752 Drum

ADRFMS is defined as $5BF7_{16}$ if the library unit is one of the

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following devices:

1733-856 Cartridge Disk with 4.4 million words.

1739-1 Cartridge Disk

ADRFMS is defined to be 2DF9₁₆ if the library unit is a 1733-856 Disk with 2.2 million words. The definition of ADRFMS for the latter three services places the file space on the fixed platter disk for each of these devices. This has the advantage of minimizing seek time during system operation, but this positioning of file space is not required.

FSLIST is the location of the start of the file space parameters for each File Manager mass memory unit in the system. Up to nine File Manager units may be included in a system. These are numbered beginning with File Manager unit zero which is generally the system library unit. For each File Manager unit in the system a subsection of Section ADA is included to define the necessary parameters. For File Manager unit n , $0 \leq n \leq 8$, the following parameter values are given:

1. NUMFS n - Total number of sectors available for file space on this unit. This is the value given on the MSOS ordering form. If the user wishes to add a File Manager to an existing system, the method of determining NUMFS n is described in Article 5.1.
2. LUE n - Bits 7 - 15 of LUE n give the number of words in the subsection for this File Manager unit. (This is a variable since the length of the file space list can vary as described under item 4.) Bits 0 - 6 of LUE n contain the system logical unit number corresponding to this File Manager unit.
3. The starting os sector of space available on this logical unit for File Manager use. For File Manager unit zero, this value is given by the parameter ADRFMS. For $n \neq 0$, the starting sector is the value of the parameter BEGLUn.

4. Definition of the file space list for this File Manager unit. At any time after system operation has begun all available file space for this File Manager unit is contained either in the file space list or the file space pool. The file space list is composed of one or more blocks of available space. Each block is a threaded sequence of available segments of mass memory such that each segment has the same length in sectors as every other segment in the block. The segment lengths to be included in the file space list for this File Manager unit are given by the subsection entries following:

ADC NUMFSn

In a standard released system there are three segment lengths included in the file space list for each File Manager unit. These lengths are one sector, two sectors, and three sectors. The lengths are defined by the following thread definitions:

NUM	0.1	THREAD OF ONE SECTOR LONG
NUM	0.2	THREAD OF TWO SECTORS LONG
NUM	0.3	THREAD OF THREE SECTORS LONG

The first word of each thread is the sector of the first segment in this block. The first word of each thread must be zero initially. All available file space on this File Manager unit which lies in a segment of a length other than those included in the file space list is included in the file space pool for this File Manager unit. The user may redefine the segment lengths in the file space list depending on the different file record lengths he intends to use in the system. The advantage of keeping as much of the available file space as possible in the file space list is that available space there can be allocated much more quickly

than can space in the file space pool. The disadvantage in having too many blocks in a file space list is that two words of core are used for each block. Consider the following example. The user determines that the file space list for File Manager unit 0 is to be composed of:

- 1} a block of segments one sector long
- 2} a block of segments two sectors long
- 3} a block of segments four sectors long
- 4} a block of segments six sectors long

In this case, the initial file space list threads for File Manager unit 0 would be as follows:

NUM	0,1	THREAD OF ONE SECTOR LONG
NUM	0,2	THREAD OF TWO SECTORS LONG
NUM	0,4	THREAD OF FOUR SECTORS LONG
NUM	0,6	THREAD OF SIX SECTORS LONG

5. The number of available file sectors. Initially this must be zero for each File Manager unit.

Following the set of subsections for all File Manager units in the system is a set of equivalences for entry points corresponding to unused File Manager units. These equivalences avoid having these entry points listed as unpatched externals at the time of system installation.

M I S C E L L A N E O U S I N F O R M A T I O N

F I L E M A N A G E R D A T A

THESE ENTRIES ALLOW PROPER SYSTEM LINKAGE FOR IF THE FILE MANAGER IS NOT SELECTED.

SPC	2		ADA00100
ENT	FSLIST		ADA00200
ENT	ADRFMS,NUMFS0		ADA00300
EQU	FSLIST(\$7FFF)		ADA00400
EQU	ADRFMS(\$7FFF),NUMFS0(\$7FFF)		ADA00500
SPC	2		ADA00600
ENT	FISLU	LOGICAL UNIT OF FIS DIRECTORY AND BLOCKS	ADA00700
ENT	MAXMMA	MAXIMUM NO. OF MASS MEMORY ATTEMPTS ON ERROR	ADA00800
ENT	RPTPER	REQUEST PROCESSOR TIMEOUT PERIOD	ADA00900
ENT	FDTPER	FILE/DIRECTORY TIMEOUT PERIOD	ADA01000
ENT	FIDSEC	FIS DIRECTORY, S SECTOR ADDRESS	ADA01100
ENT	FIBLSA	SECTOR ADDRESS OF LAST FIS BLOCK	ADA01200
ENT	FIBNIX	INDEX TO THE NEXT AVAILABLE LOCATION IN FIBLSA	ADA01300
ENT	FSLIST	START OF FILE SPACE LIST	ADA01400
ENT	FSLLEN	FILE SPACE LIST LENGTH	ADA01500
ENT	FSLLEN	END OF FILE SPACE LIST	ADA01600
ENT	ADRFMS	BEGINNING OF FILE MANAGER SPACE ON LIB UNIT	ADA01700
SPC	2		ADA01800
EQU	FISLU(LRUNIT)	LOGICAL UNIT OF FIS DIRECTORY AND BLOCKS	ADA01900
EQU	MAXMMA(1)	MAXIMUM NO. OF MASS MEMORY ATTEMPTS ON ERROR	ADA02000
EQU	FDTPER(10)	FILE/DIRECTORY TIMEOUT PERIOD (1/10 SEC.)	ADA02100
EQU	RPTPER(10)	REQUEST PROCESSOR TIMEOUT PERIOD (1/10 SEC.)	ADA02200
SPC	2		ADA02300
+14,6,			ADA02400
+15,18,			ADA02500
+17,3,			ADA02600
+18,3,			ADA02700
EXT	REGFMS		ADA02800
ADRFMS	ADC	REGFMS	ADA02900
+13,10,			ADA03000
+16,3,			ADA03100
ADRFMS	ADC	\$5RF7	ADA03200
ADRFMS	ADC	\$2DF9	ADA03300
SPC	1		ADA03400
*****		THE FOLLOWING MUST BE IN ORDER	ADA03500
FIDSEC	ADC	0	ADA03600
FIBLSA	ADC	0	ADA03700
FIBNIX	ADC	0	ADA03800
FSLIST	EQU	FSLIST(*)	ADA03900
SPC	1		ADA04000
*****		START OF LOGICAL UNIT ENTRIES	ADA04100
SPC	2		ADA04200
			ADA04300
			ADA04400
			ADA04500
			ADA04600
			ADA04700
			ADA04800

Figure 1-82. File Manager Data, Section ADA

LOGICAL UNIT DATA, UNIT 0

ENT	NUMFS0			ADA04900
EQU	NUMFS0()	NUMBER OF FILE SECTORS - UNIT 0		ADA05000
SPC	1			ADA05100
LUE0	VFD	X9/LUEL0,X7/LBUNIT	LU ENTRY LENGTH(7/15), LOGICAL UNIT(0-6)	ADA05200
	ADC	0	ADDRESS OF FILE SPACE POOL	ADA05300
	ADC	0	NUMBER OF AVAILABLE SECTORS	ADA05400
	ADC	NUMFS0	NUMBER OF SECTORS IN THIS FILE SPACE	ADA05500
	NUM	0.1	THREAD OF ONE SECTOR LONG	ADA05600
	NUM	0.2	THREAD OF TWO SECTORS LONG	ADA05700
	NUM	0.3	THREAD OF THREE SECTORS LONG	ADA05800
LUEL0	EQU	LUEL0(*-LUE0)		ADA06000
	EJT			ADA06100

LOGICAL UNIT DATA, UNIT 1

ENT	BEGLU1			ADA06200
ENT	NUMFS1			ADA06300
EQU	LUNIT1()	LOGICAL UNIT OF FILE MANAGER UNIT 1		ADA06400
EQU	BEGLU1()	BEGINNING FILE SECTOR - UNIT 1		ADA06500
EQU	NUMFS1()	NUMBER OF FILE SECTORS - UNIT 1		ADA06600
SPC	1			ADA06700
LUE1	VFD	X9/LUEL1,X7/LUNIT1	LU ENTRY LENGTH(7/15), LOGICAL UNIT(0-6)	ADA06800
	ADC	BEGLU1	ADDRESS OF FILE SPACE POOL	ADA06900
	ADC	0	NUMBER OF AVAILABLE SECTORS	ADA07000
	ADC	NUMFS1	NUMBER OF SECTORS IN THIS FILE SPACE	ADA07100
	NUM	0.1	THREAD OF ONE SECTOR LONG	ADA07200
	NUM	0.2	THREAD OF TWO SECTORS LONG	ADA07300
	NUM	0.3	THREAD OF THREE SECTORS LONG	ADA07400
LUEL1	EQU	LUEL1(*-LUE1)		ADA07500
	EJT			ADA07600

LOGICAL UNIT DATA, UNIT 2

ENT	BEGLU2			ADA07700
ENT	NUMFS2			ADA07800
EQU	LUNIT2()	LOGICAL UNIT OF FILE MANAGER UNIT 2		ADA07900
EQU	BEGLU2()	BEGINNING FILE SECTOR - UNIT 2		ADA08000
EQU	NUMFS2()	NUMBER OF FILE SECTORS - UNIT 2		ADA08100
SPC	1			ADA08200
LUE2	VFD	X9/LUEL2,X7/LUNIT2	LU ENTRY LENGTH(7/15), LOGICAL UNIT(0-6)	ADA08300
	ADC	BEGLU2	ADDRESS OF FILE SPACE POOL	ADA08400
	ADC	0	NUMBER OF AVAILABLE SECTORS	ADA08500
	ADC	NUMFS2	NUMBER OF SECTORS IN THIS FILE SPACE	ADA08600
	NUM	0.1	THREAD OF ONE SECTOR LONG	ADA08700
	NUM	0.2	THREAD OF TWO SECTORS LONG	ADA08800
	NUM	0.3	THREAD OF THREE SECTORS LONG	ADA08900
LUEL2	EQU	LUEL2(*-LUE2)		ADA09000
	EJT			ADA09100

Figure 1-82. File Manager Data, Section ADA (Continued)

		LOGICAL UNIT DATA, UNIT 3		
	ENT	REGLU3		ADA09700
	ENT	NUMFS3		ADA09800
	EQL	LUNIT3()	LOGICAL UNIT OF FILE MANAGER UNIT 3	ADA09900
	EQL	REGLU3()	BEGINNING FILE SECTOR - UNIT 3	ADA10000
	EQL	NUMFS3()	NUMBER OF FILE SECTORS - UNIT 3	ADA10100
	SPC	1		ADA10200
LUE3	VFC	X9/LUEL3,X7/LUNIT3	LU ENTRY LENGTH(7/15), LOGICAL UNIT(0-6)	ADA10300
	ADC	REGLU3	ADDRESS OF FILE SPACE POOL	ADA10400
	ADC	0	NUMBER OF AVAILABLE SECTORS	ADA10500
	ADC	NUMFS3	NUMBER OF SECTORS IN THIS FILE SPACE	ADA10600
	NUM	0.1	THREAD OF ONE SECTOR LONG	ADA10700
	NUM	0.2	THREAD OF TWO SECTORS LONG	ADA10800
	NUM	0.3	THREAD OF THREE SECTORS LONG	ADA10900
LUEL3	EQL	LUFL3(*-LUE3)		ADA11000
	EJT			ADA11100
				ADA11200
				ADA11300
				ADA11400
				ADA11500
				ADA11600
				ADA11700
				ADA11800
				ADA11900
				ADA12000
				ADA12100
LUE4	VFC	X9/LUEL4,X7/LUNIT4	LU ENTRY LENGTH(7/15), LOGICAL UNIT(0-6)	ADA12200
	ADC	REGLU4	ADDRESS OF FILE SPACE POOL	ADA12300
	ADC	0	NUMBER OF AVAILABLE SECTORS	ADA12400
	ADC	NUMFS4	NUMBER OF SECTORS IN THIS FILE SPACE	ADA12500
	NUM	0.1	THREAD OF ONE SECTOR LONG	ADA12600
	NUM	0.2	THREAD OF TWO SECTORS LONG	ADA12700
	NUM	0.3	THREAD OF THREE SECTORS LONG	ADA12800
LUEL4	EQL	LUFL4(*-LUE4)		ADA12900
	EJT			ADA13000
				ADA13100
				ADA13200
				ADA13300
				ADA13400
				ADA13500
				ADA13600
				ADA13700
				ADA13800
LUE5	VFC	X9/LUEL5,X7/LUNIT5	LU ENTRY LENGTH(7/15), LOGICAL UNIT(0-6)	ADA13900
	ADC	REGLU5	ADDRESS OF FILE SPACE POOL	ADA14000
	ADC	0	NUMBER OF AVAILABLE SECTORS	ADA14100
	ADC	NUMFS5	NUMBER OF SECTORS IN THIS FILE SPACE	ADA14200
	NUM	0.1	THREAD OF ONE SECTOR LONG	ADA14300
	NUM	0.2	THREAD OF TWO SECTORS LONG	ADA14400
	NUM	0.3	THREAD OF THREE SECTORS LONG	ADA14500
LUEL5	EQL	LUFL5(*-LUE5)		ADA14600
	EJT			ADA14700

Figure 1-82. File Manager Data, Section ADA {Continued}

LOGICAL UNIT DATA, UNIT 6

```

ENT BEGLU6
ENT NUMFS6
EQU LUNIT6()          LOGICAL UNIT OF FILE MANAGER UNIT 6
EQU BEGLU6()          BEGINNING FILE SECTOR = UNIT 6
EQU NUMFS6()          NUMBER OF FILE SECTORS = UNIT 6
SPC 1
LUE6 VFC X9/LUEL6,X7/LUNIT6 LU ENTRY LENGTH(7/15), LOGICAL UNIT(0-6)
ADC BEGLU6            ADDRESS OF FILE SPACE POOL
ADC 0                NUMBER OF AVAILABLE SECTORS
ADC NUMFS6           NUMBER OF SECTORS IN THIS FILE SPACE
NUM 0.1              THREAD OF ONE SECTOR LONG
NUM 0.2              THREAD OF TWO SECTORS LONG
NUM 0.3              THREAD OF THREE SECTORS LONG
LUEL6 EQU LUEL6(*-LUE6)
EJT

```

LOGICAL UNIT DATA, UNIT 7

```

ENT BEGLU7
ENT NUMFS7
EQU LUNIT7()          LOGICAL UNIT OF FILE MANAGER UNIT 7
EQU BEGLU7()          BEGINNING FILE SECTOR = UNIT 7
EQU NUMFS7()          NUMBER OF FILE SECTORS = UNIT 7
SPC 1
LUE7 VFC X9/LUEL7,X7/LUNIT7 LU ENTRY LENGTH(7/15), LOGICAL UNIT(0-6)
ADC BEGLU7            ADDRESS OF FILE SPACE POOL
ADC 0                NUMBER OF AVAILABLE SECTORS
ADC NUMFS7           NUMBER OF SECTORS IN THIS FILE SPACE
NUM 0.1              THREAD OF ONE SECTOR LONG
NUM 0.2              THREAD ON TWO SECTORS LONG
NUM 0.3              THREAD OF THREE SECTORS LONG
LUEL7 EQU LUEL7(*-LUE7)
EJT

```

LOGICAL UNIT DATA, UNIT 8

```

ENT BEGLU8
ENT NUMFS8
EQU LUNIT8()          LOGICAL UNIT OF FILE MANAGER UNIT 8
EQU BEGLU8()          BEGINNING FILE SECTOR = UNIT 8
EQU NUMFS8()          NUMBER OF FILE SECTORS = UNIT 8
SPC 1
LUE8 VFC X9/LUEL8,X7/LUNIT8 LU ENTRY LENGTH(7/15), LOGICAL UNIT(0-6)
ADC BEGLU8            ADDRESS OF FILE SPACE POOL
ADC 0                NUMBER OF AVAILABLE SECTORS
ADC NUMFS8           NUMBER OF SECTORS IN THIS FILE SPACE

```

Figure 1-82. File Manager Data, Section ADA (Continued)

	NUM	0,1	THREAD OF ONE SECTOR LONG	ADA19400
	NUM	0,2	THREAD ON TWO SECTORS LONG	ADA19500
	NUM	0,3	THREAD OF THREE SECTORS LONG	ADA19600
LUEL8	EQU	LUEL8(*-LUE8)		ADA19700
	SPC	2		ADA19800
FSLLEH	EQU	FSLLEH(*-FSLIST)	FILE SPACE LIST LENGTH	ADA19900
	SPC	1		ADA20000
FSLEND	NUM	-0	END OF FILE SPACE LIST	ADA20100
	EJT			ADA20200
*		FILE MANAGER DATA		ADA20300
*				ADA20400
	SPC	2		ADA20500
*		LINK UNSELECTED ENTRY POINTS		ADA20600
	SPC	2		ADA20700
	ENT	REGLU1,NUMFS1		ADA20800
	EQU	BEGLU1(\$7FFF),NLMFS1(\$7FFF)		ADA20900
	ENT	REGLU2,NUMFS2		ADA21000
	EQU	BEGLU2(\$7FFF),NLMFS2(\$7FFF)		ADA21100
	ENT	REGLU3,NUMFS3		ADA21200
	EQU	BEGLU3(\$7FFF),NLMFS3(\$7FFF)		ADA21300
	ENT	REGLU4,NUMFS4		ADA21400
	EQU	BEGLU4(\$7FFF),NLMFS4(\$7FFF)		ADA21500
	ENT	REGLU5,NUMFS5		ADA21600
	EQU	BEGLU5(\$7FFF),NLMFS5(\$7FFF)		ADA21700
	ENT	REGLU6,NUMFS6		ADA21800
	EQU	BEGLU6(\$7FFF),NLMFS6(\$7FFF)		ADA21900
	ENT	REGLU7,NUMFS7		ADA22000
	EQU	BEGLU7(\$7FFF),NLMFS7(\$7FFF)		ADA22100
	ENT	REGLU8,NUMFS8		ADA22200
	EQU	BEGLU8(\$7FFF),NLMFS8(\$7FFF)		ADA22300
	EJT			ADA22400

Figure 1-82. File Manager Data, Section ADA {Continued}

1.80 Job Processor File Parameters, Section ADB

Section ADB, Figure 1-83, gives the file parameter values used by the Job Processor. The parameter JLLUV4 is the logical unit used for storing Job Processor files. In a standard released system this is the library unit, but this may be changed if the user desires.

In order to have any Job Processor files, the system must include the pseudo tape driver. The pseudo tape capability is in turn dependent on a File Manager being in the system. If there are no pseudo tapes in the system, lines *ADB0140 through *ADB0190 will appear in SYSDAT to avoid listing JBFLV4, FBASV4 and PKEYV4 as unpatched externals at the time of system installation. In addition, if there is no File Manager in the system, lines *ADB0100 and *ADB0200 will be included in SYSDAT so that RELFIL will not be listed as an unpatched external.

If pseudo tape capability is included in the system lines *ADB0200 through *ADB0290 will appear in SYSDAT. In these lines, the values of JBFLV4, FBASV4, and PKEYV4 are defined.

The number of Job Processor files, JBFLV4, is the number specified by the user in the 1700 MS0S Ordering Form. This may be changed. The value of JBFLV4 must be such that $0 \leq \text{JBFLV4} \leq 1000$. File numbers $7\text{FF}8_{16}$ through 7FFF_{16} are reserved for foreground pseudo tapes.

The parameter RKEYV4 is the Job File purge key. PKEYV4 contains the ASCII code for the two characters used with the Job Processor *PURGE request. The value of PKEYV4 may be changed by the user.

* * * *	M I S C E L L A N E O U S I N F O R M A T I O N	ADR00100
	J O B P R O C E S S O R F I L E P A R A M E T E R S	ADR00200
		ADR00300
		ADR00400
	SPC 3	ADR00500
	ENT JLLUV4 LOGICAL UNIT OF JOB PROCESSOR FILES	ADR00600
	ENT JRFLV4 NUMBER OF JOB PROCESSOR FILES	ADR00700
	ENT FBASV4 FIRST FILE NUMBER USED BY JOB PROCESSOR	ADR00800
	ENT PKEYV4 JOB FILE PURGE KEY	ADR00900
	ENT RELFIL FILE RELEASE PROCESSOR (DUMMY)	ADR01000
	SPC 3	ADR01100
JLLUV4	ADC LBUNIT LOGICAL UNIT OF JOB PROCESSOR FILES	ADR01200
	SPC 2	ADR01300
	EQL JRFLV4(0) NUMBER OF JOB PROCESSOR FILES	ADR01400
* * *	L I N K E N T R Y P O I N T S R E F E R E N C E D B Y T H E J O B F I L E P R O C E S S O R S	ADR01500
	EQL FBASV4(\$7FFF)	ADR01600
	EQL PKEYV4(\$7FFF)	ADR01700
	EQL RELFIL(\$7FFF)	ADR01800
	EQL JRFLV4() NUMBER OF JOB PROCESSOR FILES	ADR01900
	SPC 1	ADR02000
	EQL FRASV4(\$7FFF-JRFLV4)	ADR02100
* * *	N O T E - F I L E S \$ 7 F F 8 T H R U \$ 7 F F F A R E R E S E R V E D F O R F O R E G R O U N D	ADR02200
	P S E U D O T A P E S .	ADR02300
	SPC 2	ADR02400
	EQL PKEYV4(\$3030) JOB FILE PURGE KEY	ADR02500
	EUT	ADR02600
		ADR02700
		ADR02800
		ADR02900

Figure 1-83. Job Processor File Parameters, Section ADB

1.81 Preset Protected Entry Points for Use by Unpatched Programs, Section ADC

This section, as shown in Figure 1-84, will contain the address of each protected entry point which may be accessed by an unprotected program. When an unprotected program attempts to execute a return jump to a protected location, an interrupt occurs on line 0. The protect processor determines that the interrupt was due to a program protect fault. It then searches the addresses listed in Section ADC to see if the protected address is in the table of protected entry points which may be used by unprotected programs. If not, the unprotected program is terminated and an error message is printed. If the address is one which may be accessed by an unprotected program, control is transferred to the dispatcher. In this case, when the interrupted unprotected program is again put into operation, the return jump to the protected location is executed.

For all systems the Job Processor entry, JPRETN, is accessible from unprotected programs. If the debugging checkout package is in the system lines *ADC0160 through *ADC0200 are included so that SNAP0L, the snap dump entry, will be accessible from the background. If the File Manager is in the system, lines *ADC0210 through *ADC0710 are included so that the File Manager may be accessed by unprotected programs.

Section ADC in a given SYSDAT contains the ASCII code for each entry point to be included. During system installation this list is used to determine corresponding entry point locations.

APRSET is the start of Section ADC and LPRSET is the length of this section. These values are referenced in the communications region, Section AAE, as discussed in Article 1.5.

Care must be used if the user wishes to add an entry point reference to this section. A protected program which may be entered from unprotected core should inhibit interrupts and save the return address and other necessary information in volatile storage before enabling interrupts to prevent erroneous entries. Before returning to an unprotected program, the protected program should check to see if the job has been cancelled. If the job has been terminated, exit should be made to the dispatcher. In addition, the protected program must check parameters passed from unprotected programs to prevent storing into protected core. Maximum system protection is provided when the list of entry points referenced in Section ADC is kept to a minimum.

P R E S E T R E G I O N			
			ADC00100
			ADC00200
			ADC00300
			ADC00400
APRSET	EQU APRSET(*)		ADC00500
	ENT JPRET		ADC00600
	SPC 2		
		J O B P R O C E S S O R P R E S E T	ADC00700
	SPC 2		ADC00800
	EXT JPRETN		ADC00900
	ALF 3,JPRETN		ADC01000
JPRET	ADC JPRETN	J O B P R O C E S S O R R E T U R N	ADC01100
			ADC01200
	SPC 2		ADC01300
		S N A P D U M P P R E S E T	ADC01400
	SPC 2		ADC01500
	EXT SNAPOL		ADC01600
	ALF 3,SNAPOL		ADC01700
	ADC SNAPOL	R E G I S T E R S N A P S H O T	ADC01800
			ADC01900
	SPC 2		ADC02000
		F I L E M A N A G E R P R E S E T S	ADC02100
	SPC 2		ADC02200
	EXT DEFFIL		ADC02300
	ALF 3,DEFFIL		ADC02400
	ADC DEFFIL	D E F I N E F I L E	ADC02500
			ADC02600
	EXT RELFIL		ADC02700
	ALF 3,RELFIL		ADC02800
	ADC RELFIL	R E L E A S E F I L E	ADC02900
			ADC03000
	EXT DEFIDX		ADC03100
	ALF 3,DEFIDX		ADC03200
	ADC DEFIDX	D E F I N E I N D E X E D F I L E	ADC03300
			ADC03400
	EXT LOKFIL		ADC03500
			ADC03600

Figure 1-84. Preset Protected Entry Points for Use by Unprotected Programs, Section ADC

ALF	3,LOKFIL			ADC03700
ADC	LOKFIL	LOCK FILE		ADC03800
EXT	UNLFIL			ADC03900
ALF	3,UNLFIL			ADC04000
ADC	UNLFIL	UNLOCK FILE		ADC04100
EXT	STOSEQ			ADC04200
ALF	3,STOSEQ			ADC04300
ADC	STOSEQ	STORE SEQUENTIAL RECORD		ADC04400
EXT	STODIR			ADC04500
ALF	3,STODIR			ADC04600
ADC	STODIR	STORE DIRECT		ADC04700
EXT	STOIDX			ADC04800
ALF	3,STOIDX			ADC04900
ADC	STOIDX	STORE INDEXED RECORD		ADC05000
EXT	RTVSEQ			ADC05100
ALF	3,RTVSEQ			ADC05200
ADC	RTVSEQ	RETRIEVE SEQUENTIAL RECORD		ADC05300
EXT	RTVDIR			ADC05400
ALF	3,RTVDIR			ADC05500
ADC	RTVDIR	RETRIEVE DIRECT		ADC05600
EXT	RTVIDX			ADC05700
ALF	3,RTVIDX			ADC05800
ADC	RTVIDX	RETRIEVE INDEXED RECORD		ADC05900
EXT	RTVIDC			ADC06000
ALF	3,RTVIDC			ADC06100
ADC	RTVIDC	RETRIEVE INDEXED-ORDERED RECORD		ADC06200
EJT				ADC06300
				ADC06400
				ADC06500
				ADC06600
				ADC06700
				ADC06800
				ADC06900
				ADC07000
				ADC07100

Figure 1-84. Preset Protected Entry Points for Use by Unprotected Programs, Section ADC (Continued)

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1.82 System Library Directory, Section ADD

This section defines the start of the system library directory. The directory will follow SYSDAT. It is set up by the System Initializer Program. {Refer to Figure 1-85.}

307

```
*          S Y S T E M   L I B R A R Y   D I R E C T O R Y          ADD00100
*
*          C O M P I L E D   F R O M   * Y .   * Y M   B Y   S Y S T E M   I N I T I A L I Z E R   ADD00200
*          S L D I R Y   E Q L   S L D I R Y ( * )          C O M P I L E D   F R O M   * Y .   * Y M   B Y   S Y S T E M   I N I T I A L I Z E R   ADD00300
*          E N D          ADD00400
MON          ADD00500
1202   R E C O R D S   T R A N S F E R R E D          ADD00600
```

Figure 1-85. System Library Directory, Section ADD



2.1 Binaries Included in the Install File

Included in the installation materials sent to a user is a binary copy of every 1700 MSOS 4.1 module. A number of these modules may not be included in the user's installed operating system, but the binary copies will, nevertheless, be included in the set of system binaries. In addition, copies of certain 1700 FORTRAN 3.2 modules are included if the user has a FORTRAN compiler license. The install file itself contains only those modules to be included in the system. The system binaries media and the install file media may be magnetic tape, punched cards, or paper tape. The system skeleton as discussed in Article 2.2 is set up to select those modules from the set of system binaries which are to be part of a given system.

Some of the modules included in binary form

must be included in every 1700 MSOS 4.1 system. Some of the modules are optional. They can be included for added system capabilities, but are not necessary. Other modules are part of a group such that at least one module from the group must be included, or part or all of the group is required under certain conditions. Following is a list of the MSOS 4.1 modules and the FORTRAN 3.2 modules which may be included in binary form in the install file. A module in the list is optional unless there is an X in the column headed "Required in All Systems".

If the module is a part of a group, such that one or more modules in the group are required under certain conditions, a non-blank identifier appears in the left-hand column of the list.

At the beginning of a group, the requirement for selection within the group is explained.

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GROUP IDENTIFICATION {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	REMARKS
	X	SYSDAT	The binary copy of SYSDAT as it has been customized for a particular installation. The binary form of SYSDAT corresponds to the source copy of SYSDAT also included in the installation materials for the system.
	X	SPACE	
	X	NMONI	
One of the modules in Group E is required.			
E		RDISP	This is the scheduler/dispatcher module which must be used when reentrant FORTRAN is in the system.

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GROUP IDENTIFICATION
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM NAME

REMARKS

E

NDISP

It may be used if reentrant FORTRAN is not in the system, but in this case it causes core to be used unnecessarily. This is the scheduler/dispatcher module which is normally used when reentrant FORTRAN is not in the system.

X

RW

X

T14

X

T16

X

PARAME

X

COMMON

X

NIPROC

LINIV4

This program is used to analyze interrupts on line one. It is required only if more than one device in the system is connected to interrupt line one.

X

ALVOL

X

OFVOL

X

ALCORE

X

DCORE

PRTCDR

This module is necessary only if partitioned core is in the system.

X

NFNR

X

NCMPRQ

X

MAKQ

X

ADEV

X

TMINT

X

DTIMER

X

TOD

X

MINT

X

TRVEC

311

GROUP IDENTIFICATION {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	REMARKS
		SNAP0L	Provides registers snap dump capability, useful in debugging programs.
		DMP42X	Provides core dump capability if the 1742-30 or 1742-120 line printer is in the system.
		DMP421	Provides core dump capability if the 1740/501 or 1742-1 line printer is in the system.

Group F consists of the set of SYSCOP bootstrap programs. One of the programs in this group must be included if SYSCOP is in the system. The module selected depends on the system hardware configuration.

F		B1733Z	SYSCOP bootstrap for the 1733-2/856-2/856-4 Cartridge Disk.
F		BDK85X	SYSCOP bootstrap for the 1733-1/853/854 Disk or the 1738/853/854 Disk.
F		B17391	SYSCOP bootstrap for the 1739-1 Cartridge Disk.
F		B1752	SYSCOP bootstrap for the 1752 Drum
F		B1751	SYSCOP bootstrap for the 1751 Drum

All the modules in Group G are required if the File Manager is in the system.

G		FILMGR	Core resident File Manager module.
G		RSPCV4	Core resident File Manager module.
G		SRHFIS	Core resident File Manager module.
	X	EFDATA	
	X	DUMMY	Driver for dummy device.
		ALAQ	This module is required if more than one device in the system requires A/Q allocation.

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GROUP IDENTIFICATION {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	REMARKS
		AL1706	This module is required if two or more devices in the system share a 1706 Buffered Data Channel.
One comment device driver from Group H must be included.			
H		D1711	Core resident 1711/1712/1713/713 Keyboard driver
H		D1713K	Core resident 1713 Keyboard driver
One of the core resident mass storage drivers from Group I is required. A maximum of one disk driver and one drum driver may be present in a system.			
I		D17332	1733-2/856-2/856-4 Cartridge Disk driver.
I		D1738	1738/853/854 Disk driver.
I		D17331	1733-1/853/854 Disk driver.
I		D17391	1739-1 Cartridge Disk driver.
I		D1751	1751 Drum driver.
I		D1752	1752 Drum driver.
		REWCK	Core resident rewind check module required if magnetic tape is present in the system.
		DSBUFR	Provides software buffering capability.
		MMEXEC	Handles mass memory resident drivers. Required if any driver in the system is mass memory resident.

Group J consists of those drivers which are optionally core resident or mass memory resident. The system requires an input device driver and an output device driver. These need not be two separate drivers if the input device and the output device are the same device and one

GROUP IDENTIFICATION {IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

REMARKS

driver handles both input and output. The input device and output device may be the same as the comment device in which case the driver from Group H satisfies the input device/output device driver requirement. Otherwise this requirement must be satisfied from Group J.

J	D1713R	1713 Paper Tape Reader driver.
J	D1713P	1713 Paper Tape Punch driver.
J	DCOSY	COSY interface driver.
J	D17322	1732-2/615-73/615-93/10300 Buffered Magnetic Tape driver.
	D327TR	This module is the 7-track interface. It is required by D17322 only if 7-track tapes will be used.
J	D1732U	1732-1/608/609 Magnetic Tape driver {unbuffered}.
J	D1732B	1732-1/608/609 Magnetic Tape driver
J	D1731U	These five modules comprise the 1731/601 Magnetic Tape driver {unbuffered}.
J	FRWA	
J	FRWB	
J	RWBA	
J	MAXRVU	
J	D1731B	These five modules comprise the 1731/1706/601 Buffered Magnetic Tape driver.
J	FRWAB	
J	FRWBB	
J	RWBAB	
J	MAXRVB	
J	DPSUDO	Pseudo tape driver
J	D42312	Driver for the 1742-30 Line Printer or the 1742-120 Train Printer. The 1742-120 Printer requires T5954 to be appended to the driver.
	T5954	Train image table for the 1742-120 Printer.

GROUP IDENTIFICATION
{IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

REMARKS

J

D40421

Driver for the 1740/501 Line Printer or the 1742-1 Line Printer.

J

D17293

1729-3 Card Reader driver. This module requires either CR026 or CR029.

CR026

Table used for converting Hollerith {026} punched code to ASCII.

CR029

Table used for converting EBCDIC {029} punched code to ASCII.

J

D1728

1728/430 Card Reader/Punch driver. This module requires either the pair of modules CP026 and CR026 or the pair CP029 and CR029.

CP026

Table used for converting ASCII to Hollerith {026} punched code.

CP029

Table used for converting ASCII to EBCDIC {029} punched code.

J

D17292

1729-2 Card Reader driver. Use of this module requires either CR026 or CR029.

J

D1726U

1726/405 Card Reader driver {unbuffered}. Use of this module requires the inclusion of either CR026 or CR029.

J

D1726B

1726/1706/405 Buffered Card Reader driver. Use of this module requires the inclusion of either CR026 or CR029.

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GROUP IDENTIFICATION {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	REMARKS
J		D1777R	1721/1722/1777 Paper Tape Reader driver.
J		D1777P	1723/1724/1777 Paper Tape Punch driver.
J		D3644	364-4 Communications Multiplexor driver.
J		D1747	1747 Data Set Controller driver
J		D1744	1744/274 Digigraphic Unit driver
J		D1501	1525-3/1501-10/1501-11 High Level Solid State Analog Input driver.
J		D1536	1525-3/1536-2/1502-80 Low Level Relay Analog Input driver.
J		D1544	1544-1 to -4 Digital Input driver.
J		D5355	1553-1 to -4/1555-1 to -3 Digital Output driver.
		S15721	1572-1 Sample Rate Generator driver. This driver may be included only if the sample rate mode of the timer is not being used as the basic system time base. If included, S15721 must be core resident.
		L15721	1572-1 Line Synchronized Timer driver. This driver may be included only if the line synchronized timer aspects of the 1572-1 are not being used as the system time base. If included, L15721 must be core resident.

The modules in Group K are all required if reentrant FORTRAN is in the system. They are all core resident.

K	FORTR
K	Q&PRMR

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GROUP IDENTIFICATION
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM NAME

REMARKS

K	PARABR
K	QBEXPR
K	QBABR
K	SQRTFR
K	SIGNR
K	FXFLR
K	EXPRGR
K	LNPRGR
K	TANHR
K	SNCSR
K	ARCPGR
K	IFALTR
K	FLCATR
K	QBQIOR
K	BINARR
K	IOCODR
K	INITLR
K	RSTORR
K	GETCHR
K	IPACKR
K	UPDATR
K	DECPLR
K	INTGRR
K	SPACER
K	HOLR
K	DCHXR
K	HXASCR
K	AFMTOR
K	RFMTOR
K	AFMTIR
K	RFMTIR
K	ASCHXR

GROUP
IDENTIFICATION
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM NAME

REMARKS

317

K		HXDCR
K		FLOTIR
K		FOUTR
K		EOUTR
K		EWRTIR
K		INTIIR
K		FORMTR
K		QBQFIR
K		QBQFLR
K		QBQFXR

The modules in Group K1 are usually included with reentrant FORTRAN although any module in the group may be omitted if the reentrant FORTRAN programs in the system are restricted to not call the corresponding formatting routine. Each of these formatting routines is described in the 1700 MS0S 4 MS FORTRAN Version 3 A/B Reference Manual.

K1		HEXAR	Permits reentrant FORTRAN calls to HEXASC.
K1		HEXDR	Permits reentrant FORTRAN calls to HEXDEC.
K1		ASCIIR	Permits reentrant FORTRAN calls to ASCII.
K1		DECHXR	Permits reentrant FORTRAN calls to DECHEX.
K1		AFORMR	Permits reentrant FORTRAN to call AFORM.
K1		RFORMR	Permits reentrant FORTRAN to call RFORM.
K1		FLOTGR	Permits reentrant FORTRAN to call FLOATG.

GROUP IDENTIFICATION
{IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

REMARKS

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The modules in Group L are all required if double precision reentrant FORTRAN is in the system. They are all core resident.

L		QBDBLR
L		DEXPR
L		QBDABR
L		DSQRTR
L		DSIGNR
L		SNGLR
L		QBDFLR
L		DEXPFR
L		DLOGR
L		DRSTRR
L		DSNCSR
L		DATANR
L		DFLOTR
L		QBQDFR
L		DOUTR
	X	NXTLOC
	X	LIBEDT
	X	LOAD1
	X	BRNCH1
	X	LIDRV1
	X	LCDRV1
	X	LMDRV1
	X	LLDRV1
	X	ADJOF1
	X	CNVRT1
	X	LSTOT1

This is a dummy program. The purpose of loading it is that the load location of NXTLOC marks the next available core location above the monitor and reentrant FORTRAN.

GROUP
IDENTIFICATION
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM NAME

REMARKS

319

X	LINK11
X	LOADR1
X	NAMPRI
X	RBDBZ1
X	ENTEX1
X	XFRPRI
X	STBASE
X	LNKENT
X	LNKCR1
X	PATCH
X	TBSCH1
X	HASH
X	TBSTR1
X	PAGE
X	PROGLD
X	SCAN1
X	CHPU1
X	ADJOV2
X	ADRPRI
X	JOBENT
X	T11
X	T7
X	T5
X	T3
X	JOBPRO
	ONE

Mass memory resident.

A dummy program. The loading of ONE provides for the capability of the user to later replace ONE by a non-dummy program of the same name. The replacement program may be loaded by using LIBEDT.

GROUP IDENTIFICATION {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	REMARKS
		TWO	Each of these are dummy programs with the same purpose as ONE.
		THREE	
One module from Group M is required as the protect processor for the system.			
M		UPROTK	Unbuffered protect processor. Using UPROTK, unprotected core is not swapped out while unprotected input/output is in progress.
M		BPROTK	Buffered protect processor. Using BPROTK, unprotected core may be swapped out while unprotected input/output is in progress if certain conditions are met. {Refer to Article 1.58.}
	X	JBKILL	
	X	JPLOAD	
	X	JPCHGE	
	X	ASCHEX	
	X	T13	
	X	JCRDV4	
	X	JLGOV4	
	X	JPSTV4	
	X	NAMEV4	
	X	JPFLV4	
	X	JPF2V4	
	X	RESTOR	

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GROUP IDENTIFICATION {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	REMARKS
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The modules in Group N are required only if the recovery package is in the system.

N		RCOVER	
N		OUTSEL	
N		RDMPV4	
N		MASDMP	

The modules in Group 0 are required only if the breakpoint package is in the system.

0		BRKPTD	
0		SIFT	
0		BIASCI	
0		RETJMP	
0		JMPT0	
0		ENTER	
0		ENTCOR	
0		PRTREG	
0		TERMIN	
0		RESUME	
0		DPCORB	
0		MSDMPB	
0		SETBRP	
0		0DEBUG	

Required only if the debugging package is in the system.

Modules in Group P are required only if the system checkout package is in the system.

P		SYSCOP	
P		CO1ST	
P		CO2ND	
P		CO3RD	
P		COLAST	

GROUP IDENTIFICATION {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	REMARKS
	X	MIPRO	
	X	TDFUNC	
	X	EFSTOR	
	X	EFLIST	
		SCMEXC	Required only if the SCMM package is in the system.
	X	VERFY1	Used for system verification tests that accompany the system install file.

Modules in Group Q are loaded as dummy ordinals. The purpose of these dummy programs is to allow the user to later insert programs into the system library. Any of these may be omitted if it is not intended to later enter a corresponding system library program.

Q	DUMMY1
Q	DUMMY2
Q	DUMMY3
Q	DUMMY4
Q	DUMMY5
Q	DUMMY6
Q	DUMMY7
Q	DUMMY8
Q	DUMMY9
Q	DUMMY0

Note that the drivers from Group J which are to be mass resident are loaded following the modules in Group Q.

Modules in Group R are the mass resident File Manager request processors. These are included only if the File Manager is in the system. The module FMDUMY is a dummy module of 96 words. Each time it is loaded it causes the sector pointer to point to a new sector. The module RPEND is also a dummy module. It contains zero words. It is used to record the end of a request

GROUP
IDENTIFICATION
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM NAME

REMARKS

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processor, and is therefore loaded after the set of modules for each request processor. The function of a given request processor in Group R is given in the remarks column.

R	DEFFIL	Define file
R	FILSPC	
R	RELFIL	Release file
R	RELSPC	
R	DEFIDX	Define file indexed
R	SQRTFM	
R	FILSPC	
R	LOKFIL	Lock file
R	UNLFIL	Unlock file
R	STOSEQ	Store sequential
R	FILSPC	
R	STODIR	Store direct
R	STOIDX	Store indexed
R	HASHCD	
R	GETKID	
R	FILSPC	
R	RTVSEQ	Retrieve sequential
R	RTNSPC	
R	RTVDIR	Retrieve direct
R	RTNSPC	
R	RTVIDX	Retrieve indexed
R	HASHCD	
R	GETKID	
R	RTNSPC	
R	RTVIDO	Retrieve indexed ordered
R	GETKID	
R	RTNSPC	

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GROUP IDENTIFICATION {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	REMARKS
Modules in Group S are Macro Assembler modules and thus are included only if the Macro Assembler is in the system.			
S		LIBMAC	Allows generation of a new macro library.
S		ASSEM	Sequences the Macro Assembler passes.
S		PASS1	
S		PA1PR2	
S		PASS2	
S		PA2PR2	
S		PASS3	
S		PA3PR2	
S		PA3PR3	
S		MACSKL	} Macro library
S		MACROS	

Either the FORTRAN 3.2 A compiler or the FORTRAN 3.2 B compiler may be included. The B compiler is faster, but requires larger overlays. {Refer to the 1700 MSOS 4 MS FORTRAN Version 3 A/B Reference Manual, Publication No. 60362000.} The COSY deck identifier for each module of the A compiler ends with "FORTRAN 3.2A". The COSY deck identifier for each module of the B compiler ends with "FORTRAN 3.2B".

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

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Following is the list of modules to be loaded for the FORTRAN 3.2 A compiler. Note that some redundancies occur. The modules must be loaded in this order including the redundancies so that each compiler overlay will be properly defined. The skeleton records for the FORTRAN 3.2A compiler are listed in Article 6.3.2.

PROGRAM NAME	COSY DECK IDENTIFIER
FTNA	
FTNA	
GOA	F02
IOPREA	F08
CNVT	A01
CONV	F03
DTAG	F04
GETC	F13
GETSYM	F12
OUTENT	A07
PACK	F09
QAFRMS	F10
STCRE	F11
SYMBOL	A03
LOCLAA	F17
DUMYAA	F18
PHASEA	A08
ARAYSZ	A42
COLOOP	A43
ENDDC	A29
GNST	A06
IGETCF	F14
OPTICN	F15
PLABEL	A09
QRGBDS	A10
RDLABL	A11
SAVEID	A04
STCHAR	A12
ENDLCC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

326

PROGRAM NAME	COSY DECK IDENTIFIER
FTNA	
GOA	F02
IOPREA	F08
CMVT	A01
CONV	F03
DTAG	F04
GFTC	F13
GETSYM	F12
OUTENT	A07
PACK	F09
QAPRMS	F10
STCRE	F11
SYMBOL	A03
LOCLAB	F19
DUMYAB	F20
RYEQPR	A19
DELOT	F06
DUMVCL	F07
DXP9	F05
GETF	A05
GDLT	A02
SAVEID	A04
STCHAR	A12
SUBPR	A23
TYPE	A13
ENDLOC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

327

FTNA	
GOA	F02
IQRBA	F08
CNVT	A01
CONV	F03
DTAG	F04
GETC	F13
GETSYM	F12
OUTENT	A07
PACK	F09
QAPRMS	F10
STCR	F11
SYMBOL	A03
LOCLAC	F21
DUMYAC	F22
ASGNPR	A32
BDOPR	A33
CFIVCC	A34
CKIVC	A35
CKNAME	A36
COMNPR	A15
DFLOT	F06
DIMPR	A16
DUMVCL	F07
DXF9	F05
EREPR	A38
EXRLPR	A24
GETF	A05
GPUT	A02
RDLABL	A11
TYPEPR	A18
ENDLCC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

328

FTNA	F02
GOA	F08
IOPREA	A01
CONV	F03
CONV	F04
DTAG	F13
GETC	F12
GETSYM	A07
OUTENT	F09
PACK	F10
QPERMS	F11
STORE	A03
SYMBOL	F23
LOCLAD	F24
DUMYAD	A40
ASEMFR	F06
DFLOT	F07
DUMVCL	F05
DXP9	A05
GETF	A02
G-UT	F14
IGETCF	A27
PIINT	A11
RDLABL	A17
SIJESCR	F16
FNDLCC	

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

329

FTNA	
GOA	F02
IOPRBA	F08
CNVT	A01
CONV	F03
DIAG	F04
GETC	F13
GETSYM	F12
OUTENT	A07
PACK	F09
QAPRMS	F10
STORE	F11
SYMBCL	A03
LOCLAE	F25
DUMYAE	F26
CONSUB	A30
DATAPR	A31
DFLOT	F06
DUMVCL	F07
DXP9	F05
GETF	A05
GPOT	A02
STCHAR	A12
ENDLCC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

330

FTNA	
GOA	F02
IOFREA	F08
CMVT	A01
CONV	F03
DTAG	F04
GFIC	F13
GETSYM	F12
OUTENT	A07
PACK	F09
QAPRMS	F10
STCR	F11
SYMBOL	A03
LOCLAF	F27
DIINYAF	F28
CHECKF	A20
FGETC	A21
FORK	A22
FFGVS	A25
PONTM	A26
STCHAR	A12
SYMSON	A28
ENDLCC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

331

FTNA	F02
GOA	F08
IOPRBA	A01
CNVT	F03
CONV	F04
DIAG	F13
GETC	F12
GETSYM	A07
OUTENT	F09
PACK	F10
QBPRMS	F11
STORE	A03
SYMBOL	F29
LOCLAG	F30
DUMYAG	A14
ARITH	F14
IGETCF	A27
PUNT	A41
TREE	F16
ENDLOC	

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

332

FTNA	
GOA	F02
IDFRBA	F08
CNVT	A01
CONV	F03
DIAG	F04
GETC	F13
GETSYM	F12
OUTENT	A07
PACK	F09
QBFRMS	F10
STORE	F11
SYMBCL	A03
LOCLAH	F31
DUMYAH	F32
IGETCF	F14
MODMAR	A39
PUNT	A27
ENDLCC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

333

FTNA	F02
GOA	F08
IOPRBA	A01
CMVT	F03
CONV	F04
DIAG	F13
GETC	F12
GETSYM	A07
OUTENT	F09
PACK	F10
QRPRMS	F11
STCR	A03
SYMBCL	F33
LOCLAI	F34
DUMYAI	A37
IOSPR	A29
ENDDC	A11
ROLABL	A12
STCHAR	F16
ENDLOC	

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

334

FTNA	
GOE	F35
CMVT	A01
DUMMY	B01
FCMSTK	B02
GETSYM	F12
IDFRBB	F36
KCFART	B03
KOLTFT	B04
KCCSTK	B05
KCC3FR	B06
KSYMGN	B07
LABKPC	B08
LABLER	B09
PRINT	B10
QPRRMS	F10
STCRFB	F37
SYMBCL	B11
TSALCC	B12
LOCLEA	F38
DUMYEA	F39
PHASER	B21
INXRST	B19
NOPRCC	B20
READIR	F22
ENDLCC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

335

FTNA	
GOB	F35
CNVT	A01
DIMMY	B01
FCMSTK	B02
GETSYM	F12
IOPRBB	F36
KCPART	B03
KOLTFT	B04
KPCSTK	B05
KPC3PR	B06
KSYMGN	B07
LABKPC	B08
LABLER	B09
FIJNT	B10
QAPRMS	F10
STOREB	F37
SYMBOL	B11
TSALCC	B12
LOCLBB	F40
DUMYBB	F41
AFIDL	B25
ASSEM	B13
BANANA	B14
END	B16
ENTCCD	B17
INXRST	B19
SUBFUN	B23
ENDLCC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

336

FTNA	F35
GOB	A01
CNVT	B01
DUMMY	B02
FCMSTK	F12
GETSYM	F36
IOPRBB	B03
KCPART	B04
KOLTPT	B05
KPCSTK	B06
KPC3PR	B07
KSYMGN	B08
LABKPC	B09
LABLER	B10
PUNT	F10
QBPRMS	F37
STCREB	B11
SYMBGL	B12
TSALOC	F42
LOCLEC	F43
DJMYEC	B26
ASLPER	A42
ARAYSZ	B15
BGINDO	B27
CGGTC	B18
HELEN	A28
SYMSCN	F16
ENDLOC	

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

337

FTNA	
GOB	F35
CNVT	A01
DUMMY	B01
FCMSTK	B02
GETSYM	F12
IOPREB	F36
KCFART	B03
KOLTPT	B04
KBCSTK	B05
KBCBPR	B06
KSYMGN	B07
LARKPC	B08
LABLER	B09
PUNT	B10
QAPRMS	F10
STOREB	F37
SYMBOL	B11
TSALOC	B12
LOCLED	F44
DUMYED	F45
ARITER	B34
FINK	B28
ENDLOC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

338

FTNA	F35
GOB	A01
CNVT	B01
DUMMY	B02
FCMSTK	F12
GFTSYM	F36
IOPRBB	B03
KCFART	B04
KOLTPT	B05
KPCSTK	B06
KPC3PR	B07
KSYMGN	B08
LABKPC	B09
LABLER	B10
PJNT	F10
QBPRMS	F37
STCREB	B11
SYMBOL	B12
TSALCC	F46
LOCLBE	F47
DUMYBE	B24
ACP	F16
ENDLCC	

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

339

FTNA	
GOB	F35
CMVT	A01
DUMMY	B01
FCMSTK	B02
GETSYM	F12
IOPRBB	F36
KCFART	B03
KOLTPT	B04
KPCSTK	B05
KPC3PR	B06
KSYMGN	B07
LABKPC	B08
LABLER	B09
PUNT	B10
QBFRMS	F10
STCREB	F37
SYMBOL	B11
TSALOC	B12
LOCLBF	F48
DUMYBF	F49
SUBPR3	B33
INTRAM	B29
PARTSB	B30
SUBPR1	B31
SUBPR2	B32
ENDLCC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

340

FTNA	
GOC	F50
IOPRBC	F51
BKDWN	C01
BLDUP	C02
BSS	C03
CHKWD	C04
CON	C07
COUNT	C08
DATAST	C09
GETSYM	C10
INCUT	C11
LABEL	C14
LABIN	C15
QRFRMS	F10
REED	C17
SYMSCN	C19
LOCLCA	F52
DUMYCA	F53
PHASEC	C13
ENDLOC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

34

FTNA	F50
GOC	F51
IOPREC	C01
BKDN	C02
BLDUP	C03
BSS	C04
CHKWD	C07
CON	C08
COUNT	C09
DATAST	C10
GETSYM	C11
INCUT	C14
LABEL	C15
LABIN	F10
QBPRMS	C17
REED	C19
SYMSCN	F54
LOCLCB	C05
CHCP	C06
CL12	C18
SKIP	C12
IXOPT	C16
QyLD	F16
ENDLOC	

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

342

FTNA	
GOOD	F55
INDEX	D01
IOPRED	F56
NPUNCH	D02
QAPRMS	F10
LOCLDA	F58
DIIMYDA	F59
PHASE6	D03
REGINO	D21
CONV	F57
FINISH	D22
GETSYM	D16
IACON	D17
INCON	D18
NWRITE	D19
PACK	F09
SYMSON	D20
ENDLCC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

343

FTNA	F55
GOOD	D01
INDEX	F56
IOPRBD	D02
NPUNCH	F10
QBFRMS	F60
LOCLCB	F61
DUMYDB	D04
AMCUT	D06
BKDWA	D07
COUNT	D14
GETSYM	D08
LABOUT	D09
NR2OUT	D10
RRCX	D11
RRPK	D15
SYMSCN	D12
TABDEC	D13
UNFUNC	F16
ENDLOC	

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

344

FTNA	
GOCD	F55
INDEX	D01
IOFRBD	F56
NPUNCH	D02
QBFRMS	F10
LOCLCC	F62
ADMAX	D05
GETSYM	D14
TABDEC	D12
SYMSCN	D15
ENDLCC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

345

FTNA	
GOE	F63
INDEX	E01
IOPRED	F56
NOUNCH	E02
QAPRYS	F10
LOCLDA	F58
DIIMYDA	F59
PHASE6	E03
BEGIN0	E19
CONV	F57
FINISH	E20
GETSYM	E14
IACON	E15
IHCON	E16
NWRITE	E17
PACK	F09
SETPHT	E18
SYMSON	D20
ENDLOC	F16

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

346

FTNA	F63
GNE	E01
INDEX	F56
IOPRED	E02
NPUNCH	F10
QRFRMS	F60
LOCLCB	F61
DUMYCB	E04
AMCUT	E06
RKDWN	F57
CONV	E07
COUNT	E14
GFTSYM	E15
IACON	E16
IHCON	E08
LABOUT	E09
NO2OLT	E17
NWRITE	F09
PACK	E10
RDCX	E11
RPFK	E18
SETPRT	O20
SYMSCN	E12
TABDEC	E13
UNFUNC	F16
ENDLCC	

347

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

FTNA.	F63
GOE.	E01
INDEX	F56
IOPRBD	E02
NPUNCH	F10
QBPRMS	F62
LOCLCC	E05
ADMAX	E14
GETSYM	D20
SYMSCN	E12
TAEDEC	F16
ENCLCC	

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

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Following is the list of modules to be loaded for the FORTRAN 3.2 B Compiler. As for the A compiler, there are some redundancies. The modules must be loaded in the order listed to properly define each compiler overlay. The skeleton records for the FORTRAN 3.2B compiler are listed in Appendix B.

FTNB	
FTNB	
GOA	02F
CFIVCC	34A
CKNAME	36A
CNVT	01A
CONV	03F
DIAG	04F
DXF9	05F
DFLOT	06F
DUMVCL	35F
GETC	14F
GETF	04A
GETSYM	07F
GFUT	02A
IGETCF	15F
IOPRBA	08F
PACK	09F
QBFRMS	10F
RDLABL	10A
STCRE	11F
SYMBCL	03A
ENDDC	29A
GNST	05A
OPTIGN	16F
OUTENT	06A
PHASEA	07A
PLABEL	08A
STCHAR	11A
TYPE	12A
SAVEID	13A
LOCLA1	12F
DUMYA1	13F
QGBBDS	09A
ENDLCC	17F

GROUP IDENTIFIER
{IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

COSY DECK IDENTIFIER

FTNB	02F
GOA	
CFIVCC	34A
CKNAME	36A
CNVT	01A
CONV	03F
DIAG	04F
DXP9	05F
DFLOT	06F
DUMVCL	35F
GETC	14F
GETF	04A
GETSYM	07F
GPUR	02A
IGETCF	15F
IOPRBA	08F
PACK	09F
Q8PRMS	10F
RDLABL	10A
STCRE	11F
SYMBCL	03A
ENDDC	29A
GNST	05A
OPTIGN	16F
OUTENT	06A
PHASEA	07A
PLABEL	08A
STCHAR	11A
TYPE	12A
SAVEID	13A
LOCLA2	18F
DUMYA2	19F
BYEQFR	19A
CHECKF	20A
COMNPR	15A
CONSLB	30A
DATAPR	31A
DIMPR	16A
EXLFR	24A
FGETC	21A
FORK	22A
PEGVS	25A
PRNTM	26A
SUBPPR	23A
SYMSCN	28A
TYPEPR	18A
ENDLOC	17F

GROUP IDENTIFIER {IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

COSY DECK IDENTIFIER

FTNB	
GOA	02F
CFIVCC	34A
CKNAME	36A
CNVT	01A
CONV	03F
DIAG	04F
DXP9	05F
DFLOT	06F
DUMVCL	35F
GETC	14F
GETF	04A
GETSYM	07F
GPUR	02A
IGETCF	15F
IOPREA	08F
PACK	09F
QBFRMS	10F
RDLABL	10A
STCRE	11F
SYMBCL	03A
ENDDC	29A
GNST	05A
OPTICN	16F
OUTENT	06A
PHASEA	07A
PLABEL	08A
STCHAR	11A
TYPE	12A
SAVEID	13A
LOCLA3	20F
DUMYA3	21F
ARAYSZ	42A
ASEMFR	40A
ASGNFR	32A
BDCPR	33A
CHECKF	20A
CKIVC	35A
CONSLR	30A
CPLOCP	43A
FGETC	21A
FORK	22A
ERBPR	38A
MODMXR	39A
PUNT	27A
ENDLCC	17F

GROUP IDENTIFIER {IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

COSY DECK IDENTIFIER

FTNB	
GOA	02F
CFIVCC	34A
CKNAME	36A
CNVT	01A
CONV	03F
DIAG	04F
DXF9	05F
DFLOT	06F
DUMVCL	35F
GETC	14F
GETF	04A
GETSYM	07F
GPLT	02A
IGETCF	15F
IOPREA	08F
PACK	09F
QBFRMS	10F
RDLABEL	10A
STCRE	11F
SYMBCL	03A
ENDDC	29A
GNST	05A
OPTICN	16F
OUTENT	06A
PHASEA	07A
PLABEL	08A
STCHAR	11A
TYPE	12A
SAVEID	13A
LOCLA4	22F
DUMYA4	23F
ARITH	14A
SUBSCR	17A
TREE	41A
ENDLCC	17F

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

352

FTNB	
GOA	02F
CFIVCC	34A
CKNAME	36A
CMVT	01A
CONV	03F
DIAG	04F
DXP9	05F
DFLOT	06F
DUMVCL	35F
GETC	14F
GETF	04A
GETSYM	07F
GPLT	02A
IGETCF	15F
IOPRBA	08F
PACK	09F
QBFRMS	10F
RDLABL	10A
STCRE	11F
SYMBOL	03A
ENDDC	29A
GNST	05A
OPTICN	16F
OUTENT	06A
PHASEA	07A
PLABEL	08A
STCHAR	11A
TYPE	12A
SAVEID	13A
LOCLAS	24F
DUMYAS	25F
BDCPR	33A
CKIVC	35A
IOSPR	37A
ENCLCC	17F

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

353

FTNB	
GOB	26F
CNVT	01A
DUMMY	01B
FCMSTK	02B
GETSYM	07F
IOPRBB	27F
KCPART	03B
KOLTPT	04B
KPCSTK	05B
KPC3PR	06B
KSYMGN	07B
LABKPC	08B
LABLER	09B
PUNT	10B
CONV	03F
QBFRMS	10F
STCREB	34F
SYMBOL	11B
TSALCC	12B
ARAYSZ	42A
ASSEM	13B
BANANA	14B
BGINCO	15B
END	16B
ENTCCD	17B
HELEN	18B
INXRST	19B
NOPRCC	20B
PHASEB	21B
READIR	22B
SUBFUN	23B
SYMSCN	28A
ACP	24B
AFIDL	25B
ASUPER	26B
CGCTC	27B
FINK	28B
INTRAM	29B
PARTSB	30B
SUEPR1	31B
SUEPR2	32B
SUEPR3	33B
ARITHR	34B
ENDLCC	17F

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GROUP IDENTIFIER {IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

COSY DECK IDENTIFIER

FTAB	28F
GOC	01C
BKDW	02C
HLDUP	03C
BSS	04C
CHKWD	05C
CHCP	06C
CL12	07C
CON	08C
COLNT	09C
DATAS	10C
GETSYM	11C
INCUT	29F
IOFRBC	12C
IXCPT	14C
LABEL	15C
LAEIN	13C
PHASEC	10F
QBFRMS	16C
QXLD	17C
REED	18C
SKIP	19C
SYMSCN	17F
ENDLCC	

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GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

FTNB	
GOCD	30F
AMCUT	01C
ADMAX	02C
BEGINO	03D
BKDNW	04C
COUNT	05C
FINISH	06C
GETSYM	10C
IACON	07C
IHCON	08C
INDEX	09C
IOPRBD	31F
LAEOUT	10C
NPZOUT	11C
NPLNCH	12C
NWRITE	13C
PACK	09F
PHASE6	14C
QBFRMS	10F
RBCX	15C
RRFK	16C
SYMSCN	17C
TABDEC	18D
UNFUNC	19C
CONV	33F
ENDLCC	17F

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

356

FTNB	
GOE	32F
AMCUT	01F
ADMAX	02E
BEGIN0	03E
BKOWN	04E
CONV	33F
COLNT	05F
FINISH	06E
GETSYM	10C
IACON	07E
IHCON	08E
INDEX	09E
IOPRED	31F
LAHOLT	10E
NP2OLT	11E
NPLNCH	12E
NWRITE	13E
PACK	09F
PHASE6	14E
QAPRMS	10F
RBCX	15E
RBFK	16E
SETPRT	17E
SYMSCN	17C
TAEDEC	18E
UNFUNC	19E
ENCLCC	17F

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GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

FORTN

F01

If background FORTRAN is in the system, this module is usually included. It provides the FORTRAN/monitor interface. It may be omitted if background FORTRAN programs are restricted so as not to use the functions LINK and ICLOCK and not to use the following statements:

358

GROUP IDENTIFIER {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	COSY DECK IDENTIFIER
			CALL READ
			CALL WRITE
			CALL FREAD
			CALL FWRITE
			CALL SCHEDL
			CALL TIMER
			CALL RELEASE
			CALL DISP
			CALL OUTINS
			CALL INPINS
			CALL ICONCT
			CALL OCONCT

All the modules in Group T must be present if the system is to allow FORTRAN programs to run in the background. The modules in this group supply mathematical function capability.

GROUP IDENTIFIER {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	COSY DECK IDENTIFIER
T		QBPRMS	G01
T		QBEXPN	G02
T		QBAB	G03
T		SQRTF	G04
T		SIGN	G05
T		FXFL	G06
T		EXPPRG	G07
T		LNUPRG	G08
T		TANH	G09
T		SINCOS	G10
T		ARCTPG	G11
T		IFALT	G12
T		FLOAT	G13

Either Group U or Group V must be present together with Group T to form a background FORTRAN library. Group U provides more capabilities than Group V. Group V provides the same capabilities as those of the foreground FORTRAN library. If a FORTRAN program is to be checked out in the background for later use in the foreground it is recommended that it be tested with Group V, since this insures that the program will run properly without relying on the greater capabilities of Group U. Group U is called the FORTRAN Input/Output Library. Group V is called the FORTRAN Limited Input/Output Library. In a given system, the user may wish to allow the programmer to select either Group U or Group V at run time. The method for doing this is explained in the note entitled FORTRAN Background Library Run Time Options. This note follows the list of modules in Group VI. Some of the differences between Group U and Group V are as follows:

Group U, FORTRAN Input/Output Library	Group V, FORTRAN Limited Input/Output Library
E and D formats may be used for input as well as output.	E and D formats are limited to output specifications only.
Using F format for output, the range of the internal number is not restricted.	Using F format for output, the range of the internal number represented must be from 10^{-5} to $10^{+5}-1$.
An integer-type variable may be a one-word variable or a two-word variable.	All integer-type variables must be one word integer-type variables, i.e., the K compile-time option is not available.

Group U, FORTRAN Input/
Output Library

Input records may be of
any length.

FORTRAN mass storage files
input/output is available.

READ/WRITE statements may
be used for mass memory
input/output.

Calls to SETBFR are not
necessary.

Group V, FORTRAN Limited
Input/Output Library

Each input record is limited
to 80 characters. Thus the
following is illegal:

```
READ{10,20}{X{I,J},I=1,10},J=1,20}  
20 FORMAT{10F8.4}
```

FORTRAN mass storage files are
not accessible.

Mass memory input/output may not be
performed using READ/WRITE statements.
{Mass memory input/output is possible
using CALL READ, CALL WRITE, CALL FREAD,
CALL FWRITE if the FORTN module is
included.}

Calls to SETBFR are required.

For a full explanation of the differences, refer to the 1700 MS0S
4 MS FORTRAN Version 3 A/B Reference Manual, Publication No.
60362000.

GROUP
IDENTIFIER
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM
NAME

COSY
DECK
IDENTIFIER

361

The modules in Group U, the FORTRAN Input/Output Library are as follows:

U	QBIFRM	H01
U	QBFS	H02
U	QBTRAN	H03
U	QBQINI	H04
U	QBQEND	H05
U	QBCMP	H06
U	QBRWBU	H07
U	QBERRM	H08
U	QBDFIO	H09
U	QBQX	H10
U	QBQUNI	H11
U	QBFGET	H12
U	QB MAGT	H13
U	TAPCON	H14
U	IOCK	H15
U	PSSTOP	H16
U	QB PAND	H17
U	QB EXP1	H18
U	QB EXP9	H19
U	QB QGTX	H20

The modules in Group V, the FORTRAN Limited Input/Output Library are as follows. Note the remarks with the module QBQIO.

V	IOCODE	J01
V	PSUEDO	J02
V	IGETCH	J03
V	IPACK	J04
V	UPDATN	J05
V	DECPL	J06
V	INTGR	J07

(362)

GROUP IDENTIFIER {IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

COSY DECK IDENTIFIER

V
V
V
V
V
V
V
V
V
V
V
V
V
V
V
V
V
V
V
V
V

SPACEN J08
HOLRTH J09
DCHX J10
HXASC J11
AFRMOT J12
RFRMOT J13
AFRMIN J14
RFRMIN J15
ASCHX J16
HXDC J17
FLOTIN J18
FOUT J19
EOUT J20
EWRITE J21
INITL1 J22
FORMIN J23
QBQFI J24
QBQFL J25
QBQFX J26
QBQIO H21

Although QBQIO is necessary for the FORTRAN Limited Input/Output Library, the module QBQIO is not present in a standard released system

A QBQIO binary may be extracted from a binaries file. {Refer to Article 6.5.1.}

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GROUP IDENTIFIER {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	COSY DECK IDENTIFIER
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This binary may then be loaded so that the FORTRAN background library will be the FORTRAN Limited Input/Output Library. When loading Q8QIO, the modules Q8QX, Q8QINI, Q8QEND, and Q8QGTx must not be present in the system.

Any or all of the modules in Group V1 may be included if desired with Group V, the FORTRAN Limited Input/Output/Output Library. Each module in Group V1 is a formatting routine with the same function as one of the reentrant formatting routines in Group K1.

V1		HEXASC	J27
V1		HEXDEC	J28
V1		ASCII	J29
V1		DECHEX	J30
V1		AFORM	J31
V1		RFORM	J32
V1		FLOATG	J33

364

FORTTRAN Background Library Run-time Options

There are four different combinations of FORTRAN background library modules which may be included in the system. These combinations are as follows:

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COSY DECK IDENTIFIERS FOR MODULES INCLUDED	GROUP U FORTRAN INPUT/ OUTPUT LIBRARY, PRESENT	GROUP V FORTRAN LIMITED INPUT/OUTPUT LIBRARY, PRESENT	RUN-TIME OPTIONS
H01-H20 {with or without J27-J33}	Yes	No	No option avail- able. All back- ground FORTRAN programs will run under Group U, FORTRAN Input/Output Library.
J01-J26, H21 {with or without J27-J33}	No	Yes	No option avail- able. All back- ground FORTRAN programs will run under Group V, FORTRAN Limited Input/Output Library.
H01-H03, H06-H09, H11-H19, J01-J26, H21 {with or without J27-J33}	In Part	Yes	A background FORTRAN program will run under Group V unless the programmer inserts binaries for modules H04, H05, H10 and H20 after his binary deck at run-time. If he inserts these modules his program will run under Group U. By in- serting H04, H05, H10 and H20, linkage is performed so that Group V is not used. This combination is useful if most back- ground FORTRAN programs are to run with the FORTRAN Limited Input/Output Library, but it is desired to leave the option open for running with the FORTRAN Input/ Output Library.

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COSY DECK IDENTIFIERS
FOR MODULES INCLUDED

GROUP U
FORTRAN INPUT/
OUTPUT LIBRARY,
PRESENT

GROUP V
FORTRAN LIMITED
INPUT/OUTPUT
LIBRARY, PRESENT

RUN-TIME
OPTIONS

When using this combination, the background program has access to the pause command to tape motion requests, and to mass storage files. None of these are, however available to a foreground FORTRAN program.

COSY DECK IDENTIFIERS
FOR MODULES INCLUDED

GROUP U
FORTRAN INPUT/
OUTPUT LIBRARY,
PRESENT

GROUP V
FORTRAN LIMITED
INPUT/OUTPUT
LIBRARY, PRESENT

RUN-TIME
OPTIONS

H01-H20, J01-J26
{with or without
J27-J33}

Yes

In Part

A background FORTRAN program will run under Group U unless the programmer, at runtime, inserts module H21, Q&QIO, in binary form following his binary deck. {See note regarding Q&QIO in the list of modules for Group V.} If a Q&QIO binary is inserted, linkage will be performed so that only the modules in Group V will be used. This combination is useful if most background FORTRAN programs are to use the FORTRAN Input/Output Library, but it is desired that the FORTRAN Limited Input/Output Library be available as an option. This is what the user gets in a released system.

368

Group W is required for FORTRAN background double precision independent of whether Group U or Group V is selected. {If Group W is not present, the module DBLDMY must be included.}

369

GROUP IDENTIFIER {IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

COSY DECK IDENTIFIER

W
W
W
W
W
W
W
W
W
W
W
W
W
W
W
W
W
W
W
W

DEXPN K01
Q8DBLE K02
SNGL K03
Q8DABN K04
DSQRTN K05
Q8DSIG K06
Q8DFLT K07
DEXPFN K08
DLOGN K09
DSNCSN K10
DATANN K11
DFLOTN K12
DUMVOL K13
DRSTOR K14
Q8DXP1 K15
Q8DXP9 K16
Q8QDFN K17
DOUTN K18
DBLDMY K19

TRACE

LULIST

LISTR

If Group W, FORTRAN background double precision is not selected, the module DBLDMY is required to link unpatched externals.

Provides background trace of user's program.

Provides capability of listing logical units.

Lists name and record length of each program on binary tape, cards, or paper tape.

370

GROUP IDENTIFICATION
{IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

REMARKS

OPSORT

Provides cross references of 1700 assembly language operands. This module is not required by the macro assembler since the assembler performs its own cross references sort.

EESORT

Provides a list of entry points and externals; lengths of program, common, and data; program name and name card comments for assembly language programs. {Not used by the Macro Assembler.}

COSY

Compression of source deck information utility.

LCOSY

Lists names of programs in a COSY library.

CYFT

COSY format program; inserts COSY control cards into source decks.

IOUP

IOUP Background utility.

IOUPV4

} Performs data transfer, data verification, and motion control.

GROUP
IDENTIFICATION
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM NAME

REMARKS

371

The modules in Group Y are required if the DSKTAP save/recovery package is to be included in the system. The modules in this group may be used to save a copy of the system by transferring the system from mass memory to magnetic tape. They may subsequently be used to transfer the information saved on tape back to mass memory, thus restoring the system. In addition to the modules in Group Y the DSKTAP package requires one module from Group Y1 to be used as the mass memory driver. Note that there is no DSKTAP driver for the 1751 Drum since DSKTAP has no provision for use of that device.

Y		DTLP	
Y		DSKTAP	
Y		DSKEQC	
Y		DSKDHX	
Y		DSKCDR	
Y		DSKMTI	
Y		DSKMT0	
Y1		MDRV56	DSKTAP driver for the 1733-2/856-2/856-4 Cartridge Disk.
Y1		DSKMMD	DSKTAP driver for the 1733-1/853/854 Disk or the 1738/853/854 Disk.
Y1		MDRIV4	DSKTAP driver for the 1739-1 Cartridge Disk.
Y1		MDR52	DSKTAP driver for the 1752 Drum.

The modules in Group Z are required for the binary tape editing and update program, SETUP. Some of the modules are listed twice since they must be loaded twice for use by SETUP.

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GROUP IDENTIFICATION (IF ANY)	REQ'D IN ALL SYSTEMS	PROGRAM NAME	REMARKS
Z		SPCALL	
Z		SPOLY1	
Z		STPV4	
Z		IERROR	
Z		MCTDK	
Z		GETPAG	
Z		CONPRT	
Z		REDCON	
Z		CONDEC	
Z		ORDERM	
Z		IREAD	
Z		ASCOUT	
Z		PARAMS	
Z		DISKIO	
Z		SPOLY2	
Z		SUP	
Z		IERROR	
Z		GETPAG	
Z		BTOA	
Z		ISTAT	
Z		SCIO	
Z		SCRD	
Z		REDCON	
Z		ICAT	
Z		BUFIN	
Z		MOVE	
Z		IREAD	
Z		ASCOUT	
Z		PARAMS	
Z		DISKIO	

The modules in Group AB are used by the small computer maintenance monitor, SCMM. Each module provides an SCMM test for one or more hardware devices.

373

GROUP IDENTIFICATION
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM NAME

REMARKS

AB

SCMTTY

SCMTTY is included if SCMM is in the system. SCMTTY contains the SCMM test for the comment device.

AB

SCMCRD

SCMM test for the 1729-3 card reader, the 1728/430 card reader, or the 1729-2 card reader.

AB

SCM405

SCMM test for the 1726/405 card reader or the 1726/1706/405 buffered card reader.

AB

SCMCD1

SCMM test for the 1733-2/856-2/856-4 cartridge disk or the 1739-1 cartridge disk.

AB

SCMCD2

Included with SCMCD1 if there is more than one 1733-2/856-2/856-4 cartridge disk unit in the system. Using both SCMCD1 and SCMCD2 overlap seek can be used.

AB

SCMDK1

SCMM test for the 1733-1/853/854 disk or the 1738/853/854 disk.

AB

SCMDVP

Contains variable positioning seek test and two position seek test for the 1733-1/853/854 disk or the 1738/853/854 disk.

AB

SCMDK2

Included if more than one 1733-1/853/854 disk unit or more than one 1738/853/854 disk unit is in the system. With SCMDK1 and SCMDK2 overlap seek can be used in the tests.

AB

SCMPRT

SCMM test for any of the following:

1742-30 Line Printer

1742-120 Train Printer

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GROUP IDENTIFICATION
{IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

REMARKS

AB

SCMPTT

1740/501 Line Printer

AB

SCMDRM

1742-1 Line Printer

AB

SCMDM1

SCMM tests for magnetic tape.

AB

SCMPTR

SCMM test for the 1752 drum.

SCMM test for the 1751 drum.

SCMM test for the 1713 paper

tape reader or for the 1721/

1722/1777 paper tape reader

driver.

AB

SCMPTP

SCMM test for the 1713 paper

tape punch or the 1723/1724/1777

paper tape punch.

X

LIBILD

X

LIBIDO

X

CONVRS

X

MESSY

X

LJA2B

X

MOVECH

X

PICKUP

X

IOSUB

X

HELPER

X

MOVECH

X

HELPO

X

HELP1

X

HELP2

X

HELP3

X

HELP4

X

HELPS

X

HELP8

X

HELP9

X

HELP10

X

HELP11

X

HELP12

X

HELP13

X

HELP14

Library Builder modules.

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GROUP IDENTIFICATION {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	REMARKS
	X	SKED	Skeleton editor modules
	X	SKFILE	
	X	SILP	
	X	CONTRL	System initializer loader program.
	X	ILOAD	
	X	LDRTBL	
	X	I1	
	X	I2	
	X	Q1711	

Groups AC, AD, AE and AF pertain to system initializer modules. The set of modules selected from these groups are the same ones which are included in the bootstrap version of the system initializer which is the first file of the installation materials. The particular modules selected depend on the information specified by the user in the 1700 MSOS Ordering Form.

If a line printer is to be used by the system initializer as the list device, one of the modules in Group AC must be included. {If a line printer is not to be used, Q1711 supplies the teletypewriter driver used by the system initializer as the list device.}

AC		Q42312	Provides the system initializer driver for a 1742-30 or 1742-120 Line Printer. If the 1742-120 Line Printer is used, the module T5954 must be included to load the train image.
		T5954	See remarks for Q42312.
AC		Q40421	Provides the system initializer driver for a 1740/501 or 1742-1 Line Printer.
	X	IDRIV	System initializer required module

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GROUP IDENTIFICATION {IF ANY}

REQ'D IN ALL SYSTEMS

PROGRAM NAME

REMARKS

One of the modules in Group AD is required as an input driver for the System Initializer.

AD

QMT7TK

Provides the system initializer driver for 7-track magnetic tape. This module is required if 7-track magnetic tape is to be used by the system initializer.

AD

QMT9TK

Provides the system initializer driver for 9-track magnetic tape. This module is required if 9-track magnetic tape is to be used by the system initializer.

AD

QCARD

Provides the system initializer card reader driver. This module together with either CR026 or CR029 must be included if cards are to be used for system installation.

CR026

Table used for converting Hollerith {026} punched code to ASCII. {See QCARD remarks.}

CR029

Table used for converting EBCDIC {029} punched code to ASCII. {See QCARD remarks.}

AD

QPTAPE

Provides the system initializer driver for paper tape. This module is required if paper tape is to be used for system installation.

X

MDRIV

Required system initializer module.

One of the modules in Group AE is required to provide a mass memory driver for the system initializer.

AE

Q17332

System initializer driver for the 1733-2/856-2/856-4 Cartridge Disk.

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GROUP IDENTIFICATION {IF ANY}	REQ'D IN ALL SYSTEMS	PROGRAM NAME	REMARKS
AE		QDK85X	System initializer driver for the 1733-1/853/854 Disk or the 1738/853/854 Disk.
AE		Q17391	System initializer driver for the 1739-1 Cartridge Disk.
AE		Q1752	System initializer driver for the 1752 Drum.
AE		Q1751	System initializer driver for the 1751 Drum.

The modules in Group AF are dummy programs. A module in Group AF should be included only if the system initializer driver for the corresponding device is not included. The purpose of including a dummy module is to avoid having unpatched externals for unused devices printed at the time of system installation.

AF		QPRDMY	Dummy module for line printer.
AF		QMTDMY	Dummy module for magnetic tape.
AF		QCDDMY	Dummy module for cards.
AF		QPTDMY	Dummy module for paper tape.
		SMM1	System Maintenance Monitor {SMM} bootstrap to read an absolutized binary program from a 7-track magnetic tape.
		SMM2	SMM bootstrap to read an absolutized binary program from a 9-track magnetic tape.
		SMM3	SMM bootstrap to read an absolutized binary program from a paper tape.

GROUP
IDENTIFICATION
{IF ANY}

REQ'D
IN ALL
SYSTEMS

PROGRAM NAME

REMARKS

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SMM4

SMM bootstrap to read an
absolutized binary program from a
1728-430 card reader. It may
read from a 1726-405 card
reader if the function code is
changed.

2.2 MSOS SKELETON

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The system skeleton is a file which consists of requests to the install file builder program, LIBILD. These requests specify the order and identification of the binary programs that are to be retrieved from a set of binary programs and included in an install file that is being built. The skeleton contains no binary programs itself, merely commands which specify which binaries are to be retrieved. Given an install file, the skeleton can be extracted from the install file and modified by using the skeleton editor program, SKED. By using SKED, the skeleton may then be output on cards, magnetic tape, or paper tape, to be used by LIBILD to create a new install file.

Each module to be included in the system has a corresponding *B statement in the skeleton, for example:

```
*B 'SYSDAT'
```

In addition to the *B statements, a given skeleton may include LIBILD control statements, LIBEDT control statements, system initializer control statements, and other MSOS control statements.

System parameter values may be modified by the use of *S system initializer control statements. These statements may be included in the system skeleton, or they may be typed in from the comment device at the beginning of system installation before the

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*V control statement. A *S statement which is typed in from the comment device defining the value of a parameter overrides any *S statement defining that parameter in the skeleton. For instance if the statement *S,SYSMON,¢3039 appeared in the skeleton and the operator entered *S,SYSMON,¢3130 at the beginning of installations, the parameter SYSMON would have the value ¢3130. It is important to note that system parameters which are entries in SYSDAT may not be modified in this way. This is because a *S statement and a SYSDAT entry point defining the same parameter would cause duplicate definitions of an entry point and thus cause SYSDAT not to be loaded. The following is a list of comments about certain of the control statements included within a skeleton.

- | | |
|-------------|-----------------------------------------------------------------------------------|
| *S,SYSMON,m | The hexadecimal value, m, is the ASCII code for the month of system construction. |
| *S,SYSDAY,d | The hexadecimal value, d, is the ASCII code for the day of system construction. |

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*S,SYSYER,y

The hexadecimal value, y, is the ASCII code for the last two digits of the year of system construction.

*S,SYSLVL,l

The hexadecimal value, l, is the ASCII code for the PSR level of the system.

*S,ENDOV4,n

The hexadecimal value, n, is the last core location in Part 0. The value of n must be such that $n \leq 7FFF_{16}$. This is because blank common is in Part 0 and FORTRAN requires common locations to be specified by only 15 bits.

In a system with partitioned core the value of ENDOV4 is one less than the starting address of partitioned core. In a system without partitioned core the value of ENDOV4 is equal to BGNMON-1.

Referring to Figure 2-1, there are two types of system changes which effect the value of ENDOV4. First, ENDOV4 must be modified if the size of partitioned core is to be changed. Second, the addition or deletion of any core resident modules which effect BGNMON effect ENDOV4. For example, if BGNMON is decreased by 400_{16} words, and the size of partitioned core is to remain the same, then ENDOV4 must also be decreased by 400_{16} words. The conditions for changing BGNMON follow.

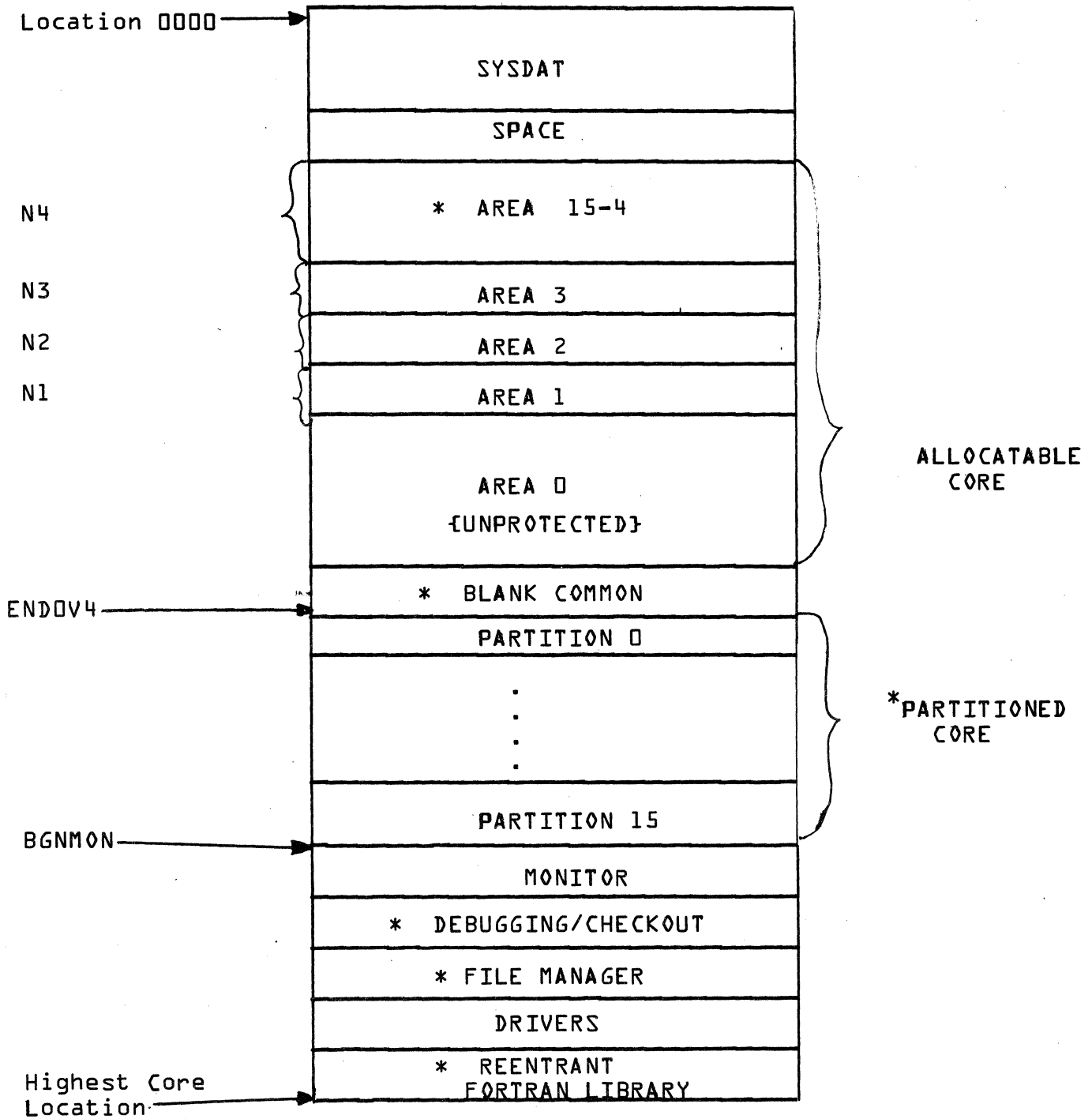


FIGURE 2-1 Core Memory Arrangement

* Optional Items

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*S,BGNMON,b

The hexadecimal value, b, is the location of the first word of the monitor.

The value of BGNMON must be changed if any of the following are added, deleted, or changed in size:

- Monitor modules
- Debugging/checkout modules
- File Manager core resident routines
- Core resident drivers
- Reentrant FORTRAN library modules

{Refer to Figure 2-1}

If a routine of unknown length is to be added, the necessary decrease in the value of BGNMON may be determined by using LIBILD to extract the routine from the MSOS binary library and then using EESORT to list its length. For example, suppose the File Manager is to be added. Further suppose that the user does not have listings of the File Manager core resident modules. To determine the lengths of these modules he first prepares the following LIBILD control records {on tape or cards}:

```
*B          'FILMGR'  
*B          'RSPCV4'  
*B          'SRHFIS'  
*T  
*END
```

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The MSOS binary library is then loaded into an input device. Typing on the teletype would be as follows if the MSOS library is to be read from logical unit 6, the LIBILD control records are to be read from the standard input device, logical unit 10, and the LIBILD output is to be written on logical unit 13.

MI

*PATCH
L,10 FAILED 06

ACTION
CL

J

*JOB

J

*LIBILD
CONTROL LU =
DEFS LU =
INSTALL LU = 13
NEWLIB LU =

LIB 01 LU = 6
LIB 02 LU =

SKELETON LU = 10

LOAD SKEL/INSTAL, CR WHEN READY

LIBRARY BUILD COMPLETE
TYPE *Z TO TERMINATE OR

TYPE *C TO CONTINUE WITH CURRENT SKELETON AND/OR
OUTPUT LIBRARY LU'S *Z

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At this point a short install file has been generated. It includes a binary copy of FILMGR, RSPCV4, and SRHFIS. Following the SRHFIS binary is a *T record. This record is needed to terminate input to EESORT. Typing on the teletype continues as follows:

```
J  
*PEW,13  
-----  
J  
*K,I13  
-----  
J  
*EESORT  
ENTER LIST FOR LIST ONLY, SORT FOR LIST AND SORTLIST  
J
```

The output from EESORT is shown in Figure 2-2.

LISTING OF PROGRAM NAMES, ENTRY POINTS AND EXTERNALS

PROGRAM NAME	PROGRAM LENGTH	COMMON SIZE	DATA SIZE	NAME COMMENTS	CARD
FILMGR	0225	0000	0000	DECK-ID F01	MSOS 4.1

PROGRAM ENTRY POINTS

DEFFIL	RELFIL	DEFIDX	LOKFIL	UNLFIL	STOSFQ	STODIR
STOIDX	RTVSEG	RTVIDR	RTVIDX	RTVIDC	FMPFLG	FMRERR
ARSPAR	CKCFIS	CKPRCT	CKUADR	GETSPC	MMREAD	MMWRIT
FILREQ	ACTREG	ARGPRO	RPTABL	FILNBR	FSTLOC	FIDLOC
RPTINT	FDTINT	NWFISD	NWFISR			

PROGRAM EXTERNALS

ENDOV4	RSAFLG	FIBSIZ	TMRTYP	FIDSEC	FCTPER	RPTPER
MAXMMA	FISLU	SWAPCK	UNFIO	LPTRS	LCCF	RACSPC
FMREND	FMRP12	FMRP11	FMRP10	FMRP09	FMRP08	FMRP07
FMRP06	FMRP05	FMRP04	FMRP03	FMRP02	FMRP01	

PROGRAM NAME	PROGRAM LENGTH	COMMON SIZE	DATA SIZE	NAME COMMENTS	CARD
RSPCV4	00F0	0000	0000	DECK-ID F02	MSOS 4.1

PROGRAM ENTRY POINTS

FSLMMA	RACSPC	RSAFLG			
--------	--------	--------	--	--	--

PROGRAM EXTERNALS

FISLU	FSELEND	FSELLTH	FSLIST	FDTINT	FIDLOC	TMRTYP
FSTLOC	FILNBR	RPTABL	ARGPRC	ACTREG	FILREQ	

FIGURE 2-2. EESORT Output Listing Module Length

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PROGRAM NAME	PROGRAM LENGTH	COMMON SIZE	DATA SIZE	NAME CARD COMMENTS
SRHFIS	0189	0000	0000	DECK-ID F03 MSOS 4.1

PROGRAM ENTRY POINTS

SRHFIS FILBLK NWFBLK FIBSIZ

PROGRAM EXTERNALS

MMREAD GETSPC FMRERR NWFISB NWFISD FDTINT FIDLOC
FSTLOC FIDSEC FISLU
*T

FIGURE 2-2 Continued

As seen from the EESORT output, the lengths of the modules FILMGR, RSPCV4, and SRHFIS are 225₁₇, F0₁₆, and 1B9₁₆, respectively. Thus the total space needed for the File Manager core resident modules is the sum of these three lengths, 4CE₁₆. When adding these modules it is necessary to decrease both BGNMON and ENDOV4 by 4CE₁₆. When adding these modules it is necessary to decrease both BGNMON and ENDOV4 by 4CE₁₆.

An alternate method of obtaining the total length of a number of modules is to use the *L job processor command, as shown in Article B.3.2.

S,MSIZV4,s

The hexadecimal value, s, is the number of words of core available in the system hardware.

*S,SECTOR,c

The hexadecimal value, c, is the maximum sector number which may be used in addressing mass memory. SECTOR is equated to MAXSEC in SYSDAT.

*S,N1,P
*S,N2,P
*S,N3,n3
*S,N4,n4



These statements set the values of core allocation parameters. Refer to Article 1.58. In addition to the considerations in Article 1.58, it may be necessary to decrease the value of N4 when customizing the system. If a program library program or file is added to the system which is larger than the previous unprotected area then N4 must be decreased. This decreases the size of allocatable area 4 and increases the size of unprotected core.

*S,FMRP01,S
*S,FMRP02,S
. .
*S,FMRP11,S
*S,FMRP12,S



FMRP01 is set to the number of the last sector used by the first file manager request processor, FMRP02 is set to the number of the last sector used by the second file manager request processor, etc.

*S,BEGFMS,S

The starting sector of File Manager space is defined by this statement under certain conditions. Refer to Article 1.79.

GENERAL PROCEDURE FOR SYSTEM MODIFICATION

The system modifications that will be discussed involve the addition or deletion of one or more modules from the system. An individual module could be changed internally by a user who has a good understanding of the module's function. It is not, however, the purpose of this manual to discuss all possible changes which might be made to individual program modules. In order to add or delete program modules, three of the items which may be used are the system install file, the set of system binaries and the MSOS COSY. These three items are defined as follows:

1. The system install file consists of one binary copy of each module in a particular MSOS installation, together with system initializer control statements and LIBEDT control statements. The system install file provides the information needed to build a particular 1700 MSOS system. This information consists of two parts, that used by the system initializer and that used by LIBEDT. An install file is a part of the installation materials for every installation. It is provided on cards, magnetic tape or paper tape depending on the particular installation.

2. The set of MSOS binaries is a complete set of the currently available 1700 MSOS program modules, each in binary form. This set differs from the install file in two ways:
 - a) A module is included, even though it may not be present in the particular MSOS system.
 - b) No control cards are present.The set of MSOS binaries is supplied with every installation. Binaries of FORTRAN modules are included if the user has a FORTRAN compiler license. The binaries are provided on cards, magnetic tape, or paper tape, depending on the particular installation.

- 3. The MSOS COSY is a complete set of the available 1700 MSOS program modules, each in compressed source {COSY} format. An MSOS COSY is not supplied with the installation materials unless specified by the user. In general, COSY is not used in system modification. The two instances in which the MSOS COSY is used are as follows:
 - a. when adding the background FORTRAN Limited Input/Output Library to the system
 - b. when performing PSR updates prior to the quarterly release of binary updates.

In an MSOS COSY, each module is preceded by a COSY identifier. The MSOS COSY may be on cards, magnetic tape or paper tape.

The general procedure for system modification includes the following steps. More details, dependent on the type of modification being made, are included in chapters 4 through 8.

MATERIALS USED	METHODS INCLUDING MSOS PACKAGES WHICH MAY BE USED	RESULT
① SYSDAT Source	Use of keypunch to modify source cards if cards can be used in system. {LIBEDT may have to be used first to transfer SYSDAT source from tape to cards.} If cards cannot be used, COSY can be used to modify SYSDAT source.	Modified SYSDAT Source.

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MATERIALS USED	METHODS INCLUDING MSOS PACKAGES WHICH MAY BE USED	RESULT
② Modified SYSDAT Source	Macro Assembler	Binary copy of modified SYSDAT
③ Install File	SKED	Modified system skeleton
④ a. Modified system skeleton b. MSOS binaries {including FORTRAN binaries in some cases} c. Binary copy of modified SYSDAT d. Update binaries {if system is being updated}	LIBILD	Modified install file
⑤ Modified install file	System Initializer	Modified MSOS on mass memory

If a number of major modifications are to be made to a system the user may wish to consider re-ordering the system. This may be done by completing a 1700 MSOS 4 Ordering Form.



Chapter 4 - Procedure for Adding a Driver

4.1 SYSDAT Changes

The SYSDAT source must be modified in the following ways when adding a driver. In each case, the user is referred to the discussion of the corresponding SYSDAT section in Chapter 1.

1. The interrupt trap region, Section AAF, must be modified to include an entry for the device being added.
2. The interrupt mask table, Section AAH, must be modified.
3. The size of the interrupt stack, Section AAK, must be increased if necessary. If the size has been left as the standard size of 16 entries, it will be unnecessary to modify Section AAK.
4. An entry for the logical unit of the added device must be added to the LOG1A Table, Section AAM. If there is to be a diagnostic logical unit for the added device, an entry for the diagnostic logical unit must be added to the LOG1A Table also.
5. The LOG1 Table, Section AAN, must be modified.
6. The LOG2 Table, Section AA0, must be modified.
7. An entry must be added to the diagnostic timer table in Section AAP if the diagnostic timer is to be used with the added device.
8. The line one table, Section AAR, must be modified only if the added device is connected to interrupt line one.
9. A physical device table must be added for the new device.
10. It may be necessary to modify the 170b buffered data channel handler entries, Section ACP, if the device added is a buffered device.
11. It will be necessary to modify Section ACQ, A/Q Channel Allocation, if the new device is one of those which requires A/Q channel allocation.
12. Section ACR must be modified only if a mass resident driver is being added to a system which previously contained no mass resident driver.

4.2 Skeleton Changes

The skeleton must be modified to add a *M statement. In addition, a *B statement for each driver module must be added. If the driver requires one

or more auxiliary modules for conversion code tables or a train image table, a *B statement for each of these must also be added. If the driver being added is to be mass resident, two control statements must follow the *B statement for the last module required by the driver. These *B statements are as follows. The name of each MSOS driver has the form

$$Dx_1x_2x_3x_4x_5$$

where x_1, x_2, x_3, x_4, x_5 are alphanumeric characters which will include one or more trailing blanks if the name of the driver contains fewer than six characters. The two control statements added for a mass resident driver have the following forms:

$$*S, Sx_1x_2x_3x_4x_5, S$$

$$*S, Lx_1x_2x_3x_4x_5, P$$

These statements are interpreted by the system initializer so that $Sx_1x_2x_3x_4x_5$ is the sector number of the last sector containing a module required by the driver, $Dx_1x_2x_3x_4x_5$, and $Lx_1x_2x_3x_4x_5$ is the word length of the driver including all necessary modules. For example, if the driver D1777P is to be added as a mass resident driver, the following control statements must be added:

$$*S, S1777P, S$$

$$*S, L1777P, P$$

When adding a driver, the corresponding SCMM test module should be added if it is not already in the system. The SCMM test modules are listed in Group AB of Chapter 2. Each test module has a name in the form, $SCMa_1a_2a_3$, for example, SCMCRD. To add an SCMM test module, seven records must be added to the SCMM program library entries in the skeleton. These records are as follows:


```
*K,I
*K,PB
*P,F
*B 'SCMa1a2a3'
*T
*K,IB
*N,SCMa1a2a3,,B
```

These records instruct LIBEDT to accept the binary for SCMa₁a₂a₃ from the standard input device, absolutize it, and output on the library unit, logical unit B. The *T record indicates the end of the binary programs to be absolutized. The *K,IB control statement indicates input is the absolutized information output to the library unit. The *N statement instructs LIBEDT to store this information in a file named SCMa₁a₂a₃.

The System Maintenance Monitor bootstrap corresponding to the added hardware device may be added by inserting a *L statement and a *B statement into the SMM bootstraps program library entries in the skeleton. These entries are of the form

```
*L,SMMa1a2a3
*B 'SMMn'
```

Where a₁a₂a₃ is the mnemonic for the added test and n indicates which SMM bootstrap is to be added. {Refer to Chapter 2.}

4.3

Example of Driver Addition Using Magnetic Tape

To illustrate the addition of a driver, an example will be considered in detail. In this example, the user has a system including magnetic tape, but no cards. The logical units in the system are as follows:

1	CORE ALLOCATOR
2	DUMMY LOGICAL UNIT
3	DUMMY LOGICAL UNIT
4	1711 TELETYPE, 713-10 CRT
5	COSY DRIVER, FIRST UNIT
6	UNBUFFERED 1731-601 MAG TAPE, UNIT 0
7	DUMMY LOGICAL UNIT
8	1738 853/4 DISK, UNIT 0
9	1740-501/1742 LINE PRINTER
10	UNBUFFERED 1731-601 MAG TAPE, UNIT 0
11	UNBUFFERED 1731-601 MAG TAPE, UNIT 0
12	1740-501/1742 FORTRAN LINE PRINTER
13	UNBUFFERED 1731-601 MAG TAPE, UNIT 1
14	DIAGNOSTIC 1711 TELETYPE, 713-10 CRT
15	DIAGNOSTIC 1738 853/4 DISK, UNIT 0
16	DIAGNOSTIC 1740-501/1742 LINE PRINTER
17	DIAGNOSTIC 1731-601 MAG TAPE, UNIT 1
18	DIAGNOSTIC 1731-601 MAG TAPE, UNIT 0

The user wishes to add the capability of using cards by connecting a 1728/430 Card Reader/Punch to his system hardware configuration. To use this device he must add the driver D1728. In the example, this driver will be added as a mass resident driver.

4.3.1 Changing SYSDAT by Using COSY

According to the procedure outlined in Chapter 3, the first step is to modify SYSDAT. This will be done by converting the SYSDAT source on magnetic tape to COSY form and then using COSY to make the modifications. A source copy of SYSDAT is the fourth file of the installation tape. To position the tape to the beginning of SYSDAT, it is necessary to advance past the first three files on the tape. This may be done by using 0DEBUG. The user mounts the installation tape on tape unit 0, logical unit 10. After an autoload, the printing on the comment device appears as follows:

```
MI
DB
DEBUG IN
ADF,10,3
NEXT
OFF
DEBUG OUT
```

The following procedures are simplified by changing the standard input device from logical unit 10 to logical unit 4.

This may be done by changing the contents of the word $F9_{16}$ in core and in the mass memory core image from A_{16} to 4. {Refer to Article 1.5}. The following discussion assumes this change has been made. If the change is not made, logical unit 10 must be unready each time *BATCH is entered as a control statement.

A COSY tape of SYSDAT is needed. It is also necessary to have an ASCII listing of SYSDAT which includes the COSY sequence numbers. The COSY sequence numbers will be used when revising SYSDAT. These sequence numbers will be different from the sequence numbers in the original SYSDAT and will not include the section identifier in columns 74-76. To convert SYSDAT to an acceptable COSY input format it is necessary to write a DCK/identifier and a Hollerith identifier preceding the SYSDAT source information. It is also necessary to write an END/record after the end of the SYSDAT source information. To do this the CYFT program may be used. CYFT will be used to transfer the source of SYSDAT to magnetic tape unit one, adding the necessary COSY control records.

The user mounts a scratch tape on magnetic tape unit 1 {logical unit 13}. The tape on logical unit 10 is positioned at the beginning of SYSDAT.

The printing on the comment device is as follows:

```

MI
*BATCH
J
*JOB
J
*K, I10, P13
J
*CYFT
J

```

To convert the SYSDAT with COSY control cards on logical unit 13 to COSY, the installation tape is removed from logical unit 10 and a scratch tape is mounted in its place. The printing on the comment device is then as follows:

```
*REW, 13
J
*K, I13
J
*CSY, I13, P10
J
*COZY
J
```

To obtain a SYSDAT listing with COZY sequence numbers the typing on the comment device is as follows:

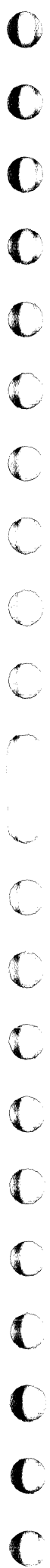
```
*K, I5, P2
J
*CSY, I10
J
*ASSEM
J
```

The above assigns the COZY driver, logical unit 5, to be the standard input device. Thus, the assembler receives its input from the COZY driver. The COZY driver, in turn, receives its input from logical unit 10.

The actual SYSDAT corrections may now be made. {These corrections are discussed in Section 4.3.2.} The COSY of SYSDAT is on tape unit 0. A scratch tape is mounted on tape unit 1. Typing on the teletype continues as follows:

(401)

	*K, I4			
	J			
	*CSY, I10, P13			
	J			
	*COSY			
	SYS DAT DCK / I.C			
	DEL / 399,400			
	NUM 14			
	ADC R1728			
	DEL / 444,481			
	NUM \$089B			
MASKT	NUM \$089B			
	NUM \$089B			
	NUM \$089B			
	NUM \$089B			
	NUM \$089B			
	NUM \$089B			
	NUM \$089B			
	NUM \$089B			
	NUM \$0893			
	NUM \$0883			
	NUM \$0883			
	NUM \$0883			
	DEL / 628,629			
	ADC P1728	10		1728/430 CARD READER
	ADC P1728	11		1728/430 CARD PUNCH
	INS / 636			
X1728	ADC P1728	19		DIAGNOSTIC 1728/430 CARD READER/PUNCH
	DEL / 659,660			
	ADC 0+S	10		1728/430 CARD READER
	ADC 0+S	11		1728/430 CARD PUNCH
	INS / 667			
	ADC 0+S	19		DIAGNOSTIC 1728/430 READER/PUNCH
	DEL / 684,685			
	NUM \$FFFF	10		1728/430 CARD READER
	NUM \$FFFF	11		1728/430 CARD PUNCH
	INS / 692			
	NUM \$FFFF	19		DIAGNOSTIC 1728/430 READER/PUNCH
	INS / 709			
	ADC P1728	10		1728/430 CARD READER
	INS / 1098			



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* 1728/430 CARD READER/PUNCH

EQU U1728(X1728-LOG1A)

EQU M1728(1)

EXT MASDRV

EXT MASCON

EXT MASERR

I1728 JMP+ MASDRV

C1728 JMP+ MASCON

E1728 JMP+ MASERR

EXT L1728

EXT S1728

R1728 LDC =XP1728

JMP* (P1728+2)

EJT

* 1728/430 CARD READER/PUNCH

P1728 ADC \$520E

ADC I1728

ADC C1728

ADC E1728

NUM -1

NUM 0

NUM 0

NUM \$05A1

NUM \$18C6

NUM 0

NUM 0

NUM 0

NUM 0

ADC L1728

ADC S1728

NUM 0

NUM \$800F

ADC BUF28

NUM 0

NUM 0

NUM 0

NUM 0

NUM 0

NUM 0

NUM 0

NUM 0

NUM 0

ADC I11728

BZS BUF28(80)

EJT

DEL/ 1263

EQU NUMAQ(3)

END/

Each of the preceding COSY corrections was preceded by a line feed so that the typing would be readable. Line feeds are ignored by the COSY driver.

A binary copy of the corrected SYSDAT is obtained by requesting the Macro Assembler to process the corrected COSY copy of SYSDAT. A scratch tape is mounted on tape unit 0. The SYSDAT binary will be written on this tape. Typing on the comment device continues as follows:

```
*K,I5,P13
```

```
J
```

```
*CSY,I13,P2
```

```
*J
```

```
*ASSEM
```

```
J
```

An assembly listing is obtained on the line printer.

If there are further corrections to be made to SYSDAT, these may be made by repeating the above procedure, substituting the COSY of the revised SYSDAT for the COSY of the original SYSDAT.

4.3.2 Specific SYSDAT Changes

The following sections of SYSDAT were changed or added in order to add the 1728/430 Card Reader driver:

Interrupt Trap Region, Section AAF

Interrupt Mask Table, Section AAH

LOG1A Table, Section AAM

LOG1 Table, Section AAN

LOG2 Table, Section AA0

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Diagnostic Tables, Section AAP
 1728/430 Card Reader/Punch Physical
 Device Table, Section ABR
 A/Q Channel Allocation, Section ACQ

The changes in each of these sections are shown in the following printouts.

• Interrupt Trap Region, Section AAF

As shown in Article 1-6, Figure 1-6, the standard interrupt line for the 1728/430 Card Reader is line 11. Following is the line 11 entry in Section AAF before modification. (The sequence numbers are COSY sequence numbers)

*			00396
LINE11	NUM 0	INTERRUPT LINE ENTRY	00397
	RTJ- (3FE)	GO TO INTERRUPT HANDLER ROUTINE	00398
	NUM 0	PRIORITY LEVEL OF INTERRUPT	00399
	ADC INVINT	INTERRUPT RESPONSE FOR THE INVALID INTERRUPTS	00400

The line 11 entry in Section AAF after modifications is as follows:

*			00396
LINE11	NUM 0	INTERRUPT LINE ENTRY	00397
	RTJ- (3FE)	GO TO INTERRUPT HANDLER ROUTINE	00398
	NUM 14		00399
	ADC R1728		00400

- Interrupt Mask Table, Section AAH

Information regarding standard priority levels is contained in Figure 1-6, Article 1.6. The standard priority level for the 1728/430 Card Reader driver is level 14. The 1728/430 interrupt should be permitted at priority levels 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13. As explained in Article 1.8, the interrupt is not permitted at level 14, since the driver runs at level 14 and the driver is not re-entrant. The interrupt mask table prior to modification is as follows:

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 (406)

INTERRUPT MASK TABLE

ENT MASK INTERRUPT MASKS INDEXED BY PRIORITY LEVEL

		----- INTERRUPT LINE NUMBER																
		15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
*****		*****																
P	-1	*	0	0	0	0	0	0	0	1	0	0	1	1	0	1	1	*
R	0	*	0	0	0	0	0	0	0	1	0	0	1	1	0	1	1	*
I	1	*	0	0	0	0	0	0	0	1	0	0	1	1	0	1	1	*
O	2	*	0	0	0	0	0	0	0	1	0	0	1	1	0	1	1	*
R	3	*	0	0	0	0	0	0	0	1	0	0	1	1	0	1	1	*
I	4	*	0	0	0	0	0	0	0	1	0	0	1	1	0	1	1	*
T	5	*	0	0	0	0	0	0	0	1	0	0	1	1	0	1	1	*
Y	6	*	0	0	0	0	0	0	0	1	0	0	1	1	0	1	1	*
	7	*	0	0	0	0	0	0	0	1	0	0	1	1	0	1	1	*
L	8	*	0	0	0	0	0	0	0	1	0	0	1	1	0	1	1	*
E	9	*	0	0	0	0	0	0	0	1	0	0	1	1	0	1	1	*
V	10	*	0	0	0	0	0	0	0	1	0	0	0	0	0	1	1	*
E	11	*	0	0	0	0	0	0	0	1	0	0	0	0	0	1	1	*
L	12	*	0	0	0	0	0	0	0	1	0	0	0	0	0	1	1	*
.	13	*	0	0	0	0	0	0	0	1	0	0	0	0	0	1	1	*
.	14	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	*
V	15	*	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*
*****		*****																

00438
 00439
 00440
 00441
 00442
 00443
 00444
 00445
 00446
 00447
 00448
 00449
 00450
 00451
 00452
 00453
 00454
 00455
 00456
 00457
 00458
 00459
 00460
 00461
 00462
 00463
 00464
 00465
 00466
 00467
 00468
 00469
 00470
 00471
 00472
 00473
 00474
 00475
 00476
 00477
 00478
 00479
 00480
 00481
 00482
 00483

MASKT	NUM	\$009R	PRIORITY LEVEL -1
	NUM	\$009R	PRIORITY LEVEL 00
	NUM	\$009R	PRIORITY LEVEL 01
	NUM	\$009B	PRIORITY LEVEL 02
	NUM	\$009R	PRIORITY LEVEL 03
	NUM	\$009R	PRIORITY LEVEL 04
	NUM	\$009R	PRIORITY LEVEL 05
	NUM	\$009R	PRIORITY LEVEL 06
	NUM	\$009R	PRIORITY LEVEL 07
	NUM	\$009R	PRIORITY LEVEL 08
	NUM	\$0093	PRIORITY LEVEL 09
	NUM	\$0083	PRIORITY LEVEL 10
	NUM	\$0083	PRIORITY LEVEL 11
	NUM	\$0083	PRIORITY LEVEL 12
	NUM	\$0083	PRIORITY LEVEL 13
	NUM	\$0001	PRIORITY LEVEL 14
	NUM	\$0000	PRIORITY LEVEL 15

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After modification, the bits set in the masks for priority levels -1, 0, 1, 2, ... 12, 13 include the bits previously set as well as bit 11 corresponding to interrupt line 11 for the 172B/430 Card Reader. The mask table after modification is as follows. Note that lines 444-466 of the original table were deleted so as to avoid misleading comments in the listing. The new sequence numbers are those assigned by COSY.

```
*      I N T E R R U P T   M A S K   T A B L E
*
*
*      ENT   MASKT           INTERRUPT MASKS INDEXED BY PRIORITY LEVEL
*
*
MASKT  NUM  $0898
NUM    NUM  $0898
NUM    NUM  $0898
NUM    NUM  $0898
NUM    NUM  $0898
NUM    NUM  $0898
NUM    NUM  $0898
NUM    NUM  $0898
NUM    NUM  $0898
NUM    NUM  $0898
NUM    NUM  $0893
NUM    NUM  $0883
NUM    NUM  $0883
NUM    NUM  $0883
NUM    NUM  $0883
NUM    NUM  $0883
NUM    NUM  $0801           PRIORITY LEVEL 14
NUM    NUM  $0000           PRIORITY LEVEL 15
```

ENT	MASKT	INTERRUPT MASKS INDEXED BY PRIORITY LEVEL
		00438
		00439
		00440
		00441
		00442
		00443
		00444
		00445
		00446
		00447
		00448
		00449
		00450
		00451
		00452
		00453
		00454
		00455
		00456
		00457
		00458
		00459
		00460

• LOG1A Table, Section AAM

Before modification the LOG1A table is as follows:

* LOGICAL UNIT TABLES (LOGIA) *				00612
				00613
				00614
				00615
				00616
ENT LOGIA PHYSICAL DEVICES ADDRESSES BY LOGICAL UNIT				
				00618
				00619
				00620
				00621
				00622
				00623
				00624
				00625
				00626
				00627
				00628
				00629
				00630
				00631
				00632
				00633
				00634
				00635
				00636
				00637
LOGIA	ADC	NUMLU	NUMBER OF LOGICAL UNITS	
	ADC	PCORE	1 CORE ALLOCATOR	
	ADC	PDUMMY	2 DUMMY LOGICAL UNIT	
	ADC	PDUMMY	3 DUMMY LOGICAL UNIT	
	ADC	P1711	4 1711 TELETYPE, 713-10 CRT	
	ADC	PCOSY1	5 COSY DRIVER, FIRST UNIT	
	ADC	P731U0	6 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	
	ADC	PDUMMY	7 DUMMY LOGICAL UNIT	
	ADC	P17380	8 1738 853/4 DISK, UNIT 0	
	ADC	P40421	9 1740-501/1742 LINE PRINTER	
	ADC	P731U0	10 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	
	ADC	P731U0	11 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	
FTN740	ADC	P40421	12 1740-501/1742 FORTRAN LINE PRINTER	
	ADC	P731U1	13 UNBUFFERED 1731-601 MAG TAPE, UNIT 1	
X1711	ADC	P1711	14 DIAGNOSTIC 1711 TELETYPE, 713-10 CRT	
X17380	ADC	P17380	15 DIAGNOSTIC 1738 853/4 DISK, UNIT 0	
X40421	ADC	P40421	16 DIAGNOSTIC 1740-501/1742 LINE PRINTER	
X731U1	ADC	P731U1	17 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 1	
X731U0	ADC	P731U0	18 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 0	
NUMLU	EQU	NUMLU(*-LOGIA-1)		

Three entries are to be added to the LOGIA table, one for the 1728/430 Card Reader, one for the 1728/430 Card Punch, and one for the Diagnostic 1728/430 Reader/Punch. The assignment of a logical unit number to each of these entries is dependent on the wishes of the user.

The grouped entries in Figure 1-15 are sometimes helpful in making logical unit number assignments. In this example,

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the card reader is to become the standard input device, logical unit 10, and the card punch, the standard output device, logical unit 11. The unbuffered 1732-601 Magnetic Tape, Unit 0, previously assigned to both logical units 10 and 11 is also assigned to logical unit 6. Therefore, there is no need to give magnetic tape unit 0 another logical unit number. Nineteen was chosen as the logical unit number for the diagnostic logical unit corresponding to the 1728/430 Card Reader/Punch. The modified LOG1A Table is as follows:

LOGICAL UNIT TABLES (LOG1A)

ENT	LOG1A	PHYSICAL DEVICES ADDRESSES BY LOGICAL UNIT		
ENT	NUMLU			
LOG1A	ADC	NUMLU	NUMBER OF LOGICAL UNITS	00589
	ADC	PCORE	1 CORE ALLCCATOR	00590
	ADC	PDUMMY	2 DUMMY LOGICAL UNIT	00591
	ADC	PDUMMY	3 DUMMY LOGICAL UNIT	00592
	ADC	P1711	4 1711 TELETYPE, 713-10 CRT	00593
	ADC	PCOSY1	5 COSY DRIVER, FIRST UNIT	00595
	ADC	P731U0	6 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	00596
	ADC	PDUMMY	7 DUMMY LOGICAL UNIT	00597
	ADC	P17380	8 1738 853/4 DISK, UNIT 0	00598
	ADC	P40421	9 1740-501/1742 LINE PRINTER	00599
	ADC	P1728	10 1728/430 CARD READER	00600
	ADC	P1728	11 1728/430 CARD PUNCH	00601
FTN740	ADC	P40421	12 1740-501/1742 FORTRAN LINE PRINTER	00602
	ADC	P731U1	13 UNBUFFERED 1731-601 MAG TAPE, UNIT 1	00603
X1711	ADC	P1711	14 DIAGNOSTIC 1711 TELETYPE, 713-10 CRT	00604
X17380	ADC	P17380	15 DIAGNOSTIC 1738 853/4 DISK, UNIT 0	00605
X40421	ADC	P40421	16 DIAGNOSTIC 1740-501/1742 LINE PRINTER	00606
X731U1	ADC	P731U1	17 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 1	00607
X731U0	ADC	P731U0	18 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 0	00608
X1728	ADC	P1728	19 DIAGNOSTIC 1728/430 CARD READER/PUNCH	00609
NUMLU	EQU	NUMLU(*-LOG1A-1)		00610

• LOG1 Table, Section AAN

The LOG1 Table entries must be made to correspond to the LOG1A Table entries. Before modification the LOG1 Table is as follows:

*		LOGICAL UNIT TABLES (LOG1)	00639	
*			00640	
*			00641	
ENT	LOG1	LOGICAL UNIT INFORMATION BY LOGICAL UNIT	00642	
*		BIT 14 = 1, IMPLIES LU SHARES DEVICE	00643	
*		BIT 13 = 1, IMPLIES LU IS MARKED DOWN	00644	
*		BITS 0 - 11 IS ALTERNATE LOGICAL UNIT	00645	
*		ALTERNATE = 0, IMPLIES NONE	00646	
S	EQU	S(\$4000) SHARED BIT	00647	
LOG1	ADC	NUMLU	NUMBER OF LOGICAL UNITS	00649
	ADC	0	1 CORE ALLOCATOR	00650
	ADC	0+S	2 DUMMY LOGICAL UNIT	00651
	ADC	0+S	3 DUMMY LOGICAL UNIT	00652
	ADC	2+S	4 1711 TELETYPE, 713-10 CRT	00653
	ADC	0	5 COSY DRIVER, FIRST UNIT	00654
	ADC	0+S	6 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	00655
	ADC	0+S	7 DUMMY LOGICAL UNIT	00656
	ADC	0+S	8 1738 853/4 DISK, UNIT 0	00657
	ADC	0+S	9 1740-501/1742 LINE PRINTER	00658
	ADC	0+S	10 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	00659
	ADC	0+S	11 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	00660
	ADC	0+S	12 1740-501/1742 FORTRAN LINE PRINTER	00661
	ADC	0+S	13 UNBUFFERED 1731-601 MAG TAPE, UNIT 1	00662
	ADC	0+S	14 DIAGNOSTIC 1711 TELETYPE, 713-10 CRT	00663
	ADC	0+S	15 DIAGNOSTIC 1738 853/4 DISK, UNIT 0	00664
	ADC	0+S	16 DIAGNOSTIC 1740-501/1742 LINE PRINTER	00665
	ADC	0+S	17 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 1	00666
	ADC	0+S	18 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 0	00667

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 (411)

The entries for logical units 10 and 11 need not be changed, except for the comments field. An entry for logical unit 19 must be added. After the changes have been made, the LOG1 table is as follows:

* LOGICAL UNIT TABLES (LOG1)				00617
* ENT LOG1 LOGICAL UNIT INFORMATION BY LOGICAL UNIT				00618
* BIT 14 = 1, IMPLIES LU SHARES DEVICE				00619
* BIT 13 = 1, IMPLIES LU IS MARKED DOWN				00620
* BITS 0 - 11 IS ALTERNATE LOGICAL UNIT				00621
* ALTERNATE = 0, IMPLIES NONE				00622
S	EQU	S(\$4000)	SHARED BIT	00623
S	EQU	S(\$4000)	SHARED BIT	00624
S	EQU	S(\$4000)	SHARED BIT	00625
LOG1	ADC	NMLU	NUMBER OF LOGICAL UNITS	00627
	ADC	0	1 CORE ALLCATOR	00628
	ADC	0+S	2 DUMMY LOGICAL UNIT	00629
	ADC	0+S	3 DUMMY LOGICAL UNIT	00630
	ADC	2+S	4 1711 TELETYPE, 713-10 CRT	00631
	ADC	0	5 COSY DRIVER, FIRST UNIT	00632
	ADC	0+S	6 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	00633
	ADC	0+S	7 DUMMY LOGICAL UNIT	00634
	ADC	0+S	8 1738 853/4 DISK, UNIT 0	00635
	ADC	0+S	9 1740-501/1742 LINE PRINTER	00636
	ADC	0+S	10 1728/430 CARD READER	00637
	ADC	0+S	11 1728/430 CARD PUNCH	00638
	ADC	0+S	12 1740-501/1742 FORTRAN LINE PRINTER	00639
	ADC	0+S	13 UNBUFFERED 1731-601 MAG TAPE, UNIT 1	00640
	ADC	0+S	14 DIAGNOSTIC 1711 TELETYPE, 713-10 CRT	00641
	ADC	0+S	15 DIAGNOSTIC 1738 853/4 DISK, UNIT 0	00642
	ADC	0+S	16 DIAGNOSTIC 1740-501/1742 LINE PRINTER	00643
	ADC	0+S	17 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 1	00644
	ADC	0+S	18 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 0	00645
	ADC	0+S	19 DIAGNOSTIC 1728/430 READER/PUNCH	00646

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• LOG2 Table, Section AA0

The LOG2 Table, before modification, is as follows:

* LOGICAL UNIT TABLES (LOG2)				00669
* ENT LOG2 TOP OF I/O THREAD ADDRESSES BY LOGICAL UNIT				00670
* LOG2 ADC NUMLU NUMBER OF LOGICAL UNITS				00671
				00672
LOG2	ADC	NUMLU	NUMBER OF LOGICAL UNITS	00674
	NUM	\$FFFF	1 CORE ALLOCATOR	00675
	NUM	\$FFFF	2 DUMMY LOGICAL UNIT	00676
	NUM	\$FFFF	3 DUMMY LOGICAL UNIT	00677
	NUM	\$FFFF	4 1711 TELETYPE, 713-10 CRT	00678
	NUM	\$FFFF	5 COSY DRIVER, FIRST UNIT	00679
	NUM	\$FFFF	6 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	00680
	NUM	\$FFFF	7 DUMMY LOGICAL UNIT	00681
	NUM	\$FFFF	8 1738 853/4 DISK, UNIT 0	00682
	NUM	\$FFFF	9 1740-501/1742 LINE PRINTER	00683
	NUM	\$FFFF	10 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	00684
	NUM	\$FFFF	11 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	00685
	NUM	\$FFFF	12 1740-501/1742 FORTRAN LINE PRINTER	00686
	NUM	\$FFFF	13 UNBUFFERED 1731-601 MAG TAPE, UNIT 1	00687
	NUM	\$FFFF	14 DIAGNOSTIC 1711 TELETYPE, 713-10 CRT	00688
	NUM	\$FFFF	15 DIAGNOSTIC 1738 853/4 DISK, UNIT 0	00689
	NUM	\$FFFF	16 DIAGNOSTIC 1740-501/1742 LINE PRINTER	00690
	NUM	\$FFFF	17 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 1	00691
	NUM	\$FFFF	18 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 0	00692

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The entries of the LOG2 Table must be made to correspond to those in the LOG1A Table. After modification the LOG2 Table is as follows:

LOGICAL UNIT TABLES (LOG2)

	ENT	LOG2	NUMLU	TOP OF I/O THREAD ADDRESSES BY LOGICAL UNIT	
					00648
					00649
					00650
					00651
LOG2	ADC		NUMLU	NUMBER OF LOGICAL UNITS	00653
	NUM	\$FFFF		1 CORE ALLOCATOR	00654
	NUM	\$FFFF		2 DUMMY LOGICAL UNIT	00655
	NUM	\$FFFF		3 DUMMY LOGICAL UNIT	00656
	NUM	\$FFFF		4 COST DRIVER, FIRST UNIT	00658
	NUM	\$FFFF		6 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	00659
	NUM	\$FFFF		7 DUMMY LOGICAL UNIT	00660
	NUM	\$FFFF		8 1738 853/4 DISK, UNIT 0	00661
	NUM	\$FFFF		9 1740-501/1742 LINE PRINTER	00662
	NUM	\$FFFF		10 1728/430 CARD READER	00663
	NUM	\$FFFF		11 1728/430 CARD PUNCH	00664
	NUM	\$FFFF		12 1740-501/1742 FORTRAN LINE PRINTER	00665
	NUM	\$FFFF		13 UNBUFFERED 1731-601 MAG TAPE, UNIT 1	00666
	NUM	\$FFFF		14 DIAGNOSTIC 1711 TELETYPE, 713-10 CRT	00667
	NUM	\$FFFF		15 DIAGNOSTIC 1738 853/4 DISK, UNIT 0	00668
	NUM	\$FFFF		16 DIAGNOSTIC 1740-501/1742 LINE PRINTER	00669
	NUM	\$FFFF		17 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 1	00670
	NUM	\$FFFF		18 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 0	00671
	NUM	\$FFFF		19 DIAGNOSTIC 1728/430 READER/PUNCH	00672

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• Diagnostic Tables, Section AAP

Before modification Section AAP is as follows:

*			D I A G N O S T I C T A B L E S	00694	
*				00695	
*				00696	
	ENT	ALTERR	ALTERNATE DEVICE ERROR TABLE	00697	
	ALTERR	ADC	NUMLU	ERROR TABLE SIZE	00699
		BZS	(NUMLU)	SPACE FOR MAXIMUM SIMULTANEOUS FAILURES	00700
	ENT	DGNTAB	DIAGNOSTIC TIMER TABLE	00702	
	DGNTAB	EQU	DGNTAB(*)	START OF TABLE	00704
		ADC	PCORE	1 CORE ALLOCATOR	00705
		ADC	P1711	4 1711 TELETYPE. 713-10 CRT	00706
		ADC	P731U0	6 UNBUFFERED 1731-601 MAG TAPE. UNIT 0	00707
		ADC	P17380	8 1738 853/4 DISK. UNIT 0	00708
		ADC	P40421	9 1740-501/1742 LINE PRINTER	00709
		ADC	P731U1	13 UNBUFFERED 1731-601 MAG TAPE. UNIT 1	00710
		NUM	\$FFFF	END OF TABLE	00711

An entry must be added for logical unit 10, the 1728/430 Card Reader. After this addition, Section AAP appears as follows:

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			D I A G N O S T I C T A B L E S	00674
				00675
				00676
				00677
ENT	ALTERR		ALTERNATE DEVICE ERROR TABLE	
ALTERR	ADC	N6MLU	ERROR TABLE SIZE	00679
	BZS	(NUMLU)	SPACE FOR MAXIMUM SIMULTANEOUS FAILURES	00680
ENT	DGNTAB		DIAGNOSTIC TIMER TABLE	00682
DGNTAB	EQU	DGNTAB(*)	START OF TABLE	00684
	ADC	P60RE	1 CORE ALLOCATOR	00685
	ADC	P1711	4 1711 TELETYPE, 713-10 CRT	00686
	ADC	P731U0	6 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	00687
	ADC	P17380	8 1738 853/4 DISK, UNIT 0	00688
	ADC	P00421	9 1740-501/1742 LINE PRINTER	00689
	ADC	P1728	10 1728/430 CARD READER	00690
	ADC	P731U1	13 UNBUFFERED 1731-601 MAG TAPE, UNIT 1	00691
	NUM	SFFFF	END OF TABLE	00692

• 1728/430 Card Reader/Punch Physical Device Table, Section ABR

A physical device table must be added for the 1728/430 Card Reader/Punch. This is obtained by extracting the appropriate lines from Figure 1-47, Article 1.44. There will be a diagnostic logical unit corresponding to this device.

Thus, the symbol U1728 is equated to an index into the LOG1A Table. The lines corresponding to a mass resident driver are selected. Word 16 of the table contains the standard entry. The physical device table appears as follows after it has been added to SYSDAT:

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*	1728/430 CARD READER/PUNCH	*****	01080
	EQU U1728(X1728-LOG1A)		01081
	EQU M1728(1)		01082
	EXT MASDRV		01083
	EXT MASCON		01084
	EXT MASERR		01085
I1728	JMP+ MASDRV		01086
C1728	JMP+ MASCON		01087
E1728	JMP+ MASERR		01088
	EXT L1728		01089
	EXT S1728		01090
R1728	LDQ =XP1728		01091
	JMP* (P1728+2)		01092

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1728/430 CARD READER/PUNCH

*
P1728 ADC \$520E
ADC I1728
ADC C1728
ADC E1728
NUM -1
NUM 0
NUM 0
NUM \$05A1
NUM \$18C6
NUM 0
NUM 0
NUM 0
ADC L1728
ADC S1728
NUM 0
NUM \$800F
ADC BUF28
NUM 0
NUM 0
NUM 0
NUM 0
NUM 0
NUM 0
NUM 0
NUM 0
NUM 0
NUM 0
ADC U1728
EVS BUF28(80)

01094
01095
01096
01097
01098
01099
01100
01101
01102
01103
01104
01105
01106
01107
01108
01109
01110
01111
01112
01113
01114
01115
01116
01117
01118
01119
01120
01121
01122
01123

• A/Q Channel Allocation, Section ACQ

Prior to modification Section ACQ is as follows:

*	M I S C E L L A N E O U S	P R O G R A M S	01239
*	A / Q	C H A N N E L A L L O C A T I O N	01240
*			01241
*			01242
*	DEVICES REQUIRING ALLOCATION		01244
*	1711/713	TELETYPE / CRT	01245
*	1713	KEYBOARD	01246
*	1713	PAPER TAPE READER	01247
*	1713	PAPER TAPE PUNCH	01248
*	1721	PAPER TAPE READER	01249
*	1723	PAPER TAPE PUNCH	01250
*	1777	PAPER TAPE READER	01251
*	1777	PAPER TAPE PUNCH	01252
*	1728/430	CARD READER/PUNCH	01253
*	1729-2	CARD READER	01254
*	1729-3	CARD READER	01255
*	1731/601	UNBUFFERED MAG TAPE	01256
*	1732-1/608/609	UNBUFFERED MAG TAPE	01257
			01258
	ENT AQSTCK	STACK FOR REQUESTS	01260
	ENT AQSSIZ	STACK SIZE	01261
	EQU NUMAQ(2)	QUANTITY OF ALLOCATION DEVICES	01263
	AQSTCK BZS	AQSTCK(3*NUMAQ-3)	01265
	AQSSIZ ADC	NUMAQ*3-3	01266

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The 1728/430 Card Reader/Punch requires A/Q channel allocation. Therefore the value of NUMAQ must be increased by one. After making this change, Section ACQ appears as follows:

* M I S C E L L A N E O U S P R O G R A M S	01265
* A / Q C H A N N E L A L L O C A T I O N	01266
*	01267
*	01268

* D E V I C E S R E Q U I R I N G A L L O C A T I O N	01270
*	01271
* 1711/713 TELETYPE / CRT	01272
* 1713 KEYBOARD	01273
* 1713 PAPER TAPE READER	01274
* 1713 PAPER TAPE PUNCH	01275
* 1721 PAPER TAPE READER	01276
* 1723 PAPER TAPE PUNCH	01277
* 1777 PAPER TAPE READER	01278
* 1777 PAPER TAPE PUNCH	01279
* 1728/430 CARD READER/PUNCH	01280
* 1729-2 CARD READER	01281
* 1729-3 CARD READER	01282
* 1731/601 UNBUFFERED MAG TAPE	01283
* 1732-1/608/609 UNBUFFERED MAG TAPE	01284

ENT ACSTCK STACK FOR REQUESTS	01286
ENT AQSSIZ STACK SIZE	01287

EQU NUMAQ(3)	01289
--------------	-------

ACSTCK EZS ACSTCK(3*NUMAQ-3)	01291
------------------------------	-------

AQSSIZ ADC NUMAQ*3-3	01292
----------------------	-------

4.26
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4.3.3

Modifying the Skeleton

The first step in modifying the skeleton is to obtain a listing of the skeleton. The install file is the second file on the install tape. The program SKED may be used to extract the skeleton from the install file and to obtain a sequenced listing of it. The install tape is mounted on tape unit 0. After entering the job processor, typing on the teletype appears as follows:

```
J
*SKED
SKED IN

NEXT
ADF,10,1
NEXT
BUILD,10

ANY MORE INPUT.  ENTER LU

NEXT
CATLOG
```

The BUILD command extracts the skeleton from the install file. The CATLOG command results in a resequencing and listing of the skeleton as follows:

421

1 *S.SYSMON.\$3039
2 *S.SYSDAY.\$3035
3 *S.SYSYER.\$3734
4 *S.SYSLVL.\$3836
5 *V
6 *V 1700 MASS STORAGE OPERATING SYSTEM - VER. 4.1
7 *V
8 *V ANN'S TEST SYSTEM
9 *V
10 *YM.LIBEDT.1
11 *YM.LOADS0.2
12 *YM.JOBENT.3
13 *YM.JOBPRO.4
14 *YM.PROTEC.5
15 *YM.JPLOAD.6
16 *YM.JPCHGE.7
17 *YM.JPT13.8
18 *YM.JCRDV4.9
19 *YM.JLGOV4.10
20 *YM.JPSTV4.11
21 *YM.NAMEV4.12
22 *YM.JPFLV4.13
23 *YM.AFILV4.14
24 *YM.RESTOR.15
25 *YM.RCOVER.16
26 *YM.BRKPT.17
27 *YM.ODEBUG.18
28 *YM.SYSCOP.19
29 *YM.SYSSEG.20
30 *YM.MIPRO.21
31 *YM.TDFUNC.22
32 *YM.EFSTOR.23
33 *YM.EFLIST.24
34 *YM.SCMM17.25
35 *YM.VERIFY.26
36 *YM.DUMMY1.27
37 *YM.DUMMY2.28
38 *YM.DUMMY3.29
39 *YM.DUMMY4.30
40 *YM.DUMMY5.31
41 *YM.DUMMY6.32
42 *YM.DUMMY7.33
43 *YM.DUMMY8.34
44 *YM.DUMMY9.35
45 *YM.DUMMY0.36
46 *S.N4.\$3C4^
47 *S.FND0V4.\$6BFF
48 *S.RGNMON.\$6C00
49 *S.MSIZV4.\$7FFF
50 *S.SECTOR.\$7EDA
51 *
52 *L SYSTEM DATA PROGRAM
53 *R 'SYSDAT' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
54 *L SPACE REQUEST PROCESSOR
55 *R 'SPACE' ' DECK-ID A02 MSOS 4.1'
56 *
57 * SYSTEM CORE RESIDENT PROGRAMS
58 *
59 *LP MONITOR

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60	*B	'NMONI'	'	DECK-ID A03	MSOS 4.1'
61	*B	'NDISP'	'	DECK-ID A04	MSOS 4.1'
62	*B	'RW'	'	DECK-ID A06	MSOS 4.1'
63	*B	'T14'	'	DECK-ID A07	MSOS 4.1'
64	*B	'T16'	'	DECK-ID A08	MSOS 4.1'
65	*B	'PARAMF'	'	DECK-ID A09	MSOS 4.1'
66	*B	'COMMON'	'	DECK-ID A10	MSOS 4.1'
67	*B	'NIPROC'	'	DECK-ID A11	MSOS 4.1'
68	*B	'ALVOL'	'	DECK-ID A13	MSOS 4.1'
69	*B	'OFVOL'	'	DECK-ID A14	MSOS 4.1'
70	*B	'ALCORE'	'	DECK-ID A15	MSOS 4.1'
71	*B	'DCORE'	'	DECK-ID A16	MSOS 4.1'
72	*B	'NFNR'	'	DECK-ID A18	MSOS 4.1'
73	*B	'NCMPRO'	'	DECK-ID A19	MSOS 4.1'
74	*B	'MAKQ'	'	DECK-ID A20	MSOS 4.1'
75	*B	'ADEV'	'	DECK-ID A21	MSOS 4.1'
76	*B	'TMINT'	'	DECK-ID A22	MSOS 4.1'
77	*B	'DTIMER'	'	DECK-ID A23	MSOS 4.1'
78	*B	'TOD'	'	DECK-ID A24	MSOS 4.1'
79	*B	'MINT'	'	DECK-ID A25	MSOS 4.1'
80	*B	'TRVEC'	'	DECK-ID D01	MSOS 4.1'
81	*LP	DEBUGGING / CHECKOUT			
82	*B	'SNAPOL'	'	DECK-ID H01	MSOS 4.1'
83	*B	'DMP421'	'	DECK-ID H03	MSOS 4.1'
84	*B	'BOK85X'	'	DECK-ID H05	MSOS 4.1'
85	*LP	CORE RESIDENT DRIVERS			
86	*B	'FFDATA'	'	DECK-ID C01	MSOS 4.1'
87	*B	'DUMMY'	'	DECK-ID C02	MSOS 4.1'
88	*B	'ALAQ'	'	DECK-ID C03	MSOS 4.1'
89	*B	'D1711'	'	DECK-ID C05	MSOS 4.1'
90	*B	'D173H'	'	DECK-ID C08	MSOS 4.1'
91	*B	'REWCK'	'	DECK-ID C13	MSOS 4.1'
92	*B	'MMEXEC'	'	DECK-ID C15	MSOS 4.1'
93	*B	'NXTLOC'	'	NEXT AVAILABLE LOCATION'	
94	*				
95	*	SYSTEM MASS RESIDENT PROGRAMS			
96	*				
97	*M	LIBEDT		1	
98	*B	'LIBEDT'	'	DECK-ID D02	MSOS 4.1'
99	*M	LOADSD		2	
100	*B	'LOAD1'	'	DECK-ID D03	MSOS 4.1'
101	*B	'RPNCH1'	'	DECK-ID D04	MSOS 4.1'
102	*B	'LIDRV1'	'	DECK-ID D05	MSOS 4.1'
103	*B	'LCDRV1'	'	DECK-ID D06	MSOS 4.1'
104	*B	'LMDRV1'	'	DECK-ID D07	MSOS 4.1'
105	*B	'LLDRV1'	'	DECK-ID D08	MSOS 4.1'
106	*B	'ADJOF1'	'	DECK-ID D09	MSOS 4.1'
107	*B	'CNVRT1'	'	DECK-ID D10	MSOS 4.1'
108	*B	'LSTOT1'	'	DECK-ID D11	MSOS 4.1'
109	*B	'LINK11'	'	DECK-ID D12	MSOS 4.1'
110	*B	'LOADR1'	'	DECK-ID D13	MSOS 4.1'
111	*B	'NAMPR1'	'	DECK-ID D14	MSOS 4.1'
112	*B	'RBRZ1'	'	DECK-ID D15	MSOS 4.1'
113	*B	'ENTEX1'	'	DECK-ID D16	MSOS 4.1'
114	*B	'XFRPR1'	'	DECK-ID D17	MSOS 4.1'
115	*B	'STBASE'	'	DECK-ID D18	MSOS 4.1'
116	*B	'LNKENT'	'	DECK-ID D19	MSOS 4.1'
117	*B	'LNKCR1'	'	DECK-ID D20	MSOS 4.1'
118	*B	'PATCH'	'	DECK-ID D21	MSOS 4.1'
119	*B	'TBSCH1'	'	DECK-ID D22	MSOS 4.1'

120	*R	'HASH'	' DECK-ID	D23	MSOS	4.1'
121	*R	'TBSTR1'	' DECK-ID	D24	MSOS	4.1'
122	*R	'PAGE'	' DECK-ID	D25	MSOS	4.1'
123	*R	'PROGLD'	' DECK-ID	D26	MSOS	4.1'
124	*R	'SCAN1'	' DECK-ID	D27	MSOS	4.1'
125	*R	'CHPU1'	' DECK-ID	D28	MSOS	4.1'
126	*R	'ADJOV2'	' DECK-ID	D29	MSOS	4.1'
127	*R	'ADRPR1'	' DECK-ID	D30	MSOS	4.1'
128	*M		JOBENT	3		
129	*R	'JOBENT'	' DECK-ID	D31	MSOS	4.1'
130	*R	'T11'	' DECK-ID	D32	MSOS	4.1'
131	*R	'T7'	' DECK-ID	D33	MSOS	4.1'
132	*R	'T5'	' DECK-ID	D34	MSOS	4.1'
133	*R	'T3'	' DECK-ID	D35	MSOS	4.1'
134	*S,N1,P					
135	*M		JOBPRO	4		
136	*R	'JOBPRO'	' DECK-ID	D36	MSOS	4.1'
137	*R	'ONE'	' DECK-ID	D37	MSOS	4.1'
138	*R	'TWO'	' DECK-ID	D38	MSOS	4.1'
139	*R	'THREE'	' DECK-ID	D39	MSOS	4.1'
140	*S,N2,P					
141	*M		PROTEC	5		
142	*R	'BPROTK'	' DECK-ID	D41	MSOS	4.1'
143	*R	'JRKILL'	' DECK-ID	D42	MSOS	4.1'
144	*M		JLOAD	6		
145	*R	'JLOAD'	' DECK-ID	D43	MSOS	4.1'
146	*M		JPCHGE	7		
147	*R	'JPCHGE'	' DECK-ID	D44	MSOS	4.1'
148	*R	'ASCHEX'	' DECK-ID	D45	MSOS	4.1'
149	*M		JPT13	8		
150	*R	'T13'	' DECK-ID	D46	MSOS	4.1'
151	*M		JCRDV4	9		
152	*R	'JCRDV4'	' DECK-ID	D47	MSOS	4.1'
153	*M		JLGOV4	10		
154	*R	'JLGOV4'	' DECK-ID	D48	MSOS	4.1'
155	*M		JPSTV4	11		
156	*R	'JPSTV4'	' DECK-ID	D49	MSOS	4.1'
157	*M		NAMEV4	12		
158	*R	'NAMEV4'	' DECK-ID	D50	MSOS	4.1'
159	*M		JPFLV4	13		
160	*R	'JPFLV4'	' DECK-ID	D51	MSOS	4.1'
161	*M		AFILV4	14		
162	*R	'JPF2V4'	' DECK-ID	D52	MSOS	4.1'
163	*M		RESTOR	15		
164	*R	'RESTOR'	' DECK-ID	D53	MSOS	4.1'
165	*M		RCOVER	16		
166	*R	'RCOVER'	' DECK-ID	H09	MSOS	4.1'
167	*R	'OUTSEL'	' DECK-ID	H10	MSOS	4.1'
168	*R	'ROMPV4'	' DECK-ID	H11	MSOS	4.1'
169	*R	'MASDMP'	' DECK-ID	H12	MSOS	4.1'
170	*M		BRKPT	17		
171	*R	'BRKPTD'	' DECK-ID	H13	MSOS	4.1'
172	*R	'SIFT'	' DECK-ID	H14	MSOS	4.1'
173	*R	'BIASCI'	' DECK-ID	H15	MSOS	4.1'
174	*R	'RETJMP'	' DECK-ID	H16	MSOS	4.1'
175	*R	'JUMPTO'	' DECK-ID	H17	MSOS	4.1'
176	*R	'ENTER'	' DECK-ID	H18	MSOS	4.1'
177	*R	'ENTCOR'	' DECK-ID	H19	MSOS	4.1'
178	*R	'PRTREG'	' DECK-ID	H20	MSOS	4.1'
179	*R	'TERMIN'	' DECK-ID	H21	MSOS	4.1'

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180 *R *RESUME* * DECK-ID H22 MSOS 4.1'
181 *R *DPCORR* * DECK-ID H23 MSOS 4.1'
182 *R *MSDMPH* * DECK-ID H24 MSOS 4.1'
183 *R *SETBRP* * DECK-ID H25 MSOS 4.1'
184 *M ODEBUG 18
185 *R *ODEBUG* * DECK-ID H26 MSOS 4.1'
186 *M SYSCOP 19
187 *R *SYSCOP* * DECK-ID H27 MSOS 4.1'
188 *M SYSEEG 20
189 *R *CO1ST* * DECK-ID H28 MSOS 4.1'
190 *R *CO2ND* * DECK-ID H29 MSOS 4.1'
191 *R *CO3RD* * DECK-ID H30 MSOS 4.1'
192 *R *COLAST* * DECK-ID H31 MSOS 4.1'
193 *M MIPRO 21
194 *R *MIPRO* * DECK-ID A26 MSOS 4.1'
195 *M TDFUNC 22
196 *R *TDFUNC* * DECK-ID A27 MSOS 4.1'
197 *M EFSTOR 23
198 *R *EFSTOR* * DECK-ID A28 MSOS 4.1'
199 *M EFLIST 24
200 *R *EFLIST* * DECK-ID A29 MSOS 4.1'
201 *M SCMM17 25
202 *R *SCMEXC* * DECK-ID E01 MSOS 4.1'
203 *M VERIFY 26
204 *R *VERIFY1* * DECK-ID A30 MSOS 4.1'
205 *M DUMMY1 27
206 *M DUMMY2 28
207 *M DUMMY3 29
208 *M DUMMY4 30
209 *M DUMMY5 31
210 *M DUMMY6 32
211 *M DUMMY7 33
212 *M DUMMY8 34
213 *M DUMMY9 35
214 *M DUMMY0 36

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215 *
216 * MASS RESIDENT DRIVERS
217 *
218 *M COSY DRIVER
219 *R *DCOSY* * DECK-ID C28 MSOS 4.1'
220 *S.SCOSY.S
221 *S.LCOSY.P
222 *M 1731 601 MAG TAPE
223 *R *D1731U* * DECK-ID C33 MSOS 4.1'
224 *R *FRWA* * DECK-ID C34 MSOS 4.1'
225 *R *FRWB* * DECK-ID C35 MSOS 4.1'
226 *R *RWBA* * DECK-ID C36 MSOS 4.1'
227 *R *MAXRVU* * DECK-ID C37 MSOS 4.1'
228 *S.S1731U.S
229 *S.L1731U.P
230 *M 1740-501/1742 LINE PRINTER
231 *R *D40421* * DECK-ID C46 MSOS 4.1'
232 *S.S40421.S
233 *S.L40421.P
234 *T END OF SYSTEM
235 *JOB,INSTAL,SYSTEM
236 *K,I6
237 *LIBEDT
238 *K,I6
239 *V DEFINE REQUEST PRIORITIES

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240 *S.001.03.M
241 *S.002.00.M
242 *S.003.01.M
243 *S.004.02.M
244 *S.005.03.M
245 *S.006.02.M
246 *S.007.02.M
247 *S.008.02.M
248 *S.009.02.M
249 *S.010.02.M
250 *S.011.02.M
251 *S.012.03.M
252 *S.013.03.M
253 *S.014.03.M
254 *S.015.02.M
255 *S.016.03.M
256 *S.017.03.M
257 *S.018.04.M
258 *S.019.04.M
259 *S.020.04.M
260 *S.021.04.M
261 *S.022.04.M
262 *S.023.04.M
263 *S.024.04.M
264 *S.025.04.M
265 *S.026.04.M
266 *S.027.04.M
267 *S.028.04.M
268 *S.029.04.M
269 *S.030.04.M
270 *S.031.04.M
271 *S.032.04.M
272 *S.033.04.M
273 *S.034.04.M
274 *S.035.04.M
275 *S.036.04.M
276 *V 1700 MACRO ASSEMBLER 3.
277 *K.I6
278 *L.LIBMAC
279 *R 'LIBMAC' ' DECK-ID G01 MSOS 4.1'
280 *L.ASSEM
281 *R 'ASSEM' ' DECK-ID G02 MSOS 4.1'
282 *K.P8
283 *P.F
284 *R 'PASS1' ' DECK-ID G03 MSOS 4.1'
285 *R 'PA1PR2' ' DECK-ID G04 MSOS 4.1'
286 *T
287 *K.I8
288 *N.PASS1...R
289 *K.I6
290 *K.P8
291 *P.F
292 *R 'PASS2' ' DECK-ID G05 MSOS 4.1'
293 *R 'PA2PR2' ' DECK-ID G06 MSOS 4.1'
294 *T
295 *K.I8
296 *N.PASS2...R
297 *K.I6
298 *K.P8
299 *P.F

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300 *B 'PASS3' ' DECK-ID G07 MSOS 4.1'
 301 *B 'PA3PR2' ' DECK-ID G08 MSOS 4.1'
 302 *B 'PA3PR3' ' DECK-ID G09 MSOS 4.1'
 303 *T
 304 *K,I8
 305 *N,PASS3,,,R
 306 *K,I6
 307 *K,P8
 308 *P,F
 309 *B 'TABLST' ' DECK-ID G10 MSOS 4.1'
 310 *T
 311 *K,I8
 312 *N,TABLST,,,R
 313 *K,I6
 314 *K,P8
 315 *P,F
 316 *B 'XREF' ' DECK-ID G11 MSOS 4.1'
 317 *K,I8
 318 *N,XREF,,,R
 319 *K,I6
 320 *N,MACSKL,,,R
 321 *B 'MACSKL'
 322 *N,MACROS,,,R
 323 *B 'MACROS'
 324 *V DEBUGGING AND CHECKOUT
 325 *K,I6
 326 *L,TRACE
 327 *B 'TRACE' ' DECK-ID H32 MSOS 4.1'
 328 *V SYSTEM UTILITY PROGRAMS
 329 *K,I6
 330 *L,LULIST
 331 *B 'LULIST' ' DECK-ID J01 MSOS 4.1'
 332 *L,LISTR
 333 *B 'LISTR' ' DECK-ID J02 MSOS 4.1'
 334 *L,OPSORT
 335 *B 'OPSORT' ' DECK-ID J03 MSOS 4.1'
 336 *L,EESORT
 337 *B 'EESORT' ' DECK-ID J04 MSOS 4.1'
 338 *L,COSY
 339 *B 'COSY' ' DECK-ID J05 MSOS 4.1'
 340 *L,LCOSY
 341 *B 'LCOSY' ' DECK-ID J06 MSOS 4.1'
 342 *L,CYFT
 343 *B 'CYFT' ' DECK-ID J07 MSOS 4.1'
 344 *L,IOUP
 345 *B 'IOUP' ' DECK-ID J08 MSOS 4.1'
 346 *K,P8
 347 *P,F
 348 *B 'IOUP' ' DECK-ID J08 MSOS 4.1'
 349 *B 'IOUPV4' ' DECK-ID J09 MSOS 4.1'
 350 *T
 351 *K,I8
 352 *N,IOUPV4,,,R
 353 *K,I6
 354 *L,DTLP
 355 *B 'DTLP' ' DECK-ID J10 MSOS 4.1'
 356 *K,P8
 357 *P,F
 358 *B 'DSKTAP' ' DECK-ID J11 MSOS 4.1'
 359 *B 'DSKEQC' ' DECK-ID J12 MSOS 4.1'

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360 *R *DSKDHX' * DECK-ID J13 MSOS 4.1'
361 *R *DSKCDP' * DECK-ID J14 MSOS 4.1'
362 *R *DSKMTI' * DECK-ID J15 MSOS 4.1'
363 *R *DSKMTD' * DECK-ID J16 MSOS 4.1'
364 *R *DSKMMD' * DECK-ID J18 MSOS 4.1'
365 *T
366 *K.I8
367 *N.DSKTAP...R
368 *K.I6
369 *L.SETPV4
370 *B *SPCALL' * DECK-ID J21 MSOS 4.1'
371 *K.PR
372 *D.F
373 *R *SPOLY1' * DECK-ID J22 MSOS 4.1'
374 *R *STPV4' * DECK-ID J23 MSOS 4.1'
375 *R *TERROR' * DECK-ID J24 MSOS 4.1'
376 *R *MCTDK' * DECK-ID J25 MSOS 4.1'
377 *R *GETPAG' * DECK-ID J26 MSOS 4.1'
378 *R *CONPRT' * DECK-ID J27 MSOS 4.1'
379 *B *REFCON' * DECK-ID J28 MSOS 4.1'
380 *R *CONDEC' * DECK-ID J29 MSOS 4.1'
381 *R *ORDERM' * DECK-ID J30 MSOS 4.1'
382 *R *IREAD' * DECK-ID J31 MSOS 4.1'
383 *R *ASCOIT' * DECK-ID J32 MSOS 4.1'
384 *R *PARAMS' * DECK-ID J33 MSOS 4.1'
385 *R *DISKIO' * DECK-ID J34 MSOS 4.1'
386 *T
387 *K.I8
388 *N.STP1V4...R
389 *K.I6
390 *K.PR
391 *D.F
392 *R *SPOLY2' * DECK-ID J35 MSOS 4.1'
393 *R *SIIP' * DECK-ID J36 MSOS 4.1'
394 *R *TERROR' * DECK-ID J24 MSOS 4.1'
395 *R *GETPAG' * DECK-ID J26 MSOS 4.1'
396 *R *RTOA' * DECK-ID J37 MSOS 4.1'
397 *R *ISTAT' * DECK-ID J38 MSOS 4.1'
398 *R *SCIO' * DECK-ID J39 MSOS 4.1'
399 *R *SCRD' * DECK-ID J40 MSOS 4.1'
400 *R *REFCON' * DECK-ID J28 MSOS 4.1'
401 *R *ICAT' * DECK-ID J41 MSOS 4.1'
402 *R *RUFIN' * DECK-ID J42 MSOS 4.1'
403 *R *MOVE' * DECK-ID J43 MSOS 4.1'
404 *B *IREAD' * DECK-ID J31 MSOS 4.1'
405 *R *ASCOIT' * DECK-ID J32 MSOS 4.1'
406 *R *PARAMS' * DECK-ID J33 MSOS 4.1'
407 *R *DISKIO' * DECK-ID J34 MSOS 4.1'
408 *T
409 *K.I8
410 *N.STP2V4...R
411 *V SCMM TEST ROUTINES
412 *K.I6
413 *K.PR
414 *D.F
415 *R *SCMTTY' * DECK-ID E02 MSOS 4.1'
416 *T
417 *K.I8
418 *N.SCMTTY...R
419 *K.I6

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420 *K.P8
 421 *P.F
 422 *R 'SCMDK1' ' DECK-ID E08 MSOS 4.1'
 423 *T
 424 *K.IR
 425 *N.SCMDK1...R
 426 *K.I6
 427 *K.P8
 428 *P.F
 429 *R 'SCMDVP' ' DECK-ID F09 MSOS 4.1'
 430 *T
 431 *K.IR
 432 *N.SCMDVP...R
 433 *K.I6
 434 *K.P8
 435 *P.F
 436 *R 'SCMPRT' ' DECK-ID E11 MSOS 4.1'
 437 *T
 438 *K.IR
 439 *N.SCMPRT...R
 440 *K.I6
 441 *K.P8
 442 *P.F
 443 *R 'SCMMTT' ' DECK-ID E12 MSOS 4.1'
 444 *T
 445 *K.IR
 446 *N.SCMMTT...R
 447 *V INSTALL LIBRARY BUILDER
 448 *K.I6
 449 *L.LIBILD
 450 *R 'LIBILD' ' DECK-ID B01 MSOS 4.1'
 451 *K.P8
 452 *P.F
 453 *R 'LIBID0' ' DECK-ID B02 MSOS 4.1'
 454 *R 'CONVRS' ' DECK-ID B03 MSOS 4.1'
 455 *R 'MFESSY' ' DECK-ID B04 MSOS 4.1'
 456 *R 'LJAPR' ' DECK-ID B05 MSOS 4.1'
 457 *R 'MOVECH' ' DECK-ID B06 MSOS 4.1'
 458 *R 'PICKUP' ' DECK-ID B07 MSOS 4.1'
 459 *R 'TOSUR' ' DECK-ID B08 MSOS 4.1'
 460 *T
 461 *K.IR
 462 *N.LIBID0...R
 463 *K.I6
 464 *K.P8
 465 *P.F
 466 *R 'HELPER' ' DECK-ID B09 MSOS 4.1'
 467 *R 'MOVECH' ' DECK-ID B06 MSOS 4.1'
 468 *R 'HELPO' ' DECK-ID B10 MSOS 4.1'
 469 *R 'HELP1' ' DECK-ID B11 MSOS 4.1'
 470 *R 'HELP2' ' DECK-ID B12 MSOS 4.1'
 471 *R 'HELP3' ' DECK-ID B13 MSOS 4.1'
 472 *R 'HELP4' ' DECK-ID B14 MSOS 4.1'
 473 *R 'HELP5' ' DECK-ID B15 MSOS 4.1'
 474 *R 'HELPO' ' DECK-ID B16 MSOS 4.1'
 475 *R 'HELP9' ' DECK-ID B17 MSOS 4.1'
 476 *R 'HELP10' ' DECK-ID B18 MSOS 4.1'
 477 *R 'HELP11' ' DECK-ID B19 MSOS 4.1'
 478 *R 'HELP12' ' DECK-ID B20 MSOS 4.1'
 479 *R 'HELP13' ' DECK-ID B21 MSOS 4.1'

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480 *R 'HELP14' ' DECK-ID B22 MSOS 4.1'  
481 *T  
482 *K.I8  
483 *N.HELPER...R  
484 *V INSTALL SKELETON EDITOR  
485 *K.I6  
486 *L.SKED  
487 *R 'SKED' ' DECK-ID B23 MSOS 4.1'  
488 *K.P8  
489 *P.F  
490 *R 'SKFILE' ' DECK-ID B24 MSOS 4.1'  
491 *T  
492 *K.I8  
493 *N.SKFILE...R  
494 *V SYSTEM INITIALIZER  
495 *K.I6  
496 *L.SILP  
497 *R 'SILP' ' DECK-ID B25 MSOS 4.1'  
498 *K.P8  
499 *P.F  
500 *R 'CONTRL' ' DECK-ID B26 MSOS 4.1'  
501 *R 'ILOAD' ' DECK-ID B27 MSOS 4.1'  
502 *R 'LDRTRL' ' DECK-ID B28 MSOS 4.1'  
503 *R 'I1' ' DECK-ID B29 MSOS 4.1'  
504 *R 'I2' ' DECK-ID B30 MSOS 4.1'  
505 *R '01711' ' DECK-ID B31 MSOS 4.1'  
506 *R '040421' ' DECK-ID B34 MSOS 4.1'  
507 *R 'IDRIV' ' DECK-ID B35 MSOS 4.1'  
508 *R 'QMT7TK' ' DECK-ID B36 MSOS 4.1'  
509 *R 'MDRIV' ' DECK-ID B42 MSOS 4.1'  
510 *R 'QDK85X' ' DECK-ID B44 MSOS 4.1'  
511 *R 'QCDDMY' ' DECK-ID B50 MSOS 4.1'  
512 *R 'OPTDMY' ' DECK-ID B51 MSOS 4.1'  
513 *T  
514 *K.I8  
515 *N.SI...R  
516 *V SMM ROOTSTRAPS  
517 *K.I6  
518 *L.SMM7T  
519 *R 'SMM1' ' DECK-ID B54 MSOS 4.1'  
520 *7  
521 *K.I10.P11.L9  
522 *CTO. MSOS 4.1 INSTALLATION COMPLETED - YOU MAY AUTOLOAD  
523 *7
```

4.26
430

The date of the system build is to be changed. The value of N4 will be changed as an example of the modification of one of the parameters discussed in Article 2.2. {In this instance the value of N4 had been previously customized to a value such that unprotected core could not accommodate the largest program library file.} To change the build date and the value of N4 the commands to SKED are as follows:

```
NEXT  
DELETE,1,3
```

```
NEXT DELETE,4b,4b
```

```
NEXT  
INSERT,4,4  
*S,N4,#3000  
*S,SYSMON,#3130  
*S,SYSDAY,#3130  
*S,SYSYER,#3734
```

The *M record for the 1728/430 driver, driver modules, and the associated *S cards defining driver length and last sector load address must be added to the mass resident drivers portion of the skeleton.

The skeleton statements to be added will follow record 233. To insert these statements the requests to SKED continue, with typing on the teletype as follows:

4-37

431

```

NEXT
INSERT,233,4
*M          1728/430 CARD READER/PUNCH
*B 'D1728'
*B 'CP026'
*B 'CR026'
*S,S1728,S
*S,L1728,P

```

The skeleton statements to add the SCMM test module, SCMCRD will follow record 446. The typing on the teletype continues as follows:

```

NEXT
INSERT,446,4
*K,IB
*K,PB
*P,F
*B 'SCMCRD'
*T
*K,IB
*N,SCMCRD,,,B

```

The skeleton statements to add the SMM bootstrap for the 1728/430 Card Reader are as follows:

```

NEXT
INSERT,519,4
*L,SMM430
*B 'SMM4'

```

A scratch tape is mounted on tape unit one prior to the following dump request.

```

NEXT
DUMP,13

```

```

NEXT
EXIT

```

J

432

The DUMP,13 request results in the revised skeleton being written on tape unit one and a listing of the revised skeleton being listed on the line printer. The revised skeleton is as follows:

433

1 *S.SYSLVL.\$3836
2 *S.N4.\$3000
3 *S.SYSMON.\$3130
4 *S.SYSDAY.\$3130
5 *S.SYSYER.\$3734
6 *V
7 *V 1700 MASS STORAGE OPERATING SYSTEM - VER. 4.1
8 *V
9 *V ANN'S TEST SYSTEM
10 *V
11 *YM.LIBEDT.1
12 *YM.LOADS0.2

434

13 *YM.JOBENT.3
14 *YM.JOBPRO.4
15 *YM.PROTEC.5
16 *YM.JLOAD.6
17 *YM.JPCHGE.7
18 *YM.JPT13.8
19 *YM.JCRDV4.9
20 *YM.JLGOV4.10
21 *YM.JPSTV4.11
22 *YM.NAMEV4.12
23 *YM.JPFLV4.13
24 *YM.AFILV4.14
25 *YM.RESTOP.15
26 *YM.RCOVER.16
27 *YM.BRKPT.17
28 *YM.ONDEBUG.18
29 *YM.SYSCOP.19
30 *YM.SYSSEG.20
31 *YM.MIPRO.21
32 *YM.TDFUNC.22
33 *YM.EFSTOR.23
34 *YM.EFLIST.24
35 *YM.SCMM17.25
36 *YM.VERIFY.26
37 *YM.DUMMY1.27
38 *YM.DUMMY2.28
39 *YM.DUMMY3.29
40 *YM.DUMMY4.30
41 *YM.DUMMY5.31
42 *YM.DUMMY6.32
43 *YM.DUMMY7.33
44 *YM.DUMMY8.34
45 *YM.DUMMY9.35
46 *YM.DUMMY10.36
47 *S.ENDQV4.\$6PFF
48 *S.BGNMON.\$6C00
49 *S.MSTZV4.\$7FFF
50 *S.SECTOR.\$7FDA
51 *
52 *L SYSTEM DATA PROGRAM
53 *R 'SYSDAT' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
54 *L SPACE REQUEST PROCESSOR
55 *R 'SPACE' ' DECK-ID A02 MSOS 4.1'
56 *
57 * SYSTEM CORE RESIDENT PROGRAMS
58 *
59 *LP MONITOR
60 *R 'NMONI' ' DECK-ID A03 MSOS 4.1'
61 *R 'NDISP' ' DECK-ID A04 MSOS 4.1'
62 *R 'RW' ' DECK-ID A06 MSOS 4.1'
63 *R 'T14' ' DECK-ID A07 MSOS 4.1'
64 *R 'T16' ' DECK-ID A08 MSOS 4.1'
65 *R 'PARAMF' ' DECK-ID A09 MSOS 4.1'
66 *R 'COMMON' ' DECK-ID A10 MSOS 4.1'
67 *R 'NIPROC' ' DECK-ID A11 MSOS 4.1'
68 *R 'ALVOL' ' DECK-ID A13 MSOS 4.1'
69 *R 'QFVOL' ' DECK-ID A14 MSOS 4.1'
70 *R 'ALCORE' ' DECK-ID A15 MSOS 4.1'
71 *R 'DCORE' ' DECK-ID A16 MSOS 4.1'
72 *R 'NENR' ' DECK-ID A18 MSOS 4.1'

435

```

73 *R 'NCMPRO' ' DECK-ID A19 MSOS 4.1'
74 *R 'MAKQ' ' DECK-ID A20 MSOS 4.1'
75 *R 'ADEV' ' DECK-ID A21 MSOS 4.1'
76 *R 'TMINT' ' DECK-ID A22 MSOS 4.1'
77 *R 'DTIMER' ' DECK-ID A23 MSOS 4.1'
78 *R 'TOD' ' DECK-ID A24 MSOS 4.1'
79 *R 'MINT' ' DECK-ID A25 MSOS 4.1'
80 *R 'TRVEC' ' DECK-ID D01 MSOS 4.1'
81 *LP DEBUGGING / CHECKOUT
82 *R 'SNAPOL' ' DECK-ID H01 MSOS 4.1'
83 *R 'DMP421' ' DECK-ID H03 MSOS 4.1'
84 *R 'BOK85X' ' DECK-ID H05 MSOS 4.1'
85 *LP CORE RESIDENT DRIVERS
86 *R 'FFDATA' ' DECK-ID C01 MSOS 4.1'
87 *R 'DUMMY' ' DECK-ID C02 MSOS 4.1'
88 *R 'ALAQ' ' DECK-ID C03 MSOS 4.1'
89 *R 'D1711' ' DECK-ID C05 MSOS 4.1'
90 *R 'D1738' ' DECK-ID C08 MSOS 4.1'
91 *R 'REWCK' ' DECK-ID C13 MSOS 4.1'
92 *R 'MMEXEC' ' DECK-ID C15 MSOS 4.1'
93 *R 'NXTLOC' ' NEXT AVAILABLE LOCATION'
94 *
95 * SYSTEM MASS RESIDENT PROGRAMS
96 *
97 *M LIBEDT 1
98 *R 'LIBEDT' ' DECK-ID D02 MSOS 4.1'
99 *M LOADSD 2
100 *R 'LOAD1' ' DECK-ID D03 MSOS 4.1'
101 *R 'BRNCH1' ' DECK-ID D04 MSOS 4.1'
102 *R 'LIDRV1' ' DECK-ID D05 MSOS 4.1'
103 *R 'LCDRV1' ' DECK-ID D06 MSOS 4.1'
104 *R 'LMDRV1' ' DECK-ID D07 MSOS 4.1'
105 *R 'LLDRV1' ' DECK-ID D08 MSOS 4.1'
106 *R 'ADJOF1' ' DECK-ID D09 MSOS 4.1'
107 *R 'CNVRT1' ' DECK-ID D10 MSOS 4.1'
108 *R 'LSTOT1' ' DECK-ID D11 MSOS 4.1'
109 *R 'LINK11' ' DECK-ID D12 MSOS 4.1'
110 *R 'LOADR1' ' DECK-ID D13 MSOS 4.1'
111 *R 'NAMPR1' ' DECK-ID D14 MSOS 4.1'
112 *R 'RBDBZ1' ' DECK-ID D15 MSOS 4.1'
113 *R 'ENTEX1' ' DECK-ID D16 MSOS 4.1'
114 *R 'XFRPR1' ' DECK-ID D17 MSOS 4.1'
115 *R 'STBASE' ' DECK-ID D18 MSOS 4.1'
116 *R 'LNKENT' ' DECK-ID D19 MSOS 4.1'
117 *R 'LNKCR1' ' DECK-ID D20 MSOS 4.1'
118 *R 'PATCH' ' DECK-ID D21 MSOS 4.1'
119 *R 'TRSCH1' ' DECK-ID D22 MSOS 4.1'
120 *R 'HASH' ' DECK-ID D23 MSOS 4.1'
121 *R 'TRSTR1' ' DECK-ID D24 MSOS 4.1'
122 *R 'PAGE' ' DECK-ID D25 MSOS 4.1'
123 *R 'PROGLD' ' DECK-ID D26 MSOS 4.1'
124 *R 'SCAN1' ' DECK-ID D27 MSOS 4.1'
125 *R 'CHPU1' ' DECK-ID D28 MSOS 4.1'
126 *R 'ADJOV2' ' DECK-ID D29 MSOS 4.1'
127 *R 'ADRPR1' ' DECK-ID D30 MSOS 4.1'
128 *M JORENT 3
129 *R 'JOBENT' ' DECK-ID D31 MSOS 4.1'
130 *R 'T11' ' DECK-ID D32 MSOS 4.1'
131 *R 'T7' ' DECK-ID D33 MSOS 4.1'
132 *R 'T5' ' DECK-ID D34 MSOS 4.1'

```

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133	*R	'T3'	'	DECK-ID	D35	MSOS	4.1'
134	*S	.N1.P					
135	*M			JOBPRO		4	
136	*R	'JOBPRO'	'	DECK-ID	D36	MSOS	4.1'
137	*R	'ONE'	'	DECK-ID	D37	MSOS	4.1'
138	*R	'TWO'	'	DECK-ID	D38	MSOS	4.1'
139	*R	'THREE'	'	DECK-ID	D39	MSOS	4.1'
140	*S	.N2.P					
141	*M			PROTEC		5	
142	*R	'RPROTK'	'	DECK-ID	D41	MSOS	4.1'
143	*R	'J3KILL'	'	DECK-ID	D42	MSOS	4.1'
144	*M			JLOAD		6	
145	*R	'JLOAD'	'	DECK-ID	D43	MSOS	4.1'
146	*M			JPCHEG		7	
147	*R	'JPCHEG'	'	DECK-ID	D44	MSOS	4.1'
148	*R	'ASCHEX'	'	DECK-ID	D45	MSOS	4.1'
149	*M			JPT13		8	
150	*R	'T13'	'	DECK-ID	D46	MSOS	4.1'
151	*M			JCRDV4		9	
152	*R	'JCRDV4'	'	DECK-ID	D47	MSOS	4.1'
153	*M			JLGOV4		10	
154	*R	'JLGOV4'	'	DECK-ID	D48	MSOS	4.1'
155	*M			JPSTV4		11	
156	*R	'JPSTV4'	'	DECK-ID	D49	MSOS	4.1'
157	*M			NAMEV4		12	
158	*R	'NAMEV4'	'	DECK-ID	D50	MSOS	4.1'
159	*M			JPFLV4		13	
160	*R	'JPFLV4'	'	DECK-ID	D51	MSOS	4.1'
161	*M			AFILV4		14	
162	*R	'JPF2V4'	'	DECK-ID	D52	MSOS	4.1'
163	*M			RESTOR		15	
164	*R	'RESTOR'	'	DECK-ID	D53	MSOS	4.1'
165	*M			RCOVER		16	
166	*R	'RCOVER'	'	DECK-ID	H09	MSOS	4.1'
167	*R	'OUTSEL'	'	DECK-ID	H10	MSOS	4.1'
168	*R	'RDMPV4'	'	DECK-ID	H11	MSOS	4.1'
169	*R	'MASDMP'	'	DECK-ID	H12	MSOS	4.1'
170	*M			BRKPT		17	
171	*R	'BRKPTD'	'	DECK-ID	H13	MSOS	4.1'
172	*R	'SIFT'	'	DECK-ID	H14	MSOS	4.1'
173	*R	'RIASCI'	'	DECK-ID	H15	MSOS	4.1'
174	*R	'RETJMP'	'	DECK-ID	H16	MSOS	4.1'
175	*R	'JUMPTO'	'	DECK-ID	H17	MSOS	4.1'
176	*R	'ENTER'	'	DECK-ID	H18	MSOS	4.1'
177	*R	'ENTCOR'	'	DECK-ID	H19	MSOS	4.1'
178	*R	'PRTREG'	'	DECK-ID	H20	MSOS	4.1'
179	*R	'TERMIN'	'	DECK-ID	H21	MSOS	4.1'
180	*R	'RESUME'	'	DECK-ID	H22	MSOS	4.1'
181	*R	'DPCORR'	'	DECK-ID	H23	MSOS	4.1'
182	*R	'MSDMPR'	'	DECK-ID	H24	MSOS	4.1'
183	*R	'SETBRP'	'	DECK-ID	H25	MSOS	4.1'
184	*M			ODEBUG		18	
185	*R	'ODEBUG'	'	DECK-ID	H26	MSOS	4.1'
186	*M			SYSCOP		19	
187	*R	'SYSCOP'	'	DECK-ID	H27	MSOS	4.1'
188	*M			SYSSEG		20	
189	*R	'CO1ST'	'	DECK-ID	H28	MSOS	4.1'
190	*R	'CO2ND'	'	DECK-ID	H29	MSOS	4.1'
191	*R	'CO3RD'	'	DECK-ID	H30	MSOS	4.1'
192	*R	'COLAST'	'	DECK-ID	H31	MSOS	4.1'

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```
193 *M MIPRO 21
194 *B 'MIPRO' ' DECK-ID A26 MSOS 4.1'
195 *M TDFUNC 22
196 *B 'TDFUNC' ' DECK-ID A27 MSOS 4.1'
197 *M EFSTOR 23
198 *B 'EFSTOR' ' DECK-ID A28 MSOS 4.1'
199 *M EFLIST 24
200 *B 'EFLIST' ' DECK-ID A29 MSOS 4.1'
201 *M SCMM17 25
202 *B 'SCMEXC' ' DECK-ID E01 MSOS 4.1'
203 *M VERIFY 26
204 *B 'VERFY1' ' DECK-ID A30 MSOS 4.1'
205 *M DUMMY1 27
206 *M DUMMY2 28
207 *M DUMMY3 29
208 *M DUMMY4 30
209 *M DUMMY5 31
210 *M DUMMY6 32
211 *M DUMMY7 33
212 *M DUMMY8 34
213 *M DUMMY9 35
214 *M DUMMY0 36
215 *
216 * MASS RESIDENT DRIVERS
217 *
218 *M COSY DRIVER
219 *B 'DCOSY' ' DECK-ID C28 MSOS 4.1'
220 *S,SCOSY,S
221 *S,LCOSY,P
222 *M 1731 601 MAG TAPE
223 *B 'D1731U' ' DECK-ID C33 MSOS 4.1'
224 *B 'FRWA' ' DECK-ID C34 MSOS 4.1'
225 *B 'FRWB' ' DECK-ID C35 MSOS 4.1'
226 *B 'RWBA' ' DECK-ID C36 MSOS 4.1'
227 *B 'MAXRVU' ' DECK-ID C37 MSOS 4.1'
228 *S,S1731U,S
229 *S,L1731U,P
230 *M 1740-501/1742 LINE PRINTER
231 *B 'D40421' ' DECK-ID C46 MSOS 4.1'
232 *S,S40421,S
233 *S,L40421,P
234 *M
235 *B 'D1728'
236 *B 'CP026'
237 *B 'CR026'
238 *S,S1728,S
239 *S,L1728,P
240 *T END OF SYSTEM
241 *JOR,INSTAL,SYSTEM
242 *K,I6
243 *LIREDT
244 *K,I6
245 *V DEFINE REQUEST PRIORITIES
246 *S,001,03,M
247 *S,002,00,M
248 *S,003,01,M
249 *S,004,02,M
250 *S,005,03,M
251 *S,006,02,M
252 *S,007,02,M
```

TM

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```
253 *S,008,02,M
254 *S,009,02,M
255 *S,010,02,M
256 *S,011,02,M
257 *S,012,03,M
258 *S,013,03,M
259 *S,014,03,M
260 *S,015,02,M
261 *S,016,03,M
262 *S,017,03,M
263 *S,018,04,M
264 *S,019,04,M
265 *S,020,04,M
266 *S,021,04,M
267 *S,022,04,M
268 *S,023,04,M
269 *S,024,04,M
270 *S,025,04,M
271 *S,026,04,M
272 *S,027,04,M
273 *S,028,04,M
274 *S,029,04,M
275 *S,030,04,M
276 *S,031,04,M
277 *S,032,04,M
278 *S,033,04,M
279 *S,034,04,M
280 *S,035,04,M
281 *S,036,04,M
282 *V 1700 MACRO ASSEMBLER 3.
283 *K,I6
284 *L,LIBMAC
285 *R 'LIBMAC' ' DECK-ID G01 MSOS 4.1'
286 *L,ASSEM
287 *B 'ASSEM' ' DECK-ID G02 MSOS 4.1'
288 *K,P8
289 *P,F
290 *B 'PASS1' ' DECK-ID G03 MSOS 4.1'
291 *B 'PA1PR2' ' DECK-ID G04 MSOS 4.1'
292 *T
293 *K,I8
294 *N,PASS1,..R
295 *K,I6
296 *K,P8
297 *P,F
298 *R 'PASS2' ' DECK-ID G05 MSOS 4.1'
299 *R 'PA2PR2' ' DECK-ID G06 MSOS 4.1'
300 *T
301 *K,I8
302 *N,PASS2,..B
303 *K,I6
304 *K,P8
305 *P,F
306 *B 'PASS3' ' DECK-ID G07 MSOS 4.1'
307 *B 'PA3PR2' ' DECK-ID G08 MSOS 4.1'
308 *B 'PA3PR3' ' DECK-ID G09 MSOS 4.1'
309 *T
310 *K,I8
311 *N,PASS3,..B
312 *K,I6
```

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313 *K,PR
314 *P,F
315 *B 'TABLST' ' DECK-ID G10 MSOS 4.1'
316 *T
317 *K,IR
318 *N,TABLST...R
319 *K,I6
320 *K,PR
321 *P,F
322 *B 'XREF' ' DECK-ID G11 MSOS 4.1'
323 *K,IR
324 *N,XREF...R
325 *K,I6
326 *N,MACSKL...R
327 *B 'MACSKL'
328 *N,MACROS...R
329 *B 'MACPOS'
330 *V DEBUGGING AND CHECKOUT
331 *K,I6
332 *L,TRACE
333 *B 'TRACE' ' DECK-ID H32 MSOS 4.1'
334 *V SYSTEM UTILITY PROGRAMS
335 *K,I6
336 *L,LULIST
337 *B 'LULIST' ' DECK-ID J01 MSOS 4.1'
338 *L,LISTR
339 *B 'LISTR' ' DECK-ID J02 MSOS 4.1'
340 *L,OPSORT
341 *B 'OPSORT' ' DECK-ID J03 MSOS 4.1'
342 *L,EESORT
343 *B 'EESORT' ' DECK-ID J04 MSOS 4.1'
344 *L,COSY
345 *B 'COSY' ' DECK-ID J05 MSOS 4.1'
346 *L,LCOSY
347 *B 'LCOSY' ' DECK-ID J06 MSOS 4.1'
348 *L,CYFT
349 *B 'CYFT' ' DECK-ID J07 MSOS 4.1'
350 *L,IOUP
351 *B 'IOUP' ' DECK-ID J08 MSOS 4.1'
352 *K,PR
353 *P,F
354 *B 'IOUP' ' DECK-ID J08 MSOS 4.1'
355 *B 'IOUPV4' ' DECK-ID J09 MSOS 4.1'
356 *T
357 *K,IR
358 *N,IOUPV4...R
359 *K,I6
360 *L,DTLP
361 *B 'DTLP' ' DECK-ID J10 MSOS 4.1'
362 *K,PR
363 *P,F
364 *B 'DSKTAP' ' DECK-ID J11 MSOS 4.1'
365 *B 'DSKEQC' ' DECK-ID J12 MSOS 4.1'
366 *B 'DSKDX' ' DECK-ID J13 MSOS 4.1'
367 *B 'DSKCDR' ' DECK-ID J14 MSOS 4.1'
368 *B 'DSKMTI' ' DECK-ID J15 MSOS 4.1'
369 *B 'DSKMT0' ' DECK-ID J16 MSOS 4.1'
370 *B 'DSKMMD' ' DECK-ID J18 MSOS 4.1'
371 *T
372 *K,IR

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373 *N,DSKTAP,...R
374 *K,I6
375 *L,SETPV4
376 *R 'SPCALL' ' DECK-ID J21 MSOS 4.1'
377 *K,PR
378 *P,F
379 *R 'SPOLY1' ' DECK-ID J22 MSOS 4.1'
380 *R 'STPV4' ' DECK-ID J23 MSOS 4.1'
381 *R 'IERROR' ' DECK-ID J24 MSOS 4.1'
382 *R 'MCTDK' ' DECK-ID J25 MSOS 4.1'
383 *R 'GETPAG' ' DECK-ID J26 MSOS 4.1'
384 *R 'CONPRT' ' DECK-ID J27 MSOS 4.1'
385 *R 'REDCON' ' DECK-ID J28 MSOS 4.1'
386 *R 'CONDEC' ' DECK-ID J29 MSOS 4.1'
387 *R 'ORDERM' ' DECK-ID J30 MSOS 4.1'
388 *R 'IREAD' ' DECK-ID J31 MSOS 4.1'
389 *R 'ASCOUT' ' DECK-ID J32 MSOS 4.1'
390 *R 'PARAMS' ' DECK-ID J33 MSOS 4.1'
391 *R 'DISKIO' ' DECK-ID J34 MSOS 4.1'
392 *T
393 *K,I8
394 *N,STP1V4....R
395 *K,I6
396 *K,PR
397 *P,F
398 *R 'SPOLY2' ' DECK-ID J35 MSOS 4.1'
399 *R 'SUP' ' DECK-ID J36 MSOS 4.1'
400 *R 'IERROR' ' DECK-ID J24 MSOS 4.1'
401 *R 'GETPAG' ' DECK-ID J26 MSOS 4.1'
402 *R 'RTOA' ' DECK-ID J37 MSOS 4.1'
403 *R 'ISTAT' ' DECK-ID J38 MSOS 4.1'
404 *R 'SCIO' ' DECK-ID J39 MSOS 4.1'
405 *R 'SCRD' ' DECK-ID J40 MSOS 4.1'
406 *R 'REDCON' ' DECK-ID J28 MSOS 4.1'
407 *R 'ICAT' ' DECK-ID J41 MSOS 4.1'
408 *R 'BUFIN' ' DECK-ID J42 MSOS 4.1'
409 *R 'MOVE' ' DECK-ID J43 MSOS 4.1'
410 *R 'IREAD' ' DECK-ID J31 MSOS 4.1'
411 *R 'ASCOUT' ' DECK-ID J32 MSOS 4.1'
412 *R 'PARAMS' ' DECK-ID J33 MSOS 4.1'
413 *R 'DISKIO' ' DECK-ID J34 MSOS 4.1'
414 *T
415 *K,I8
416 *N,STP2V4....R
417 *V SCMM TEST ROUTINES
418 *K,I6
419 *K,PR
420 *P,F
421 *R 'SCMTTY' ' DECK-ID E02 MSOS 4.1'
422 *T
423 *K,I8
424 *N,SCMTTY....R
425 *K,I6
426 *K,PR
427 *P,F
428 *R 'SCMDK1' ' DECK-ID E08 MSOS 4.1'
429 *T
430 *K,I8
431 *N,SCMDK1....R
432 *K,I6

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```

433 *K.PR
434 *P.F
435 *R 'SCMDVP' ' DECK-ID E09 MSOS 4.1'
436 *T
437 *K.I8
438 *N.SCMDVP...R
439 *K.I6
440 *K.PR
441 *P.F
442 *R 'SCMPRT' ' DECK-ID E11 MSOS 4.1'
443 *T
444 *K.I8
445 *N.SCMPRT...R
446 *K.I6
447 *K.PR
448 *P.F
449 *R 'SCMMTT' ' DECK-ID E12 MSOS 4.1'
450 *T
451 *K.I8
452 *N.SCMMTT...R
453 *K.I6
454 *K.PR
455 *P.F
456 *R 'SCMCRD'
457 *T
458 *K.I8
459 *N.SCMCRD...R
460 *V INSTALL LIBRARY BUILDER
461 *K.I6
462 *L.LIBILD
463 *R 'LIBILD' ' DECK-ID B01 MSOS 4.1'
464 *K.PR
465 *P.F
466 *R 'LIBID0' ' DECK-ID B02 MSOS 4.1'
467 *R 'CONVRS' ' DECK-ID B03 MSOS 4.1'
468 *R 'MESSY' ' DECK-ID B04 MSOS 4.1'
469 *R 'LJ2R' ' DECK-ID B05 MSOS 4.1'
470 *R 'MOVECH' ' DECK-ID B06 MSOS 4.1'
471 *R 'PICKUP' ' DECK-ID B07 MSOS 4.1'
472 *R 'TOSUR' ' DECK-ID B08 MSOS 4.1'
473 *T
474 *K.I8
475 *N.LIBID0...R
476 *K.I6
477 *K.PR
478 *P.F
479 *R 'HELPER' ' DECK-ID B09 MSOS 4.1'
480 *R 'MOVECH' ' DECK-ID B06 MSOS 4.1'
481 *R 'HELP0' ' DECK-ID B10 MSOS 4.1'
482 *R 'HELP1' ' DECK-ID B11 MSOS 4.1'
483 *R 'HELP2' ' DECK-ID B12 MSOS 4.1'
484 *R 'HELP3' ' DECK-ID B13 MSOS 4.1'
485 *R 'HELP4' ' DECK-ID B14 MSOS 4.1'
486 *R 'HELP5' ' DECK-ID B15 MSOS 4.1'
487 *R 'HELP8' ' DECK-ID B16 MSOS 4.1'
488 *R 'HELP9' ' DECK-ID B17 MSOS 4.1'
489 *R 'HELP10' ' DECK-ID B18 MSOS 4.1'
490 *R 'HELP11' ' DECK-ID B19 MSOS 4.1'
491 *R 'HELP12' ' DECK-ID B20 MSOS 4.1'
492 *R 'HELP13' ' DECK-ID B21 MSOS 4.1'

```


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493 *R 'HELP14' ' DECK-ID B22 MSOS 4.1'
494 *T
495 *K,I8
496 *N.HELPER...R
497 *V INSTALL SKELETON EDITOR
498 *K,I6
499 *L.SKED
500 *R 'SKED' ' DECK-ID B23 MSOS 4.1'
501 *K,P8
502 *P,F
503 *R 'SKFILE' ' DECK-ID B24 MSOS 4.1'
504 *T
505 *K,I8
506 *N.SKFILE...R
507 *V SYSTEM INITIALIZER
508 *K,I6
509 *L.SILP
510 *R 'SILP' ' DECK-ID B25 MSOS 4.1'
511 *K,P8
512 *P,F
513 *R 'CONTRL' ' DECK-ID B26 MSOS 4.1'
514 *R 'TLOAD' ' DECK-ID B27 MSOS 4.1'
515 *R 'LDRTRB' ' DECK-ID B28 MSOS 4.1'
516 *R 'I1' ' DECK-ID B29 MSOS 4.1'
517 *R 'I2' ' DECK-ID B30 MSOS 4.1'
518 *R 'Q1711' ' DECK-ID B31 MSOS 4.1'
519 *R 'Q40421' ' DECK-ID B34 MSOS 4.1'
520 *R 'IDRIV' ' DECK-ID B35 MSOS 4.1'
521 *R 'QMT7TK' ' DECK-ID B36 MSOS 4.1'
522 *R 'MDRIV' ' DECK-ID B42 MSOS 4.1'
523 *R 'QDK85X' ' DECK-ID B44 MSOS 4.1'
524 *R 'QCDDMY' ' DECK-ID B50 MSOS 4.1'
525 *R 'QPTDMY' ' DECK-ID B51 MSOS 4.1'
526 *T
527 *K,I8
528 *N.SI...R
529 *V SMM ROOTSTRAPS
530 *K,I6
531 *L.SMM7T
532 *R 'SMM1' ' DECK-ID B54 MSOS 4.1'
533 *L.SMM430
534 *R 'SMM4'
535 *Z
536 *K,I10,P11,L9
537 *CTO. MSOS 4.1 INSTALLATION COMPLETED - YOU MAY AUTOLOAD
538 *Z

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4.3.4 Creating a New Install File

In this process, three tapes are used. These tapes are as follows:

- 1} Binary copy of the modified form of SYSDAT
- 2} Modified skeleton
- 3} MSOS library tape

The program LIBILD is used to construct a new install file. Typing on the teletype is as follows:

```
MI
*BATCH
J
*JOB
J
*LIBILD
CONTROL LU =
DEFS LU =
INSTALL LU = 13
NEWLIB LU =
LIB 01 LU = 10
LIB 02 LU = 10
LIB 03 LU =
SKELETON LU = 10

LOAD LIBRARY INPUT 02 ON LU 10. CR WHEN READY.

LOAD SKEL/INSTAL, CR WHEN READY

LIBRARY BUILD COMPLETE
TYPE *Z TO TERMINATE OR
TYPE *C TO CONTINUE WITH CURRENT SKELETON AND/OR
OUTPUT LIBRARY LU'S *Z
J
```

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Note that a carriage return only is entered as the response to the query CONTROL LU=. This is because subsequent control statements will be read from the comments device. A carriage return is the response to the query DEFS LU= since the install file is to be created according to a skeleton and not according to a definitions deck. The response to INSTALL LU= indicates the install file will be written on tape unit 1. No new output library is to be created. Therefore a carriage return only is entered following the query NEWLIB LU=.

4.3.5 Loading the New System

Using the new install file and the system initializer program, the new system may be loaded. The procedure used is described in the 1700 MS0S 4 Installation Handbook, Publication No. 39520900. Typing on the teletype is as follows:

```
MI  
-----  
*BATCH  
J  
-----  
*JOB  
J  
-----  
*SILP  
THE INITIALIZER WILL BE MOVED TO LOCATION 5CFE AND EXECUTED  
  
TURN OFF PROTEC SWITCH AND TYPE CARRIAGE RETURN
```

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At this point before typing carriage return, the system disk pack is removed, and a scratch pack is mounted.

The new system will be loaded on the scratch pack.

Typing on the teletype then proceeds as follows:

MSOS 4.1 SYSTEM INITIALIZER
FWA OF CONTRL = 5CEE

DATE MM/DD/YY
10/10/74

Q
*C,7
*C,7

Q
*V
INITIALIZATION COMPLETED - YOU MAY AUTOLOAD

MSOS 4.1--PSR LEVEL 86 10/10/74

ANN'S TEST SYSTEM

32K MODE

ENTER DATE/TIME MMDDYYHHMM

1010741945

DATE: 10 OCT 74 TIME: 1945:00

MI
*BATCH
*CTO, MSOS 4.1 INSTALLATION COMPLETED - YOU MAY AUTOLOAD

MSOS 4.1--PSR LEVEL 86 10/10/74

ANN'S TEST SYSTEM

32K MODE

ENTER DATE/TIME MMDDYYHHMM

1010741958

DATE: 10 OCT 74 TIME: 1958:00

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4.3.6 SCMM Verification of Added Driver

This verification procedure is described in the Small Computer Maintenance Monitor Reference Manual, Publication No. 39520200. Three punch tests and three read tests are ~~per~~ performed. Typing on the teletype is as follows:

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MI
SCMM
SCMM IN
10/15/74 0900
CONTROL, TEST ID
SRT,CRD
BEGIN CARD R/P TEST
DLU,SECTIONS,CARDS
19,2,10
END CARD R/P TEST, 0010 CARDS 0000 ERRORS
SCMM OUT
10/15/74 0900

MI
SCMM
SCMM IN
10/15/74 0900
CONTROL, TEST ID
SRT,CRD
BEGIN CARD R/P TEST
DLU,SECTIONS,CARDS
19,4,10
END CARD R/P TEST, 0010 CARDS 0000 ERRORS
SCMM OUT
10/15/74 0901

MI
SCMM
SCMM IN
10/15/74 0901
CONTROL, TEST ID
SRT,CRD
BEGIN CARD R/P TEST
DLU,SECTIONS,CARDS
19,8,10
SPECIAL PATTERN FOR TEST 3
ABC
END CARD R/P TEST, 0010 CARDS 0000 ERRORS
SCMM OUT
10/15/74 0901

MI
SCMM
SCMM IN
10/15/74 0914
CONTROL, TEST ID
SRT,CRD
BEGIN CARD R/P TEST
DLU,SECTIONS,CARDS
19,80,10
SPECIAL PATTERN FOR TEST 3
ABC
END CARD R/P TEST, 0010 CARDS 0000 ERRORS
SCMM OUT
10/15/74 0915

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MI
SCMM
SCMM IN
10/15/74 0916
CONTROL, TEST ID
SRT,CRD
BEGIN CARD R/P TEST
DLU,SECTIONS,CARDS
19,20,10
END CARD R/P TEST, 0010 CARDS 0000 ERRORS
SCMM OUT
10/15/74 0916

MI
SCMM
SCMM IN
10/15/74 0917
CONTROL, TEST ID
SRT,CRD
BEGIN CARD R/P TEST
DLU,SECTIONS,CARDS
19,40,10
END CARD R/P TEST, 0010 CARDS 0000 ERRORS
SCMM OUT
10/15/74 0918



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CHAPTER 5. ADDITION OF THE FILE MANAGER5.1 SYSDAT CHANGES REQUIRED

To add a File Manager it is necessary to modify SYSDAT Section ADA, File Manager Data, SYSDAT Section ADB, Job Processor File Parameters; and SYSDAT Section ADC, Preset Protected Entry Points for Use by Unprotected Programs.

5.1.1 Section ADA, File Manager Data Parameter

In modifying Section ADA, File Manager Data, it is necessary to define the parameters needed by the File Manager. These include the parameters FISLU, MAXMMA, RPTPER, FDTPER, FIDSEC, FIBLSA, FIBNIX, FSLIST, FSLLTH, FSLEND, and ADRFMS. The method of defining each of these parameters is described in Article 1.79.

There may be from one to nine File Manager mass memory units. For File Manager mass memory unit n , $0 \leq n \leq 8$ the following parameter values must be defined:

- 1} file space list for this device
- 2} LUEn
- 3} NUMFSn
- 4} starting sector of file space on this device {This is ADRFMS if $n = 0$; BEGLUn otherwise.}

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The file space list for a given device is defined as described in Article 1.79. If only non-indexed files are to be defined, these having a variety of file record lengths,

and more than one mass memory unit is available for file space, it may be most efficient to have different segment lengths in each file space list. For example, if there are two units available for file space, and file records will have lengths 1, 2, 3, 4, and 6 sectors, the following file space lists might be defined.

File Manager Unit 0 file space list:

NUM	0,1	THREAD OF SEGMENTS ONE SECTOR LONG
NUM	0,2	THREAD OF SEGMENTS TWO SECTORS LONG

File Manager Unit 1 file space list:

NUM	0,3	THREAD OF SEGMENTS THREE SECTORS LONG
NUM	0,4	THREAD OF SGEMENTS FOUR SECTORS LONG
NUM	0,6	THREAD OF SEGMENTS SIX SECTORS LONG

The parameter LUEn is defined according to the description in Article 1.79.

The size and starting sector of the file space are dependent on the File Manager unit number *n*, and on whether that unit is the library unit. There are three cases to consider.

5.1.1.1 Size and Start of File Space on File Manager Unit 0 Where Unit 0 is the Library Unit

First consider the case $n=0$ where FISLU is the logical unit number of the library unit. In this case, NUMFS0 is the number of sectors on the library unit to be used for file space. The value of NUMFS0 must be large enough to contain the FIS directory {one sector}, the FIS blocks {approximately 100 sectors for 100-500 files}, plus any file indexed directories and file records to be stored on the library unit. The value of NUMFS0 must be small enough so that required scratch, the system, and NUMFS0 sectors of file space will fit on the library unit. The principal size requirement for the scratch area is that it be large enough to contain temporary storage for the Macro Assembler to assemble the largest program in the system. The Macro Assembler requires three sectors of scratch for each record in a program. Typically, SYSDAT is the largest program in the system. For example, SYSDAT might contain 2000 records. Then 6000 sectors of scratch would be required. The scratch requirement may be partly or wholly satisfied outside the library unit if other mass memory units are available. The number of sectors required by the unmodified system is contained in core location $C1_{16}$. {Refer to Article 1.5.}

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The modified system will require additional sectors for storage of the added File Manager modules. Typically the system will require 1000 sectors or less.

Let T = total sectors on library unit
 S = number of sectors needed to contain system
 C = number of sectors required for scratch on the library unit
 M = number of sectors required for FIS directory, FIS blocks and any file records or file indexed directories to be stored on library unit.

Then it is required that $NUMFS0$ satisfy the following:

$$M \leq NUMFS0 \leq T - \{S + C\}$$

The approximation of M , S , and C have been discussed. The value of T is dependent on the mass memory device used as the library unit. The value of T may be obtained from Figure 5-1.

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DEVICE	TOTAL NUMBER SECTORS
1733-1/853 Disk	15,995
1738/853 Disk	15,995
1733-1/854 Disk	32,475
1738/854 Disk	32,475
1733-2/856-2 Cartridge Disk	23,543
1733-2/856-4 Cartridge Disk	32,767
1739-1 Cartridge Disk	23,543
1751-A Drum	679
1751-C Drum	1,361
1751-D Drum	2,044
1751-E Drum	2,727
1751-F Drum	3,409
1751-G Drum	4,092
1751-H Drum	4,775
1751-J Drum	5,457
1752-1 Drum	2,043
1752-2 Drum	6,140
1752-3 Drum	12,284
1752-4 Drum	16,380

FIGURE 5-1

NUMBER OF SECTORS ON MASS MEMORY DEVICES

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The start of file space on the library unit is defined by ADRFMS as described in Article 1.79.

5.1.1.2 Size and Start of File Space on File Manager Unit 0, Where Unit 0 is Not the Library Unit

In this case FISLU specifies a logical unit other than the library unit and there is no file space on the library unit. Referring to Article 5.1.1.1, the minimal requirement for file space size is the same. However, the upper bound on file space size is different from the upper bound imposed by the library unit. A non-library mass memory unit in a released system contains one of the following:

- 1} scratch only,
 - 2} scratch and software buffer areas
 - 3} software buffer area{s} and file space,
 - 4} file space only,
- or
- 5} user data only

5-6A
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For a non-library unit, NUMFSn must be small enough so that any required scratch, software buffer areas, or user data together with the file space will fit on the unit. The scratch requirements were discussed in Article 5.1.1.1. If there is software buffering in the system, then one or more of the following sections will appear in SYSDAT:

- Section ABA, 1713 Paper Tape Software Buffered Physical Device Table
- Section ABV, 1728-430 Punch Software Buffered Physical Device Table
- Section ABY, 1777/1723 Paper Tape Punch Software Buffered Physical Device Table
- Section ACA, 364-4 Communications Multiplexor Software Buffered Physical Device Table

To determine if there is software buffering on a particular logical unit, it is necessary to examine the value of MMLU, logical unit number, in each software buffered physical device table in SYSDAT. The size and position of a given software buffer may be determined by examining the values of FMSB, FLSB, and LLSB in the BUFFER macro expansion. {Refer to Article 1.27.} All software buffers are placed together after the end of scratch on a non-library unit. Thus, to determine the beginning of the software buffer area on a non-library unit, it is only necessary to consider the pair FMSB, FLSB which have minimum value as a pair. For this pair the sector, S , and word within the sector W_S may be computed as follows:

$$S = \left[\frac{\{FMSB \cdot 8000_{16} + FLSB\}}{96} \right] + 1$$

$$W_S = FMSB \cdot 8000_{16} + FLSB - 96 \cdot \{S - 1\}$$

Where $\lfloor x \rfloor$ is the greatest integer less than or equal to x . For example, if FMSB is 1 and FLSB is $6D58_{16}$, then S is 633 and W_S is 88. Thus the number of sectors required for software buffering on this unit is

$$B = T' - \{S - 1\}$$

Where T' is the total sectors on this unit {from Figure 5-1}.

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Let T' = total sectors on this unit {from Figure 5-1}
 B = total sectors needed for software buffering on
this unit
 S' = number of sectors needed for scratch on this unit
 U = number of sectors required for user tables.

 M' = number of sectors required for FIS directory.
FIS blocks and any file records or file indexed
directories to be stored on this unit {Refer to
Article 5.1.1.1}.

Then it is required that

$$M' \leq \text{NUMFS0} \leq T' - \{B + S' + U\}$$

The positioning of file space may be anywhere on the unit as long as sufficient space is left for any necessary scratch, user tables, and/or software buffers. Thus, in general, the value of ADRFMS will not be one of the entries in Figure 1-82, since those entries are based on the assumption that File Manager Unit 0 is the library unit. A typical entry defining ADRFMS would be

ADRFMS ADC 1

5.1.1.3 Size and Start of File Space on File Manager Unit n , $n > 0$

The size of file space on any File Manager unit other than the unit specified by FISLU is determined in the manner described in Article 5.1.1.2 except that the minimum file space required does not include space for the FIS directory and FIS blocks.

Let

T^n = total sectors on this unit {from Figure 5-1}

B' = total sectors needed for software buffering on this unit

S^n = number of sectors needed for scratch on this unit

U' = number of sectors required for user tables on this unit

M^n = number of sectors required for any file records and file indexed directories to be stored on this unit.

Then it is required that

$$M^n \leq \text{NUMFS}_n \leq T^n - \{B' + S^n + U'\}$$

The methods of evaluating T^n , B' , S^n and U' are the same as for evaluating T' , B , S' and U , as described in Article 5.1.1.2.

The start of file space on this unit is designated by BEGLUn. File space may begin at any sector as long as sufficient space is left for any necessary scratch, user tables, and/or software

buffers. A typical entry for File Manager unit 1 would be

```
EQU BEGLU1{1}
```

5.1.2 Section ADB, Job Processor File Parameters

When adding the File Manager it is necessary to delete the dummy definition of RELFIL in this section to avoid duplicate entry points in the system. The records to be deleted are those shown in lines *ABD0100 and *ABD0200 of Figure 1-83.

5.1.3 Section ADC, Presets

Records *ADC0210 through *ADC0710 of Figure 1-84 must be added to Section ADC so that the File Manager request processors may be accessed by unprotected programs.

5.2

SKELETON CHANGES REQUIRED

It may be necessary to change the value of N4, the length of allocatable area 4. File Manager requirements for the size of area 4 are discussed in Article 1.5B. The form of the *S statement which determines the value of N4 is shown in Article 2.2.

The File Manager requires the addition of the core resident File Manager modules as listed in Group G, Article 2.1, and the mass resident File Manager modules as listed in Group R, Article 2.1. The accompanying *S statements which determine the values of FRMP01, FRMP02, ..., FMRM12 are shown in Article 2.2.

The values of BGNMON and END0V4 must be decreased to allow room for the core resident File Manager modules. {Refer to Article 2.2.}

Skeleton modifications are shown in the example in Article 5.3.3.



5.3 Example of File Manager Addition

The example described in this section is the addition of the File Manager to the system built in the Chapter 4 example. The capability to use cards, as added in Chapter 4, will be utilized to illustrate the method of system customization by use of cards.

5.3.1 Dumping SYSDAT and the Skeleton onto Cards

In the Chapter 4 example, a COSY tape of SYSDAT was created. To convert the COSY information to Hollerith and dump it onto cards, the following control cards may be read from the card reader following a *BATCH request from the teletype:

```
*JOB
*COZY
SYSDAT  DCK/ I=06, H=11
        END/
```

To dump the skeleton from the previous example onto cards, the program SKED is used to build the skeleton from the previous install file tape and to dump the information onto cards.

5.3.2 Modification of SYSDAT

● Addition of Second Mass Memory Unit

In order to have more than one File Manager mass memory unit in this example, a second disk drive was added to the system. Specifically, a 1738-B53 was added. The addition of this

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device necessitates changes in the following sections of SYSDAT:

- LOG1A Logical Units Table, Section AAM
- LOG1 Logical Units Table, Section AAN
- LOG2 Logical Units Table, Section AA0
- Diagnostic Tables, Section AAP
- 1738-853/854 Physical Device Table, Section ABJ

The above sections would not normally be modified when adding a File Manager. Therefore the SYSDAT modifications of these sections will not be shown here, except for the LOG1A Table.

The previous LOG1A Table is shown in Chapter 4. The new table includes logical unit 20, the 1738-853/854 Disk, unit 1; and logical unit 21, the diagnostic logical unit for the 1738-853/854 Disk, unit 1. After modification, the LOG1A Table is as follows:

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*
*
*
LOGICAL UNIT TABLES (LOG1A)

ENT	LOG1A	PHYSICAL DEVICES ADDRESSES BY LOGICAL UNIT		
FNT	NUMLU			
LOG1A	ADC	NUMLU	NUMBER OF LOGICAL UNITS	00595
	ADC	PCORE	1 CORE ALLOCATOR	00596
	ADC	PDDUMMY	2 DUMMY LOGICAL UNIT	00597
	ADC	PDDUMMY	3 DUMMY LOGICAL UNIT	00598
	ADC	P1711	4 1711 TELETYPE, 713-10 CRT	00599
	ADC	PCOSY1	5 COSY DRIVER, FIRST UNIT	00600
	ADC	P731U0	6 UNBUFFERED 1731-601 MAG TAPE, UNIT 0	00601
	ADC	PDDUMMY	7 DUMMY LOGICAL UNIT	00602
	ADC	P17380	8 1738 853/4 DISK, UNIT 0	00603
	ADC	P40421	9 1740-501/1742 LINE PRINTER	00604
	ADC	P1728	10 1728/430 CARD READER	***00605
	ADC	P1728	11 1728/430 CARD PUNCH	***00606
FTN740	ADC	P40421	12 1740-501/1742 FORTRAN LINE PRINTER	00607
	ADC	P731U1	13 UNBUFFERED 1731-601 MAG TAPE, UNIT 1	00608
X1711	ADC	P1711	14 DIAGNOSTIC 1711 TELETYPE, 713-10 CRT	00609
X17380	ADC	P17380	15 DIAGNOSTIC 1738 853/4 DISK, UNIT 0	00610
X40421	ADC	P40421	16 DIAGNOSTIC 1740-501/1742 LINE PRINTER	00611
X731U1	ADC	P731U1	17 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 1	00612
X731U0	ADC	P731U0	18 DIAGNOSTIC 1731-601 MAG TAPE, UNIT 0	00613
X1728	ADC	P1728	19 DIAGNOSTIC 1728/430 CARD READER/PUNCH	***00614
	ADC	P17381	20 1738 853/4 DISK, UNIT 1	
X17381	ADC	P17381	21 DIAGNOSTIC 1738 853/4 DISK, UNIT 1	
NUMLU	EQU	NUMLU(*-LOG1A-1)		00615



● File Manager Parameters

File Manager Data, Section ADA, must be changed to define those parameters required by the File Manager. Prior to the addition of the File Manager, Section ADA was as follows:

M I S C E L L A N E O U S	I N F O R M A T I O N	01414	
F I L E	M A N A G E R	D A T A	01415
			01416
			01417
THESE ENTRIES ALLOW PROPER SYSTEM LINKAGE FOR IF THE FILE			01418
MANAGER IS NOT SELECTED.			01419
ENT	FSLIST		01421
ENT	ADRFMS,NUMFS0		01422
EQU	FSLIST(\$7FFF)		01423
EQU	ADRFMS(\$7FFF),NUMFS0(\$7FFF)		01424
ENT	BEGLU1,NUMFS1		01425
EQU	BEGLU1(\$7FFF),NUMFS1(\$7FFF)		01426
ENT	BEGLU2,NUMFS2		01427
EQU	BEGLU2(\$7FFF),NUMFS2(\$7FFF)		01428
ENT	BEGLU3,NUMFS3		01429
EQU	BEGLU3(\$7FFF),NUMFS3(\$7FFF)		01430
ENT	BEGLU4,NUMFS4		01431
EQU	BEGLU4(\$7FFF),NUMFS4(\$7FFF)		01432
ENT	BEGLU5,NUMFS5		01433
EQU	BEGLU5(\$7FFF),NUMFS5(\$7FFF)		01434
ENT	BEGLU6,NUMFS6		01435
EQU	BEGLU6(\$7FFF),NUMFS6(\$7FFF)		01436
ENT	BEGLU7,NUMFS7		01437
EQU	BEGLU7(\$7FFF),NUMFS7(\$7FFF)		01438
ENT	BEGLU8,NUMFS8		01439
EQU	BEGLU8(\$7FFF),NUMFS8(\$7FFF)		01440

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The parameters in this section are defined according to the description in Article 5.1.1. In this example, there are two File Manager mass memory units.

● Library Unit {Unit 0} File Space

The File Manager will principally use the library unit for storage of the following:

FIS Directory	1 sector
FIS Blocks	3 sectors each
Non-indexed file records	1 sector each

Therefore a file space list of one-sector segments and three sector segments is defined. A relatively small amount of file space is to be used on the library unit. The value selected for NUMFS0 is 1000 sectors. According to Article 5.1.1, the starting sector of file space on the library unit is defined by BEGFMS when the library unit is a 1738-853/854 Disk. The parameters defining file space on the library unit are shown in Figure 5-2.

● Unit 1 File Space

File Manager Unit 1 will be used for the storage of indexed as well as non-indexed files. The indexed files will

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principally have a key length of four words and will not be indexed linked. The number of expected key values for any of these files is between 1000 and 2000. According to the File Manager Reference Manual, Section A. 4.1, a KIS Directory for such a file will have a length defined as

$$\lceil \frac{4 + \text{SRNEKV}}{96} \rceil \text{ Sectors}$$

where $\lceil x \rceil$ is the least integer greater than or equal to x .

Thus, the KIS Directory length is between $\lceil \frac{4+31}{96} \rceil$ and

$$\lceil \frac{4+44}{96} \rceil$$

or one sector. According to Section A.4.2 of the File Manager Reference Manual, a KIS block for such a file will have a length of

$$\lceil \frac{3 + \{2 \cdot \text{NUMPTR} + \text{KEYLTH}\} \cdot \text{SRNEKV}}{96} \rceil \text{ sectors.}$$

This implies KIS block length is between

$$\lceil \frac{3 + 6 \cdot 31}{96} \rceil \quad \text{and} \quad \lceil \frac{3 + 6 \cdot 44}{96} \rceil$$

Some KIS blocks will, thus, be 2 sectors long and some will be 3 sectors long. Files stored on File Manager Unit 1 will have records of length 1, 2, 3 and 4 sectors. For these reasons the file space list will contain segments of 1, 2, 3, and 4 sectors on this unit. The entire unit is to be devoted to file space. Therefore BEGLU1=1. The value of NUMFS1 is 15,995, as obtained from Figure 5-1. The parameters defining file space on this unit are shown in Figure 5-2.

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M I S C E L L A N E O U S I N F O R M A T I O N
 *
 *
 *
 *
 F I L E M A N A G E R D A T A

01414
 01415
 01416
 01417

ENT FISLU LOGICAL UNIT OF FIS DIRECTORY AND BLOCKS
 ENT MAXMMA MAXIMUM NO. OF MASS MEMORY ATTEMPTS ON ERROR
 ENT RPTPER REQUEST PROCESSOR TIMEOUT PERIOD
 ENT FIDSEC FIS DIRECTORY, S SECTOR ADDRESS
 ENT FIBLSA SECTOR ADDRESS OF LAST FIS BLOCK
 ENT FOTPER FILE/DIRECTORY TIMEOUT PERIOD
 ENT FIBNIX INDEX TO THE NEXT AVAILABLE LOCATION IN FIBLSA
 ENT FSLIST START OF FILE SPACE LIST
 ENT FSLLEN FILE SPACE LIST LENGTH
 ENT FSELEND END OF FILE SPACE LIST
 ENT ADRFMS BEGINNING OF FILE MANAGER SPACE ON LIB UNIT

EQU FISLU(LBUNIT)
 EQU MAXMMA(1)
 EQU FOTPER(10) (1/10 SEC.)
 EQU RPTPER(10) (1/10 SEC.)

EXT BEGFMS
 ADRFMS ADC BEGFMS BEGINNING OF FILE MANAGER SPACE ON LIB UNIT
 ***** THE FOLLOWING MUST BE IN ORDER *****
 FIDSEC ADC 0
 FIBLSA ADC 0
 FIBNIX ADC 0
 FSLIST EQU FSLIST(*)

***** START OF LOGICAL UNIT ENTRIES *****

LOGICAL UNIT DATA, UNIT 0
 ENT NUMFS0
 EQU NUMFS0(1000) NUMBER OF FILE SECTORS - UNIT 0
 LUE0 VFD X9/LUE0,X7/LBUNIT LU ENTRY LENGTH(7-15), LOGICAL UNIT(0-6)
 ADC 0 ADDRESS OF FILE SPACE POOL
 ADC 0 NUMBER OF AVAILABLE SECTORS
 ADC NUMFS0 NUMBER OF SECTORS IN THIS FILE SPACE
 NUM 0,1 THREAD OF ONE SECTOR LONG
 NUM 0,3 THREAD OF THREE SECTORS LONG
 LUE0 EQU LUE0(*-LUE0)

FIGURE 5-2 SYSDAT Section ADA, File Manager Data, After
 Addition of File Manager

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LOGICAL UNIT DATA, UNIT 1

```

ENT BEGLU1
ENT NUMFS1
EQU LUNIT1(20) LOGICAL UNIT OF FILE MANAGER UNIT 1
EQU BEGLU1(1) BEGINNING FILE SECTOR - UNIT 1
EQU NUMFS1(15995) NUMBER OF FILE SECTORS - UNIT 1

LUE1 VFD X9/LUEL1,X7/LUNIT1 LU ENTRY LENGTH(7-15), LOGICAL UNIT(0-6)
ADC BEGLU1 ADDRESS OF FILE SPACE POOL
ADC 0 NUMBER OF AVAILABLE SECTORS
ADC NUMFS1 NUMBER OF SECTORS IN THIS FILE SPACE
NUM 0,1 THREAD OF ONE SECTOR LONG

NUM 0,2 THREAD OF TWO SECTORS LONG

NUM 0,3 THREAD OF THREE SECTORS LONG

NUM 0,4 THREAD OF FOUR SECTORS LONG

LUEL1 EQU LUEL1(*-LUE1)

```

Figure 5-2 Continued

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● Section ADB, Job Processor File Parameters and Section
ADC, Presets

Sections ADB and ADC are modified as described in Article 5.1.2 and 5.1.3.

5.3.3 Modifying the Skeleton

The skeleton is modified according to the procedure described in Article 5.2. In this case it is unnecessary to modify the value of N4, since N4 is already large enough to allow File Manager requests from the background. In this example BGNMON and ENDOV4 are each decreased by 500₁₆ to allow space for the File Manager core resident modules. The system construction date is modified by changing SYSMON, SYSDAY, and SYSYER. The skeleton before modification is the second of the two skeletons listed in Article 4.3.3. The skeleton after modification is as follows:

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1  *S,SYSLVL,$3836
2  *S,N4,$3000
3  *S,SYSMON,$3130
4  *S,SYSDAY,$3234
5  *S,SYSYER,$3734
6  *V
7  *V      1700 MASS STORAGE OPERATING SYSTEM - VER. 4.1
8  *V
9  *V      ANN'S TEST SYSTEM
10 *V
11 *YM,LIBEOT,1
12 *YM,LOADSD,2
13 *YM,JOBENT,3
14 *YM,JOBPRO,4
15 *YM,PROTEC,5
16 *YM,JPLOAD,6
17 *YM,JPCHGE,7
18 *YM,JPT13,8
19 *YM,JCRDV4,9
20 *YM,JLGOV4,10
21 *YM,JPSTV4,11
22 *YM,NAMEV4,12
23 *YM,JPFLV4,13
24 *YM,AFILV4,14
25 *YM,RESTOR,15
26 *YM,RCOVER,16
27 *YM,BRKPT,17
28 *YM,OBJEBUG,18
29 *YM,SYSCOP,19
30 *YM,SYSSEG,20
31 *YM,MIPRO,21
32 *YM,TDFUNC,22
33 *YM,EFSTOR,23
34 *YM,EFLIST,24
35 *YM,SCMM17,25
36 *YM,VERIFY,26
37 *YM,DUMMY1,27
38 *YM,DUMMY2,28
39 *YM,DUMMY3,29
40 *YM,DUMMY4,30
41 *YM,DUMMY5,31
42 *YM,DUMMY6,32
43 *YM,DUMMY7,33
44 *YM,DUMMY8,34
45 *YM,DUMMY9,35
46 *YM,DUMMY0,36
47 *S,ENDOV4,$66FF
48 *S,BGNMON,$6700
49 *S,MSIZV4,$7FFF
50 *S,SECTOR,$7EDA
51 *
52 *L      SYSTEM DATA PROGRAM
53 *B 'SYSDAT' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
54 *L      SPACE REQUEST PROCESSOR
55 *B 'SPACE' ' DECK-ID A02  MSOS 4.1'
56 *
57 *      SYSTEM CORE RESIDENT PROGRAMS
58 *
59 *LP     MONITOR
60 *B 'NMONI' ' DECK-ID A03  MSOS 4.1'

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61	*B	'NDISP'	'	DECK-ID A04	MSOS 4.1'
62	*B	'RW'	'	DECK-ID A06	MSOS 4.1'
63	*B	'T14'	'	DECK-ID A07	MSOS 4.1'
64	*B	'T16'	'	DECK-ID A08	MSOS 4.1'
65	*B	'PARAME'	'	DECK-ID A09	MSOS 4.1'
66	*B	'COMMON'	'	DECK-ID A10	MSOS 4.1'
67	*B	'NIPROC'	'	DECK-ID A11	MSOS 4.1'
68	*B	'ALVOL'	'	DECK-ID A13	MSOS 4.1'
69	*R	'OFVOL'	'	DECK-ID A14	MSOS 4.1'
70	*B	'ALCORE'	'	DECK-ID A15	MSOS 4.1'
71	*B	'DCORE'	'	DECK-ID A16	MSOS 4.1'
72	*B	'NFNR'	'	DECK-ID A18	MSOS 4.1'
73	*B	'NCMPRQ'	'	DECK-ID A19	MSOS 4.1'
74	*B	'MAKQ'	'	DECK-ID A20	MSOS 4.1'
75	*B	'ADEV'	'	DECK-ID A21	MSOS 4.1'
76	*B	'TMINT'	'	DECK-ID A22	MSOS 4.1'
77	*B	'DTIMER'	'	DECK-ID A23	MSOS 4.1'
78	*B	'TOD'	'	DECK-ID A24	MSOS 4.1'
79	*B	'MINT'	'	DECK-ID A25	MSOS 4.1'
80	*B	'TRVEC'	'	DECK-ID 001	MSOS 4.1'
81	*LP	DEBUGGING / CHECKOUT			
82	*B	'SNAPOL'	'	DECK-ID H01	MSOS 4.1'
83	*B	'DMP421'	'	DECK-ID H03	MSOS 4.1'
84	*B	'BOK85X'	'	DECK-ID H05	MSOS 4.1'
85	*LP	FILE MANAGER			
86	*B	'FILMGR'	'	DECK-ID F01	MSOS 4.1'
87	*B	'RSPCV4'	'	DECK-ID F02	MSOS 4.1'
88	*B	'SRHFIS'	'	DECK-ID F03	MSOS 4.1'
89	*LP	CORE RESIDENT DRIVERS			
90	*B	'EFDATA'	'	DECK-ID C01	MSOS 4.1'
91	*B	'DUMMY'	'	DECK-ID C02	MSOS 4.1'
92	*B	'ALAQ'	'	DECK-ID C03	MSOS 4.1'
93	*B	'D1711'	'	DECK-ID C05	MSOS 4.1'
94	*B	'D1738'	'	DECK-ID C08	MSOS 4.1'
95	*B	'REWCK'	'	DECK-ID C13	MSOS 4.1'
96	*B	'MMEXEC'	'	DECK-ID C15	MSOS 4.1'
97	*B	'NXTLOC'	'	NEXT AVAILABLE LOCATION'	
98	*				
99	*	SYSTEM MASS RESIDENT PROGRAMS			
100	*				
101	*M	LIBEDT			1
102	*B	'LIBEDT'	'	DECK-ID D02	MSOS 4.1'
103	*M	LOADSD			2
104	*B	'LOAD1'	'	DECK-ID D03	MSOS 4.1'
105	*B	'BRNCH1'	'	DECK-ID D04	MSOS 4.1'
106	*B	'LIDRV1'	'	DECK-ID D05	MSOS 4.1'
107	*B	'LCDRV1'	'	DECK-ID D06	MSOS 4.1'
108	*B	'LMDRV1'	'	DECK-ID D07	MSOS 4.1'
109	*B	'LLDRV1'	'	DECK-ID D08	MSOS 4.1'
110	*B	'ADJOF1'	'	DECK-ID D09	MSOS 4.1'
111	*B	'CNVRT1'	'	DECK-ID D10	MSOS 4.1'
112	*B	'LSTOT1'	'	DECK-ID D11	MSOS 4.1'
113	*B	'LINK11'	'	DECK-ID D12	MSOS 4.1'
114	*B	'LOADR1'	'	DECK-ID D13	MSOS 4.1'
115	*B	'NAMPR1'	'	DECK-ID D14	MSOS 4.1'
116	*B	'RBOBZ1'	'	DECK-ID D15	MSOS 4.1'
117	*B	'BNTX1'	'	DECK-ID D16	MSOS 4.1'
118	*B	'XFRPR1'	'	DECK-ID D17	MSOS 4.1'
119	*B	'STBASE'	'	DECK-ID D18	MSOS 4.1'
120	*B	'LNKENT'	'	DECK-ID D19	MSOS 4.1'

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121	*B	'LNKCR1'	DECK-ID 020	MSOS 4.1'
122	*B	'PATCH'	DECK-ID 021	MSOS 4.1'
123	*B	'TBSCH1'	DECK-ID 022	MSOS 4.1'
124	*B	'HASH'	DECK-ID 023	MSOS 4.1'
125	*B	'TBSTR1'	DECK-ID 024	MSOS 4.1'
126	*B	'PAGE'	DECK-ID 025	MSOS 4.1'
127	*B	'PROGLD'	DECK-ID 026	MSOS 4.1'
128	*B	'SCAN1'	DECK-ID 027	MSOS 4.1'
129	*B	'CHPU1'	DECK-ID 028	MSOS 4.1'
130	*B	'ADJOV2'	DECK-ID 029	MSOS 4.1'
131	*B	'ADRPR1'	DECK-ID 030	MSOS 4.1'
132	*M		JOBENT 3	
133	*B	'JOBENT'	DECK-ID 031	MSOS 4.1'
134	*B	'T11'	DECK-ID 032	MSOS 4.1'
135	*B	'T7'	DECK-ID 033	MSOS 4.1'
136	*B	'T5'	DECK-ID 034	MSOS 4.1'
137	*B	'T3'	DECK-ID 035	MSOS 4.1'
138	*S,N1,P			
139	*M		JORPRO 4	
140	*B	'JOBPRO'	DECK-ID 036	MSOS 4.1'
141	*B	'ONE'	DECK-ID 037	MSOS 4.1'
142	*B	'TWO'	DECK-ID 038	MSOS 4.1'
143	*B	'THREE'	DECK-ID 039	MSOS 4.1'
144	*S,N2,P			
145	*M		PROTEC 5	
146	*B	'BPROTK'	DECK-ID 041	MSOS 4.1'
147	*B	'JBKILL'	DECK-ID 042	MSOS 4.1'
148	*M		JPLOAD 6	
149	*B	'JPLOAD'	DECK-ID 043	MSOS 4.1'
150	*M		JPCHGE 7	
151	*B	'JPCHGE'	DECK-ID 044	MSOS 4.1'
152	*B	'ASCHEX'	DECK-ID 045	MSOS 4.1'
153	*M		JPT13 8	
154	*B	'T13'	DECK-ID 046	MSOS 4.1'
155	*M		JCRDV4 9	
156	*B	'JCRDV4'	DECK-ID 047	MSOS 4.1'
157	*M		JLGOV4 10	
158	*B	'JLGOV4'	DECK-ID 048	MSOS 4.1'
159	*M		JPSTV4 11	
160	*B	'JPSTV4'	DECK-ID 049	MSOS 4.1'
161	*M		NAMEV4 12	
162	*B	'NAMEV4'	DECK-ID 050	MSOS 4.1'
163	*M		JPFLV4 13	
164	*B	'JPFLV4'	DECK-ID 051	MSOS 4.1'
165	*M		AFILV4 14	
166	*B	'JPF2V4'	DECK-ID 052	MSOS 4.1'
167	*M		RESTOR 15	
168	*B	'RESTOR'	DECK-ID 053	MSOS 4.1'
169	*M		RCOVER 16	
170	*B	'RCOVER'	DECK-ID H09	MSOS 4.1'
171	*B	'OUTSEL'	DECK-ID H10	MSOS 4.1'
172	*B	'RDMPV4'	DECK-ID H11	MSOS 4.1'
173	*B	'NASDMP'	DECK-ID H12	MSOS 4.1'
174	*M		BRKPT 17	
175	*B	'BRKPTD'	DECK-ID H13	MSOS 4.1'
176	*B	'SIFT'	DECK-ID H14	MSOS 4.1'
177	*B	'BIASCI'	DECK-ID H15	MSOS 4.1'
178	*B	'RETJMP'	DECK-ID H16	MSOS 4.1'
179	*B	'JUMPTO'	DECK-ID H17	MSOS 4.1'
180	*B	'ENTER'	DECK-ID H18	MSOS 4.1'

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181	*B	'ENTCOR'	'	DECK-ID H19	MSOS	4.1'
182	*B	'PRTREG'	'	DECK-ID H20	MSOS	4.1'
183	*B	'TERMIN'	'	DECK-ID H21	MSOS	4.1'
184	*B	'RESUME'	'	DECK-ID H22	MSOS	4.1'
185	*B	'DPCORB'	'	DECK-ID H23	MSOS	4.1'
186	*B	'MSDMPB'	'	DECK-ID H24	MSOS	4.1'
187	*B	'SETAPP'	'	DECK-ID H25	MSOS	4.1'
188	*M			ODEBUG		18
189	*B	'ODEBUG'	'	DECK-ID H26	MSOS	4.1'
190	*M			SYSCOP		19
191	*B	'SYSCOP'	'	DECK-ID H27	MSOS	4.1'
192	*M			SYSSEG		20
193	*B	'CO1ST'	'	DECK-ID H28	MSOS	4.1'
194	*B	'CO2ND'	'	DECK-ID H29	MSOS	4.1'
195	*B	'CO3RD'	'	DECK-ID H30	MSOS	4.1'
196	*B	'COLAST'	'	DECK-ID H31	MSOS	4.1'
197	*M			MIPRO		21
198	*B	'MIPRO'	'	DECK-ID A26	MSOS	4.1'
199	*M			TDFUNC		22
200	*B	'TDFUNC'	'	DECK-ID A27	MSOS	4.1'
201	*M			EFSTOR		23
202	*B	'EFSTOR'	'	DECK-ID A28	MSOS	4.1'
203	*M			EFLIST		24
204	*B	'EFLIST'	'	DECK-ID A29	MSOS	4.1'
205	*M			SCMM17		25
206	*B	'SCMEXC'	'	DECK-ID E01	MSOS	4.1'
207	*M			VERIFY		26
208	*B	'VEPFY1'	'	DECK-ID A30	MSOS	4.1'
209	*M			DUMMY1		27
210	*M			DUMMY2		28
211	*M			DUMMY3		29
212	*M			DUMMY4		30
213	*M			DUMMY5		31
214	*M			DUMMY6		32
215	*M			DUMMY7		33
216	*M			DUMMY8		34
217	*M			DUMMY9		35
218	*M			DUMMY0		36
219	*					
220	*			MASS RESIDENT DRIVERS		
221	*					
222	*M			COSY DRIVER		
223	*B	'DCOSY'	'	DECK-ID C28	MSOS	4.1'
224	*S	'COSY,S				
225	*P	'COSY,P				
226				1731 601 MAG TAPE		
227	*B	'D1731U'	'	DECK-ID C33	MSOS	4.1'
228	*B	'FRWA'	'	DECK-ID C34	MSOS	4.1'
229	*B	'FRWB'	'	DECK-ID C35	MSOS	4.1'
230	*B	'RWBA'	'	DECK-ID C36	MSOS	4.1'
231	*B	'MAXPVU'	'	DECK-ID C37	MSOS	4.1'
232	*S	'S1731U,S				
233	*S	'L1731U,P				
234	*M			1740-501/1742 LINE PRINTER		
235	*B	'D40421'	'	DECK-ID C46	MSOS	4.1'
236	*S	'S40421,S				
237	*S	'L40421,P				
238	*M					
239	*B	'D1728'	'	DECK-ID C48	MSOS	4.1'
240	*B	'CP026'	'	DECK-ID C54	MSOS	4.1'

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241 *B 'CR026' ' DECK-ID C52 MSOS 4.1'
242 *S,S1728,S
243 *S,L1728,P
244 *
245 * MASS RESIDENT FILE MANAGER
246 *
247 *M
248 *B 'DEFFIL' ' DECK-ID F05 MSOS 4.1'
249 *B 'FILSPC' ' DECK-ID F06 MSOS 4.1'
250 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
251 *S,FMRP01,S
252 *M
253 *B 'RELFIL' ' DECK-ID F08 MSOS 4.1'
254 *B 'RELSPC' ' DECK-ID F09 MSOS 4.1'
255 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
256 *S,FMRP02,S
257 *M
258 *B 'DEFIDX' ' DECK-ID F10 MSOS 4.1'
259 *B 'SQRTFM' ' DECK-ID F11 MSOS 4.1'
260 *B 'FILSPC' ' DECK-ID F06 MSOS 4.1'
261 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
262 *S,FMRP03,S
263 *M
264 *B 'LOKFIL' ' DECK-ID F12 MSOS 4.1'
265 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
266 *S,FMRP04,S
267 *M
268 *B 'UNLFIL' ' DECK-ID F13 MSOS 4.1'
269 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
270 *S,FMRP05,S
271 *M
272 *B 'STOSEQ' ' DECK-ID F14 MSOS 4.1'
273 *B 'FILSPC' ' DECK-ID F06 MSOS 4.1'
274 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
275 *S,FMRP06,S
276 *M
277 *B 'STODIR' ' DECK-ID F15 MSOS 4.1'
278 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
279 *S,FMRP07,S
280 *M
281 *B 'STOIDX' ' DECK-ID F16 MSOS 4.1'
282 *B 'HASHCD' ' DECK-ID F17 MSOS 4.1'
283 *B 'GETKID' ' DECK-ID F18 MSOS 4.1'
284 *B 'FILSPC' ' DECK-ID F06 MSOS 4.1'
285 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
286 *S,FMRP08,S
287 *M
288 *B 'RTVSEQ' ' DECK-ID F19 MSOS 4.1'
289 *B 'RTNSPC' ' DECK-ID F20 MSOS 4.1'
290 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
291 *S,FMRP09,S
292 *M
293 *B 'RTVDIR' ' DECK-ID F21 MSOS 4.1'
294 *B 'RTNSPC' ' DECK-ID F20 MSOS 4.1'
295 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
296 *S,FMRP10,S
297 *M
298 *B 'RTVIDX' ' DECK-ID F22 MSOS 4.1'
299 *B 'HASHCD' ' DECK-ID F17 MSOS 4.1'
300 *B 'GETKID' ' DECK-ID F18 MSOS 4.1'

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301 *B 'RTNSPC' ' DECK-ID F20 MSOS 4.1'
302 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
303 *S,FMRP11,S
304 *M
305 *B 'RTVIDO' ' DECK-ID F23 MSOS 4.1'
306 *B 'GETKID' ' DECK-ID F18 MSOS 4.1'
307 *B 'RTNSPC' ' DECK-ID F20 MSOS 4.1'
308 *B 'RPEND' ' DECK-ID F07 MSOS 4.1'
309 *S,FMRP12,S
310 *M
311 *B 'FMDUMY' ' DECK-ID F04 MSOS 4.1'
312 *S,FMREND,S
313 *S,BEGFMS,S SPECIFY THE SYSTEM FILE SPACE
314 *M,BEGFMS+1000
315 *M
316 *B 'FMDUMY' ' DECK-ID F04 MSOS 4.1'
317 *T END OF SYSTEM
318 *JOB,INSTAL,SYSTEM
319 *K,I6
320 *LIBEDT
321 *K,I6
322 *V DEFINE REQUEST PRIORITIES
323 *S,001,03,M
324 *S,002,00,M
325 *S,003,01,M
326 *S,004,02,M
327 *S,005,03,M
328 *S,006,02,M
329 *S,007,02,M
330 *S,008,02,M
331 *S,009,02,M
332 *S,010,02,M
333 *S,011,02,M
334 *S,012,03,M
335 *S,013,03,M
336 *S,014,03,M
337 *S,015,02,M
338 *S,016,03,M
339 *S,017,03,M
340 *S,018,04,M
341 *S,019,04,M
342 *S,020,04,M
343 *S,021,04,M
344 *S,022,04,M
345 *S,023,04,M
346 *S,024,04,M
347 *S,025,04,M
348 *S,026,04,M
349 *S,027,04,M
350 *S,028,04,M
351 *S,029,04,M
352 *S,030,04,M
353 *S,031,04,M
354 *S,032,04,M
355 *S,033,04,M
356 *S,034,04,M
357 *S,035,04,M
358 *S,036,04,M
359 *V 1700 MACRO ASSEMBLER 3.
360 *K,I6
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361 *L,LIBMAC
362 *B 'LIBMAC' ' DECK-ID G01 MSOS 4.1'
363 *L,ASSEM
364 *B 'ASSEM' ' DECK-ID G02 MSOS 4.1'
365 *K,P8
366 *P,F
367 *B 'PASS1' ' DECK-ID G03 MSOS 4.1'
368 *B 'PA1PR2' ' DECK-ID G04 MSOS 4.1'
369 *T
370 *K,I8
371 *N,PASS1,,,B
372 *K,I6
373 *K,P8
374 *P,F
375 *B 'PASS2' ' DECK-ID G05 MSOS 4.1'
376 *B 'PA2PR2' ' DECK-ID G06 MSOS 4.1'
377 *T
378 *K,I8
379 *N,PASS2,,,B
380 *K,I6
381 *K,P8
382 *P,F
383 *B 'PASS3' ' DECK-ID G07 MSOS 4.1'
384 *B 'PA3PR2' ' DECK-ID G08 MSOS 4.1'
385 *B 'PA3PR3' ' DECK-ID G09 MSOS 4.1'
386 *T
387 *K,I8
388 *N,PASS3,,,B
389 *K,I6
390 *K,P8
391 *P,F
392 *B 'TABLST' ' DECK-ID G10 MSOS 4.1'
393 *T
394 *K,I8
395 *N,TABLST,,,B
396 *K,I6
397 *K,P8
398 *P,F
399 *B 'XREF' ' DECK-ID G11 MSOS 4.1'
400 *K,I8
401 *N,XREF,,,B
402 *K,I6
403 *N,MACSKL,,,B
404 *B 'MACSKL'
405 *N,MACROS,,,B
406 *B 'MACPOS'
407 *V DEBUGGING AND CHECKOUT
408 *K,I6
409 *L,TRACE
410 *B 'TRACE' ' DECK-ID H32 MSOS 4.1'
411 *V SYSTEM UTILITY PROGRAMS
412 *K,I6
413 *L,LULIST
414 *B 'LULIST' ' DECK-ID J01 MSOS 4.1'
415 *L,LISTR
416 *B 'LISTR' ' DECK-ID J02 MSOS 4.1'
417 *L,OPSORT
418 *B 'OPSORT' ' DECK-ID J03 MSOS 4.1'
419 *L,EESORT
420 *B 'EESORT' ' DECK-ID J04 MSOS 4.1'

```

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421 *L,COSY
422 *R 'COSY' ' DECK-ID J05 MSOS 4.1'
423 *L,LCOSY
424 *R 'LCOSY' ' DECK-ID J06 MSOS 4.1'
425 *L,CYFT
426 *R 'CYFT' ' DECK-ID J07 MSOS 4.1'
427 *L,IOUP
428 *R 'IOUP' ' DECK-ID J08 MSOS 4.1'
429 *K,P8
430 *P,F
431 *R 'IOUP' ' DECK-ID J08 MSOS 4.1'
432 *R 'IOUPV4' ' DECK-ID J09 MSOS 4.1'
433 *T
434 *K,I8
435 *N,IOUPV4,,,B
436 *K,I6
437 *L,DTLP
438 *R 'DTLP' ' DECK-ID J10 MSOS 4.1'
439 *K,P8
440 *P,F
441 *R 'DSKTAP' ' DECK-ID J11 MSOS 4.1'
442 *R 'DSKEQC' ' DECK-ID J12 MSOS 4.1'
443 *R 'DSKQHX' ' DECK-ID J13 MSOS 4.1'
444 *R 'DSKQDR' ' DECK-ID J14 MSOS 4.1'
445 *R 'DSKMTI' ' DECK-ID J15 MSOS 4.1'
446 *R 'DSKMTD' ' DECK-ID J16 MSOS 4.1'
447 *R 'DSKMMD' ' DECK-ID J18 MSOS 4.1'
448 *T
449 *K,I8
450 *N,DSKTAP,,,B
451 *K,I6
452 *L,SETPV4
453 *R 'SPCALL' ' DECK-ID J21 MSOS 4.1'
454 *K,P8
455 *P,F
456 *R 'SPOLY1' ' DECK-ID J22 MSOS 4.1'
457 *R 'STPV4' ' DECK-ID J23 MSOS 4.1'
458 *R 'IEPROR' ' DECK-ID J24 MSOS 4.1'
459 *R 'MCTDK' ' DECK-ID J25 MSOS 4.1'
460 *R 'GETPAG' ' DECK-ID J26 MSOS 4.1'
461 *R 'CONPRT' ' DECK-ID J27 MSOS 4.1'
462 *R 'REDCON' ' DECK-ID J28 MSOS 4.1'
463 *R 'CONDEC' ' DECK-ID J29 MSOS 4.1'
464 *R 'ORDERM' ' DECK-ID J30 MSOS 4.1'
465 *R 'IREAD' ' DECK-ID J31 MSOS 4.1'
466 *R 'ASCOUT' ' DECK-ID J32 MSOS 4.1'
467 *R 'PARAMS' ' DECK-ID J33 MSOS 4.1'
468 *R 'DISKIO' ' DECK-ID J34 MSOS 4.1'
469 *T
470 *K,I8
471 *N,STP1V4,,,B
472 *K,I6
473 *K,P8
474 *P,F
475 *R 'SPOLY2' ' DECK-ID J35 MSOS 4.1'
476 *R 'SUP' ' DECK-ID J36 MSOS 4.1'
477 *R 'IEPROR' ' DECK-ID J24 MSOS 4.1'
478 *R 'GETPAG' ' DECK-ID J26 MSOS 4.1'
479 *R 'BTOA' ' DECK-ID J37 MSOS 4.1'
480 *R 'ISTAT' ' DECK-ID J38 MSOS 4.1'

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481 *B 'SCIO' ' DECK-ID J39 MSOS 4.1'
482 *B 'SCRD' ' DECK-ID J40 MSOS 4.1'
483 *B 'REDCON' ' DECK-ID J28 MSOS 4.1'
484 *B 'ICAT' ' DECK-ID J41 MSOS 4.1'
485 *B 'BUFIN' ' DECK-ID J42 MSOS 4.1'
486 *B 'MOVE' ' DECK-ID J43 MSOS 4.1'
487 *B 'IREAD' ' DECK-ID J31 MSOS 4.1'
488 *B 'ASCOUT' ' DECK-ID J32 MSOS 4.1'
489 *B 'PARAMS' ' DECK-ID J33 MSOS 4.1'
490 *B 'DISKIO' ' DECK-ID J34 MSOS 4.1'
491 *T
492 *K,I8
493 *N,STP2V4,,,B
494 *V SCMM TEST ROUTINES
495 *K,I6
496 *K,P8
497 *P,F
498 *B 'SCMTTY' ' DECK-ID E02 MSOS 4.1'
499 *T
500 *K,I8
501 *N,SCMTTY,,,B
502 *K,I6
503 *K,P8
504 *P,F
505 *B 'SCMDK1' ' DECK-ID E08 MSOS 4.1'
506 *T
507 *K,I8
508 *N,SCMDK1,,,B
509 *K,I6
510 *K,P8
511 *P,F
512 *B 'SCMDVP' ' DECK-ID E09 MSOS 4.1'
513 *T
514 *K,I8
515 *N,SCMDVP,,,B
516 *K,I6
517 *K,P8
518 *P,F
519 *B 'SCMPRT' ' DECK-ID E11 MSOS 4.1'
520 *T
521 *K,I8
522 *N,SCMPRT,,,B
523 *K,I6
524 *K,P8
525 *P,F
526 *B 'SCMATT' ' DECK-ID E12 MSOS 4.1'
527 *T
528 *K,I8
529 *N,SCMATT,,,B
530 *K,I6
531 *K,P8
532 *P,F
533 *B 'SCMCRD' ' DECK-ID E03 MSOS 4.1'
534 *T
535 *K,I8
536 *N,SCMCRD,,,B
537 *V INSTALL LIBRARY BUILDER
538 *K,I6
539 *L,LIBILD
540 *B 'LIBILD' ' DECK-ID B01 MSOS 4.1'

```

541 *K,P8
542 *P,F
543 *B 'LIBIDO' ' DECK-ID 802 MSOS 4.1'
544 *B 'CONVRS' ' DECK-ID 803 MSOS 4.1'
545 *B 'MESSY' ' DECK-ID 804 MSOS 4.1'
546 *B 'LJA2B' ' DECK-ID 805 MSOS 4.1'
547 *B 'MOVECH' ' DECK-ID 806 MSOS 4.1'
548 *B 'PICKUP' ' DECK-ID 807 MSOS 4.1'
549 *B 'IOSUB' ' DECK-ID 808 MSOS 4.1'
550 *T
551 *K,I8
552 *N,LIBIDO,,,B
553 *K,I6
554 *K,P8
555 *P,F
556 *B 'HELPER' ' DECK-ID 809 MSOS 4.1'
557 *B 'MOVECH' ' DECK-ID 806 MSOS 4.1'
558 *B 'HELPO' ' DECK-ID 810 MSOS 4.1'
559 *B 'HELP1' ' DECK-ID 811 MSOS 4.1'
560 *B 'HELP2' ' DECK-ID 812 MSOS 4.1'
561 *B 'HELP3' ' DECK-ID 813 MSOS 4.1'
562 *B 'HELP4' ' DECK-ID 814 MSOS 4.1'
563 *B 'HELP5' ' DECK-ID 815 MSOS 4.1'
564 *B 'HELP8' ' DECK-ID 816 MSOS 4.1'
565 *B 'HELP9' ' DECK-ID 817 MSOS 4.1'
566 *B 'HELP10' ' DECK-ID 818 MSOS 4.1'
567 *B 'HELP11' ' DECK-ID 819 MSOS 4.1'
568 *B 'HELP12' ' DECK-ID 820 MSOS 4.1'
569 *B 'HELP13' ' DECK-ID 821 MSOS 4.1'
570 *B 'HELP14' ' DECK-ID 822 MSOS 4.1'
571 *T
572 *K,I8
573 *N,HELPER,,,B
574 *V INSTALL SKELETON EDITOR
575 *K,I6
576 *L,SKED
577 *B 'SKED' ' DECK-ID 823 MSOS 4.1'
578 *K,P8
579 *P,F
580 *B 'SKFILE' ' DECK-ID 824 MSOS 4.1'
581 *T
582 *K,I8
583 *N,SKFILE,,,B
584 *V SYSTEM INITIALIZER
585 *K,I6
586 *L,SILP
587 *B 'SILP' ' DECK-ID 825 MSOS 4.1'
588 *K,P8
589 *P,F
590 *B 'CONTRL' ' DECK-ID 826 MSOS 4.1'
591 *B 'ILOAD' ' DECK-ID 827 MSOS 4.1'
592 *B 'LDRTBL' ' DECK-ID 828 MSOS 4.1'
593 *B 'I1' ' DECK-ID 829 MSOS 4.1'
594 *B 'I2' ' DECK-ID 830 MSOS 4.1'
595 *B 'Q1711' ' DECK-ID 831 MSOS 4.1'
596 *B 'Q40421' ' DECK-ID 834 MSOS 4.1'
597 *B 'IDRIV' ' DECK-ID 835 MSOS 4.1'
598 *B 'QMT7TK' ' DECK-ID 836 MSOS 4.1'
599 *B 'MDRIV' ' DECK-ID 842 MSOS 4.1'
600 *B 'QDK85X' ' DECK-ID 844 MSOS 4.1'

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```
601 *B 'QCODMY' ' DECK-ID B50 MSOS 4.1'  
602 *R 'QPTDMY' ' DECK-ID B51 MSOS 4.1'  
603 *T  
604 *K,I8  
605 *N,SI,,,R  
606 *V SMM BOOTSTRAPS  
607 *K,I6  
608 *L,SMM7T  
609 *B 'SMM1' ' DECK-ID B54 MSOS 4.1'  
610 *L,SMM430  
611 *B 'SMM4' ' DECK-ID B57 MSOS 4.1'  
612 *Z  
613 *K,I10,P11,L9  
614 *CTO, MSOS 4.1 INSTALLATION COMPLETED - YOU MAY AUTOLOAD  
615 *Z  
616 *END
```


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5.3.4 Creating a New Install File

The program LIBILD is used to create a new install file. The MSOS library tape will be read from tape unit 0. The modified binary of SYSDAT and the modified skeleton will be read from the card reader. The install file will be output on tape unit 1. Typing on the teletype is as follows. {Logical unit numbers are as defined in the LOGIA Table in Article 5.3.2.}

```

MI
*BATCH
L,10 FAILED 02
ACTION
CU
J
*JOB
J
*LIBILD
CONTROL LU =
DEFS LU =
INSTALL LU = 13
NEWLIB LU =
LIB 01 LU = 6
LIB 02 LU = 10
LIB 03 LU =
SKELETON LU = 10

LOAD LIBRARY INPUT 02 ON LU 10. CR WHEN READY.

LOAD SKEL/INSTAL, CR WHEN READY

LIBRARY BUILD COMPLETE
TYPE *Z TO TERMINATE OR
TYPE *C TO CONTINUE WITH CURRENT SKELETON AND/OR
OUTPUT LIBRARY LU'S *Z
J

```

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5.3.5 Loading the New System

Using the new install tape, the new system may be loaded by means of the system initializer program. The procedure is as described in the 1700 MSOS 4 Installation Handbook. Typing on the teletype, continued from the teletype print-out in Article 5.3.4, is as follows:

~~*SILP
THE INITIALIZER WILL BE MOVED TO LOCATION 5CFF AND EXECUTED~~

~~TURN OFF PROTEC SWITCH AND TYPE CARRIAGE RETURN~~

At this point the system disk pack is removed. A scratch pack on which the new system will be built is mounted. A carriage return is entered on the teletype, and the procedure continues.

~~MSOS 4.1 SYSTEM INITIALIZER
FWA OF CONTRL = 5CFF~~

~~DATE MM/DD/YY
10/21/74~~

~~G
*C,7
*C,7~~

~~Q
*V~~

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INITIALIZATION COMPLETED - YOU MAY AUTOLOAD

MSOS 4.1--PSR LEVEL 86 10/24/74

SET PROGRAM PROTECT

ANN'S TEST SYSTEM

65K MODE

CHECKING FILES - OK

ENTER DATE/TIME MMDDYYHHMM

1024741118

DATE: 24 OCT 74 TIME: 1118:00

MI

*BATCH

*CTO, MSOS 4.1 INSTALLATION COMPLETED - YOU MAY AUTOLOAD

MSOS 4.1--PSR LEVEL 86 10/24/74

SET PROGRAM PROTECT

ANN'S TEST SYSTEM

65K MODE

CHECKING FILES - OK

ENTER DATE/TIME MMDDYYHHMM

1024711136

DATE: 24 OCT 74 TIME: 1136:00

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5.3.6 Verification of File Manager Addition

The new system including the File Manager may be verified as described in the Installation Handbook.

CHAPTER 6 - ADDITION OF FORTRAN

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6.1 Addition of Compiler

A user who does not have a FORTRAN Compiler in the MSOS 4 System he originally ordered from Control Data may add either the FORTRAN 3.2A Compiler or the FORTRAN 3.2B Compiler to his system. A description of the two compilers may be found in the 1700 MSOS 4 MS FORTRAN version 3A/B Reference Manual. To add FORTRAN, the user must first order the installation materials for the compiler he desires from Control Data. (Refer to the 1700 MSOS 4 Ordering Bulletin.) The FORTRAN installation materials sent to the user are as follows:

- 1) A FORTRAN install file including either the FORTRAN 3.2A compiler or the FORTRAN 3.2B compiler together with those FORTRAN Input/Output Library modules specified by the user.
- 2) FORTRAN binaries file, which includes the following in binary form:
 - a. each module of the particular FORTRAN compiler ordered.
 - b. each module of the FORTRAN Reentrant Library
 - c. each module of the FORTRAN Input/Output Library
 - d. each module of the FORTRAN Limited Input/Output Library

The FORTRAN installation file contains all necessary control cards so that the compiler and the FORTRAN Input/Output Library may be installed by placing this file in the standard input device and entering a manual interrupt followed by a *BATCH command from the comment device. Alternately, a skeleton may be built using the FORTRAN install file and the SKED program. The skeleton records thus produced are then incorporated into

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the current system skeleton. A new install tape is created using LIBILD and a new system is built according to the procedure in the Installation Handbook. This method is the one used in the example in Article 6.3. It is longer than the first method, but it has the advantage of including FORTRAN within the skeleton so that a FORTRAN update may be made when necessary. (Refer to Chapter 7.)

6.2 Addition of FORTRAN Reentrant Library

The FORTRAN Reentrant Library is the library used by a FORTRAN program running in the foreground. The modules always included in this library are listed in Article 2.1, Group K. Modules optionally included in this library are listed in Article 2.1, Group K1 and Group L. A user who ordered FORTRAN, but not the reentrant FORTRAN library, with the MSOS system originally ordered from Control Data, will have the FORTRAN reentrant library modules included in the MSOS binary file he received at the time of system installation. A user who orders FORTRAN subsequent to a complete system order will have the FORTRAN reentrant library modules included in the FORTRAN binary file he receives as a part of the FORTRAN installation materials.

6.2.1 SYSDAT Changes Required

The volatile block stack in SYSDAT Section AAK must be modified. The number of FORTRAN foreground priority levels must be equated to the variable NFTNLV and to the variable NFDLVL in this section.

(Refer to Article 1.11)

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The maximum number of FORTRAN foreground priority levels is three. If only one FORTRAN level is to be available in the foreground, no additional volatile is needed, and the variables NFTNLV and NFDLVL may remain at zero.

Reentrant FORTRAN Information, Section ACS of SYSDAT, must be added. (Refer to Article 1.71 for a general description and to Article 6.3.1 for an example)

Section ACY of SYSDAT must be modified. (Refer to Article 1.77 for a general description and to Article 6.3.1 for an example.}

6.2.2 Skeleton Changes Required

To add reentrant FORTRAN, the *B control record for NDISP must be replaced by a *B record for RDISP.

To add reentrant FORTRAN, control records must be added to the system skeleton. To add the minimum reentrant FORTRAN library, the following control records must be inserted in the skeleton, between the *B record for the last core resident driver and the *B record for the module NXTLOC. The deck identification field of each *B record may be omitted. If reentrant FORTRAN programs are to use the formatting routines HEXASC, HEXDEC, ASCII, DECHEX, AFORM, RFORM, and FLOATG, a *B record for each of the modules in Group K1, Article 2.1, should be inserted between the *B record for Q8QFXR and the *B record for NXTLOC. (Refer to the example in Article 6.3.2)

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6 5

*LD REENTRANT FORTRAN RUNTIME LIBRARY

*E	*FORTR*	*	DECK-ID	A01	3.2	FTN	RUNTIME*
*E	*QBPRMP*	*	DECK-ID	B01	3.2	FTN	RUNTIME*
*E	*PARAPR*	*	DECK-ID	B02	3.2	FTN	RUNTIME*
*E	*QBEXPR*	*	DECK-ID	B03	3.2	FTN	RUNTIME*
*E	*QBABR*	*	DECK-ID	B04	3.2	FTN	RUNTIME*
*E	*SORTFR*	*	DECK-ID	B05	3.2	FTN	RUNTIME*
*E	*SIGNR*	*	DECK-ID	B06	3.2	FTN	RUNTIME*
*E	*FXFLR*	*	DECK-ID	B07	3.2	FTN	RUNTIME*
*E	*EXPRGR*	*	DECK-ID	B08	3.2	FTN	RUNTIME*
*E	*LNPRGR*	*	DECK-ID	B09	3.2	FTN	RUNTIME*
*E	*TANHR*	*	DECK-ID	B10	3.2	FTN	RUNTIME*
*E	*SNCSR*	*	DECK-ID	B11	3.2	FTN	RUNTIME*
*E	*ARCPGR*	*	DECK-ID	B12	3.2	FTN	RUNTIME*
*E	*TFALTR*	*	DECK-ID	B13	3.2	FTN	RUNTIME*
*E	*FLGATR*	*	DECK-ID	B14	3.2	FTN	RUNTIME*
*E	*QBGTOR*	*	DECK-ID	C01	3.2	FTN	RUNTIME*
*E	*RINARR*	*	DECK-ID	C02	3.2	FTN	RUNTIME*
*E	*IOCODR*	*	DECK-ID	D01	3.2	FTN	RUNTIME*
*E	*INITLR*	*	DECK-ID	D02	3.2	FTN	RUNTIME*
*E	*RSTORR*	*	DECK-ID	D03	3.2	FTN	RUNTIME*
*E	*GETCHR*	*	DECK-ID	D04	3.2	FTN	RUNTIME*
*E	*IPACKR*	*	DECK-ID	D05	3.2	FTN	RUNTIME*
*E	*UPDATR*	*	DECK-ID	D06	3.2	FTN	RUNTIME*
*E	*DECPLR*	*	DECK-ID	D07	3.2	FTN	RUNTIME*
*E	*INTGRR*	*	DECK-ID	D08	3.2	FTN	RUNTIME*
*E	*SPACER*	*	DECK-ID	D09	3.2	FTN	RUNTIME*
*E	*HOLR*	*	DECK-ID	D10	3.2	FTN	RUNTIME*
*E	*DCHXR*	*	DECK-ID	D11	3.2	FTN	RUNTIME*
*E	*HXASCR*	*	DECK-ID	D12	3.2	FTN	RUNTIME*
*E	*AFM TOR*	*	DECK-ID	D13	3.2	FTN	RUNTIME*
*E	*RFM TOR*	*	DECK-ID	D14	3.2	FTN	RUNTIME*
*E	*AFM TIR*	*	DECK-ID	D15	3.2	FTN	RUNTIME*
*E	*RFM TIR*	*	DECK-ID	D16	3.2	FTN	RUNTIME*
*E	*ASCHXR*	*	DECK-ID	D17	3.2	FTN	RUNTIME*
*E	*HXDCR*	*	DECK-ID	D18	3.2	FTN	RUNTIME*
*E	*FLOTIR*	*	DECK-ID	D19	3.2	FTN	RUNTIME*
*E	*FOUR*	*	DECK-ID	D20	3.2	FTN	RUNTIME*
*E	*FOCTR*	*	DECK-ID	D21	3.2	FTN	RUNTIME*
*E	*FWRITR*	*	DECK-ID	D22	3.2	FTN	RUNTIME*
*E	*INTIIR*	*	DECK-ID	D23	3.2	FTN	RUNTIME*
*E	*FORMTR*	*	DECK-ID	D24	3.2	FTN	RUNTIME*
*E	*QBG FIR*	*	DECK-ID	D25	3.2	FTN	RUNTIME*
*E	*QBGFLR*	*	DECK-ID	D26	3.2	FTN	RUNTIME*
*E	*QBGFXR*	*	DECK-ID	D27	3.2	FTN	RUNTIME*

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If reentrant FORTRAN programs are to use double precision a *B record is necessary for each module listed in Group L of Article 2.2.

In most cases the values of ENDOV4 and BGNMON must be decreased when adding the reentrant FORTRAN library. (Refer to Article 2.2) The value of BGNMON must be small enough so that the reentrant FORTRAN library will fit into core. (See figure 2-1) The total length of the modules to be added may be obtained by using EESORT as described in Article 2.2, or by loading the modules in the background, using the *L Job Processor command. The latter method is simpler when obtaining the total length of a large number of modules. This method is illustrated in Article 6.3.2.

When adding the reentrant FORTRAN library two things happen which decrease the size of unprotected core.

- 1) The value of ENDOV4 is decreased by the total length of the FORTRAN reentrant library modules added.
- 2) The size of SYSDAT is increased as described in Article 6.2.1. The effects of these changes may be seen by referring to Figure 2-1.

It may be necessary for the user to decrease the value of N4 so that the size of unprotected core will be large enough.

The FORTRAN requirements for unprotected core are shown in Table 6-1. An example showing how N4 is changed is included in Article 6.3.2.

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TABLE 6-1
FORTRAN REQUIREMENTS FOR
UNPROTECTED CORE

	<u>MINIMUM NUMBER WORDS IN UNPROTECTED</u>	
FORTRAN 3.2A COMPILER	8,192	(= 2000_{16})
FORTRAN 3.2B COMPILER	16,384	(= 4000_{16})
INPUT/OUTPUT LIBRARY OR LIMITED INPUT/OUTPUT LIBRARY WITH SINGLE PRECISION NUMBERS ONLY	4,096	(= 1000_{16})
INPUT/OUTPUT LIBRARY OR LIMITED INPUT/OUTPUT LIBRARY WITH DOUBLE PRECISION AND SINGLE PRECISION NUMBERS	12,288	(= 3000_{16})

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The user should modify the values of SYSMON, SYSDAY, and SYSYER to correspond to the new system build date.

6.2.3 Creating a New Install File and Building a New System

A new install file may be created by using the program LIBILD together with the following:

- 1) the revised skeleton
- 2) a binary of the revised SYSDAT
- 3) the current install file
- 4) the FORTRAN binaries file (if FORTRAN was ordered subsequent to initial MSOS 4.1 installation)
- 5) the MSOS binaries file (if FORTRAN was included in the initial MSOS 4.1 installation or if this file includes the most recent versions of RDISP)
- 6) the MSOS updates file (if this file includes the most recent version of RDISP)

It is important that the revised SYSDAT binary be presented as input to LIBILD before the current install file so that the one revised version of SYSDAT will be the one on the new install file. Using the new install file and the program SILP the new system including the reentrant FORTRAN library may be built. An example of this procedure appears in Article 6.3.3.

6.3 Example of the Addition of FORTRAN Compiler, the FORTRAN Input/Output Library and the FORTRAN Reentrant Library.

In this example, the user's current system is the system built in Article 5.3. The user wishes to add the FORTRAN 3.2A compiler, the FORTRAN Input/Output Library to be used by background FORTRAN programs, and the FORTRAN reentrant library to be

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used by foreground FORTRAN programs. Only single precision routines are to be included in the foreground and background FORTRAN libraries. The special formatting routines are to be included in both libraries. This example uses magnetic tape as the installation medium.

6.3.1 SYSDAT Changes

The addition of the FORTRAN compiler and the background FORTRAN library do not effect SYSDAT. The addition of the Reentrant FORTRAN library effects three sections of SYSDAT as described in Article 6.2.1. In Section AAK of SYSDAT before revision, the variables NFTNLV and NFDLVL are equated to zero. In this example the user wishes to have three priority levels available for FORTRAN foreground programs. Thus NFTNLV and NFDLVL are equated to three, as seen in the revised version of SYSDAT Section AAK, as follows:

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0070₁₆ to indicate that priority levels 4, 5, and 6 are to be FORTRAN foreground priority levels.

This section of SYSDAT appears as follows in this example:

495 ~~6-19A~~
6-2A

M I S C E L L A N E O U S I N F O R M A T I O N
F O R T R A N R E E N T R A N T I N F O R M A T I O N

ENT FMASK, FLIST
EXT Q8QF2I, Q8QI2F, Q8QF2F, QSAVE, E4SAVE
EXT RETAD, KG47V4, KG48V4, KG49V4
EXT ABS, SQRT, SIGN, IFIX, FLOAT
EXT EXP, ALOG, TANH, SIN, COS, ATAN, IFALT
EXT FLOT, ARGUO

FMASK NUM 80070 FORTRAN REENTRANT LEVELS (BIT 0 = LEVEL 0)

* TABLE OF FORTRAN ENTRY POINTS SAVED TO MAINTAIN REENTRANCY

ENTRY POINT	PROGRAM	DESCRIPTION
FLIST	ACC FEND	
ADC Q8QF2I	Q8EXPR	FLOAT-TO-INTEGGER FUNCTION
ADC Q8QI2F	Q8EXPR	INTEGER-TO-FLOAT FUNCTION
ADC Q8QF2F	Q8EXPR	FLOAT-TO-FLOAT FUNCTION
ADC QSAVE	Q8EXPR	Q-REGISTER STORAGE
ADC E4SAVE	Q8EXPR	LOCATION SE4 STORAGE
ADC RETAD	Q8EXPR	RETURN ADDRESS STORAGE
ADC KG47V4	Q8EXPR	TEMPORARY STORAGE
ADC KG48V4	Q8EXPR	TEMPORARY STORAGE
ADC KG49V4	Q8EXPR	TEMPORARY STORAGE
ADC ABS	Q8ABR	ABSOLUTE VALUE FUNCTION
ADC SQRT	SQRTR	SQUARE ROOT FUNCTION
ADC SIGN	SIGNR	INTRINSIC FUNCTION SIGN
ADC IFIX	FXFLR	INTRINSIC FUNCTION IFIX
ADC FLOAT	FXFLR	INTRINSIC FUNCTION FLOAT
ADC EXP	EXPRGR	EXTERNAL FUNCTION EXP
ADC ALOG	LNPRGR	EXTERNAL FUNCTION ALOG
ADC TANH	TANHR	EXTERNAL FUNCTION TANH
ADC SIN	SINGR	EXTERNAL FUNCTION SIN
ADC COS	SINGR	EXTERNAL FUNCTION COS
ADC ATAN	ARCPGR	EXTERNAL FUNCTION ATAN
ADC IFALT	IFALTR	EXTERNAL FUNCTION IFALT
ADC FLOT	FLOATR	FLOATING POINT PROCESSOR
ADC ARGUO	Q8QIO	TEMPORARY STORAGE
FEND	EQU FEND(*-FLTST-1)	

* F O R T R A N R E E N T R A N T I N F O R M A T I O N

* THIS ENTRY IS PROVIDED TO ALLOW COMPATIBILITY BETWEEN THE
* NON-REENTRANT (BACKGROUND) FORTRAN AND REENTRANT FORTRAN

ENT Q8STP

Q8STP NOP 0 FORTRAN STOP

JMP- (A8TSP)

* LINK THE DOUBLE PRECISION ENTRY POINT REFERENCED BY 'FORMTR'

ENT DOUT
EQU DOUT(*ZEEF)

4961

Section ACY of SYSDAT must be modified. Prior to modification this section of SYSDAT is as follows:

* MISCELLANEOUS INFORMATION
*
* SYSTEM CHECKOUT PARAMETERS

* THE STARTING SECTOR OF THE FAILED CORE IMAGE IS SPECIFIED BY
* THE NAME COBOPS. THIS AREA MUST BE SIZED TO ACCOMODATE A
* FAILED IMAGE OF THE SIZE SPECIFIED BY NAME MSIZV4. THE FAILED
* IMAGE MUST RESIDE ON THE LIBRARY MASS MEMORY UNIT. IF THE
* MASS MEMORY LIBRARY UNIT IS A CARTRIDGE DISK, THE IMAGE AREA
* CANNOT OVERLAP FROM ONE PLATTER TO THE OTHER.

ENT COBOPS
COBOPS EQU COBOPS(\$7D83) START SECTOR OF FAILED IMAGE

* THIS ENTRY IS PROVIDED TO LINK THE FORTRAN REENTRANCY DATA
* ENTRY POINTS

ENT FMASK,FLIST
EQU FMASK(\$7FFF),FLIST(\$7FFF)

497

The patches for FMASK and FLIST must be removed, as these entry points are now defined in Section ACS of SYSDAT. The patch for NDISP must be inserted, as the module NDISP will be replaced by RDISP in building the new system. After modification Section ACY of SYSDAT is as follows:

```
*
* MISCELLANEOUS INFORMATION
*
* SYSTEM CHECKOUT PARAMETERS
```

```
*
* THE STARTING SECTOR OF THE FAILED CORE IMAGE IS SPECIFIED BY
* THE NAME COBOPS. THIS AREA MUST BE SIZED TO ACCOMODATE A
* FAILED IMAGE OF THE SIZE SPECIFIED BY NAME MSIZV4. THE FAILED
* IMAGE MUST RESIDE ON THE LIBRARY MASS MEMORY UNIT. IF THE
* MASS MEMORY LIBRARY UNIT IS A CARTRIDGE DISK, THE IMAGE AREA
* CANNOT OVERLAP FROM ONE PLATTER TO THE OTHER.
```

```
ENT COBOPS
COBOPS EQU COBOPS($7000) START SECTOR OF FAILED IMAGE
```

```
*
* THIS ENTRY IS PROVIDED TO LINK THE NO-FORTRAN DISPATCHER
* ENTRY POINT
```

```
ENT NDISP
EQU NDISP($7FFF)
```

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6-15

6.3.2 Skeleton Modifications

Before modification the skeleton is as listed in Article 5.3.3. Referring to this listing, record 61 must be replaced by a *B record for the module RDISP. The control records needed to load the reentrant FORTRAN library are inserted between record 97 and record 98 of the previous skeleton. The formatting routines listed in Group K1 of Article 2.1 are included, but the double precision routines are not included. The control records needed to load the FORTRAN 3.2A Compiler and the FORTRAN Input/Output Library are inserted between records 611 and 612 of the previous skeleton.

In this example the control records needed to load the compiler, the background FORTRAN library, and the reentrant FORTRAN library must be punched on cards. To obtain the control cards to load the compiler and the background library the program SKED is used to build a skeleton from the FORTRAN install tape, and to dump the skeleton onto cards. The procedure is as follows. The FORTRAN install tape is mounted on tape unit 0, logical unit 6. Typing on the comment device is as follows:

```
*JOB  
J  
*SKED  
SKED IN  
  
NEXT  
BUILD,6  
  
NEXT  
DUMP,11
```

The resulting skeleton is as follows:

499

1 *V FORTRAN COMPIER 3.2 A
2 *K,I6
3 *L,FTN
4 *E #FTNA# # COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#
5 *K,P8
6 *P,F
7 *E #FTNA# # COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#

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500

8	*B	*GOA*	*	DECK-ID	F02	FORTRAN	3.2A*	
9	*B	*IOPRBA*	*	DECK-ID	F08	FORTRAN	3.2A*	
10	*B	*CNVT*	*	DECK-ID	A01	FORTRAN	3.2A*	
11	*B	*CONV*	*	DECK-ID	F03	FORTRAN	3.2A*	
12	*B	*DIAG*	*	DECK-ID	F04	FORTRAN	3.2A*	
13	*B	*GETC*	*	DECK-ID	F13	FORTRAN	3.2A*	
14	*B	*GETSYM*	*	DECK-ID	F12	FORTRAN	3.2A*	
15	*B	*OUTENT*	*	DECK-ID	A07	FORTRAN	3.2A*	
16	*B	*PACK*	*	DECK-ID	F09	FORTRAN	3.2A*	
17	*B	*QBPRMS*	*	DECK-ID	F10	FORTRAN	3.2A*	
18	*B	*STORE*	*	DECK-ID	F11	FORTRAN	3.2A*	
19	*B	*SYMBOL*	*	DECK-ID	A03	FORTRAN	3.2A*	
20	*B	*LOCLAA*	*	DECK-ID	F17	FORTRAN	3.2A*	
21	*B	*DUMYAA*	*	DECK-ID	F18	FORTRAN	3.2A*	
22	*B	*PHASEA*	*	DECK-ID	A08	FORTRAN	3.2A*	
23	*B	*ARAYSZ*	*	DECK-ID	A42	FORTRAN	3.2A*	
24	*B	*CPLOOP*	*	DECK-ID	A43	FORTRAN	3.2A*	
25	*B	*ENDDO*	*	DECK-ID	A20	FORTRAN	3.2A*	
26	*B	*GNST*	*	DECK-ID	A06	FORTRAN	3.2A*	
27	*B	*IGETCF*	*	DECK-ID	F14	FORTRAN	3.2A*	
28	*B	*OPTION*	*	DECK-ID	F15	FORTRAN	3.2A*	
29	*B	*PLABEL*	*	DECK-ID	A00	FORTRAN	3.2A*	
30	*B	*QBGBDS*	*	DECK-ID	A10	FORTRAN	3.2A*	
31	*B	*RDLABL*	*	DECK-ID	A11	FORTRAN	3.2A*	
32	*B	*SAVEID*	*	DECK-ID	A04	FORTRAN	3.2A*	
33	*B	*STCHAR*	*	DECK-ID	A12	FORTRAN	3.2A*	
34	*B	*ENDLOC*	*	DECK-ID	F16	FORTRAN	3.2A*	
35	*T							
36	*K,I8							
37	*N,FORTAA...							
38	*K,I6							
39	*K,F8							
40	*F,F...	MARKER						
41	*B	*FTNA*	*	COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0*				
42	*B	*GOA*	*	DECK-ID	F02	FORTRAN	3.2A*	
43	*B	*IOPRBA*	*	DECK-ID	F08	FORTRAN	3.2A*	
44	*B	*CNVT*	*	DECK-ID	A01	FORTRAN	3.2A*	
45	*B	*CONV*	*	DECK-ID	F03	FORTRAN	3.2A*	
46	*B	*DIAG*	*	DECK-ID	F04	FORTRAN	3.2A*	
47	*B	*GETC*	*	DECK-ID	F13	FORTRAN	3.2A*	
48	*B	*GETSYM*	*	DECK-ID	F12	FORTRAN	3.2A*	
49	*B	*OUTENT*	*	DECK-ID	A07	FORTRAN	3.2A*	
50	*B	*PACK*	*	DECK-ID	F09	FORTRAN	3.2A*	
51	*B	*QBPRMS*	*	DECK-ID	F10	FORTRAN	3.2A*	
52	*B	*STORE*	*	DECK-ID	F11	FORTRAN	3.2A*	
53	*B	*SYMBOL*	*	DECK-ID	A03	FORTRAN	3.2A*	
54	*B	*LOCLAB*	*	DECK-ID	F19	FORTRAN	3.2A*	
55	*B	*DUMYAB*	*	DECK-ID	F20	FORTRAN	3.2A*	
56	*B	*BYEQPR*	*	DECK-ID	A10	FORTRAN	3.2A*	
57	*B	*DFLOT*	*	DECK-ID	F06	FORTRAN	3.2A*	
58	*B	*DUMVOL*	*	DECK-ID	F07	FORTRAN	3.2A*	
59	*B	*DXP9*	*	DECK-ID	F08	FORTRAN	3.2A*	
60	*B	*GETF*	*	DECK-ID	A05	FORTRAN	3.2A*	
61	*B	*GPLT*	*	DECK-ID	A02	FORTRAN	3.2A*	
62	*B	*SAVEID*	*	DECK-ID	A04	FORTRAN	3.2A*	
63	*B	*STCHAR*	*	DECK-ID	A12	FORTRAN	3.2A*	
64	*B	*SUBPPR*	*	DECK-ID	A23	FORTRAN	3.2A*	
65	*B	*TYPE*	*	DECK-ID	A13	FORTRAN	3.2A*	
66	*B	*ENDLOC*	*	DECK-ID	F16	FORTRAN	3.2A*	
67	*T							

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68	*K,I8								
69	*N,FORTAB,,R								
70	*K,I6								
71	*K,F8								
72	*F,F,,MARKER								
73	*B #FTNA#	#	COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#						
74	*B #GOA#	#	DECK-ID F02 FORTRAN 3.2A#						
75	*B #IOPRBA#	#	DECK-ID F08 FORTRAN 3.2A#						
76	*B #CNVT#	#	DECK-ID A01 FORTRAN 3.2A#						
77	*B #CONV#	#	DECK-ID F03 FORTRAN 3.2A#						
78	*B #DIAG#	#	DECK-ID F04 FORTRAN 3.2A#						
79	*B #GETC#	#	DECK-ID F13 FORTRAN 3.2A#						
80	*B #GETSYM#	#	DECK-ID F12 FORTRAN 3.2A#						
81	*B #OUTENT#	#	DECK-ID A07 FORTRAN 3.2A#						
82	*B #PACK#	#	DECK-ID F09 FORTRAN 3.2A#						
83	*B #QBPRMS#	#	DECK-ID F10 FORTRAN 3.2A#						
84	*B #STORE#	#	DECK-ID F11 FORTRAN 3.2A#						
85	*B #SYMBOL#	#	DECK-ID A03 FORTRAN 3.2A#						
86	*B #LOCLAC#	#	DECK-ID F21 FORTRAN 3.2A#						
87	*B #DUMYAC#	#	DECK-ID F22 FORTRAN 3.2A#						
88	*B #ASGNPR#	#	DECK-ID A32 FORTRAN 3.2A#						
89	*B #BDOPR#	#	DECK-ID A33 FORTRAN 3.2A#						
90	*B #CFIVOC#	#	DECK-ID A34 FORTRAN 3.2A#						
91	*B #CKIVC#	#	DECK-ID A35 FORTRAN 3.2A#						
92	*B #CKNAME#	#	DECK-ID A36 FORTRAN 3.2A#						
93	*B #COMNPR#	#	DECK-ID A15 FORTRAN 3.2A#						
94	*B #DFLOT#	#	DECK-ID F06 FORTRAN 3.2A#						
95	*B #DIMPR#	#	DECK-ID A16 FORTRAN 3.2A#						
96	*B #DUMVOL#	#	DECK-ID F07 FORTRAN 3.2A#						
97	*B #DXPR#	#	DECK-ID F08 FORTRAN 3.2A#						
98	*B #ERBPR#	#	DECK-ID A38 FORTRAN 3.2A#						
99	*B #EXRLPR#	#	DECK-ID A24 FORTRAN 3.2A#						
100	*B #GETF#	#	DECK-ID A05 FORTRAN 3.2A#						
101	*B #GPLT#	#	DECK-ID A02 FORTRAN 3.2A#						
102	*B #RDLABL#	#	DECK-ID A11 FORTRAN 3.2A#						
103	*B #TYPEPR#	#	DECK-ID A18 FORTRAN 3.2A#						
104	*B #ENDLOC#	#	DECK-ID F16 FORTRAN 3.2A#						
105	*T								
106	*K,I8								
107	*N,FORTAC,,R								
108	*K,I6								
109	*K,F8								
110	*F,F,,MARKER								
111	*B #FTNA#	#	COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#						
112	*B #GOA#	#	DECK-ID F02 FORTRAN 3.2A#						
113	*B #IOPRBA#	#	DECK-ID F08 FORTRAN 3.2A#						
114	*B #CNVT#	#	DECK-ID A01 FORTRAN 3.2A#						
115	*B #CONV#	#	DECK-ID F03 FORTRAN 3.2A#						
116	*B #DIAG#	#	DECK-ID F04 FORTRAN 3.2A#						
117	*B #GETC#	#	DECK-ID F13 FORTRAN 3.2A#						
118	*B #GETSYM#	#	DECK-ID F12 FORTRAN 3.2A#						
119	*B #OUTENT#	#	DECK-ID A07 FORTRAN 3.2A#						
120	*B #PACK#	#	DECK-ID F09 FORTRAN 3.2A#						
121	*B #QBPRMS#	#	DECK-ID F10 FORTRAN 3.2A#						
122	*B #STORE#	#	DECK-ID F11 FORTRAN 3.2A#						
123	*B #SYMBOL#	#	DECK-ID A03 FORTRAN 3.2A#						
124	*B #LOCLAD#	#	DECK-ID F23 FORTRAN 3.2A#						
125	*B #DUMYAD#	#	DECK-ID F24 FORTRAN 3.2A#						
126	*B #ASEMPR#	#	DECK-ID A40 FORTRAN 3.2A#						
127	*B #DFLOT#	#	DECK-ID F06 FORTRAN 3.2A#						

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128 *E #DUMVOL# * DECK-ID F07 FORTRAN 3.2A#
129 *E #DX#9# * DECK-ID F08 FORTRAN 3.2A#
130 *E #GETF# * DECK-ID A05 FORTRAN 3.2A#
131 *E #GPLT# * DECK-ID A02 FORTRAN 3.2A#
132 *E #IGETCF# * DECK-ID F14 FORTRAN 3.2A#
133 *E #PUKT# * DECK-ID A27 FORTRAN 3.2A#
134 *E #RDLABL# * DECK-ID A11 FORTRAN 3.2A#
135 *E #SUBSCR# * DECK-ID A17 FORTRAN 3.2A#
136 *E #ENCLOC# * DECK-ID F16 FORTRAN 3.2A#
137 *T
138 *K, I8
139 *A, FORTAD,,,R
140 *K, I6
141 *K, P8
142 *F, F,,,MARKER
143 *E #FTNA# * COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#
144 *E #GOA# * DECK-ID F02 FORTRAN 3.2A#
145 *E #IOPRBA# * DECK-ID F08 FORTRAN 3.2A#
146 *E #CNVT# * DECK-ID A01 FORTRAN 3.2A#
147 *E #CONV# * DECK-ID F03 FORTRAN 3.2A#
148 *E #DIAG# * DECK-ID F04 FORTRAN 3.2A#
149 *E #GETC# * DECK-ID F13 FORTRAN 3.2A#
150 *E #GETSYM# * DECK-ID F12 FORTRAN 3.2A#
151 *E #OUTENT# * DECK-ID A07 FORTRAN 3.2A#
152 *E #PACK# * DECK-ID F09 FORTRAN 3.2A#
153 *E #Q8PRMS# * DECK-ID F10 FORTRAN 3.2A#
154 *E #STORE# * DECK-ID F11 FORTRAN 3.2A#
155 *E #SYMBOL# * DECK-ID A03 FORTRAN 3.2A#
156 *E #LOCLAE# * DECK-ID F25 FORTRAN 3.2A#
157 *E #DUMYAE# * DECK-ID F26 FORTRAN 3.2A#
158 *E #CONSUB# * DECK-ID A30 FORTRAN 3.2A#
159 *E #DATAPR# * DECK-ID A31 FORTRAN 3.2A#
160 *E #DFLOT# * DECK-ID F06 FORTRAN 3.2A#
161 *E #DUMVOL# * DECK-ID F07 FORTRAN 3.2A#
162 *E #DX#9# * DECK-ID F08 FORTRAN 3.2A#
163 *E #GETF# * DECK-ID A05 FORTRAN 3.2A#
164 *E #GPLT# * DECK-ID A02 FORTRAN 3.2A#
165 *E #STCHAR# * DECK-ID A12 FORTRAN 3.2A#
166 *E #ENCLOC# * DECK-ID F16 FORTRAN 3.2A#
167 *T
168 *K, I8
169 *A, FORTAE,,,R
170 *K, I6
171 *K, P8
172 *F, F,,,MARKER
173 *E #FTNA# * COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#
174 *E #GOA# * DECK-ID F02 FORTRAN 3.2A#
175 *E #IOPRBA# * DECK-ID F08 FORTRAN 3.2A#
176 *E #CNVT# * DECK-ID A01 FORTRAN 3.2A#
177 *E #CONV# * DECK-ID F03 FORTRAN 3.2A#
178 *E #DIAG# * DECK-ID F04 FORTRAN 3.2A#
179 *E #GETC# * DECK-ID F13 FORTRAN 3.2A#
180 *E #GETSYM# * DECK-ID F12 FORTRAN 3.2A#
181 *E #OUTENT# * DECK-ID A07 FORTRAN 3.2A#
182 *E #PACK# * DECK-ID F09 FORTRAN 3.2A#
183 *E #Q8PRMS# * DECK-ID F10 FORTRAN 3.2A#
184 *E #STORE# * DECK-ID F11 FORTRAN 3.2A#
185 *E #SYMBOL# * DECK-ID A03 FORTRAN 3.2A#
186 *E #LOCLAF# * DECK-ID F27 FORTRAN 3.2A#
187 *E #DUMYAF# * DECK-ID F28 FORTRAN 3.2A#

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188	*B	*CHECKF*	*	DECK-ID	A20	FORTTRAN	3.2A*
189	*B	*FGETC*	*	DECK-ID	A21	FORTTRAN	3.2A*
190	*B	*FORK*	*	DECK-ID	A22	FORTTRAN	3.2A*
191	*B	*PEQVS*	*	DECK-ID	A25	FORTTRAN	3.2A*
192	*B	*PRNTNM*	*	DECK-ID	A26	FORTTRAN	3.2A*
193	*B	*STCHAR*	*	DECK-ID	A12	FORTTRAN	3.2A*
194	*B	*SYMSCN*	*	DECK-ID	A28	FORTTRAN	3.2A*
195	*B	*ENDLOC*	*	DECK-ID	F16	FORTTRAN	3.2A*
196	*T						
197	*K,I8						
198	*N,FORTAF,,B						
199	*K,I6						
200	*K,F8						
201	*F,F,,MARKER						
202	*B	*FTNA*	*	COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0*			
203	*B	*GOA*	*	DECK-ID	F02	FORTTRAN	3.2A*
204	*B	*IOPRBA*	*	DECK-ID	F08	FORTTRAN	3.2A*
205	*B	*CNVT*	*	DECK-ID	A01	FORTTRAN	3.2A*
206	*B	*CONV*	*	DECK-ID	F03	FORTTRAN	3.2A*
207	*B	*DIAG*	*	DECK-ID	F04	FORTTRAN	3.2A*
208	*B	*GETC*	*	DECK-ID	F13	FORTTRAN	3.2A*
209	*B	*GETSYM*	*	DECK-ID	F12	FORTTRAN	3.2A*
210	*B	*OUTENT*	*	DECK-ID	A07	FORTTRAN	3.2A*
211	*B	*PACK*	*	DECK-ID	F09	FORTTRAN	3.2A*
212	*B	*QBPRMS*	*	DECK-ID	F10	FORTTRAN	3.2A*
213	*B	*STORE*	*	DECK-ID	F11	FORTTRAN	3.2A*
214	*B	*SYMBOL*	*	DECK-ID	A03	FORTTRAN	3.2A*
215	*B	*LOCLAG*	*	DECK-ID	F29	FORTTRAN	3.2A*
216	*B	*DUMYAG*	*	DECK-ID	F30	FORTTRAN	3.2A*
217	*B	*ARITH*	*	DECK-ID	A14	FORTTRAN	3.2A*
218	*B	*IGETCF*	*	DECK-ID	F14	FORTTRAN	3.2A*
219	*B	*PUNT*	*	DECK-ID	A27	FORTTRAN	3.2A*
220	*B	*TREE*	*	DECK-ID	A41	FORTTRAN	3.2A*
221	*B	*ENDLOC*	*	DECK-ID	F16	FORTTRAN	3.2A*
222	*T						
223	*K,I8						
224	*N,FORTAG,,B						
225	*K,I6						
226	*K,F8						
227	*F,F,,MARKER						
228	*B	*FTNA*	*	COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0*			
229	*B	*GOA*	*	DECK-ID	F02	FORTTRAN	3.2A*
230	*B	*IOPRBA*	*	DECK-ID	F08	FORTTRAN	3.2A*
231	*B	*CNVT*	*	DECK-ID	A01	FORTTRAN	3.2A*
232	*B	*CONV*	*	DECK-ID	F03	FORTTRAN	3.2A*
233	*B	*DIAG*	*	DECK-ID	F04	FORTTRAN	3.2A*
234	*B	*GETC*	*	DECK-ID	F13	FORTTRAN	3.2A*
235	*B	*GETSYM*	*	DECK-ID	F12	FORTTRAN	3.2A*
236	*B	*OUTENT*	*	DECK-ID	A07	FORTTRAN	3.2A*
237	*B	*PACK*	*	DECK-ID	F09	FORTTRAN	3.2A*
238	*B	*QBPRMS*	*	DECK-ID	F10	FORTTRAN	3.2A*
239	*B	*STORE*	*	DECK-ID	F11	FORTTRAN	3.2A*
240	*B	*SYMBOL*	*	DECK-ID	A03	FORTTRAN	3.2A*
241	*B	*LOCLAH*	*	DECK-ID	F31	FORTTRAN	3.2A*
242	*B	*DUMYAH*	*	DECK-ID	F32	FORTTRAN	3.2A*
243	*B	*IGETCF*	*	DECK-ID	F14	FORTTRAN	3.2A*
244	*B	*MODMXR*	*	DECK-ID	A39	FORTTRAN	3.2A*
245	*B	*PUNT*	*	DECK-ID	A27	FORTTRAN	3.2A*
246	*B	*ENDLOC*	*	DECK-ID	F16	FORTTRAN	3.2A*
247	*T						

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248 *K, I8
249 *N, FORTAH, , , B
250 *K, I6
251 *K, PB

252 *F, F, MARKER
253 *F, FTNA# * COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#
254 *F, GOA# * DECK-ID F02 FORTRAN 3.2A#
255 *F, IO#RBA# * DECK-ID F08 FORTRAN 3.2A#
256 *F, CNVT# * DECK-ID A01 FORTRAN 3.2A#
257 *F, CONV# * DECK-ID F03 FORTRAN 3.2A#
258 *F, DIAG# * DECK-ID F04 FORTRAN 3.2A#
259 *F, GETC# * DECK-ID F13 FORTRAN 3.2A#
260 *F, GETSYM# * DECK-ID F12 FORTRAN 3.2A#
261 *F, OUTENT# * DECK-ID A07 FORTRAN 3.2A#
262 *F, PACK# * DECK-ID F09 FORTRAN 3.2A#
263 *F, Q8#RMS# * DECK-ID F10 FORTRAN 3.2A#
264 *F, STORE# * DECK-ID F11 FORTRAN 3.2A#
265 *F, SYMBOL# * DECK-ID A09 FORTRAN 3.2A#
266 *F, LOCLAI# * DECK-ID F33 FORTRAN 3.2A#
267 *F, DUMYAI# * DECK-ID F34 FORTRAN 3.2A#
268 *F, IOSPR# * DECK-ID A37 FORTRAN 3.2A#
269 *F, ENODO# * DECK-ID A29 FORTRAN 3.2A#
270 *F, RD LABI# * DECK-ID A11 FORTRAN 3.2A#
271 *F, STCHAR# * DECK-ID A12 FORTRAN 3.2A#
272 *F, ENDLOC# * DECK-ID F16 FORTRAN 3.2A#
273 *T

274 *K, I8
275 *N, FORTAI, , , B
276 *K, I6
277 *K, PB

278 *F, F
279 *F, FTNA# * COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#
280 *F, GOB# * DECK-ID F35 FORTRAN 3.2A#
281 *F, CNVT# * DECK-ID A01 FORTRAN 3.2A#
282 *F, DUMMY# * DECK-ID B01 FORTRAN 3.2A#
283 *F, FCMSTK# * DECK-ID B02 FORTRAN 3.2A#
284 *F, GETSYM# * DECK-ID F12 FORTRAN 3.2A#
285 *F, IO#RBB# * DECK-ID F36 FORTRAN 3.2A#
286 *F, KCPART# * DECK-ID B03 FORTRAN 3.2A#
287 *F, KOUTPT# * DECK-ID B04 FORTRAN 3.2A#
288 *F, KPCSTK# * DECK-ID B05 FORTRAN 3.2A#
289 *F, KPC3PR# * DECK-ID B06 FORTRAN 3.2A#
290 *F, KSYMGN# * DECK-ID B07 FORTRAN 3.2A#
291 *F, LABKPC# * DECK-ID B08 FORTRAN 3.2A#
292 *F, LABLER# * DECK-ID B09 FORTRAN 3.2A#
293 *F, PURT# * DECK-ID B10 FORTRAN 3.2A#
294 *F, CONV# * DECK-ID F09 FORTRAN 3.2A#
295 *F, Q8#RMS# * DECK-ID F10 FORTRAN 3.2A#
296 *F, STOREB# * DECK-ID F37 FORTRAN 3.2A#
297 *F, SYMBOL# * DECK-ID B11 FORTRAN 3.2A#
298 *F, TSALOC# * DECK-ID B12 FORTRAN 3.2A#
299 *F, LOCLBA# * DECK-ID F38 FORTRAN 3.2A#
300 *F, DUMYBA# * DECK-ID F39 FORTRAN 3.2A#
301 *F, PHASEB# * DECK-ID B21 FORTRAN 3.2A#
302 *F, INXRST# * DECK-ID B19 FORTRAN 3.2A#
303 *F, NO#PROC# * DECK-ID B20 FORTRAN 3.2A#
304 *F, READIR# * DECK-ID B22 FORTRAN 3.2A#
305 *F, ENDLOC# * DECK-ID F16 FORTRAN 3.2A#
306 *T
307 *K, I8

505

308 *N,FORTBA,,,B
309 *K,I6
310 *K,P8

311 *F,MARKER
312 *F,FTNA*

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313 *F *GOB* * DECK-ID F35 FORTRAN 3.2A*
314 *F *CNVT* * DECK-ID A01 FORTRAN 3.2A*
315 *F *DUMMY* * DECK-ID B01 FORTRAN 3.2A*
316 *F *FCMSTK* * DECK-ID B02 FORTRAN 3.2A*
317 *F *GETSYM* * DECK-ID F12 FORTRAN 3.2A*
318 *F *IOPRBB* * DECK-ID F36 FORTRAN 3.2A*
319 *F *KCPART* * DECK-ID B03 FORTRAN 3.2A*
320 *F *KOLTPT* * DECK-ID B04 FORTRAN 3.2A*
321 *F *KPCSTK* * DECK-ID B05 FORTRAN 3.2A*
322 *F *KPC3PR* * DECK-ID B06 FORTRAN 3.2A*
323 *F *KSYMGN* * DECK-ID B07 FORTRAN 3.2A*
324 *F *LABKPC* * DECK-ID B08 FORTRAN 3.2A*
325 *F *LABLER* * DECK-ID B09 FORTRAN 3.2A*
326 *F *PUNT* * DECK-ID B10 FORTRAN 3.2A*
327 *F *CONV* * DECK-ID F03 FORTRAN 3.2A*
328 *F *Q8PRMS* * DECK-ID F10 FORTRAN 3.2A*
329 *F *STOREB* * DECK-ID F37 FORTRAN 3.2A*
330 *F *SYMBOL* * DECK-ID B11 FORTRAN 3.2A*
331 *F *TSALOC* * DECK-ID B12 FORTRAN 3.2A*
332 *F *LOCLBB* * DECK-ID F40 FORTRAN 3.2A*
333 *F *DUMYBB* * DECK-ID F41 FORTRAN 3.2A*
334 *F *AFIDL* * DECK-ID B25 FORTRAN 3.2A*
335 *F *ASSEM* * DECK-ID B13 FORTRAN 3.2A*
336 *F *BARANA* * DECK-ID B14 FORTRAN 3.2A*
337 *F *END* * DECK-ID B16 FORTRAN 3.2A*
338 *F *ENTCOD* * DECK-ID B17 FORTRAN 3.2A*
339 *F *INXRST* * DECK-ID B19 FORTRAN 3.2A*
340 *F *SUBFUN* * DECK-ID B23 FORTRAN 3.2A*
341 *F *ENDLOC* * DECK-ID F16 FORTRAN 3.2A*
342 *T

343 *K,I8
344 *N,FORTBB,,,B
345 *K,I6
346 *K,P8

347 *F,MARKER
348 *F,FTNA*

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349 *F *GOB* * DECK-ID F35 FORTRAN 3.2A*
350 *F *CNVT* * DECK-ID A01 FORTRAN 3.2A*
351 *F *DUMMY* * DECK-ID B01 FORTRAN 3.2A*
352 *F *FCMSTK* * DECK-ID B02 FORTRAN 3.2A*
353 *F *GETSYM* * DECK-ID F12 FORTRAN 3.2A*
354 *F *IOPRBB* * DECK-ID F36 FORTRAN 3.2A*
355 *F *KCPART* * DECK-ID B03 FORTRAN 3.2A*
356 *F *KOLTPT* * DECK-ID B04 FORTRAN 3.2A*
357 *F *KPCSTK* * DECK-ID B05 FORTRAN 3.2A*
358 *F *KPC3PR* * DECK-ID B06 FORTRAN 3.2A*
359 *F *KSYMGN* * DECK-ID B07 FORTRAN 3.2A*
360 *F *LABKPC* * DECK-ID B08 FORTRAN 3.2A*
361 *F *LABLER* * DECK-ID B09 FORTRAN 3.2A*
362 *F *PUNT* * DECK-ID B10 FORTRAN 3.2A*
363 *F *CONV* * DECK-ID F03 FORTRAN 3.2A*
364 *F *Q8PRMS* * DECK-ID F10 FORTRAN 3.2A*
365 *F *STOREB* * DECK-ID F37 FORTRAN 3.2A*
366 *F *SYMBOL* * DECK-ID B11 FORTRAN 3.2A*
367 *F *TSALOC* * DECK-ID B12 FORTRAN 3.2A*

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368 *E *LOCLBC* * DECK-ID F42 FORTRAN 3.2A*
369 *E *DUMYBC* * DECK-ID F43 FORTRAN 3.2A*
370 *E *ASUPER* * DECK-ID B26 FORTRAN 3.2A*
371 *E *ARAYSZ* * DECK-ID A42 FORTRAN 3.2A*
372 *E *BGINDO* * DECK-ID B15 FORTRAN 3.2A*
373 *E *CGOTO* * DECK-ID B27 FORTRAN 3.2A*
374 *E *HELEN* * DECK-ID B18 FORTRAN 3.2A*
375 *E *SYMSCN* * DECK-ID A28 FORTRAN 3.2A*
376 *E *ENDLOC* * DECK-ID F16 FORTRAN 3.2A*
377 *T

378 *K,I8
379 *N,FORTBC,,,B

380 *K,I6
381 *K,P8

382 *F,F,,MARKER

383 *E *FTNA* * COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0*

384 *E *GOB* * DECK-ID F35 FORTRAN 3.2A*
385 *E *CNVT* * DECK-ID A01 FORTRAN 3.2A*

386 *E *DUMMY* * DECK-ID B01 FORTRAN 3.2A*
387 *E *FCMSTK* * DECK-ID B02 FORTRAN 3.2A*

388 *E *GETSYM* * DECK-ID F12 FORTRAN 3.2A*
389 *E *IOPRBB* * DECK-ID F36 FORTRAN 3.2A*

390 *E *KCPART* * DECK-ID B03 FORTRAN 3.2A*
391 *E *KOUTPT* * DECK-ID B04 FORTRAN 3.2A*

392 *E *KPCSTK* * DECK-ID B05 FORTRAN 3.2A*
393 *E *KPC3PR* * DECK-ID B06 FORTRAN 3.2A*

394 *E *KSYMGN* * DECK-ID B07 FORTRAN 3.2A*
395 *E *LABKPC* * DECK-ID B08 FORTRAN 3.2A*

396 *E *LABLER* * DECK-ID B09 FORTRAN 3.2A*
397 *E *PUNT* * DECK-ID B10 FORTRAN 3.2A*

398 *E *CONV* * DECK-ID F09 FORTRAN 3.2A*
399 *E *Q8PRMS* * DECK-ID F10 FORTRAN 3.2A*

400 *E *STOREB* * DECK-ID F37 FORTRAN 3.2A*
401 *E *SYMBOL* * DECK-ID B11 FORTRAN 3.2A*

402 *E *TSALOC* * DECK-ID B12 FORTRAN 3.2A*
403 *E *LOCLBD* * DECK-ID F44 FORTRAN 3.2A*

404 *E *DUMYBD* * DECK-ID F45 FORTRAN 3.2A*
405 *E *ARITHR* * DECK-ID B34 FORTRAN 3.2A*

406 *E *EIBK* * DECK-ID B28 FORTRAN 3.2A*
407 *E *ENDLOC* * DECK-ID F16 FORTRAN 3.2A*

408 *T
409 *K,I8

410 *N,FORTBD,,,P
411 *K,I6

412 *K,P8
413 *F,F,,MARKER

414 *E *FTNA* * COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0*

415 *E *GOB* * DECK-ID F35 FORTRAN 3.2A*

416 *E *CNVT* * DECK-ID A01 FORTRAN 3.2A*
417 *E *DUMMY* * DECK-ID B01 FORTRAN 3.2A*

418 *E *FCMSTK* * DECK-ID B02 FORTRAN 3.2A*
419 *E *GETSYM* * DECK-ID F12 FORTRAN 3.2A*

420 *E *IOPRBB* * DECK-ID F36 FORTRAN 3.2A*
421 *E *KCPART* * DECK-ID B03 FORTRAN 3.2A*

422 *E *KOUTPT* * DECK-ID B04 FORTRAN 3.2A*
423 *E *KPCSTK* * DECK-ID B05 FORTRAN 3.2A*

424 *E *KPC3PR* * DECK-ID B06 FORTRAN 3.2A*
425 *E *KSYMGN* * DECK-ID B07 FORTRAN 3.2A*

426 *E *LABKPC* * DECK-ID B08 FORTRAN 3.2A*
427 *E *LABLER* * DECK-ID B09 FORTRAN 3.2A*

507

428	*R	#PUNT#	#	DECK-ID	B10	FORTRAN	3.2A#
429	*R	#CONV#	#	DECK-ID	F03	FORTRAN	3.2A#
430	*R	#Q8PRMS#	#	DECK-ID	F10	FORTRAN	3.2A#
431	*E	#STOREB#	#	DECK-ID	F37	FORTRAN	3.2A#
432	*E	#SYMBOL#	#	DECK-ID	B11	FORTRAN	3.2A#
433	*R	#TSALOC#	#	DECK-ID	B12	FORTRAN	3.2A#
434	*E	#LOCLBE#	#	DECK-ID	F46	FORTRAN	3.2A#
435	*E	#DUMYBE#	#	DECK-ID	F47	FORTRAN	3.2A#
436	*E	#ACP#	#	DECK-ID	B24	FORTRAN	3.2A#
437	*E	#ENDLOC#	#	DECK-ID	F16	FORTRAN	3.2A#
438	*T						
439	*K	,I8					
440	*N	,F0RTEB,.,.R					
441	*K	,I6					
442	*K	,P8					
443	*P	,F,.,MARKER					
444	*E	#FTNA#	#	COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#			
445	*E	#GOB#	#	DECK-ID	F35	FORTRAN	3.2A#
446	*R	#CNVT#	#	DECK-ID	A01	FORTRAN	3.2A#
447	*E	#DUMMY#	#	DECK-ID	B01	FORTRAN	3.2A#
448	*E	#FCMSTK#	#	DECK-ID	B02	FORTRAN	3.2A#
449	*E	#GETSYM#	#	DECK-ID	F12	FORTRAN	3.2A#
450	*E	#IOPRFB#	#	DECK-ID	F36	FORTRAN	3.2A#
451	*E	#KCPART#	#	DECK-ID	B03	FORTRAN	3.2A#
452	*E	#KOLTPT#	#	DECK-ID	B04	FORTRAN	3.2A#
453	*E	#KPCSTK#	#	DECK-ID	B05	FORTRAN	3.2A#
454	*E	#KPC3PR#	#	DECK-ID	B06	FORTRAN	3.2A#
455	*E	#KSYMGN#	#	DECK-ID	B07	FORTRAN	3.2A#
456	*E	#LABKPC#	#	DECK-ID	B08	FORTRAN	3.2A#
457	*E	#LABLER#	#	DECK-ID	B09	FORTRAN	3.2A#
458	*E	#PUNT#	#	DECK-ID	B10	FORTRAN	3.2A#
459	*E	#CONV#	#	DECK-ID	F03	FORTRAN	3.2A#
460	*E	#Q8PRMS#	#	DECK-ID	F10	FORTRAN	3.2A#
461	*E	#STOREB#	#	DECK-ID	F37	FORTRAN	3.2A#
462	*R	#SYMBOL#	#	DECK-ID	B11	FORTRAN	3.2A#
463	*R	#TSALOC#	#	DECK-ID	B12	FORTRAN	3.2A#
464	*E	#LOCLBF#	#	DECK-ID	F48	FORTRAN	3.2A#
465	*E	#DUMYBF#	#	DECK-ID	F49	FORTRAN	3.2A#
466	*E	#SUBPR3#	#	DECK-ID	B33	FORTRAN	3.2A#
467	*E	#INTRAM#	#	DECK-ID	B29	FORTRAN	3.2A#
468	*E	#PARTSR#	#	DECK-ID	B30	FORTRAN	3.2A#
469	*E	#SUBPRI#	#	DECK-ID	B31	FORTRAN	3.2A#
470	*E	#SUBPR2#	#	DECK-ID	B32	FORTRAN	3.2A#
471	*E	#ENDLOC#	#	DECK-ID	F16	FORTRAN	3.2A#
472	*T						
473	*K	,I8					
474	*N	,F0RTEF,.,.R					
475	*K	,I6					
476	*K	,P8					
477	*P	,F					
478	*E	#FTNA#	#	COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#			
479	*E	#GOC#	#	DECK-ID	F50	FORTRAN	3.2A#
480	*E	#IOPRBC#	#	DECK-ID	F51	FORTRAN	3.2A#
481	*E	#BKDOWN#	#	DECK-ID	C01	FORTRAN	3.2A#
482	*E	#BLDUP#	#	DECK-ID	C02	FORTRAN	3.2A#
483	*E	#BSS#	#	DECK-ID	C03	FORTRAN	3.2A#
484	*E	#CHKWD#	#	DECK-ID	C04	FORTRAN	3.2A#
485	*E	#CON#	#	DECK-ID	C07	FORTRAN	3.2A#
486	*E	#C0UNT#	#	DECK-ID	C08	FORTRAN	3.2A#
487	*E	#DATAST#	#	DECK-ID	C09	FORTRAN	3.2A#

508

488	*B	*GETSYM*	*	DECK-ID	C10	FORTRAN	3.2A*
489	*B	*INOUT*	*	DECK-ID	C11	FORTRAN	3.2A*
490	*B	*LABEL*	*	DECK-ID	C14	FORTRAN	3.2A*
491	*B	*LABIN*	*	DECK-ID	C15	FORTRAN	3.2A*
492	*B	*QBPRMS*	*	DECK-ID	F10	FORTRAN	3.2A*
493	*B	*REED*	*	DECK-ID	C17	FORTRAN	3.2A*
494	*B	*SYMSCN*	*	DECK-ID	C19	FORTRAN	3.2A*
495	*B	*LOCLCA*	*	DECK-ID	F52	FORTRAN	3.2A*
496	*B	*DUMYCA*	*	DECK-ID	F53	FORTRAN	3.2A*
497	*B	*PHASEC*	*	DECK-ID	C13	FORTRAN	3.2A*
498	*B	*ENDLOC*	*	DECK-ID	F16	FORTRAN	3.2A*
499	*T						
500	*K,I8						
501	*N,FORTCA,,,B						
502	*K,I6						
503	*K,P8						
504	*F,F,MARKER						
505	*B	*FTNA*	*	COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0*			
506	*B	*GOC*	*	DECK-ID	F50	FORTRAN	3.2A*
507	*B	*IOPRBC*	*	DECK-ID	F51	FORTRAN	3.2A*
508	*B	*BKDOWN*	*	DECK-ID	C01	FORTRAN	3.2A*
509	*B	*BLDUP*	*	DECK-ID	C02	FORTRAN	3.2A*
510	*B	*BSS*	*	DECK-ID	C03	FORTRAN	3.2A*
511	*B	*CHKWD*	*	DECK-ID	C04	FORTRAN	3.2A*
512	*B	*CON*	*	DECK-ID	C07	FORTRAN	3.2A*
513	*B	*COLNT*	*	DECK-ID	C08	FORTRAN	3.2A*
514	*B	*DATAST*	*	DECK-ID	C09	FORTRAN	3.2A*
515	*B	*GETSYM*	*	DECK-ID	C10	FORTRAN	3.2A*
516	*B	*INOUT*	*	DECK-ID	C11	FORTRAN	3.2A*
517	*B	*LABEL*	*	DECK-ID	C14	FORTRAN	3.2A*
518	*B	*LABIN*	*	DECK-ID	C15	FORTRAN	3.2A*
519	*B	*QBPRMS*	*	DECK-ID	F10	FORTRAN	3.2A*
520	*B	*REED*	*	DECK-ID	C17	FORTRAN	3.2A*
521	*B	*SYMSCN*	*	DECK-ID	C19	FORTRAN	3.2A*
522	*B	*LOCLCB*	*	DECK-ID	F54	FORTRAN	3.2A*
523	*B	*CHCP*	*	DECK-ID	C05	FORTRAN	3.2A*
524	*B	*CL12*	*	DECK-ID	C06	FORTRAN	3.2A*
525	*B	*SKIP*	*	DECK-ID	C18	FORTRAN	3.2A*
526	*B	*IXOPT*	*	DECK-ID	C12	FORTRAN	3.2A*
527	*B	*QXLD*	*	DECK-ID	C16	FORTRAN	3.2A*
528	*B	*ENDLOC*	*	DECK-ID	F16	FORTRAN	3.2A*
529	*T						
530	*K,I8						
531	*N,FORTCB,,,B						
532	*K,I6						
533	*K,P8						
534	*F,F						
535	*B	*FTNA*	*	COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0*			
536	*B	*GOOD*	*	DECK-ID	F55	FORTRAN	3.2A*
537	*B	*INDEX*	*	DECK-ID	D01	FORTRAN	3.2A*
538	*B	*IOPRBD*	*	DECK-ID	F56	FORTRAN	3.2A*
539	*B	*NPNCH*	*	DECK-ID	D02	FORTRAN	3.2A*
540	*B	*QBPRMS*	*	DECK-ID	F10	FORTRAN	3.2A*
541	*B	*LOCLDA*	*	DECK-ID	F58	FORTRAN	3.2A*
542	*B	*DUMYDA*	*	DECK-ID	F59	FORTRAN	3.2A*
543	*B	*PHASE6*	*	DECK-ID	D03	FORTRAN	3.2A*
544	*B	*BEGIN0*	*	DECK-ID	D21	FORTRAN	3.2A*
545	*B	*CORV*	*	DECK-ID	F57	FORTRAN	3.2A*
546	*B	*FINISH*	*	DECK-ID	D22	FORTRAN	3.2A*
547	*B	*GETSYM*	*	DECK-ID	D16	FORTRAN	3.2A*

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548 *E *IACON# * DECK-ID D17 FORTRAN 3.2A#
549 *E *IHCON# * DECK-ID D18 FORTRAN 3.2A#
550 *E *NWRITE# * DECK-ID D19 FORTRAN 3.2A#
551 *E *PACK# * DECK-ID F09 FORTRAN 3.2A#
552 *E *SYMSCN# * DECK-ID D20 FORTRAN 3.2A#
553 *E *ENDLOC# * DECK-ID F16 FORTRAN 3.2A#
554 *T

555 *K,I8
556 *N,FORTDA,,,R

557 *K,I6
558 *K,P8

559 *F,F,,MARKER
560 *E *FTNA# * COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#

561 *E *GOOD# * DECK-ID F55 FORTRAN 3.2A#
562 *E *INDEX# * DECK-ID D01 FORTRAN 3.2A#

563 *E *IOPRBD# * DECK-ID F56 FORTRAN 3.2A#
564 *E *NPLNCH# * DECK-ID D02 FORTRAN 3.2A#

565 *E *Q8PRMS# * DECK-ID F10 FORTRAN 3.2A#
566 *E *LOCLDB# * DECK-ID F60 FORTRAN 3.2A#

567 *E *DUMYDR# * DECK-ID F61 FORTRAN 3.2A#
568 *E *AMOUT# * DECK-ID D04 FORTRAN 3.2A#

569 *E *BKDWN# * DECK-ID D06 FORTRAN 3.2A#
570 *E *COLNT# * DECK-ID D07 FORTRAN 3.2A#

571 *E *GETSYM# * DECK-ID D14 FORTRAN 3.2A#
572 *E *LABOUT# * DECK-ID D08 FORTRAN 3.2A#

573 *E *NP2OUT# * DECK-ID D09 FORTRAN 3.2A#
574 *E *RBDX# * DECK-ID D10 FORTRAN 3.2A#

575 *E *RBPk# * DECK-ID D11 FORTRAN 3.2A#
576 *E *SYMSCN# * DECK-ID D15 FORTRAN 3.2A#

577 *E *TABDEC# * DECK-ID D12 FORTRAN 3.2A#
578 *E *UNPUNC# * DECK-ID D13 FORTRAN 3.2A#

579 *E *ENDLOC# * DECK-ID F16 FORTRAN 3.2A#
580 *T

581 *K,I8
582 *N,FORTDB,,,R

583 *K,I6
584 *K,P8

585 *F,F,,MARKER
586 *E *FTNA# * COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#

587 *E *GOOD# * DECK-ID F55 FORTRAN 3.2A#
588 *E *INDEX# * DECK-ID D01 FORTRAN 3.2A#

589 *E *IOPRBD# * DECK-ID F56 FORTRAN 3.2A#
590 *E *NPLNCH# * DECK-ID D02 FORTRAN 3.2A#

591 *E *Q8PRMS# * DECK-ID F10 FORTRAN 3.2A#
592 *E *LOCLDC# * DECK-ID F62 FORTRAN 3.2A#

593 *E *ADMAX# * DECK-ID D05 FORTRAN 3.2A#
594 *E *GETSYM# * DECK-ID D14 FORTRAN 3.2A#
595 *E *TABDEC# * DECK-ID D12 FORTRAN 3.2A#
596 *E *SYMSCN# * DECK-ID D15 FORTRAN 3.2A#
597 *E *ENDLOC# * DECK-ID F16 FORTRAN 3.2A#
598 *T

599 *K,I8
600 *N,FORTDC,,,R

601 *K,I6
602 *K,P8

603 *F,F
604 *E *FTNA# * COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0#

605 *E *GOE# * DECK-ID F63 FORTRAN 3.2A#
606 *E *INDEX# * DECK-ID E01 FORTRAN 3.2A#
607 *E *IOPRBD# * DECK-ID F56 FORTRAN 3.2A#

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510

608	*P	*NPLNCH*	*	DECK-ID	E02	FORTRAN	3.2A*
609	*P	*Q8PRMS*	*	DECK-ID	F10	FORTRAN	3.2A*
610	*P	*LOCLDA*	*	DECK-ID	F58	FORTRAN	3.2A*
611	*P	*DUMYDA*	*	DECK-ID	F59	FORTRAN	3.2A*
612	*P	*PHASE6*	*	DECK-ID	E03	FORTRAN	3.2A*
613	*P	*REGINO*	*	DECK-ID	E19	FORTRAN	3.2A*
614	*P	*CONV*	*	DECK-ID	F57	FORTRAN	3.2A*
615	*P	*FINISH*	*	DECK-ID	E20	FORTRAN	3.2A*
616	*P	*GETSYM*	*	DECK-ID	E14	FORTRAN	3.2A*
617	*P	*IACON*	*	DECK-ID	E15	FORTRAN	3.2A*
618	*P	*IHCON*	*	DECK-ID	E16	FORTRAN	3.2A*
619	*P	*NWRITE*	*	DECK-ID	E17	FORTRAN	3.2A*
620	*P	*PACK*	*	DECK-ID	F09	FORTRAN	3.2A*
621	*P	*SETPRT*	*	DECK-ID	E18	FORTRAN	3.2A*
622	*P	*SYMSCN*	*	DECK-ID	D20	FORTRAN	3.2A*
623	*P	*ENDLOC*	*	DECK-ID	F16	FORTRAN	3.2A*
624	*T						
625	*K,I8						
626	*N,FORTEA,,,R						
627	*K,I6						
628	*K,P8						
629	*P,F,,,MARKER						
630	*P	*FTNA*	*	COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0*			
631	*P	*GOE*	*	DECK-ID	F63	FORTRAN	3.2A*
632	*P	*INDEX*	*	DECK-ID	E01	FORTRAN	3.2A*
633	*P	*IOPRBD*	*	DECK-ID	F56	FORTRAN	3.2A*
634	*P	*NPLNCH*	*	DECK-ID	E02	FORTRAN	3.2A*
635	*P	*Q8PRMS*	*	DECK-ID	F10	FORTRAN	3.2A*
636	*P	*LOCLDB*	*	DECK-ID	F60	FORTRAN	3.2A*
637	*P	*DUMYDB*	*	DECK-ID	F61	FORTRAN	3.2A*
638	*P	*AMCUT*	*	DECK-ID	E04	FORTRAN	3.2A*
639	*P	*PKDOWN*	*	DECK-ID	E06	FORTRAN	3.2A*
640	*P	*CONV*	*	DECK-ID	F57	FORTRAN	3.2A*
641	*P	*COUNT*	*	DECK-ID	E07	FORTRAN	3.2A*
642	*P	*GETSYM*	*	DECK-ID	E14	FORTRAN	3.2A*
643	*P	*IACON*	*	DECK-ID	E15	FORTRAN	3.2A*
644	*P	*IHCON*	*	DECK-ID	E16	FORTRAN	3.2A*
645	*P	*LABOUT*	*	DECK-ID	E08	FORTRAN	3.2A*
646	*P	*NP2OUT*	*	DECK-ID	E09	FORTRAN	3.2A*
647	*P	*NWRITE*	*	DECK-ID	F17	FORTRAN	3.2A*
648	*P	*PACK*	*	DECK-ID	F09	FORTRAN	3.2A*
649	*P	*RBDX*	*	DECK-ID	E10	FORTRAN	3.2A*
650	*P	*RBPX*	*	DECK-ID	E11	FORTRAN	3.2A*
651	*P	*SETPRT*	*	DECK-ID	E18	FORTRAN	3.2A*
652	*P	*SYMSCN*	*	DECK-ID	D20	FORTRAN	3.2A*
653	*P	*TABDEC*	*	DECK-ID	E12	FORTRAN	3.2A*
654	*P	*UNPUNC*	*	DECK-ID	E13	FORTRAN	3.2A*
655	*P	*ENDLOC*	*	DECK-ID	F16	FORTRAN	3.2A*
656	*T						
657	*K,I8						
658	*N,FORTEB,,,R						
659	*K,I6						
660	*K,P8						
661	*P,F,,,MARKER						
662	*P	*FTNA*	*	COPYRIGHT CONTROL DATA CORPORATION 1973 SL-0*			
663	*P	*GOE*	*	DECK-ID	F63	FORTRAN	3.2A*
664	*P	*INDEX*	*	DECK-ID	E01	FORTRAN	3.2A*
665	*P	*IOPRBD*	*	DECK-ID	F56	FORTRAN	3.2A*
666	*P	*NPLNCH*	*	DECK-ID	E02	FORTRAN	3.2A*
667	*P	*Q8PRMS*	*	DECK-ID	F10	FORTRAN	3.2A*

668	*R	*LOCLDC#	#	DECK-ID	F62	FORTTRAN	3.2A#
669	*E	*ADMAX#	#	DECK-ID	E05	FORTTRAN	3.2A#
670	*R	*GETSYM#	#	DECK-ID	E14	FORTTRAN	3.2A#
671	*E	*SYMSCN#	#	DECK-ID	D20	FORTTRAN	3.2A#
672	*R	*TABDEC#	#	DECK-ID	E12	FORTTRAN	3.2A#
673	*E	*ENDLOC#	#	DECK-ID	F16	FORTTRAN	3.2A#
674	*T						
675	*K	,I8					
676	*N	,FORTEC,,.R					
677	*V	NON-REENTRANT FTN. LIBRARY					
678	*K	,I6					
679	*L	,READ					
680	*R	*FORTN#	#	DECK-ID	F01	3.2 FTN	RUNTIME#
681	*L	,G8PREP					
682	*E	*Q8PRMS#	#	DECK-ID	G01	3.2 FTN	RUNTIME#
683	*L	,G8QF2I					
684	*E	*Q8EXPN#	#	DECK-ID	G02	3.2 FTN	RUNTIME#
685	*L	,ABS					
686	*E	*Q8AR#	#	DECK-ID	G03	3.2 FTN	RUNTIME#
687	*L	,SQRT					
688	*E	*SQRTF#	#	DECK-ID	G04	3.2 FTN	RUNTIME#
689	*L	,SIGN					
690	*E	*SIGN#	#	DECK-ID	G05	3.2 FTN	RUNTIME#
691	*L	,FLOAT					
692	*E	*FXFL#	#	DECK-ID	G06	3.2 FTN	RUNTIME#
693	*L	,EXP					
694	*E	*EXPPRG#	#	DECK-ID	G07	3.2 FTN	RUNTIME#
695	*L	,ALOG					
696	*E	*LNUPRG#	#	DECK-ID	G08	3.2 FTN	RUNTIME#
697	*L	,TANH					
698	*E	*TANH#	#	DECK-ID	G09	3.2 FTN	RUNTIME#
699	*L	,SIN					
700	*E	*SINCOS#	#	DECK-ID	G10	3.2 FTN	RUNTIME#
701	*L	,ATAN					
702	*E	*ARCTPG#	#	DECK-ID	G11	3.2 FTN	RUNTIME#
703	*L	,IFALT					
704	*E	*IFALT#	#	DECK-ID	G12	3.2 FTN	RUNTIME#
705	*L	,FLOT					
706	*E	*FLOAT#	#	DECK-ID	G13	3.2 FTN	RUNTIME#
707	*L	,G8IFRM					
708	*E	*Q8IFRM#	#	DECK-ID	H01	3.2 FTN	RUNTIME#
709	*L	,G8FS					
710	*E	*Q8FS#	#	DECK-ID	H02	3.2 FTN	RUNTIME#
711	*L	,G8TRAN					
712	*E	*Q8TPAN#	#	DECK-ID	H03	3.2 FTN	RUNTIME#
713	*L	,G8QINI					
714	*E	*Q8QINI#	#	DECK-ID	H04	3.2 FTN	RUNTIME#
715	*L	,G8QEND					
716	*E	*Q8QEND#	#	DECK-ID	H05	3.2 FTN	RUNTIME#
717	*L	,G8CMP0					
718	*E	*Q8CMP#	#	DECK-ID	H06	3.2 FTN	RUNTIME#
719	*L	,G8RWBU					
720	*E	*Q8RWBU#	#	DECK-ID	H07	3.2 FTN	RUNTIME#
721	*L	,G8ERRM					
722	*E	*Q8ERRM#	#	DECK-ID	H08	3.2 FTN	RUNTIME#
723	*L	,G8DFNF					
724	*E	*Q8DFI0#	#	DECK-ID	H09	3.2 FTN	RUNTIME#
725	*L	,G8QX					
726	*E	*Q8QX#	#	DECK-ID	H10	3.2 FTN	RUNTIME#
727	*L	,G8QUN1					

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729	*L,G8FGET					
730	*E #Q8FGET#	#	DECK-ID H12	3.2	FTN	RUNTIME#
731	*L,G8MAGT					
732	*E #Q8MAGT#	#	DECK-ID H13	3.2	FTN	RUNTIME#
733	*L,E0F					
734	*E #TAPCON#	#	DECK-ID H14	3.2	FTN	RUNTIME#
735	*L,I0CK					
736	*E #I0CK#	#	DECK-ID H15	3.2	FTN	RUNTIME#
737	*L,G8PSE					
738	*E #PSSTOP#	#	DECK-ID H16	3.2	FTN	RUNTIME#
739	*L,G8PAND					
740	*E #Q8PAND#	#	DECK-ID H17	3.2	FTN	RUNTIME#
741	*L,G8EXP1					
742	*E #Q8EXP1#	#	DECK-ID H18	3.2	FTN	RUNTIME#
743	*L,G8EXP9					
744	*E #Q8EXP9#	#	DECK-ID H19	3.2	FTN	RUNTIME#
745	*L,SETBFR					
746	*E #Q8QGT#	#	DECK-ID H20	3.2	FTN	RUNTIME#
747	*L,ENCODE					
748	*E #I0CODE#	#	DECK-ID J01	3.2	FTN	RUNTIME#
749	*L,COMMON					
750	*E #PSUEDO#	#	DECK-ID J02	3.2	FTN	RUNTIME#
751	*L,IGETCH					
752	*E #IGETCH#	#	DECK-ID J03	3.2	FTN	RUNTIME#
753	*L,IPACK					
754	*E #IPACK#	#	DECK-ID J04	3.2	FTN	RUNTIME#
755	*L,LDATE					
756	*E #UPDATN#	#	DECK-ID J05	3.2	FTN	RUNTIME#
757	*L,DECPL					
758	*E #DECPL#	#	DECK-ID J06	3.2	FTN	RUNTIME#
759	*L,INTGR					
760	*E #INTGR#	#	DECK-ID J07	3.2	FTN	RUNTIME#
761	*L,SPACEX					
762	*E #SPACEN#	#	DECK-ID J08	3.2	FTN	RUNTIME#
763	*L,H0LRTH					
764	*E #H0LRTH#	#	DECK-ID J09	3.2	FTN	RUNTIME#
765	*L,DCHX					
766	*E #DCHX#	#	DECK-ID J10	3.2	FTN	RUNTIME#
767	*L,HXASC					
768	*E #HXASC#	#	DECK-ID J11	3.2	FTN	RUNTIME#
769	*L,AFRMOT					
770	*E #AFRMOT#	#	DECK-ID J12	3.2	FTN	RUNTIME#
771	*L,RFRMOT					
772	*E #RFRMOT#	#	DECK-ID J13	3.2	FTN	RUNTIME#
773	*L,AFRMIN					
774	*E #AFRMIN#	#	DECK-ID J14	3.2	FTN	RUNTIME#
775	*L,RFRMIN					
776	*E #RFRMIN#	#	DECK-ID J15	3.2	FTN	RUNTIME#
777	*L,ASCHX					
778	*E #ASCHX#	#	DECK-ID J16	3.2	FTN	RUNTIME#
779	*L,HXDC					
780	*E #HXDC#	#	DECK-ID J17	3.2	FTN	RUNTIME#
781	*L,FLOTIN					
782	*E #FLOTIN#	#	DECK-ID J18	3.2	FTN	RUNTIME#
783	*L,FOUT					
784	*E #FOUT#	#	DECK-ID J19	3.2	FTN	RUNTIME#
785	*L,EOUT					
786	*E #EOUT#	#	DECK-ID J20	3.2	FTN	RUNTIME#
787	*L,EWRITE					

788 *E *EWRITE# # DECK-ID J21 3.2 FTN RUNTIME#
789 *L,INITL1
790 *E *INITL1# # DECK-ID J22 3.2 FTN RUNTIME#
791 *L,FORMTR
792 *E *FORMTN# # DECK-ID J23 3.2 FTN RUNTIME#
793 *L,G8QFI
794 *E *Q8QFI# # DECK-ID J24 3.2 FTN RUNTIME#
795 *L,G8QFL
796 *E *Q8QFL# # DECK-ID J25 3.2 FTN RUNTIME#
797 *L,G8QFX
798 *E *Q8QFX# # DECK-ID J26 3.2 FTN RUNTIME#
799 *L,HEXASC
800 *E *HEXASC# # DECK-ID J27 3.2 FTN RUNTIME#
801 *L,HEXDEC
802 *E *HEXDEC# # DECK-ID J28 3.2 FTN RUNTIME#
803 *L,ASCII
804 *E *ASCII# # DECK-ID J29 3.2 FTN RUNTIME#
805 *L,DECHEX
806 *E *DECHEX# # DECK-ID J30 3.2 FTN RUNTIME#
807 *L,AFORM
808 *E *AFORM# # DECK-ID J31 3.2 FTN RUNTIME#
809 *L,RFORM
810 *E *RFORM# # DECK-ID J32 3.2 FTN RUNTIME#
811 *L,FLOATG
812 *E *FLOATG# # DECK-ID J33 3.2 FTN RUNTIME#
813 *L,G8DXP1
814 *E *DBLDMY#
815 *Z
816 *CTC, INSTALLATION COMPLETED.
817 *U

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The FORTRAN reentrant library modules are not present on the FORTRAN install tape, but are present on the FORTRAN binaries tape. In this example the user keypunches the skeleton *B cards for these modules and one *LP card to precede this set of *B cards. (Refer to Article 6.2.2) This is the simplest method of producing these cards. It is also possible to use SKED to generate these cards. However, the reentrant library modules are not together on the tape. Therefore, numerous deletions must be performed, either by the SKET DELETE command or by the user later eliminating unneeded punched cards.

The reentrant library modules in this example are the core modules listed in Article 2.1, Group K together with the formatting routines listed in Article 2.1, Group K1. (Refer to the skeleton listing later in this article.)

The addition of the reentrant FORTRAN library modules requires that the value of BGNMON and the value of ENDOV4 be decreased. The total length of the reentrant library modules to be added is obtained by loading these modules in the background, using the *L job processor command. The procedure is as follows: A skeleton is

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put together with a *B card for each module to be included in the FORTRAN reentrant library. A *B record for ENDLOC is placed after these cards.

(This could be the *B record for any module on the FORTRAN binaries tape.) After that a *END card completes the skeleton. A listing of this skeleton follows.

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1	*B	*FORTR*	*	DECK-ID	A01	3.2	FTN	RUNTIME*
2	*B	*Q8PRMR*	*	DECK-ID	B01	3.2	FTN	RUNTIME*
3	*B	*PARABR*	*	DECK-ID	B02	3.2	FTN	RUNTIME*
4	*B	*Q8EXPR*	*	DECK-ID	B03	3.2	FTN	RUNTIME*
5	*B	*Q8ABR*	*	DECK-ID	B04	3.2	FTN	RUNTIME*
6	*B	*SQRTFR*	*	DECK-ID	B05	3.2	FTN	RUNTIME*
7	*B	*SIGNR*	*	DECK-ID	B06	3.2	FTN	RUNTIME*
8	*B	*FXFLR*	*	DECK-ID	B07	3.2	FTN	RUNTIME*
9	*B	*EXPRGR*	*	DECK-ID	B08	3.2	FTN	RUNTIME*
10	*B	*LNPRGR*	*	DECK-ID	B09	3.2	FTN	RUNTIME*
11	*B	*TANHR*	*	DECK-ID	B10	3.2	FTN	RUNTIME*
12	*B	*SNCSR*	*	DECK-ID	P11	3.2	FTN	RUNTIME*
13	*B	*ARCPGR*	*	DECK-ID	B12	3.2	FTN	RUNTIME*
14	*B	*IFALTR*	*	DECK-ID	B13	3.2	FTN	RUNTIME*
15	*B	*FLCATR*	*	DECK-ID	B14	3.2	FTN	RUNTIME*
16	*B	*Q8QIOR*	*	DECK-ID	C01	3.2	FTN	RUNTIME*
17	*B	*BINARR*	*	DECK-ID	C02	3.2	FTN	RUNTIME*
18	*B	*IOEODR*	*	DECK-ID	D01	3.2	FTN	RUNTIME*
19	*B	*INITLR*	*	DECK-ID	D02	3.2	FTN	RUNTIME*
20	*B	*RSTORR*	*	DECK-ID	D03	3.2	FTN	RUNTIME*
21	*B	*GETCHR*	*	DECK-ID	D04	3.2	FTN	RUNTIME*
22	*B	*IPACKR*	*	DECK-ID	D05	3.2	FTN	RUNTIME*
23	*B	*UPDATR*	*	DECK-ID	D06	3.2	FTN	RUNTIME*
24	*B	*DECPLR*	*	DECK-ID	D07	3.2	FTN	RUNTIME*
25	*B	*INTGRR*	*	DECK-ID	D08	3.2	FTN	RUNTIME*
26	*B	*SPACER*	*	DECK-ID	D09	3.2	FTN	RUNTIME*
27	*B	*HOLR*	*	DECK-ID	D10	3.2	FTN	RUNTIME*
28	*B	*DCHXR*	*	DECK-ID	D11	3.2	FTN	RUNTIME*
29	*B	*HXASCR*	*	DECK-ID	D12	3.2	FTN	RUNTIME*
30	*B	*AFNTOR*	*	DECK-ID	D13	3.2	FTN	RUNTIME*
31	*B	*RFNTOR*	*	DECK-ID	D14	3.2	FTN	RUNTIME*
32	*B	*AFNTIR*	*	DECK-ID	D15	3.2	FTN	RUNTIME*
33	*B	*RFNTIR*	*	DECK-ID	D16	3.2	FTN	RUNTIME*
34	*B	*ASCHXR*	*	DECK-ID	D17	3.2	FTN	RUNTIME*
35	*B	*HXDCR*	*	DECK-ID	D18	3.2	FTN	RUNTIME*
36	*B	*FLOTIR*	*	DECK-ID	D19	3.2	FTN	RUNTIME*
37	*B	*FOLTR*	*	DECK-ID	D20	3.2	FTN	RUNTIME*
38	*B	*EOLTR*	*	DECK-ID	D21	3.2	FTN	RUNTIME*
39	*B	*EWAITR*	*	DECK-ID	D22	3.2	FTN	RUNTIME*
40	*B	*INTIIR*	*	DECK-ID	D23	3.2	FTN	RUNTIME*
41	*B	*FORMTR*	*	DECK-ID	D24	3.2	FTN	RUNTIME*
42	*B	*Q8GFIR*	*	DECK-ID	D25	3.2	FTN	RUNTIME*
43	*B	*Q8GFLR*	*	DECK-ID	D26	3.2	FTN	RUNTIME*
44	*B	*Q8GFXR*	*	DECK-ID	D27	3.2	FTN	RUNTIME*
45	*B	*HEXAR*	*	DECK-ID	D28	3.2	FTN	RUNTIME*
46	*B	*HEXDR*	*	DECK-ID	D29	3.2	FTN	RUNTIME*
47	*B	*ASCIIIR*	*	DECK-ID	D30	3.2	FTN	RUNTIME*
48	*B	*DECHXR*	*	DECK-ID	D31	3.2	FTN	RUNTIME*
49	*B	*AFORMR*	*	DECK-ID	D32	3.2	FTN	RUNTIME*
50	*B	*RFORMR*	*	DECK-ID	D33	3.2	FTN	RUNTIME*
51	*B	*FLOTGR*	*	DECK-ID	D34	3.2	FTN	RUNTIME*
52	*B	*ENDLOC*						
53	*B	*END						

The FORTRAN binaries tape is mounted on tape unit 0, logical unit 6. A scratch tape is mounted on tape unit 1, logical unit 13. The skeleton is loaded in the card reader. A short install tape is created by the following commands to LIBILD.

```

MI
*BATCH
L,10 FAILED 06
*ACTION
CU
J
*JOB
J
*LIBILD
CONTROL LU =
DEFS LU =
INSTALL LU = 13
NEWLIB LU =
LIB 01 LU = 06
LIB 02 LU =
SKELETON LU = 10
LOAD *SKEL/INSTAL, CR WHEN READY
LIBRARY BUILD COMPLETE
TYPE *Z TO TERMINATE OR
TYPE *C TO CONTINUE WITH CURRENT SKELETON AND/OR
OUTPUT LIBRARY LU'S *Z
J

```

The install file created is then loaded by the job processor with the following commands.

```

*REW,13,6
J
*L,13

```

The job processor output is as follows:

FORTR	4998	DECK-ID	A01	3.2	FTN	RUNTIME	SUMMARY-079
QBPRMR	4ACA	DECK-ID	B01	3.2	FTN	RUNTIME	SUMMARY-079
PAPABR	4AE1	DECK-ID	B02	3.2	FTN	RUNTIME	SUMMARY-081
QBFXPR	4AF2	DECK-ID	B03	3.2	FTN	RUNTIME	SUMMARY-079
QBAER	4BA3	DECK-ID	B04	3.2	FTN	RUNTIME	SUMMARY-079
SQRTFR	4BBA	DECK-ID	B05	3.2	FTN	RUNTIME	SUMMARY-079
SIGNR	4C15	DECK-ID	B06	3.2	FTN	RUNTIME	SUMMARY-079
FXFLR	4C41	DECK-ID	B07	3.2	FTN	RUNTIME	SUMMARY-079
EXPRGR	4CAC	DECK-ID	B08	3.2	FTN	RUNTIME	SUMMARY-079
LNPRGR	4D4E	DECK-ID	B09	3.2	FTN	RUNTIME	SUMMARY-079
TANFR	4DC8	DECK-ID	B10	3.2	FTN	RUNTIME	SUMMARY-079
SNCSR	4E34	DECK-ID	B11	3.2	FTN	RUNTIME	SUMMARY-079
ARCPGR	4F00	DECK-ID	B12	3.2	FTN	RUNTIME	SUMMARY-079
IFALTR	4F99	DECK-ID	B13	3.2	FTN	RUNTIME	SUMMARY-079
FLOATR	4FB0	DECK-ID	B14	3.2	FTN	RUNTIME	SUMMARY-079
QBQIOR	51AC	DECK-ID	C01	3.2	FTN	RUNTIME	SUMMARY-082
RINARR	530E	DECK-ID	C02	3.2	FTN	RUNTIME	SUMMARY-079
IOCCDR	535B	DECK-ID	D01	3.2	FTN	RUNTIME	SUMMARY-079
INITLR	538F	DECK-ID	D02	3.2	FTN	RUNTIME	SUMMARY-079
RSTCRR	539E	DECK-ID	D03	3.2	FTN	RUNTIME	SUMMARY-079
GETCHR	53AE	DECK-ID	D04	3.2	FTN	RUNTIME	SUMMARY-079
IPACKR	53C8	DECK-ID	D05	3.2	FTN	RUNTIME	SUMMARY-079
UPDATR	53FF	DECK-ID	D06	3.2	FTN	RUNTIME	SUMMARY-079
DECFLR	540C	DECK-ID	D07	3.2	FTN	RUNTIME	SUMMARY-079
INTERR	5432	DECK-ID	D08	3.2	FTN	RUNTIME	SUMMARY-079
SPACER	545F	DECK-ID	D09	3.2	FTN	RUNTIME	SUMMARY-079
HOLR	5477	DECK-ID	D10	3.2	FTN	RUNTIME	SUMMARY-079
DCHXR	550B	DECK-ID	D11	3.2	FTN	RUNTIME	SUMMARY-079
HXASCR	557E	DECK-ID	D12	3.2	FTN	RUNTIME	SUMMARY-079
AFMTOR	55D1	DECK-ID	D13	3.2	FTN	RUNTIME	SUMMARY-079
RFMTOR	55FB	DECK-ID	D14	3.2	FTN	RUNTIME	SUMMARY-079
AFMTIR	5614	DECK-ID	D15	3.2	FTN	RUNTIME	SUMMARY-079
RFMTIR	5642	DECK-ID	D16	3.2	FTN	RUNTIME	SUMMARY-079
ASCFXR	5659	DECK-ID	D17	3.2	FTN	RUNTIME	SUMMARY-079
HXDCK	5694	DECK-ID	D18	3.2	FTN	RUNTIME	SUMMARY-079
FLOTIR	5724	DECK-ID	D19	3.2	FTN	RUNTIME	SUMMARY-079
FOUTR	576E	DECK-ID	D20	3.2	FTN	RUNTIME	SUMMARY-079
EOUTR	57F7	DECK-ID	D21	3.2	FTN	RUNTIME	SUMMARY-079
EWPIR	58DF	DECK-ID	D22	3.2	FTN	RUNTIME	SUMMARY-079
INTIIR	58EB	DECK-ID	D23	3.2	FTN	RUNTIME	SUMMARY-079
FORMTR	5908	DECK-ID	D24	3.2	FTN	RUNTIME	SUMMARY-081
QBQFIR	5ADD	DECK-ID	D25	3.2	FTN	RUNTIME	SUMMARY-079
QBQFLR	5AF7	DECK-ID	D26	3.2	FTN	RUNTIME	SUMMARY-079
QBQFXR	5B26	DECK-ID	D27	3.2	FTN	RUNTIME	SUMMARY-079
HEXAR	5B5D	DECK-ID	D28	3.2	FTN	RUNTIME	SUMMARY-079
HEXCR	5B75	DECK-ID	D29	3.2	FTN	RUNTIME	SUMMARY-079
ASCIIR	5B92	DECK-ID	D30	3.2	FTN	RUNTIME	SUMMARY-079
DECFXR	5BA7	DECK-ID	D31	3.2	FTN	RUNTIME	SUMMARY-079
AFORMR	5BC7	DECK-ID	D32	3.2	FTN	RUNTIME	SUMMARY-079
RFORMR	5BE3	DECK-ID	D33	3.2	FTN	RUNTIME	SUMMARY-079
FLOTGR	5EFF	DECK-ID	D34	3.2	FTN	RUNTIME	SUMMARY-079
ENDLOC	5C1B	DECK-ID	F16	FORTRAN 3.2A		SUMMARY-074	

The job processor output lists the load address of each module. The total length of the modules FORTR, Q8PRMR, ..., FLOTGR is the difference between the load address for ENDLOC and the load address for FORTR, or $5C1B_{16} - 4998_{16} = 1283_{16}$. From Article 5.3.3, the previous values of ENDOV4 and BGNMON were $66FF_{16}$ and 6700_{16} respectively. In the new skeleton ENDOV4 will have the value $547C_{16}$ BGNMON, the value $547D_{16}$.

Currently, unprotected core starts at 4998_{16} . This location is contained in Section AAB of SYSDAT. It also is shown in the job processor output as the load location of the first module loaded into unprotected core. Unless N4 is changed, the new start of unprotected core will be at location 4998_{16} plus the increase in the size of SYSDAT. The increase in the size of SYSDAT due to the changes described in Article 6.3.1 is $492 = 1EC_{16}$ words. If the start of unprotected core is at $4998_{16} + 1EC_{16}$, then the new size of unprotected core will be $ENDOV4 - (4998_{16} + 1EC_{16}) = 547C_{16} - (4998_{16} + 1EC_{16}) = 8F8_{16}$. However, the FORTRAN 3.2A compiler requires 2000_{16} words of unprotected core. (Refer to Table E-11.) This means that the start of unprotected must be decreased by $2000_{16} - 8F8_{16} = 1708_{16}$ words. To decrease the start of unprotected, the size of

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allocatable core area 4 must be decreased by this amount

Therefore, in the new skeleton, the user sets $N4 =$

$3000_{16} - 1708_{16} = 18F8_{16} = 6392$. (The previous value of $N4$ was 3000_{16}) This size for allocatable

area 4 is large enough to handle background File

Manager requests and other requirements for area 4.

(Refer to Article 1.58)

In addition to modifying $ENDOV4$, $BGNMON$, and $N4$, the

user updates the values of $SYSMON$ and $SYSDAY$ in the

skeleton. The new skeleton is as follows:

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1 *S,SYSMON,$3131
2 *S,SYSDAY,$3132
3 *S,SYSYER,$3734
4 *S,SYSLVL,$3836
5 *S,N4,$18F8
6 *V
7 *V          1700 MASS STORAGE OPERATING SYSTEM - VER. 4.1
8 *V
9 *V          ANN'S TEST SYSTEM
10 *V
11 *YM,LIBEDT,1
12 *YM,LOADSD,2
13 *YM,JOBENT,3
14 *YM,JOBPRO,4
15 *YM,PROTEC,5
16 *YM,JLOAD,6
17 *YM,JPCHGE,7
18 *YM,JPT13,8
19 *YM,JCRDV4,9
20 *YM,JLGOV4,10
21 *YM,JPSTV4,11
22 *YM,NAMEV4,12
23 *YM,JPFLV4,13
24 *YM,AFILV4,14
25 *YM,RESTOR,15
26 *YM,RCOVER,16
27 *YM,BRKPT,17
28 *YM,ODEBUG,18
29 *YM,SYSCOP,19
30 *YM,SYSSEG,20
31 *YM,MIPRO,21
32 *YM,TDFUNC,22
33 *YM,EFSTOR,23
34 *YM,EFLIST,24
35 *YM,SCMM17,25
36 *YM,VERIFY,26
37 *YM,DUMMY1,27
38 *YM,DUMMY2,28
39 *YM,DUMMY3,29
40 *YM,DUMMY4,30
41 *YM,DUMMY5,31
42 *YM,DUMMY6,32
43 *YM,DUMMY7,33
44 *YM,DUMMY8,34
45 *YM,DUMMY9,35
46 *YM,DUMMY0,36
47 *S,ENDOV4,$547C
48 *S,BGNMON,$547D
49 *S,MSIZV4,$7FFF
50 *S,SECTOR,$7EDA
51 *
52 *L          SYSTEM DATA PROGRAM
53 *B 'SYSOAT' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
54 *L          SPACE REQUEST PROCESSOR
55 *B 'SPACE' ' DECK-ID A02 MSOS 4.1'
56 *
57 *          SYSTEM CORE RESIDENT PROGRAMS
58 *
59 *LP          MONITOR
60 *B 'NMONI' ' DECK-ID A03 MSOS 4.1'

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61	*R	'RDISP'							
62	*R	'RW'		DECK-ID	A06	MSOS	4.1'		
63	*R	'T14'		DECK-ID	A07	MSOS	4.1'		
64	*R	'T16'		DECK-ID	A08	MSOS	4.1'		
65	*R	'PAPAME'		DECK-ID	A09	MSOS	4.1'		
66	*R	'COMMON'		DECK-ID	A10	MSOS	4.1'		
67	*R	'NIPROC'		DECK-ID	A11	MSOS	4.1'		
68	*R	'ALVOL'		DECK-ID	A13	MSOS	4.1'		
69	*R	'QFVOL'		DECK-ID	A14	MSOS	4.1'		
70	*R	'ALCOPE'		DECK-ID	A15	MSOS	4.1'		
71	*R	'DCORE'		DECK-ID	A16	MSOS	4.1'		
72	*R	'NENR'		DECK-ID	A18	MSOS	4.1'		
73	*R	'NCMPRQ'		DECK-ID	A19	MSOS	4.1'		
74	*R	'AKQ'		DECK-ID	A20	MSOS	4.1'		
75	*R	'ADEV'		DECK-ID	A21	MSOS	4.1'		
76	*R	'TMINT'		DECK-ID	A22	MSOS	4.1'		
77	*R	'DTIMER'		DECK-ID	A23	MSOS	4.1'		
78	*R	'TOD'		DECK-ID	A24	MSOS	4.1'		
79	*R	'MINT'		DECK-ID	A25	MSOS	4.1'		
80	*R	'TRVEC'		DECK-ID	001	MSOS	4.1'		
81	*LP	DEBUGGING / CHECKOUT							
82	*R	'SNAPOL'		DECK-ID	H01	MSOS	4.1'		
83	*R	'DMP421'		DECK-ID	H03	MSOS	4.1'		
84	*R	'3DK85X'		DECK-ID	H05	MSOS	4.1'		
85	*LP	FILE MANAGER							
86	*R	'FILMGR'		DECK-ID	F01	MSOS	4.1'		
87	*R	'RSPCV4'		DECK-ID	F02	MSOS	4.1'		
88	*R	'SRHFIS'		DECK-ID	F03	MSOS	4.1'		
89	*LP	CORE RESIDENT DRIVERS							
90	*R	'EFDATA'		DECK-ID	C01	MSOS	4.1'		
91	*R	'DUMMY'		DECK-ID	C02	MSOS	4.1'		
92	*R	'ALAQ'		DECK-ID	C03	MSOS	4.1'		
93	*R	'D1711'		DECK-ID	C05	MSOS	4.1'		
94	*R	'D1738'		DECK-ID	C08	MSOS	4.1'		
95	*R	'REWCK'		DECK-ID	C13	MSOS	4.1'		
96	*R	'MMEXFC'		DECK-ID	C15	MSOS	4.1'		
97	*LP	REENTRANT FORTRAN RUNTIME LIBRARY							
98	*R	'FURTR'		DECK-ID	R01	3.2	FTN RUNTIME'		
99	*R	'Q8PRMR'		DECK-ID	R01	3.2	FTN RUNTIME'		
100	*R	'PARABP'		DECK-ID	R02	3.2	FTN RUNTIME'		
101	*R	'Q8EXPR'		DECK-ID	R03	3.2	FTN RUNTIME'		
102	*R	'Q8ABE'		DECK-ID	R04	3.2	FTN RUNTIME'		
103	*R	'SQRTFR'		DECK-ID	R05	3.2	FTN RUNTIME'		
104	*R	'SIGNP'		DECK-ID	R06	3.2	FTN RUNTIME'		
105	*R	'FXFLP'		DECK-ID	R07	3.2	FTN RUNTIME'		
106	*R	'FXPPGR'		DECK-ID	R08	3.2	FTN RUNTIME'		
107	*R	'LNPRGR'		DECK-ID	R09	3.2	FTN RUNTIME'		
108	*R	'TANHR'		DECK-ID	R10	3.2	FTN RUNTIME'		
109	*R	'SNCSF'		DECK-ID	R11	3.2	FTN RUNTIME'		
110	*R	'ARCPGR'		DECK-ID	R12	3.2	FTN RUNTIME'		
111	*R	'IFALTR'		DECK-ID	R13	3.2	FTN RUNTIME'		
112	*R	'FLOATR'		DECK-ID	R14	3.2	FTN RUNTIME'		
113	*R	'Q3QIGR'		DECK-ID	001	3.2	FTN RUNTIME'		
114	*R	'BINARR'		DECK-ID	002	3.2	FTN RUNTIME'		
115	*R	'IOCODR'		DECK-ID	001	3.2	FTN RUNTIME'		
116	*R	'INITLR'		DECK-ID	002	3.2	FTN RUNTIME'		
117	*R	'RSTORE'		DECK-ID	003	3.2	FTN RUNTIME'		
118	*R	'GETCHR'		DECK-ID	004	3.2	FTN RUNTIME'		
119	*R	'IPACKR'		DECK-ID	005	3.2	FTN RUNTIME'		
120	*R	'UPDATR'		DECK-ID	006	3.2	FTN RUNTIME'		

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121	*B	'DECPLR'	'	DECK-ID 007	3.2	FTN	RUNTIME'
122	*B	'INTGRR'	'	DECK-ID 008	3.2	FTN	RUNTIME'
123	*B	'SPACER'	'	DECK-ID 009	3.2	FTN	RUNTIME'
124	*B	'HOLR'	'	DECK-ID 010	3.2	FTN	RUNTIME'
125	*B	'DCHXR'	'	DECK-ID 011	3.2	FTN	RUNTIME'
126	*B	'HXASCR'	'	DECK-ID 012	3.2	FTN	RUNTIME'
127	*B	'AFMTOR'	'	DECK-ID 013	3.2	FTN	RUNTIME'
128	*B	'RFMTOR'	'	DECK-ID 014	3.2	FTN	RUNTIME'
129	*B	'AFMTIR'	'	DECK-ID 015	3.2	FTN	RUNTIME'
130	*B	'RFMTIR'	'	DECK-ID 016	3.2	FTN	RUNTIME'
131	*B	'ASCHXR'	'	DECK-ID 017	3.2	FTN	RUNTIME'
132	*B	'HXDCR'	'	DECK-ID 018	3.2	FTN	RUNTIME'
133	*B	'FLOTIR'	'	DECK-ID 019	3.2	FTN	RUNTIME'
134	*B	'FOUTR'	'	DECK-ID 020	3.2	FTN	RUNTIME'
135	*B	'EOUTR'	'	DECK-ID 021	3.2	FTN	RUNTIME'
136	*B	'EWRITR'	'	DECK-ID 022	3.2	FTN	RUNTIME'
137	*B	'INTI1R'	'	DECK-ID 023	3.2	FTN	RUNTIME'
138	*B	'FORMTR'	'	DECK-ID 024	3.2	FTN	RUNTIME'
139	*B	'QBQFIR'	'	DECK-ID 025	3.2	FTN	RUNTIME'
140	*B	'QBQFLR'	'	DECK-ID 026	3.2	FTN	RUNTIME'
141	*B	'QBQFXR'	'	DECK-ID 027	3.2	FTN	RUNTIME'
142	*B	'HEXAR'	'	DECK-ID 028	3.2	FTN	RUNTIME'
143	*B	'HEXDR'	'	DECK-ID 029	3.2	FTN	RUNTIME'
144	*B	'ASCIIR'	'	DECK-ID 030	3.2	FTN	RUNTIME'
145	*B	'DECHXR'	'	DECK-ID 031	3.2	FTN	RUNTIME'
146	*B	'AFORMR'	'	DECK-ID 032	3.2	FTN	RUNTIME'
147	*B	'RFORMR'	'	DECK-ID 033	3.2	FTN	RUNTIME'
148	*B	'FLOTGR'	'	DECK-ID 034	3.2	FTN	RUNTIME'
149	*B	'NXTLOC'	'	NEXT AVAILABLE LOCATION'			
150	*						

SYSTEM MASS RESIDENT PROGRAMS

151	*						
152	*						
153	*M		LIBEDT		1		
154	*B	'LIBEDT'	'	DECK-ID 002	MSOS	4.1'	
155	*M		LOADSD		2		
156	*B	'LOAD1'	'	DECK-ID 003	MSOS	4.1'	
157	*B	'BRNCH1'	'	DECK-ID 004	MSOS	4.1'	
158	*B	'LIDRV1'	'	DECK-ID 005	MSOS	4.1'	
159	*B	'LCDRV1'	'	DECK-ID 006	MSOS	4.1'	
160	*B	'LMDRV1'	'	DECK-ID 007	MSOS	4.1'	
161	*B	'LLDRV1'	'	DECK-ID 008	MSOS	4.1'	
162	*B	'ADJOF1'	'	DECK-ID 009	MSOS	4.1'	
163	*B	'CNVRT1'	'	DECK-ID 010	MSOS	4.1'	
164	*B	'LSTOT1'	'	DECK-ID 011	MSOS	4.1'	
165	*B	'LINK11'	'	DECK-ID 012	MSOS	4.1'	
166	*B	'LOADR1'	'	DECK-ID 013	MSOS	4.1'	
167	*B	'NAMPR1'	'	DECK-ID 014	MSOS	4.1'	
168	*B	'RBDZ1'	'	DECK-ID 015	MSOS	4.1'	
169	*B	'ENTEX1'	'	DECK-ID 016	MSOS	4.1'	
170	*B	'XFRPR1'	'	DECK-ID 017	MSOS	4.1'	
171	*B	'STBASE'	'	DECK-ID 018	MSOS	4.1'	
172	*B	'LNKENT'	'	DECK-ID 019	MSOS	4.1'	
173	*B	'LNKCR1'	'	DECK-ID 020	MSOS	4.1'	
174	*B	'PATCH'	'	DECK-ID 021	MSOS	4.1'	
175	*B	'TBSCH1'	'	DECK-ID 022	MSOS	4.1'	
176	*B	'HASH'	'	DECK-ID 023	MSOS	4.1'	
177	*B	'TBSTR1'	'	DECK-ID 024	MSOS	4.1'	
178	*B	'PAGE'	'	DECK-ID 025	MSOS	4.1'	
179	*B	'PROGLD'	'	DECK-ID 026	MSOS	4.1'	
180	*B	'SCAN1'	'	DECK-ID 027	MSOS	4.1'	

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181	*B	'CHPU1'	'	DECK-ID 028	MSOS 4.1'
182	*B	'ADJOV2'	'	DECK-ID 029	MSOS 4.1'
183	*B	'ADRPR1'	'	DECK-ID 030	MSOS 4.1'
184	*M			JOBENT	3
185	*B	'JOBENT'	'	DECK-ID 031	MSOS 4.1'
186	*B	'T11'	'	DECK-ID 032	MSOS 4.1'
187	*B	'T7'	'	DECK-ID 033	MSOS 4.1'
188	*B	'T5'	'	DECK-ID 034	MSOS 4.1'
189	*B	'T3'	'	DECK-ID 035	MSOS 4.1'
190	*S,N1,P				
191	*M			JOBPRO	4
192	*B	'JOBPRO'	'	DECK-ID 036	MSOS 4.1'
193	*B	'ONE'	'	DECK-ID 037	MSOS 4.1'
194	*B	'TWO'	'	DECK-ID 038	MSOS 4.1'
195	*B	'THREE'	'	DECK-ID 039	MSOS 4.1'
196	*S,N2,P				
197	*M			PROTEC	5
198	*B	'BPROTK'	'	DECK-ID 041	MSOS 4.1'
199	*B	'JBKILL'	'	DECK-ID 042	MSOS 4.1'
200	*M			JPLOAD	6
201	*B	'JPLOAD'	'	DECK-ID 043	MSOS 4.1'
202	*M			JPCHGE	7
203	*B	'JPCHGE'	'	DECK-ID 044	MSOS 4.1'
204	*B	'ASCHEX'	'	DECK-ID 045	MSOS 4.1'
205	*M			JPT13	8
206	*B	'T13'	'	DECK-ID 046	MSOS 4.1'
207	*M			JCRDV4	9
208	*B	'JCRDV4'	'	DECK-ID 047	MSOS 4.1'
209	*M			JLGOV4	10
210	*B	'JLGOV4'	'	DECK-ID 048	MSOS 4.1'
211	*M			JPSTV4	11
212	*B	'JPSTV4'	'	DECK-ID 049	MSOS 4.1'
213	*M			NAMEV4	12
214	*B	'NAMEV4'	'	DECK-ID 050	MSOS 4.1'
215	*M			JPFLV4	13
216	*B	'JPFLV4'	'	DECK-ID 051	MSOS 4.1'
217	*M			AFILV4	14
218	*B	'JPF2V4'	'	DECK-ID 052	MSOS 4.1'
219	*M			RESTOR	15
220	*B	'RESTOR'	'	DECK-ID 053	MSOS 4.1'
221	*M			RCOVER	16
222	*B	'RCOVER'	'	DECK-ID H09	MSOS 4.1'
223	*B	'OUTSEL'	'	DECK-ID H10	MSOS 4.1'
224	*B	'RDMPV4'	'	DECK-ID H11	MSOS 4.1'
225	*B	'MASDMP'	'	DECK-ID H12	MSOS 4.1'
226	*M			BRKPT	17
227	*B	'BRKPTD'	'	DECK-ID H13	MSOS 4.1'
228	*B	'SIFT'	'	DECK-ID H14	MSOS 4.1'
229	*B	'BIASCI'	'	DECK-ID H15	MSOS 4.1'
230	*B	'RETJMP'	'	DECK-ID H16	MSOS 4.1'
231	*B	'JUMPTO'	'	DECK-ID H17	MSOS 4.1'
232	*B	'ENTER'	'	DECK-ID H18	MSOS 4.1'
233	*B	'ENTCOR'	'	DECK-ID H19	MSOS 4.1'
234	*B	'PRTREG'	'	DECK-ID H20	MSOS 4.1'
235	*B	'TERMIN'	'	DECK-ID H21	MSOS 4.1'
236	*B	'RESUME'	'	DECK-ID H22	MSOS 4.1'
237	*B	'DPCORB'	'	DECK-ID H23	MSOS 4.1'
238	*B	'MSDMPB'	'	DECK-ID H24	MSOS 4.1'
239	*B	'SETBRP'	'	DECK-ID H25	MSOS 4.1'
240	*M			DDFBUG	18

241	*B	'ODEBUG'	'	DECK-ID H26	MSOS 4.1'
242	*M			SYSCOP	19
243	*B	'SYSCOP'	'	DECK-ID H27	MSOS 4.1'
244	*M			SYSSEG	20
245	*B	'CO1ST'	'	DECK-ID H28	MSOS 4.1'
246	*B	'CO2ND'	'	DECK-ID H29	MSOS 4.1'
247	*B	'CO3RD'	'	DECK-ID H30	MSOS 4.1'
248	*B	'COLAST'	'	DECK-ID H31	MSOS 4.1'
249	*M			MIPRO	21
250	*B	'MIPRO'	'	DECK-ID A26	MSOS 4.1'
251	*M			TDFUNC	22
252	*B	'TDFUNC'	'	DECK-ID A27	MSOS 4.1'
253	*M			EFSTOR	23
254	*B	'EFSTOR'	'	DECK-ID A28	MSOS 4.1'
255	*M			EFLIST	24
256	*B	'EFLIST'	'	DECK-ID A29	MSOS 4.1'
257	*M			SCMM17	25
258	*B	'SCMEXC'	'	DECK-ID E01	MSOS 4.1'
259	*M			VERIFY	26
260	*B	'VERFY1'	'	DECK-ID A30	MSOS 4.1'
261	*M			DUMMY1	27
262	*M			DUMMY2	28
263	*M			DUMMY3	29
264	*M			DUMMY4	30
265	*M			DUMMY5	31
266	*M			DUMMY6	32
267	*M			DUMMY7	33
268	*M			DUMMY8	34
269	*M			DUMMY9	35
270	*M			DUMMY0	36
271	*				
272	*			MASS RESIDENT DRIVERS	
273	*				
274	*M			COSY DRIVER	
275	*B	'DCOSY'	'	DECK-ID C28	MSOS 4.1'
276	*S	SCOSY,S			
277	*S	LCOSY,P			
278	*M			1731 601 MAG TAPE	
279	*B	'D1731U'	'	DECK-ID C33	MSOS 4.1'
280	*B	'FRWA'	'	DECK-ID C34	MSOS 4.1'
281	*B	'FRWB'	'	DECK-ID C35	MSOS 4.1'
282	*B	'RWBA'	'	DECK-ID C36	MSOS 4.1'
283	*B	'MAXRVU'	'	DECK-ID C37	MSOS 4.1'
284	*S	S1731U,S			
285	*S	L1731U,P			
286	*M			1740-501/1742 LINE PRINTER	
287	*B	'D40421'	'	DECK-ID C46	MSOS 4.1'
288	*S	S40421,S			
289	*S	L40421,P			
290	*M				
291	*B	'D1728'	'	DECK-ID C48	MSOS 4.1'
292	*B	'CP026'	'	DECK-ID C54	MSOS 4.1'
293	*B	'CR026'	'	DECK-ID C52	MSOS 4.1'
294	*S	S1728,S			
295	*S	L1728,P			
296	*				
297	*			MASS RESIDENT FILE MANAGER	
298	*				
299	*M				
300	*B	'DEFFIL'	'	DECK-ID F05	MSOS 4.1'

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301	*B	'FILSPC'	'	DECK-ID F06	MSOS 4.1'
302	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'
303	*S,	FMRP01,S			
304	*M				
305	*B	'RELFIL'	'	DECK-ID F08	MSOS 4.1'
306	*B	'RELSPC'	'	DECK-ID F09	MSOS 4.1'
307	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'
308	*S,	FMRP02,S			
309	*M				
310	*B	'DEFIDX'	'	DECK-ID F10	MSOS 4.1'
311	*B	'SQRTFM'	'	DECK-ID F11	MSOS 4.1'
312	*B	'FILSPC'	'	DECK-ID F06	MSOS 4.1'
313	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'
314	*S,	FMRP03,S			
315	*M				
316	*B	'LOKFIL'	'	DECK-ID F12	MSOS 4.1'
317	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'
318	*S,	FMRP04,S			
319	*M				
320	*B	'UNLFIL'	'	DECK-ID F13	MSOS 4.1'
321	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'
322	*S,	FMRP05,S			
323	*M				
324	*B	'STOSEQ'	'	DECK-ID F14	MSOS 4.1'
325	*B	'FILSPC'	'	DECK-ID F06	MSOS 4.1'
326	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'
327	*S,	FMRP06,S			
328	*M				
329	*B	'STODIR'	'	DECK-ID F15	MSOS 4.1'
330	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'
331	*S,	FMRP07,S			
332	*M				
333	*B	'STOIDX'	'	DECK-ID F16	MSOS 4.1'
334	*B	'HASHCD'	'	DECK-ID F17	MSOS 4.1'
335	*B	'GETKID'	'	DECK-ID F18	MSOS 4.1'
336	*B	'FILSPC'	'	DECK-ID F06	MSOS 4.1'
337	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'
338	*S,	FMRP08,S			
339	*M				
340	*B	'RTVSEQ'	'	DECK-ID F19	MSOS 4.1'
341	*B	'RTNSPC'	'	DECK-ID F20	MSOS 4.1'
342	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'
343	*S,	FMRP09,S			
344	*M				
345	*B	'RTVDIR'	'	DECK-ID F21	MSOS 4.1'
346	*B	'RTNSPC'	'	DECK-ID F20	MSOS 4.1'
347	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'
348	*S,	FMRP10,S			
349	*M				
350	*B	'RTVIDX'	'	DECK-ID F22	MSOS 4.1'
351	*B	'HASHCD'	'	DECK-ID F17	MSOS 4.1'
352	*B	'GETKID'	'	DECK-ID F18	MSOS 4.1'
353	*B	'RTNSPC'	'	DECK-ID F20	MSOS 4.1'
354	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'
355	*S,	FMRP11,S			
356	*M				
357	*B	'RTVIDO'	'	DECK-ID F23	MSOS 4.1'
358	*B	'GETKID'	'	DECK-ID F18	MSOS 4.1'
359	*B	'RTNSPC'	'	DECK-ID F20	MSOS 4.1'
360	*B	'RPEND'	'	DECK-ID F07	MSOS 4.1'

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361 *S,FMRP12,S
362 *M
363 *B 'FMDUMY' ' DECK-ID F04 MSOS 4.1'
364 *S,FMREND,S
365 *S,BEGFMS,S SPECIFY THE SYSTEM FILE SPACE
366 *M,BEGFMS+1000
367 *M
368 *B 'FMDUMY' ' DECK-ID F04 MSOS 4.1'
369 *T END OF SYSTEM
370 *JOB,INSTAL,SYSTEM
371 *K,I6
372 *LIBENT
373 *K,I6
374 *V DEFINE REQUEST PRIORITIES
375 *S,001,03,M
376 *S,002,00,M
377 *S,003,01,M
378 *S,004,02,M
379 *S,005,03,M
380 *S,006,02,M
381 *S,007,02,M
382 *S,008,02,M
383 *S,009,02,M
384 *S,010,02,M
385 *S,011,02,M
386 *S,012,03,M
387 *S,013,03,M
388 *S,014,03,M
389 *S,015,02,M
390 *S,016,03,M
391 *S,017,03,M
392 *S,018,04,M
393 *S,019,04,M
394 *S,020,04,M
395 *S,021,04,M
396 *S,022,04,M
397 *S,023,04,M
398 *S,024,04,M
399 *S,025,04,M
400 *S,026,04,M
401 *S,027,04,M
402 *S,028,04,M
403 *S,029,04,M
404 *S,030,04,M
405 *S,031,04,M
406 *S,032,04,M
407 *S,033,04,M
408 *S,034,04,M
409 *S,035,04,M
410 *S,036,04,M
411 *V 1700 MACRO ASSEMBLER 3.
412 *K,I6
413 *L,LIBMAC
414 *B 'LIBMAC' ' DECK-ID G01 MSOS 4.1'
415 *L,ASSEM
416 *B 'ASSEM' ' DECK-ID G02 MSOS 4.1'
417 *K,P8
418 *P,F
419 *B 'PASS1' ' DECK-ID G03 MSOS 4.1'
420 *B 'PA1PR2' ' DECK-ID G04 MSOS 4.1'

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421 *T
422 *K,I8
423 *N,PASS1,,,B
424 *K,I6
425 *K,P8
426 *P,F
427 *B 'PASS2' ' DECK-ID G05 MSOS 4.1'
428 *B 'PA2PR2' ' DECK-ID G06 MSOS 4.1'
429 *T
430 *K,I8
431 *N,PASS2,,,B
432 *K,I6
433 *K,P8
434 *P,F
435 *B 'PASS3' ' DECK-ID G07 MSOS 4.1'
436 *B 'PA3PR2' ' DECK-ID G08 MSOS 4.1'
437 *B 'PA3PR3' ' DECK-ID G09 MSOS 4.1'
438 *T
439 *K,I8
440 *N,PASS3,,,B
441 *K,I6
442 *K,P8
443 *P,F
444 *B 'TABLST' ' DECK-ID G10 MSOS 4.1'
445 *T
446 *K,I8
447 *N,TABLST,,,B
448 *K,I6
449 *K,P8
450 *P,F
451 *B 'XREF' ' DECK-ID G11 MSOS 4.1'
452 *K,I8
453 *N,XREF,,,B
454 *K,I6
455 *N,MACSKL,,,B
456 *B 'MACSKL'
457 *N,MACROS,,,B
458 *B 'MACROS'
459 *V DEBUGGING AND CHECKOUT
460 *K,I6
461 *L,TRACE
462 *B 'TRACE' ' DECK-ID H32 MSOS 4.1'
463 *V SYSTEM UTILITY PROGRAMS
464 *K,I6
465 *L,LULIST
466 *B 'LULIST' ' DECK-ID J01 MSOS 4.1'
467 *L,LISTR
468 *B 'LISTR' ' DECK-ID J02 MSOS 4.1'
469 *L,OPSORT
470 *B 'OPSORT' ' DECK-ID J03 MSOS 4.1'
471 *L,EESORT
472 *B 'EESORT' ' DECK-ID J04 MSOS 4.1'
473 *L,COSY
474 *B 'COSY' ' DECK-ID J05 MSOS 4.1'
475 *L,LCOSY
476 *B 'LCOSY' ' DECK-ID J06 MSOS 4.1'
477 *L,CYFT
478 *B 'CYFT' ' DECK-ID J07 MSOS 4.1'
479 *L,IOUP
480 *B 'IOUP' ' DECK-ID J08 MSOS 4.1'

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481	*K,P8			
482	*P,F			
483	*B 'IOUP'	'	DECK-ID J08	MSOS 4.1'
484	*B 'IOUPV4'	'	DECK-ID J09	MSOS 4.1'
485	*T			
486	*K,I8			
487	*N,IOUPV4,,,B			
488	*K,I6			
489	*L,DTLP			
490	*B 'DTLP'	'	DECK-ID J10	MSOS 4.1'
491	*K,P8			
492	*P,F			
493	*B 'DSKTAP'	'	DECK-ID J11	MSOS 4.1'
494	*B 'DSKEQC'	'	DECK-ID J12	MSOS 4.1'
495	*B 'DSKDHX'	'	DECK-ID J13	MSOS 4.1'
496	*B 'DSKCDR'	'	DECK-ID J14	MSOS 4.1'
497	*B 'DSKMTI'	'	DECK-ID J15	MSOS 4.1'
498	*B 'DSKMT0'	'	DECK-ID J16	MSOS 4.1'
499	*B 'DSKMMD'	'	DECK-ID J18	MSOS 4.1'
500	*T			
501	*K,I8			
502	*N,DSKTAP,,,B			
503	*K,I6			
504	*L,SETPV4			
505	*B 'SPCALL'	'	DECK-ID J21	MSOS 4.1'
506	*K,P8			
507	*P,F			
508	*B 'SPOLY1'	'	DECK-ID J22	MSOS 4.1'
509	*B 'STPV4'	'	DECK-ID J23	MSOS 4.1'
510	*B 'IERROR'	'	DECK-ID J24	MSOS 4.1'
511	*B 'MCTDK'	'	DECK-ID J25	MSOS 4.1'
512	*B 'GETPAG'	'	DECK-ID J26	MSOS 4.1'
513	*B 'CONPRT'	'	DECK-ID J27	MSOS 4.1'
514	*B 'REDCON'	'	DECK-ID J28	MSOS 4.1'
515	*B 'CONDEC'	'	DECK-ID J29	MSOS 4.1'
516	*B 'ORDERM'	'	DECK-ID J30	MSOS 4.1'
517	*B 'IREAD'	'	DECK-ID J31	MSOS 4.1'
518	*B 'ASCOUT'	'	DECK-ID J32	MSOS 4.1'
519	*B 'PARAMS'	'	DECK-ID J33	MSOS 4.1'
520	*B 'DISKIO'	'	DECK-ID J34	MSOS 4.1'
521	*T			
522	*K,I8			
523	*N,STP1V4,,,B			
524	*K,I6			
525	*K,P8			
526	*P,F			
527	*B 'SPOLY2'	'	DECK-ID J35	MSOS 4.1'
528	*B 'SUP'	'	DECK-ID J36	MSOS 4.1'
529	*B 'IERROR'	'	DECK-ID J24	MSOS 4.1'
530	*B 'GETPAG'	'	DECK-ID J26	MSOS 4.1'
531	*B 'BTOA'	'	DECK-ID J37	MSOS 4.1'
532	*B 'ISTAT'	'	DECK-ID J38	MSOS 4.1'
533	*B 'SCIO'	'	DECK-ID J39	MSOS 4.1'
534	*B 'SCRD'	'	DECK-ID J40	MSOS 4.1'
535	*B 'REDCON'	'	DECK-ID J28	MSOS 4.1'
536	*B 'ICAT'	'	DECK-ID J41	MSOS 4.1'
537	*B 'BUFIN'	'	DECK-ID J42	MSOS 4.1'
538	*B 'MOVE'	'	DECK-ID J43	MSOS 4.1'
539	*B 'IREAD'	'	DECK-ID J31	MSOS 4.1'
540	*B 'ASCOUT'	'	DECK-ID J32	MSOS 4.1'

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541 *B 'PARAMS' ' DECK-ID J33 MSOS 4.1'
542 *B 'DISKIO' ' DECK-ID J34 MSOS 4.1'
543 *T
544 *K,I8
545 *N,STP2V4,,,B
546 *V SCMM TEST ROUTINES
547 *K,I6
548 *K,P8
549 *P,F
550 *B 'SCMTTY' ' DECK-ID E02 MSOS 4.1'
551 *T
552 *K,I8
553 *N,SCMTTY,,,B
554 *K,I6
555 *K,P8
556 *P,F
557 *B 'SCMDK1' ' DECK-ID E08 MSOS 4.1'
558 *T
559 *K,I8
560 *N,SCMDK1,,,B
561 *K,I6
562 *K,P8
563 *P,F
564 *B 'SCMDVP' ' DECK-ID E09 MSOS 4.1'
565 *T
566 *K,I8
567 *N,SCMDVP,,,B
568 *K,I6
569 *K,P8
570 *P,F
571 *B 'SCMPRT' ' DECK-ID E11 MSOS 4.1'
572 *T
573 *K,I8
574 *N,SCMPRT,,,B
575 *K,I6
576 *K,P8
577 *P,F
578 *B 'SCMMTT' ' DECK-ID E12 MSOS 4.1'
579 *T
580 *K,I8
581 *N,SCMMTT,,,B
582 *K,I6
583 *K,P8
584 *P,F
585 *B 'SCMGRD' ' DECK-ID E03 MSOS 4.1'
586 *T
587 *K,I8
588 *N,SCMGRD,,,B
589 *V INSTALL LIBRARY BUILDER
590 *K,I6
591 *L,LIBILD
592 *B 'LIBILD' ' DECK-ID B01 MSOS 4.1'
593 *K,P8
594 *P,F
595 *B 'LIBIDO' ' DECK-ID B02 MSOS 4.1'
596 *B 'CONVRS' ' DECK-ID B03 MSOS 4.1'
597 *B 'MESSY' ' DECK-ID B04 MSOS 4.1'
598 *B 'LJA2B' ' DECK-ID B05 MSOS 4.1'
599 *B 'MOVECH' ' DECK-ID B06 MSOS 4.1'
600 *B 'PICKUP' ' DECK-ID B07 MSOS 4.1'

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601 *B 'IOSUB' ' DECK-ID 908 MSOS 4.1'
602 *T
603 *K,I8
604 *N,LIBID0,,,B
605 *K,I6
606 *K,P8
607 *P,F
608 *B 'HELPER' ' DECK-ID 909 MSOS 4.1'
609 *B 'MOVECH' ' DECK-ID 906 MSOS 4.1'
610 *B 'HELPO' ' DECK-ID 910 MSOS 4.1'
611 *B 'HELP1' ' DECK-ID 911 MSOS 4.1'
612 *B 'HELP2' ' DECK-ID 912 MSOS 4.1'
613 *B 'HELP3' ' DECK-ID 913 MSOS 4.1'
614 *B 'HELP4' ' DECK-ID 914 MSOS 4.1'
615 *B 'HELP5' ' DECK-ID 915 MSOS 4.1'
616 *B 'HELP8' ' DECK-ID 916 MSOS 4.1'
617 *B 'HELP9' ' DECK-ID 917 MSOS 4.1'
618 *B 'HELP10' ' DECK-ID 918 MSOS 4.1'
619 *B 'HELP11' ' DECK-ID 919 MSOS 4.1'
620 *B 'HELP12' ' DECK-ID 920 MSOS 4.1'
621 *B 'HELP13' ' DECK-ID 921 MSOS 4.1'
622 *B 'HELP14' ' DECK-ID 922 MSOS 4.1'
623 *T
624 *K,I8
625 *N,HELPER,,,B
626 *V INSTALL SKELETON EDITOR
627 *K,I6
628 *L,SKED
629 *B 'SKED' ' DECK-ID 923 MSOS 4.1'
630 *K,P8
631 *P,F
632 *B 'SKFILE' ' DECK-ID 924 MSOS 4.1'
633 *T
634 *K,I8
635 *N,SKFILE,,,B
636 *V SYSTEM INITIALIZER
637 *K,I6
638 *L,SILP
639 *B 'SILP' ' DECK-ID 925 MSOS 4.1'
640 *K,P8
641 *P,F
642 *B 'CONTRL' ' DECK-ID 926 MSOS 4.1'
643 *B 'ILOAD' ' DECK-ID 927 MSOS 4.1'
644 *B 'LDRTBL' ' DECK-ID 928 MSOS 4.1'
645 *B 'I1' ' DECK-ID 929 MSOS 4.1'
646 *B 'I2' ' DECK-ID 930 MSOS 4.1'
647 *B 'Q1711' ' DECK-ID 931 MSOS 4.1'
648 *B 'Q40421' ' DECK-ID 934 MSOS 4.1'
649 *B 'IDRIV' ' DECK-ID 935 MSOS 4.1'
650 *B 'QNT7TK' ' DECK-ID 936 MSOS 4.1'
651 *B 'MDRIV' ' DECK-ID 942 MSOS 4.1'
652 *B 'QDK85X' ' DECK-ID 944 MSOS 4.1'
653 *B 'QCDDMY' ' DECK-ID 950 MSOS 4.1'
654 *B 'QPTDMY' ' DECK-ID 951 MSOS 4.1'
655 *T
656 *K,I8
657 *N,SI,,,B
658 *V SMM BOOTSTRAPS
659 *K,I6
660 *L,SMM7T

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661 *B 'SMM1' ' DECK-ID B54 MSOS 4.1'
662 *L,SMM430
663 *B 'SMM4' ' DECK-ID B57 MSOS 4.1'
664 *V FORTRAN COMPILER 3.2A
665 *K,I6
666 *L,FTN
667 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
668 *K,P8
669 *P,F
670 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
671 *B 'GOA' ' DECK-ID F02 FORTRAN 3.2A'
672 *B 'IOPRBA' ' DECK-ID F08 FORTRAN 3.2A'
673 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'
674 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
675 *B 'DIAG' ' DECK-ID F04 FORTRAN 3.2A'
676 *B 'GETC' ' DECK-ID F13 FORTRAN 3.2A'
677 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
678 *B 'OUTENT' ' DECK-ID A07 FORTRAN 3.2A'
679 *B 'PACK' ' DECK-ID F09 FORTRAN 3.2A'
680 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
681 *B 'STORE' ' DECK-ID F11 FORTRAN 3.2A'
682 *B 'SYMBOL' ' DECK-ID A03 FORTRAN 3.2A'
683 *B 'LOCLAA' ' DECK-ID F17 FORTRAN 3.2A'
684 *B 'DUMYAA' ' DECK-ID F18 FORTRAN 3.2A'
685 *B 'PHASEA' ' DECK-ID A08 FORTRAN 3.2A'
686 *B 'ARAYSZ' ' DECK-ID A42 FORTRAN 3.2A'
687 *B 'CPLOOP' ' DECK-ID A43 FORTRAN 3.2A'
688 *B 'ENDDO' ' DECK-ID A29 FORTRAN 3.2A'
689 *B 'GNST' ' DECK-ID A06 FORTRAN 3.2A'
690 *B 'IGETCF' ' DECK-ID F14 FORTRAN 3.2A'
691 *B 'OPTION' ' DECK-ID F15 FORTRAN 3.2A'
692 *B 'PLABEL' ' DECK-ID A09 FORTRAN 3.2A'
693 *B 'Q8QBDS' ' DECK-ID A10 FORTRAN 3.2A'
694 *B 'RDLABL' ' DECK-ID A11 FORTRAN 3.2A'
695 *B 'SAVEID' ' DECK-ID A04 FORTRAN 3.2A'
696 *B 'STCHAR' ' DECK-ID A12 FORTRAN 3.2A'
697 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
698 *T
699 *K,I8
700 *N,FORTAA,,,B
701 *K,I6
702 *K,P8
703 *P,F,,,MARKER
704 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
705 *B 'GOA' ' DECK-ID F02 FORTRAN 3.2A'
706 *B 'IOPRBA' ' DECK-ID F08 FORTRAN 3.2A'
707 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'
708 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
709 *B 'DIAG' ' DECK-ID F04 FORTRAN 3.2A'
710 *B 'GETC' ' DECK-ID F13 FORTRAN 3.2A'
711 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
712 *B 'OUTENT' ' DECK-ID A07 FORTRAN 3.2A'
713 *B 'PACK' ' DECK-ID F09 FORTRAN 3.2A'
714 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
715 *B 'STORE' ' DECK-ID F11 FORTRAN 3.2A'
716 *B 'SYMBOL' ' DECK-ID A03 FORTRAN 3.2A'
717 *B 'LOCLAB' ' DECK-ID F19 FORTRAN 3.2A'
718 *B 'DUMYAB' ' DECK-ID F20 FORTRAN 3.2A'
719 *B 'BYEQPR' ' DECK-ID A19 FORTRAN 3.2A'
720 *B 'DFLOT' ' DECK-ID F06 FORTRAN 3.2A'

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721 *B 'DUMVOL' ' DECK-ID F07 FORTRAN 3.2A'
722 *B 'DXP9' ' DECK-ID F05 FORTRAN 3.2A'
723 *B 'GETF' ' DECK-ID A05 FORTRAN 3.2A'
724 *B 'GPUT' ' DECK-ID A02 FORTRAN 3.2A'
725 *B 'SAVEID' ' DECK-ID A04 FORTRAN 3.2A'
726 *B 'STCHAR' ' DECK-ID A12 FORTRAN 3.2A'
727 *B 'SUBPPR' ' DECK-ID A23 FORTRAN 3.2A'
728 *B 'TYPE' ' DECK-ID A13 FORTRAN 3.2A'
729 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
730 *T
731 *K,I8
732 *N,FORTAB,,,B
733 *K,I6
734 *K,P8
735 *P,F,,MARKER
736 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
737 *B 'GOA' ' DECK-ID F02 FORTRAN 3.2A'
738 *B 'IOPRBA' ' DECK-ID F08 FORTRAN 3.2A'
739 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'
740 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
741 *B 'DIAG' ' DECK-ID F04 FORTRAN 3.2A'
742 *B 'GETC' ' DECK-ID F13 FORTRAN 3.2A'
743 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
744 *B 'OUTENT' ' DECK-ID A07 FORTRAN 3.2A'
745 *B 'PACK' ' DECK-ID F09 FORTRAN 3.2A'
746 *B 'QBPRMS' ' DECK-ID F10 FORTRAN 3.2A'
747 *B 'STORE' ' DECK-ID F11 FORTRAN 3.2A'
748 *B 'SYMBOL' ' DECK-ID A03 FORTRAN 3.2A'
749 *B 'LOCLAC' ' DECK-ID F21 FORTRAN 3.2A'
750 *B 'DUMYAC' ' DECK-ID F22 FORTRAN 3.2A'
751 *B 'ASGNPR' ' DECK-ID A32 FORTRAN 3.2A'
752 *B 'BDOPR' ' DECK-ID A33 FORTRAN 3.2A'
753 *B 'CFIVOC' ' DECK-ID A34 FORTRAN 3.2A'
754 *B 'CKIVC' ' DECK-ID A35 FORTRAN 3.2A'
755 *B 'CKNAME' ' DECK-ID A36 FORTRAN 3.2A'
756 *B 'COMNPR' ' DECK-ID A15 FORTRAN 3.2A'
757 *B 'DFLOT' ' DECK-ID F06 FORTRAN 3.2A'
758 *B 'DIMPR' ' DECK-ID A16 FORTRAN 3.2A'
759 *B 'DUMVOL' ' DECK-ID F07 FORTRAN 3.2A'
760 *B 'DXP9' ' DECK-ID F05 FORTRAN 3.2A'
761 *B 'ERBPR' ' DECK-ID A38 FORTRAN 3.2A'
762 *B 'EXRLPR' ' DECK-ID A24 FORTRAN 3.2A'
763 *B 'GETF' ' DECK-ID A05 FORTRAN 3.2A'
764 *B 'GPUT' ' DECK-ID A02 FORTRAN 3.2A'
765 *B 'ROLABL' ' DECK-ID A11 FORTRAN 3.2A'
766 *B 'TYPEPR' ' DECK-ID A18 FORTRAN 3.2A'
767 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
768 *T
769 *K,I8
770 *N,FORTAC,,,B
771 *K,I6
772 *K,P8
773 *P,F,,MARKER
774 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
775 *B 'GOA' ' DECK-ID F02 FORTRAN 3.2A'
776 *B 'IOPRBA' ' DECK-ID F08 FORTRAN 3.2A'
777 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'
778 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
779 *B 'DIAG' ' DECK-ID F04 FORTRAN 3.2A'
780 *B 'GETC' ' DECK-ID F13 FORTRAN 3.2A'

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781 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'  
782 *B 'OUTENT' ' DECK-ID A07 FORTRAN 3.2A'  
783 *B 'PACK' ' DECK-ID F09 FORTRAN 3.2A'  
784 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'  
785 *B 'STORE' ' DECK-ID F11 FORTRAN 3.2A'  
786 *B 'SYMBOL' ' DECK-ID A03 FORTRAN 3.2A'  
787 *B 'LOCLAD' ' DECK-ID F23 FORTRAN 3.2A'  
788 *B 'DUMYAD' ' DECK-ID F24 FORTRAN 3.2A'  
789 *B 'ASEMPR' ' DECK-ID A40 FORTRAN 3.2A'  
790 *B 'DFLOT' ' DECK-ID F06 FORTRAN 3.2A'  
791 *B 'DUMVOL' ' DECK-ID F07 FORTRAN 3.2A'  
792 *B 'DXP9' ' DECK-ID F05 FORTRAN 3.2A'  
793 *B 'GETF' ' DECK-ID A05 FORTRAN 3.2A'  
794 *B 'GPUT' ' DECK-ID A02 FORTRAN 3.2A'  
795 *B 'IGETCF' ' DECK-ID F14 FORTRAN 3.2A'  
796 *B 'PUNT' ' DECK-ID A27 FORTRAN 3.2A'  
797 *B 'RDLABL' ' DECK-ID A11 FORTRAN 3.2A'  
798 *B 'SUBSCR' ' DECK-ID A17 FORTRAN 3.2A'  
799 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'  
800 *T  
801 *K,I8  
802 *N,FORTAD,,,B  
803 *K,I6  
804 *K,P8  
805 *P,F,,,MARKER  
806 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'  
807 *B 'GOA' ' DECK-ID F02 FORTRAN 3.2A'  
808 *B 'IOPRBA' ' DECK-ID F08 FORTRAN 3.2A'  
809 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'  
810 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'  
811 *B 'DIAG' ' DECK-ID F04 FORTRAN 3.2A'  
812 *B 'GETC' ' DECK-ID F13 FORTRAN 3.2A'  
813 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'  
814 *B 'OUTENT' ' DECK-ID A07 FORTRAN 3.2A'  
815 *B 'PACK' ' DECK-ID F09 FORTRAN 3.2A'  
816 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'  
817 *B 'STORE' ' DECK-ID F11 FORTRAN 3.2A'  
818 *B 'SYMBOL' ' DECK-ID A03 FORTRAN 3.2A'  
819 *B 'LOCLAE' ' DECK-ID F25 FORTRAN 3.2A'  
820 *B 'DUMYAE' ' DECK-ID F26 FORTRAN 3.2A'  
821 *B 'CONSUB' ' DECK-ID A30 FORTRAN 3.2A'  
822 *B 'DATAPR' ' DECK-ID A31 FORTRAN 3.2A'  
823 *B 'DFLOT' ' DECK-ID F06 FORTRAN 3.2A'  
824 *B 'DUMVOL' ' DECK-ID F07 FORTRAN 3.2A'  
825 *B 'DXP9' ' DECK-ID F05 FORTRAN 3.2A'  
826 *B 'GETF' ' DECK-ID A05 FORTRAN 3.2A'  
827 *B 'GPUT' ' DECK-ID A02 FORTRAN 3.2A'  
828 *B 'STCHAR' ' DECK-ID A12 FORTRAN 3.2A'  
829 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'  
830 *T  
831 *K,I8  
832 *N,FORTAE,,,B  
833 *K,I6  
834 *K,P8  
835 *P,F,,,MARKER  
836 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'  
837 *B 'GOA' ' DECK-ID F02 FORTRAN 3.2A'  
838 *B 'IOPRBA' ' DECK-ID F08 FORTRAN 3.2A'  
839 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'  
840 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
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841 *B 'DIAG' ' DECK-ID F04 FORTRAN 3.2A'
842 *B 'GETC' ' DECK-ID F13 FORTRAN 3.2A'
843 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
844 *B 'OUTENT' ' DECK-ID A07 FORTRAN 3.2A'
845 *B 'PACK' ' DECK-ID F09 FORTRAN 3.2A'
846 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
847 *B 'STORE' ' DECK-ID F11 FORTRAN 3.2A'
848 *B 'SYMBOL' ' DECK-ID A03 FORTRAN 3.2A'
849 *B 'LOCLAF' ' DECK-ID F27 FORTRAN 3.2A'
850 *B 'DUMYAF' ' DECK-ID F28 FORTRAN 3.2A'
851 *B 'CHECKF' ' DECK-ID A20 FORTRAN 3.2A'
852 *B 'FGETC' ' DECK-ID A21 FORTRAN 3.2A'
853 *B 'FORK' ' DECK-ID A22 FORTRAN 3.2A'
854 *B 'PEQVS' ' DECK-ID A25 FORTRAN 3.2A'
855 *B 'PRNTNM' ' DECK-ID A26 FORTRAN 3.2A'
856 *B 'STCHAR' ' DECK-ID A12 FORTRAN 3.2A'
857 *B 'SYMSON' ' DECK-ID A28 FORTRAN 3.2A'
858 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
859 *T
860 *K,I8
861 *N,FORTAF,,,B
862 *K,I6
863 *K,P8
864 *P,F,,,MARKER
865 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
866 *B 'GOA' ' DECK-ID F02 FORTRAN 3.2A'
867 *B 'IOPRBA' ' DECK-ID F08 FORTRAN 3.2A'
868 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'
869 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
870 *B 'DIAG' ' DECK-ID F04 FORTRAN 3.2A'
871 *B 'GETC' ' DECK-ID F13 FORTRAN 3.2A'
872 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
873 *B 'OUTENT' ' DECK-ID A07 FORTRAN 3.2A'
874 *B 'PACK' ' DECK-ID F09 FORTRAN 3.2A'
875 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
876 *B 'STORE' ' DECK-ID F11 FORTRAN 3.2A'
877 *B 'SYMBOL' ' DECK-ID A03 FORTRAN 3.2A'
878 *B 'LOCLAG' ' DECK-ID F29 FORTRAN 3.2A'
879 *B 'DUMYAG' ' DECK-ID F30 FORTRAN 3.2A'
880 *B 'ARITH' ' DECK-ID A14 FORTRAN 3.2A'
881 *B 'IGETCF' ' DECK-ID F14 FORTRAN 3.2A'
882 *B 'PUNT' ' DECK-ID A27 FORTRAN 3.2A'
883 *B 'TREE' ' DECK-ID A41 FORTRAN 3.2A'
884 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
885 *T
886 *K,I8
887 *N,FORTAG,,,B
888 *K,I6
889 *K,P8
890 *P,F,,,MARKER
891 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
892 *B 'GOA' ' DECK-ID F02 FORTRAN 3.2A'
893 *B 'IOPRBA' ' DECK-ID F08 FORTRAN 3.2A'
894 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'
895 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
896 *B 'DIAG' ' DECK-ID F04 FORTRAN 3.2A'
897 *B 'GETC' ' DECK-ID F13 FORTRAN 3.2A'
898 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
899 *B 'OUTENT' ' DECK-ID A07 FORTRAN 3.2A'
900 *B 'PACK' ' DECK-ID F09 FORTRAN 3.2A'

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901 *B 'QBPRMS' * DECK-ID F10 FORTRAN 3.2A'
902 *B 'STORE' * DECK-ID F11 FORTRAN 3.2A'
903 *B 'SYMBOL' * DECK-ID A03 FORTRAN 3.2A'
904 *B 'LOCLAH' * DECK-ID F31 FORTRAN 3.2A'
905 *B 'DUMYAH' * DECK-ID F32 FORTRAN 3.2A'
906 *B 'IGETCF' * DECK-ID F14 FORTRAN 3.2A'
907 *B 'MODMXR' * DECK-ID A39 FORTRAN 3.2A'
908 *B 'PUNT' * DECK-ID A27 FORTRAN 3.2A'
909 *B 'ENDLOC' * DECK-ID F16 FORTRAN 3.2A'
910 *T
911 *K,I8
912 *N,FORTAH,,,B
913 *K,I6
914 *K,P8
915 *P,F,,,MARKER
916 *B 'FTNA' * COPYRIGHT CONTROL DATA CORPORATION 1973'
917 *B 'GOA' * DECK-ID F02 FORTRAN 3.2A'
918 *B 'IOPRBA' * DECK-ID F08 FORTRAN 3.2A'
919 *B 'CNVT' * DECK-ID A01 FORTRAN 3.2A'
920 *B 'CONV' * DECK-ID F03 FORTRAN 3.2A'
921 *B 'DIAG' * DECK-ID F04 FORTRAN 3.2A'
922 *B 'GETC' * DECK-ID F13 FORTRAN 3.2A'
923 *B 'GETSYM' * DECK-ID F12 FORTRAN 3.2A'
924 *B 'OUTENT' * DECK-ID A07 FORTRAN 3.2A'
925 *B 'PACK' * DECK-ID F09 FORTRAN 3.2A'
926 *B 'QBPRMS' * DECK-ID F10 FORTRAN 3.2A'
927 *B 'STORE' * DECK-ID F11 FORTRAN 3.2A'
928 *B 'SYMBOL' * DECK-ID A03 FORTRAN 3.2A'
929 *B 'LOCLAI' * DECK-ID F33 FORTRAN 3.2A'
930 *B 'DUMYAI' * DECK-ID F34 FORTRAN 3.2A'
931 *B 'IOSPR' * DECK-ID A37 FORTRAN 3.2A'
932 *B 'ENDDO' * DECK-ID A29 FORTRAN 3.2A'
933 *B 'RDLABL' * DECK-ID A11 FORTRAN 3.2A'
934 *B 'STCHAR' * DECK-ID A12 FORTRAN 3.2A'
935 *B 'ENDLOC' * DECK-ID F16 FORTRAN 3.2A'
936 *T
937 *K,I8
938 *N,FORTAI,,,B
939 *K,I6
940 *K,P8
941 *P,F
942 *B 'FTNA' * COPYRIGHT CONTROL DATA CORPORATION 1973'
943 *B 'GOB' * DECK-ID F35 FORTRAN 3.2A'
944 *B 'CNVT' * DECK-ID A01 FORTRAN 3.2A'
945 *B 'DUMMY' * DECK-ID B01 FORTRAN 3.2A'
946 *B 'FCMSTK' * DECK-ID B02 FORTRAN 3.2A'
947 *B 'GETSYM' * DECK-ID F12 FORTRAN 3.2A'
948 *B 'IOPRBB' * DECK-ID F36 FORTRAN 3.2A'
949 *B 'KCPART' * DECK-ID B03 FORTRAN 3.2A'
950 *B 'KOUTPT' * DECK-ID B04 FORTRAN 3.2A'
951 *B 'KPCSTK' * DECK-ID B05 FORTRAN 3.2A'
952 *B 'KPC3PR' * DECK-ID B06 FORTRAN 3.2A'
953 *B 'KSYMGN' * DECK-ID B07 FORTRAN 3.2A'
954 *B 'LABKPC' * DECK-ID B08 FORTRAN 3.2A'
955 *B 'LABLER' * DECK-ID B09 FORTRAN 3.2A'
956 *B 'PUNT' * DECK-ID B10 FORTRAN 3.2A'
957 *B 'CONV' * DECK-ID F03 FORTRAN 3.2A'
958 *B 'QBPRMS' * DECK-ID F10 FORTRAN 3.2A'
959 *B 'STOREB' * DECK-ID F37 FORTRAN 3.2A'
960 *B 'SYMBOL' * DECK-ID B11 FORTRAN 3.2A'


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961 *B 'TSALOC' ' DECK-ID B12 FORTRAN 3.2A'
962 *B 'LOCLBA' ' DECK-ID F38 FORTRAN 3.2A'
963 *B 'DUMYBA' ' DECK-ID F39 FORTRAN 3.2A'
964 *B 'PHASEB' ' DECK-ID B21 FORTRAN 3.2A'
965 *B 'INXRST' ' DECK-ID B19 FORTRAN 3.2A'
966 *B 'NOPROC' ' DECK-ID B20 FORTRAN 3.2A'
967 *B 'READIR' ' DECK-ID B22 FORTRAN 3.2A'
968 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
969 *T
970 *K,I8
971 *N,FORTBA,,,B
972 *K,I6
973 *K,P8
974 *P,F,,,MARKER
975 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
976 *B 'GOB' ' DECK-ID F35 FORTRAN 3.2A'
977 *B 'GNVT' ' DECK-ID A01 FORTRAN 3.2A'
978 *B 'DUMMY' ' DECK-ID B01 FORTRAN 3.2A'
979 *B 'FCMSTK' ' DECK-ID B02 FORTRAN 3.2A'
980 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
981 *B 'IOPRBB' ' DECK-ID F36 FORTRAN 3.2A'
982 *B 'KCPART' ' DECK-ID B03 FORTRAN 3.2A'
983 *B 'KOUTPT' ' DECK-ID B04 FORTRAN 3.2A'
984 *B 'KPCSTK' ' DECK-ID B05 FORTRAN 3.2A'
985 *B 'KPC3PR' ' DECK-ID B06 FORTRAN 3.2A'
986 *B 'KSYMGN' ' DECK-ID B07 FORTRAN 3.2A'
987 *B 'LABKPC' ' DECK-ID B08 FORTRAN 3.2A'
988 *B 'LABLER' ' DECK-ID B09 FORTRAN 3.2A'
989 *B 'PUNT' ' DECK-ID B10 FORTRAN 3.2A'
990 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
991 *B 'QBPRMS' ' DECK-ID F10 FORTRAN 3.2A'
992 *B 'STOREB' ' DECK-ID F37 FORTRAN 3.2A'
993 *B 'SYMBOL' ' DECK-ID B11 FORTRAN 3.2A'
994 *B 'TSALOC' ' DECK-ID B12 FORTRAN 3.2A'
995 *B 'LOCLBB' ' DECK-ID F40 FORTRAN 3.2A'
996 *B 'DUMYBB' ' DECK-ID F41 FORTRAN 3.2A'
997 *B 'AFIDL' ' DECK-ID B25 FORTRAN 3.2A'
998 *B 'ASSEM' ' DECK-ID B13 FORTRAN 3.2A'
999 *B 'BANANA' ' DECK-ID B14 FORTRAN 3.2A'
1000 *B 'END' ' DECK-ID B16 FORTRAN 3.2A'
1001 *B 'ENTCOD' ' DECK-ID B17 FORTRAN 3.2A'
1002 *B 'INXRST' ' DECK-ID B19 FORTRAN 3.2A'
1003 *B 'SUBFUN' ' DECK-ID B23 FORTRAN 3.2A'
1004 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
1005 *T
1006 *K,I8
1007 *N,FORTBB,,,B
1008 *K,I6
1009 *K,P8
1010 *P,F,,,MARKER
1011 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
1012 *B 'GOB' ' DECK-ID F35 FORTRAN 3.2A'
1013 *B 'GNVT' ' DECK-ID A01 FORTRAN 3.2A'
1014 *B 'DUMMY' ' DECK-ID B01 FORTRAN 3.2A'
1015 *B 'FCMSTK' ' DECK-ID B02 FORTRAN 3.2A'
1016 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
1017 *B 'IOPRBB' ' DECK-ID F36 FORTRAN 3.2A'
1018 *B 'KCPART' ' DECK-ID B03 FORTRAN 3.2A'
1019 *B 'KOUTPT' ' DECK-ID B04 FORTRAN 3.2A'
1020 *B 'KPCSTK' ' DECK-ID B05 FORTRAN 3.2A'

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1021 *B 'KPC3PR' ' DECK-ID B06 FORTRAN 3.2A'
1022 *B 'KSYMGN' ' DECK-ID B07 FORTRAN 3.2A'
1023 *B 'LABKPC' ' DECK-ID B08 FORTRAN 3.2A'
1024 *B 'LABLER' ' DECK-ID B09 FORTRAN 3.2A'
1025 *B 'PUNT' ' DECK-ID B10 FORTRAN 3.2A'
1026 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
1027 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
1028 *B 'STOREB' ' DECK-ID F37 FORTRAN 3.2A'
1029 *B 'SYMBOL' ' DECK-ID B11 FORTRAN 3.2A'
1030 *B 'TSALOC' ' DECK-ID B12 FORTRAN 3.2A'
1031 *B 'LOCLBC' ' DECK-ID F42 FORTRAN 3.2A'
1032 *B 'DUMYBC' ' DECK-ID F43 FORTRAN 3.2A'
1033 *B 'ASUPER' ' DECK-ID B26 FORTRAN 3.2A'
1034 *B 'ARAYSZ' ' DECK-ID A42 FORTRAN 3.2A'
1035 *B 'BGINDO' ' DECK-ID B15 FORTRAN 3.2A'
1036 *B 'CGOTO' ' DECK-ID B27 FORTRAN 3.2A'
1037 *B 'HELEN' ' DECK-ID B18 FORTRAN 3.2A'
1038 *B 'SYMSCN' ' DECK-ID A28 FORTRAN 3.2A'
1039 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
1040 *T
1041 *K, I8
1042 *N, FORTBC, , , B
1043 *K, I6
1044 *K, P8
1045 *P, F, , MARKER
1046 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
1047 *B 'GOB' ' DECK-ID F35 FORTRAN 3.2A'
1048 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'
1049 *B 'DUMMY' ' DECK-ID B01 FORTRAN 3.2A'
1050 *B 'FCMSTK' ' DECK-ID B02 FORTRAN 3.2A'
1051 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
1052 *B 'IOPRBB' ' DECK-ID F36 FORTRAN 3.2A'
1053 *B 'KCPART' ' DECK-ID B03 FORTRAN 3.2A'
1054 *B 'KOUTPT' ' DECK-ID B04 FORTRAN 3.2A'
1055 *B 'KPCSTK' ' DECK-ID B05 FORTRAN 3.2A'
1056 *B 'KPC3PR' ' DECK-ID B06 FORTRAN 3.2A'
1057 *B 'KSYMGN' ' DECK-ID B07 FORTRAN 3.2A'
1058 *B 'LABKPC' ' DECK-ID B08 FORTRAN 3.2A'
1059 *B 'LABLER' ' DECK-ID B09 FORTRAN 3.2A'
1060 *B 'PUNT' ' DECK-ID B10 FORTRAN 3.2A'
1061 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
1062 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
1063 *B 'STOREB' ' DECK-ID F37 FORTRAN 3.2A'
1064 *B 'SYMBOL' ' DECK-ID B11 FORTRAN 3.2A'
1065 *B 'TSALOC' ' DECK-ID B12 FORTRAN 3.2A'
1066 *B 'LOCLBB' ' DECK-ID F40 FORTRAN 3.2A'
1067 *B 'DUMYBB' ' DECK-ID F41 FORTRAN 3.2A'
1068 *B 'AFIDL' ' DECK-ID B25 FORTRAN 3.2A'
1069 *B 'ASSEM' ' DECK-ID B13 FORTRAN 3.2A'
1070 *B 'BANANA' ' DECK-ID B14 FORTRAN 3.2A'
1071 *B 'END' ' DECK-ID B16 FORTRAN 3.2A'
1072 *B 'ENTCOD' ' DECK-ID B17 FORTRAN 3.2A'
1073 *B 'INXRST' ' DECK-ID B19 FORTRAN 3.2A'
1074 *B 'SUBFUN' ' DECK-ID B23 FORTRAN 3.2A'
1075 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
1076 *T
1077 *K, I8
1078 *N, FORTBB, , , B
1079 *K, I6
1080 *K, P8

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1081 *P,F,,MARKER
1082 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
1083 *B 'GOB' ' DECK-ID F35 FORTRAN 3.2A'
1084 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'
1085 *B 'DUMMY' ' DECK-ID B01 FORTRAN 3.2A'
1086 *B 'FCMSTK' ' DECK-ID B02 FORTRAN 3.2A'
1087 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
1088 *B 'IOPRBB' ' DECK-ID F36 FORTRAN 3.2A'
1089 *B 'KCPART' ' DECK-ID B03 FORTRAN 3.2A'
1090 *B 'KOUTPT' ' DECK-ID B04 FORTRAN 3.2A'
1091 *B 'KPCSTK' ' DECK-ID B05 FORTRAN 3.2A'
1092 *B 'KPC3PR' ' DECK-ID B06 FORTRAN 3.2A'
1093 *B 'KSYMG' ' DECK-ID B07 FORTRAN 3.2A'
1094 *B 'LABKPC' ' DECK-ID B08 FORTRAN 3.2A'
1095 *B 'LABLER' ' DECK-ID B09 FORTRAN 3.2A'
1096 *B 'PUNT' ' DECK-ID B10 FORTRAN 3.2A'
1097 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
1098 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
1099 *B 'STOREB' ' DECK-ID F37 FORTRAN 3.2A'
1100 *B 'SYMBOL' ' DECK-ID B11 FORTRAN 3.2A'
1101 *B 'TSALOC' ' DECK-ID B12 FORTRAN 3.2A'
1102 *B 'LOCLBC' ' DECK-ID F42 FORTRAN 3.2A'
1103 *B 'DUMYBC' ' DECK-ID F43 FORTRAN 3.2A'
1104 *B 'ASUPER' ' DECK-ID B26 FORTRAN 3.2A'
1105 *B 'ARAYSZ' ' DECK-ID A42 FORTRAN 3.2A'
1106 *B 'BGINDO' ' DECK-ID B15 FORTRAN 3.2A'
1107 *B 'CGOTO' ' DECK-ID B27 FORTRAN 3.2A'
1108 *B 'HELEN' ' DECK-ID B18 FORTRAN 3.2A'
1109 *B 'SYMSCN' ' DECK-ID A28 FORTRAN 3.2A'
1110 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
1111 *T
1112 *K,I8
1113 *N,FORTBC,,B
1114 *K,I6
1115 *K,P8
1116 *P,F,,MARKER
1117 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
1118 *B 'GOB' ' DECK-ID F35 FORTRAN 3.2A'
1119 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'
1120 *B 'DUMMY' ' DECK-ID B01 FORTRAN 3.2A'
1121 *B 'FCMSTK' ' DECK-ID B02 FORTRAN 3.2A'
1122 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
1123 *B 'IOPRBB' ' DECK-ID F36 FORTRAN 3.2A'
1124 *B 'KCPART' ' DECK-ID B03 FORTRAN 3.2A'
1125 *B 'KOUTPT' ' DECK-ID B04 FORTRAN 3.2A'
1126 *B 'KPCSTK' ' DECK-ID B05 FORTRAN 3.2A'
1127 *B 'KPC3PR' ' DECK-ID B06 FORTRAN 3.2A'
1128 *B 'KSYMG' ' DECK-ID B07 FORTRAN 3.2A'
1129 *B 'LABKPC' ' DECK-ID B08 FORTRAN 3.2A'
1130 *B 'LABLER' ' DECK-ID B09 FORTRAN 3.2A'
1131 *B 'PUNT' ' DECK-ID B10 FORTRAN 3.2A'
1132 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
1133 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
1134 *B 'STOREB' ' DECK-ID F37 FORTRAN 3.2A'
1135 *B 'SYMBOL' ' DECK-ID B11 FORTRAN 3.2A'
1136 *B 'TSALOC' ' DECK-ID B12 FORTRAN 3.2A'
1137 *B 'LOCLBD' ' DECK-ID F44 FORTRAN 3.2A'
1138 *B 'DUMYBD' ' DECK-ID F45 FORTRAN 3.2A'
1139 *B 'ARITHR' ' DECK-ID B34 FORTRAN 3.2A'
1140 *B 'FINK' ' DECK-ID B28 FORTRAN 3.2A'

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1141 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
1142 *T
1143 *K,I8
1144 *N,FORTBD,,,B
1145 *K,I6
1146 *K,P8
1147 *P,F,,,MARKER
1148 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
1149 *B 'GOB' ' DECK-ID F35 FORTRAN 3.2A'
1150 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'
1151 *B 'DUMMY' ' DECK-ID B01 FORTRAN 3.2A'
1152 *B 'FCMSTK' ' DECK-ID B02 FORTRAN 3.2A'
1153 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
1154 *B 'IOPRBB' ' DECK-ID F36 FORTRAN 3.2A'
1155 *B 'KCPART' ' DECK-ID B03 FORTRAN 3.2A'
1156 *B 'KOUTPT' ' DECK-ID B04 FORTRAN 3.2A'
1157 *B 'KPCSTK' ' DECK-ID B05 FORTRAN 3.2A'
1158 *B 'KPC3PR' ' DECK-ID B06 FORTRAN 3.2A'
1159 *B 'KSYMG' ' DECK-ID B07 FORTRAN 3.2A'
1160 *B 'LABKPC' ' DECK-ID B08 FORTRAN 3.2A'
1161 *B 'LABLER' ' DECK-ID B09 FORTRAN 3.2A'
1162 *B 'PUNT' ' DECK-ID B10 FORTRAN 3.2A'
1163 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
1164 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
1165 *B 'STOREB' ' DECK-ID F37 FORTRAN 3.2A'
1166 *B 'SYMBOL' ' DECK-ID B11 FORTRAN 3.2A'
1167 *B 'TSALOC' ' DECK-ID B12 FORTRAN 3.2A'
1168 *B 'LOCLBE' ' DECK-ID F46 FORTRAN 3.2A'
1169 *B 'DUMYBE' ' DECK-ID F47 FORTRAN 3.2A'
1170 *B 'ACP' ' DECK-ID B24 FORTRAN 3.2A'
1171 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
1172 *T
1173 *K,I8
1174 *N,FORTBE,,,B
1175 *K,I6
1176 *K,P8
1177 *P,F,,,MARKER
1178 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
1179 *B 'GOB' ' DECK-ID F35 FORTRAN 3.2A'
1180 *B 'CNVT' ' DECK-ID A01 FORTRAN 3.2A'
1181 *B 'DUMMY' ' DECK-ID B01 FORTRAN 3.2A'
1182 *B 'FCMSTK' ' DECK-ID B02 FORTRAN 3.2A'
1183 *B 'GETSYM' ' DECK-ID F12 FORTRAN 3.2A'
1184 *B 'IOPRBB' ' DECK-ID F36 FORTRAN 3.2A'
1185 *B 'KCPART' ' DECK-ID B03 FORTRAN 3.2A'
1186 *B 'KOUTPT' ' DECK-ID B04 FORTRAN 3.2A'
1187 *B 'KPCSTK' ' DECK-ID B05 FORTRAN 3.2A'
1188 *B 'KPC3PR' ' DECK-ID B06 FORTRAN 3.2A'
1189 *B 'KSYMG' ' DECK-ID B07 FORTRAN 3.2A'
1190 *B 'LABKPC' ' DECK-ID B08 FORTRAN 3.2A'
1191 *B 'LABLER' ' DECK-ID B09 FORTRAN 3.2A'
1192 *B 'PUNT' ' DECK-ID B10 FORTRAN 3.2A'
1193 *B 'CONV' ' DECK-ID F03 FORTRAN 3.2A'
1194 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
1195 *B 'STOREB' ' DECK-ID F37 FORTRAN 3.2A'
1196 *B 'SYMBOL' ' DECK-ID B11 FORTRAN 3.2A'
1197 *B 'TSALOC' ' DECK-ID B12 FORTRAN 3.2A'
1198 *B 'LOCLBF' ' DECK-ID F48 FORTRAN 3.2A'
1199 *B 'DUMYBF' ' DECK-ID F49 FORTRAN 3.2A'
1200 *B 'SUBPR3' ' DECK-ID B33 FORTRAN 3.2A'

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1201	*B	'INTRAM'	'	DECK-ID	B29	FORTRAN	3.2A'
1202	*B	'PARTSB'	'	DECK-ID	B30	FORTRAN	3.2A'
1203	*B	'SUBPR1'	'	DECK-ID	B31	FORTRAN	3.2A'
1204	*B	'SUBPR2'	'	DECK-ID	B32	FORTRAN	3.2A'
1205	*B	'ENDLOC'	'	DECK-ID	F16	FORTRAN	3.2A'
1206	*T						
1207	*K,I8						
1208	*N,FORTBF,,,B						
1209	*K,I6						
1210	*K,P8						
1211	*P,F						
1212	*B	'FTNA'	'	COPYRIGHT CONTROL DATA CORPORATION 1973'			
1213	*B	'GOC'	'	DECK-ID	F50	FORTRAN	3.2A'
1214	*B	'IOPRBC'	'	DECK-ID	F51	FORTRAN	3.2A'
1215	*B	'BKDWN'	'	DECK-ID	C01	FORTRAN	3.2A'
1216	*B	'BLOUP'	'	DECK-ID	C02	FORTRAN	3.2A'
1217	*B	'BSS'	'	DECK-ID	C03	FORTRAN	3.2A'
1218	*B	'CHKWD'	'	DECK-ID	C04	FORTRAN	3.2A'
1219	*B	'CON'	'	DECK-ID	C07	FORTRAN	3.2A'
1220	*B	'COUNT'	'	DECK-ID	C08	FORTRAN	3.2A'
1221	*B	'DATAST'	'	DECK-ID	C09	FORTRAN	3.2A'
1222	*B	'GETSYM'	'	DECK-ID	C10	FORTRAN	3.2A'
1223	*B	'INOUT'	'	DECK-ID	C11	FORTRAN	3.2A'
1224	*B	'LABEL'	'	DECK-ID	C14	FORTRAN	3.2A'
1225	*B	'LABIN'	'	DECK-ID	C15	FORTRAN	3.2A'
1226	*B	'Q8PRMS'	'	DECK-ID	F10	FORTRAN	3.2A'
1227	*B	'REED'	'	DECK-ID	C17	FORTRAN	3.2A'
1228	*B	'SYMSCN'	'	DECK-ID	C19	FORTRAN	3.2A'
1229	*B	'LOCLCA'	'	DECK-ID	F52	FORTRAN	3.2A'
1230	*B	'DUMYCA'	'	DECK-ID	F53	FORTRAN	3.2A'
1231	*B	'PHASEC'	'	DECK-ID	C13	FORTRAN	3.2A'
1232	*B	'ENDLOC'	'	DECK-ID	F16	FORTRAN	3.2A'
1233	*T						
1234	*K,I8						
1235	*N,FORTCA,,,B						
1236	*K,I6						
1237	*K,P8						
1238	*P,F,,,MARKER						
1239	*B	'FTNA'	'	COPYRIGHT CONTROL DATA CORPORATION 1973'			
1240	*B	'GOC'	'	DECK-ID	F50	FORTRAN	3.2A'
1241	*B	'IOPRBC'	'	DECK-ID	F51	FORTRAN	3.2A'
1242	*B	'BKDWN'	'	DECK-ID	C01	FORTRAN	3.2A'
1243	*B	'BLOUP'	'	DECK-ID	C02	FORTRAN	3.2A'
1244	*B	'BSS'	'	DECK-ID	C03	FORTRAN	3.2A'
1245	*B	'CHKWD'	'	DECK-ID	C04	FORTRAN	3.2A'
1246	*B	'CON'	'	DECK-ID	C07	FORTRAN	3.2A'
1247	*B	'COUNT'	'	DECK-ID	C08	FORTRAN	3.2A'
1248	*B	'DATAST'	'	DECK-ID	C09	FORTRAN	3.2A'
1249	*B	'GETSYM'	'	DECK-ID	C10	FORTRAN	3.2A'
1250	*B	'INOUT'	'	DECK-ID	C11	FORTRAN	3.2A'
1251	*B	'LABEL'	'	DECK-ID	C14	FORTRAN	3.2A'
1252	*B	'LABIN'	'	DECK-ID	C15	FORTRAN	3.2A'
1253	*B	'Q8PRMS'	'	DECK-ID	F10	FORTRAN	3.2A'
1254	*B	'REED'	'	DECK-ID	C17	FORTRAN	3.2A'
1255	*B	'SYMSCN'	'	DECK-ID	C19	FORTRAN	3.2A'
1256	*B	'LOCLCB'	'	DECK-ID	F54	FORTRAN	3.2A'
1257	*B	'CHOP'	'	DECK-ID	C05	FORTRAN	3.2A'
1258	*B	'CL12'	'	DECK-ID	C06	FORTRAN	3.2A'
1259	*B	'SKIP'	'	DECK-ID	C18	FORTRAN	3.2A'
1260	*B	'IXOPT'	'	DECK-ID	C12	FORTRAN	3.2A'

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1261 *B 'QXLD' * DECK-ID C16 FORTRAN 3.2A'
1262 *B 'ENDLOC' * DECK-ID F16 FORTRAN 3.2A'
1263 *T
1264 *K,I8
1265 *N,FORTCB,,,B
1266 *K,I6
1267 *K,P8
1268 *P,F
1269 *B 'FTNA' * COPYRIGHT CONTROL DATA CORPORATION 1973'
1270 *B 'GOOD' * DECK-ID F55 FORTRAN 3.2A'
1271 *B 'INDEX' * DECK-ID D01 FORTRAN 3.2A'
1272 *B 'IOPRBD' * DECK-ID F56 FORTRAN 3.2A'
1273 *B 'NPUNCH' * DECK-ID D02 FORTRAN 3.2A'
1274 *B 'Q8PRMS' * DECK-ID F10 FORTRAN 3.2A'
1275 *B 'LOCLDA' * DECK-ID F58 FORTRAN 3.2A'
1276 *B 'DUMYDA' * DECK-ID F59 FORTRAN 3.2A'
1277 *B 'PHASE6' * DECK-ID D03 FORTRAN 3.2A'
1278 *B 'BEGINO' * DECK-ID D21 FORTRAN 3.2A'
1279 *B 'CONV' * DECK-ID F57 FORTRAN 3.2A'
1280 *B 'FINISH' * DECK-ID D22 FORTRAN 3.2A'
1281 *B 'GETSYM' * DECK-ID D16 FORTRAN 3.2A'
1282 *B 'IACON' * DECK-ID D17 FORTRAN 3.2A'
1283 *B 'IHCON' * DECK-ID D18 FORTRAN 3.2A'
1284 *B 'NWRITE' * DECK-ID D19 FORTRAN 3.2A'
1285 *B 'PACK' * DECK-ID F09 FORTRAN 3.2A'
1286 *B 'SYMSCN' * DECK-ID D20 FORTRAN 3.2A'
1287 *B 'ENDLOC' * DECK-ID F16 FORTRAN 3.2A'
1288 *T
1289 *K,I8
1290 *N,FORTDA,,,B
1291 *K,I6
1292 *K,P8
1293 *P,F,,MARKER
1294 *B 'FTNA' * COPYRIGHT CONTROL DATA CORPORATION 1973'
1295 *B 'GOOD' * DECK-ID F55 FORTRAN 3.2A'
1296 *B 'INDEX' * DECK-ID D01 FORTRAN 3.2A'
1297 *B 'IOPRBD' * DECK-ID F56 FORTRAN 3.2A'
1298 *B 'NPUNCH' * DECK-ID D02 FORTRAN 3.2A'
1299 *B 'Q8PRMS' * DECK-ID F10 FORTRAN 3.2A'
1300 *B 'LOCLDB' * DECK-ID F60 FORTRAN 3.2A'
1301 *B 'DUMYDB' * DECK-ID F61 FORTRAN 3.2A'
1302 *B 'AMOUT' * DECK-ID D04 FORTRAN 3.2A'
1303 *B 'BKDOWN' * DECK-ID D06 FORTRAN 3.2A'
1304 *B 'COUNT' * DECK-ID D07 FORTRAN 3.2A'
1305 *B 'GETSYM' * DECK-ID D14 FORTRAN 3.2A'
1306 *B 'LABOUT' * DECK-ID D08 FORTRAN 3.2A'
1307 *B 'NP2OUT' * DECK-ID D09 FORTRAN 3.2A'
1308 *B 'RBDX' * DECK-ID D10 FORTRAN 3.2A'
1309 *B 'RBPK' * DECK-ID D11 FORTRAN 3.2A'
1310 *B 'SYMSCN' * DECK-ID D15 FORTRAN 3.2A'
1311 *B 'TABDEC' * DECK-ID D12 FORTRAN 3.2A'
1312 *B 'UNPUNC' * DECK-ID D13 FORTRAN 3.2A'
1313 *B 'ENDLOC' * DECK-ID F16 FORTRAN 3.2A'
1314 *T
1315 *K,I8
1316 *N,FORTDB,,,B
1317 *K,I6
1318 *K,P8
1319 *P,F,,MARKER
1320 *B 'FTNA' * COPYRIGHT CONTROL DATA CORPORATION 1973'

1321 *B 'GOOD' ' DECK-ID F55 FORTRAN 3.2A'
1322 *B 'INDEX' ' DECK-ID D01 FORTRAN 3.2A'
1323 *B 'IOPRBD' ' DECK-ID F56 FORTRAN 3.2A'
1324 *B 'NPUNCH' ' DECK-ID D02 FORTRAN 3.2A'
1325 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
1326 *B 'LOCLDC' ' DECK-ID F62 FORTRAN 3.2A'
1327 *B 'ADMAY' ' DECK-ID D05 FORTRAN 3.2A'
1328 *B 'GETSYM' ' DECK-ID D14 FORTRAN 3.2A'
1329 *B 'TABDEC' ' DECK-ID D12 FORTRAN 3.2A'
1330 *B 'SYMSCN' ' DECK-ID D15 FORTRAN 3.2A'
1331 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
1332 *T
1333 *K,I8
1334 *N,FORTDC,,,B
1335 *K,I6
1336 *K,P8
1337 *P,F
1338 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
1339 *B 'GOE' ' DECK-ID F63 FORTRAN 3.2A'
1340 *B 'INDEX' ' DECK-ID E01 FORTRAN 3.2A'
1341 *B 'IOPRBD' ' DECK-ID F56 FORTRAN 3.2A'
1342 *B 'NPUNCH' ' DECK-ID E02 FORTRAN 3.2A'
1343 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
1344 *B 'LOCLDA' ' DECK-ID F58 FORTRAN 3.2A'
1345 *B 'DUMYDA' ' DECK-ID F59 FORTRAN 3.2A'
1346 *B 'PHASE6' ' DECK-ID E03 FORTRAN 3.2A'
1347 *B 'BEGINO' ' DECK-ID E19 FORTRAN 3.2A'
1348 *B 'CONV' ' DECK-ID F57 FORTRAN 3.2A'
1349 *B 'FINISH' ' DECK-ID E20 FORTRAN 3.2A'
1350 *B 'GETSYM' ' DECK-ID E14 FORTRAN 3.2A'
1351 *B 'IACON' ' DECK-ID E15 FORTRAN 3.2A'
1352 *B 'IHCON' ' DECK-ID E16 FORTRAN 3.2A'
1353 *B 'NWRITE' ' DECK-ID E17 FORTRAN 3.2A'
1354 *B 'PACK' ' DECK-ID F09 FORTRAN 3.2A'
1355 *B 'SETPRT' ' DECK-ID E18 FORTRAN 3.2A'
1356 *B 'SYMSCN' ' DECK-ID D20 FORTRAN 3.2A'
1357 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
1358 *T
1359 *K,I8
1360 *N,FORTEA,,,B
1361 *K,I6
1362 *K,P8
1363 *P,F,,,MARKER
1364 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
1365 *B 'GOE' ' DECK-ID F63 FORTRAN 3.2A'
1366 *B 'INDEX' ' DECK-ID E01 FORTRAN 3.2A'
1367 *B 'IOPRBD' ' DECK-ID F56 FORTRAN 3.2A'
1368 *B 'NPUNCH' ' DECK-ID E02 FORTRAN 3.2A'
1369 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
1370 *B 'LOCLDB' ' DECK-ID F60 FORTRAN 3.2A'
1371 *B 'DUMYDB' ' DECK-ID F61 FORTRAN 3.2A'
1372 *B 'AMOUT' ' DECK-ID E04 FORTRAN 3.2A'
1373 *B 'BKDWN' ' DECK-ID E06 FORTRAN 3.2A'
1374 *B 'CONV' ' DECK-ID F57 FORTRAN 3.2A'
1375 *B 'COUNT' ' DECK-ID E07 FORTRAN 3.2A'
1376 *B 'GETSYM' ' DECK-ID E14 FORTRAN 3.2A'
1377 *B 'IACON' ' DECK-ID E15 FORTRAN 3.2A'
1378 *B 'IHCON' ' DECK-ID E16 FORTRAN 3.2A'
1379 *B 'LABOUT' ' DECK-ID E08 FORTRAN 3.2A'
1380 *B 'NP2OUT' ' DECK-ID E09 FORTRAN 3.2A'

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1381 *B 'NWRITE' ' DECK-ID E17 FORTRAN 3.2A'
1382 *B 'PACK' ' DECK-ID F09 FORTRAN 3.2A'
1383 *B 'RBDX' ' DECK-ID E10 FORTRAN 3.2A'
1384 *B 'RBPK' ' DECK-ID E11 FORTRAN 3.2A'
1385 *B 'SETPRT' ' DECK-ID E18 FORTRAN 3.2A'
1386 *B 'SYMSCN' ' DECK-ID D20 FORTRAN 3.2A'
1387 *B 'TABDEC' ' DECK-ID E12 FORTRAN 3.2A'
1388 *B 'UNPUNC' ' DECK-ID E13 FORTRAN 3.2A'
1389 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
1390 *T
1391 *K,I8
1392 *N,FORTEB,,,B
1393 *K,I6
1394 *K,P8
1395 *P,F,,,MARKER
1396 *B 'FTNA' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
1397 *B 'GOE' ' DECK-ID F63 FORTRAN 3.2A'
1398 *B 'INDEX' ' DECK-ID E01 FORTRAN 3.2A'
1399 *B 'IOPRBD' ' DECK-ID F56 FORTRAN 3.2A'
1400 *B 'NPUNCH' ' DECK-ID E02 FORTRAN 3.2A'
1401 *B 'Q8PRMS' ' DECK-ID F10 FORTRAN 3.2A'
1402 *B 'LOCLDC' ' DECK-ID F62 FORTRAN 3.2A'
1403 *B 'ADMAX' ' DECK-ID E05 FORTRAN 3.2A'
1404 *B 'GETSYM' ' DECK-ID E14 FORTRAN 3.2A'
1405 *B 'SYMSCN' ' DECK-ID D20 FORTRAN 3.2A'
1406 *B 'TABDEC' ' DECK-ID E12 FORTRAN 3.2A'
1407 *B 'ENDLOC' ' DECK-ID F16 FORTRAN 3.2A'
1408 *T
1409 *K,I8
1410 *N,FORTEC,,,B
1411 *V NON-REENTRANT FTN. LIBRARY
1412 *K,I6
1413 *L,READ
1414 *B 'FORTN' ' DECK-ID F01 3.2 FTN RUNTIME'
1415 *L,Q8PREP
1416 *B 'Q8PRMS' ' DECK-ID G01 3.2 FTN RUNTIME'
1417 *L,Q8QF2I
1418 *B 'Q8EXPN' ' DECK-ID G02 3.2 FTN RUNTIME'
1419 *L,ABS
1420 *B 'Q8AB' ' DECK-ID G03 3.2 FTN RUNTIME'
1421 *L,SQRT
1422 *B 'SQRTF' ' DECK-ID G04 3.2 FTN RUNTIME'
1423 *L,SIGN
1424 *B 'SIGN' ' DECK-ID G05 3.2 FTN RUNTIME'
1425 *L,FLOAT
1426 *B 'FXFL' ' DECK-ID G06 3.2 FTN RUNTIME'
1427 *L,EXP
1428 *B 'EXPPRG' ' DECK-ID G07 3.2 FTN RUNTIME'
1429 *L,ALOG
1430 *B 'LNUPRG' ' DECK-ID G08 3.2 FTN RUNTIME'
1431 *L,TANH
1432 *B 'TANH' ' DECK-ID G09 3.2 FTN RUNTIME'
1433 *L,SIN
1434 *B 'SINCOS' ' DECK-ID G10 3.2 FTN RUNTIME'
1435 *L,ATAN
1436 *B 'ARCTPG' ' DECK-ID G11 3.2 FTN RUNTIME'
1437 *L,IFALT
1438 *B 'IFALT' ' DECK-ID G12 3.2 FTN RUNTIME'
1439 *L,FLOT
1440 *B 'FLOAT' ' DECK-ID G13 3.2 FTN RUNTIME'

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1441	*L,Q8IFRM			
1442	*B 'Q8IFRM'	'	DECK-ID H01	3.2 FTN RUNTIME'
1443	*L,Q8FS			
1444	*B 'Q8FS'	'	DECK-ID H02	3.2 FTN RUNTIME'
1445	*L,Q8TRAN			
1446	*B 'Q8TRAN'	'	DECK-ID H03	3.2 FTN RUNTIME'
1447	*L,Q8QINI			
1448	*B 'Q8QINI'	'	DECK-ID H04	3.2 FTN RUNTIME'
1449	*L,Q8QEND			
1450	*B 'Q8QEND'	'	DECK-ID H05	3.2 FTN RUNTIME'
1451	*L,Q8CMP0			
1452	*B 'Q8CMP'	'	DECK-ID H06	3.2 FTN RUNTIME'
1453	*L,Q8RWBU			
1454	*B 'Q8RWBU'	'	DECK-ID H07	3.2 FTN RUNTIME'
1455	*L,Q8ERRM			
1456	*B 'Q8ERRM'	'	DECK-ID H08	3.2 FTN RUNTIME'
1457	*L,Q8DFNF			
1458	*B 'Q8DFNF'	'	DECK-ID H09	3.2 FTN RUNTIME'
1459	*L,Q8QX			
1460	*B 'Q8QX'	'	DECK-ID H10	3.2 FTN RUNTIME'
1461	*L,Q8QUN1			
1462	*B 'Q8QUN1'	'	DECK-ID H11	3.2 FTN RUNTIME'
1463	*L,Q8FGET			
1464	*B 'Q8FGET'	'	DECK-ID H12	3.2 FTN RUNTIME'
1465	*L,Q8MAGT			
1466	*B 'Q8MAGT'	'	DECK-ID H13	3.2 FTN RUNTIME'
1467	*L,EOF			
1468	*B 'TAPCON'	'	DECK-ID H14	3.2 FTN RUNTIME'
1469	*L,I0CK			
1470	*B 'I0CK'	'	DECK-ID H15	3.2 FTN RUNTIME'
1471	*L,Q8PSE			
1472	*B 'PSSTOP'	'	DECK-ID H16	3.2 FTN RUNTIME'
1473	*L,Q8PAND			
1474	*B 'Q8PAND'	'	DECK-ID H17	3.2 FTN RUNTIME'
1475	*L,Q8EXP1			
1476	*B 'Q8EXP1'	'	DECK-ID H18	3.2 FTN RUNTIME'
1477	*L,Q8EXP9			
1478	*B 'Q8EXP9'	'	DECK-ID H19	3.2 FTN RUNTIME'
1479	*L,SETBFR			
1480	*B 'Q8QGTx'	'	DECK-ID H20	3.2 FTN RUNTIME'
1481	*L,ENCODE			
1482	*B 'I0CODE'	'	DECK-ID J01	3.2 FTN RUNTIME'
1483	*L,COMMON			
1484	*B 'PSUEDO'	'	DECK-ID J02	3.2 FTN RUNTIME'
1485	*L,IGETCH			
1486	*B 'IGETCH'	'	DECK-ID J03	3.2 FTN RUNTIME'
1487	*L,IPACK			
1488	*B 'IPACK'	'	DECK-ID J04	3.2 FTN RUNTIME'
1489	*L,UPDATE			
1490	*B 'UPDATN'	'	DECK-ID J05	3.2 FTN RUNTIME'
1491	*L,DECPL			
1492	*B 'DECPL'	'	DECK-ID J06	3.2 FTN RUNTIME'
1493	*L,INTGR			
1494	*B 'INTGR'	'	DECK-ID J07	3.2 FTN RUNTIME'
1495	*L,SPACEX			
1496	*B 'SPACEN'	'	DECK-ID J08	3.2 FTN RUNTIME'
1497	*L,HOLRTH			
1498	*B 'HOLRTH'	'	DECK-ID J09	3.2 FTN RUNTIME'
1499	*L,DCHX			
1500	*B 'DCHX'	'	DECK-ID J10	3.2 FTN RUNTIME'

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1501 *L,HXASC
1502 *B 'HXASC' ' DECK-ID J11 3.2 FTN RUNTIME'
1503 *L,AFRMOT
1504 *B 'AFRMOT' ' DECK-ID J12 3.2 FTN RUNTIME'
1505 *L,RFRMOT
1506 *B 'RFRMOT' ' DECK-ID J13 3.2 FTN RUNTIME'
1507 *L,AFRMIN
1508 *B 'AFRMIN' ' DECK-ID J14 3.2 FTN RUNTIME'
1509 *L,RFRMIN
1510 *B 'RFRMIN' ' DECK-ID J15 3.2 FTN RUNTIME'
1511 *L,ASCHX
1512 *B 'ASCHX' ' DECK-ID J16 3.2 FTN RUNTIME'
1513 *L,HXOC
1514 *B 'HXOC' ' DECK-ID J17 3.2 FTN RUNTIME'
1515 *L,FLOTIN
1516 *B 'FLOTIN' ' DECK-ID J18 3.2 FTN RUNTIME'
1517 *L,FOUT
1518 *B 'FOUT' ' DECK-ID J19 3.2 FTN RUNTIME'
1519 *L,EOUT
1520 *B 'EOUT' ' DECK-ID J20 3.2 FTN RUNTIME'
1521 *L,EWRITE
1522 *B 'EWRITE' ' DECK-ID J21 3.2 FTN RUNTIME'
1523 *L,INITL1
1524 *B 'INITL1' ' DECK-ID J22 3.2 FTN RUNTIME'
1525 *L,FORMTR
1526 *B 'FORMTR' ' DECK-ID J23 3.2 FTN RUNTIME'
1527 *L,Q8QFI
1528 *B 'Q8QFI' ' DECK-ID J24 3.2 FTN RUNTIME'
1529 *L,Q8QFL
1530 *B 'Q8QFL' ' DECK-ID J25 3.2 FTN RUNTIME'
1531 *L,Q8QFX
1532 *B 'Q8QFX' ' DECK-ID J26 3.2 FTN RUNTIME'
1533 *L,HEXASC
1534 *B 'HEXASC' ' DECK-ID J27 3.2 FTN RUNTIME'
1535 *L,HEXDEC
1536 *B 'HEXDEC' ' DECK-ID J28 3.2 FTN RUNTIME'
1537 *L,ASCII
1538 *B 'ASCII' ' DECK-ID J29 3.2 FTN RUNTIME'
1539 *L,DECHEX
1540 *B 'DECHEX' ' DECK-ID J30 3.2 FTN RUNTIME'
1541 *L,AFORM
1542 *B 'AFORM' ' DECK-ID J31 3.2 FTN RUNTIME'
1543 *L,RFORM
1544 *B 'RFORM' ' DECK-ID J32 3.2 FTN RUNTIME'
1545 *L,FLOATG
1546 *B 'FLOATG' ' DECK-ID J33 3.2 FTN RUNTIME'
1547 *L,Q8DXP1
1548 *B 'DBLOMY'
1549 *Z
1550 *K,I10,P11,L9
1551 *CTO, MSOS 4.1 INSTALLATION COMPLETED - YOU MAY AUTOLOAD
1552 *Z
1553 *END

6.3.3 Creating a New Install

Tape, Building and Verifying the New System

The program LIBILD is used to create a new install file. Library Input to LIBILD consists of the following:

- {1} The FORTRAN binaries tape
- {2} Binary cards for the revised SYSDAT
- {3} The current install tape
- {4} MSOS binaries tape from which RDISP will be read

The library input is presented to LIBILD in the order listed above. The revised SYSDAT is input prior to the current install tape so that the revised version of SYSDAT will be the one incorporated into the new install file. Typing on the teletype is similar to that in Article 5.3.4. 6.37

Note:

Using the new install file, the new system may be built and verified as described in the 1700 MSOS 4 Installation Handbook. Typing on the comment device for a similar procedure is found in Article 5.3.5. 6-38

6.4 Addition of Double Precision

6.4.1 Addition of Reentrant Double Precision

To add double precision capability to the reentrant FORTRAN library it is necessary to modify SYSDAT Section ACS. The table of reentrant FORTRAN entry points in this section must be enlarged to include the entry points of the reentrant FORTRAN double precision modules. The entry point DOUT must be removed from this section. {Refer to Article 1.7.1.}

The system skeleton must be modified by adding a *B record for each module listed in Group L of Article 2.1. The binary form of each module to be added is available in the MSOS binaries file if the FORTRAN compiler was a part of the MSOS 4.1 system originally installed. If the FORTRAN compiler was added subsequent to the initial MSOS 4.1 installation, the binary form of each module to be added is to be found in the FORTRAN binaries file sent to the user when the compiler was added. 6-41

The addition of double precision to the reentrant FORTRAN library causes the values of ENDDV4 and BGNMON to be decreased. It causes the size of SYSDAT to increase. For these reasons it may be necessary to change the value of N4.

An example of the addition of double precision to the reentrant FORTRAN library is included in Article 6.4.3.

6.4.2 Addition of Double Precision to Input/Output Library 6-42

No SYSDAT changes are required to add double precision capability to the FORTRAN background library. The following records must be deleted from the skeleton:

```
#L,Q8DXPT  
*R *DBLD*Y* * DECK-ID K19 3.2 FTN RUNTIME*
```

The following records must be added to the skeleton following the *B record for the module FLOATG. {The deck identification field is optional}.

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- *L,Q8QD2I
- *B #DEXPN# # DECK-ID K01 3.2 FTN RUNTIME#
- *L,Q8DRLE
- *B #Q8DBLE# # DECK-ID K02 3.2 FTN RUNTIME#
- *L,SNGL
- *B #SNGL# # DECK-ID K03 3.2 FTN RUNTIME#
- *L,DARS
- *B #Q8CAEN# # DECK-ID K04 3.2 FTN RUNTIME#
- *L,DSORT
- *B #DSGRIN# # DECK-ID K05 3.2 FTN RUNTIME#
- *L,DSIGN
- *B #Q8DSIG# # DECK-ID K06 3.2 FTN RUNTIME#
- *L,DFLT
- *B #Q8DFLT# # DECK-ID K07 3.2 FTN RUNTIME#
- *L,DEXP
- *B #DEXPFN# # DECK-ID K08 3.2 FTN RUNTIME#
- *L,DLOG
- *B #DLGCGN# # DECK-ID K09 3.2 FTN RUNTIME#
- *L,DSIN
- *B #DSNCSN# # DECK-ID K10 3.2 FTN RUNTIME#
- *L,DATAN
- *B #DATANN# # DECK-ID K11 3.2 FTN RUNTIME#
- *L,DFLOT
- *B #DFLOTN# # DECK-ID K12 3.2 FTN RUNTIME#
- *L,AVOLA
- *B #DUMVCL# # DECK-ID K13 3.2 FTN RUNTIME#
- *L,DSTOR1
- *B #DRSTOR# # DECK-ID K14 3.2 FTN RUNTIME#
- *L,Q8DXF1
- *B #Q8DXF1# # DECK-ID K15 3.2 FTN RUNTIME#
- *L,Q8DXF9
- *B #Q8DXF9# # DECK-ID K16 3.2 FTN RUNTIME#
- *L,Q8QDFN
- *B #Q8QDFN# # DECK-ID K17 3.2 FTN RUNTIME#
- *L,DOUT
- *B #DOUTN# # DECK-ID K18 3.2 FTN RUNTIME#

b-44

A binary copy of each module to be added is in the MS0S binaries file if the FORTRAN compiler was included in the system at the time of MS0S 4.1 installation. If the compiler was added later, the binaries needed are available in the FORTRAN binaries file sent to the user at the time the compiler was added.

SSD

When adding double precision capability to the FORTRAN background library, it is important to remember that unprotected core must have a minimum size of 3000_{16} . {Table B-1}

To insure adequate unprotected areas it may be necessary to modify the value of N4.

6.4.3 Example of Double Precision Additions

In this example, double precision capability is added to both the reentrant FORTRAN library and to the FORTRAN Input/Output library. The users current system is the system built in Article 6.3.3. As explained later in this article it is necessary in this example to increase the hardware core size.

Addition of double precision to the foreground library requires the modification of SYSDAT Section ACS, as described in Article 6.4.1. After modification Section ACS is as follows:

6-450
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M I S C E L L A N E O U S I N F O R M A T I O N
F O R T R A N R E E N T R A N T I N F O R M A T I O N

ENT FMASK, FLIST
EXT Q8QF2I, Q8QI2F, Q8QF2F, QSAVE, E4SAVE
EXT RFTAD, KG47V4, KG48V4, KG49V4
EXT ABS, SQRT, SIGN, IFIX, FLOAT
EXT EXP, ALOG, TANH, SIN, COS, ATAN, IFALT
EXT FLOT, ARGU0
EXT Q8QFLT, Q8QD2I, Q8QD2F, Q8QD2D
EXT QABS, QDSORT, QDSIGN, QSNGL, QDFLT
EXT QEXP, QDLOG, QDSIN, QDCOS, QDATAN, QDFLOT

FMASK NUM 30070 FORTPAN REENTRANT LEVELS (BIT 0 = LEVEL 0)

TABLE OF FORTPAN ENTRY POINTS SAVED TO MAINTAIN REENTRANCY

ENTRY POINT	PROGRAM	DESCRIPTION
FLIST		
ADC FEND		
ADC Q8QF2I	Q8EXPR	FLOAT-TO-INTEGGER FUNCTION
ADC Q8QI2F	Q8EXPR	INTEGER-TO-FLOAT FUNCTION
ADC Q8QF2F	Q8EXPR	FLOAT-TO-FLOAT FUNCTION
ADC QSAVE	Q8EXPR	Q-REGISTER STORAGE
ADC E4SAVE	Q8EXPR	LOCATION 3E4 STORAGE
ADC RFTAD	Q8EXPR	RETURN ADDRESS STORAGE
ADC KG47V4	Q8EXPR	TEMPORARY STORAGE
ADC KG48V4	Q8EXPR	TEMPORARY STORAGE
ADC KG49V4	Q8EXPR	TEMPORARY STORAGE
ADC ABS	Q8ABR	ABSOLUTE VALUE FUNCTION
ADC SQRT	SQRTFR	SQUARE ROOT FUNCTION
ADC SIGN	SIGNR	INTRINSIC FUNCTION SIGN
ADC IFIX	FXFLR	INTRINSIC FUNCTION IFIX
ADC FLOAT	FXFLR	INTRINSIC FUNCTION FLOAT
ADC EXP	EXPRGR	EXTERNAL FUNCTION EXP
ADC ALOG	LNPRGR	EXTERNAL FUNCTION ALOG
ADC TANH	TANHR	EXTERNAL FUNCTION TANH
ADC SIN	SNCSR	EXTERNAL FUNCTION SIN
ADC COS	SNCSR	EXTERNAL FUNCTION COS
ADC ATAN	ARCPGR	EXTERNAL FUNCTION ATAN
ADC IFALT	IFALTR	EXTERNAL FUNCTION IFALT
ADC FLOT	FLOATR	FLOATING POINT PROCESSOR
ADC ARGU0	Q8QIO	TEMPORARY STORAGE

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F O R T R A N R E E N T R A N T I N F O R M A T I O N

ENTRY POINT	PROGRAM	DESCRIPTION
ADC Q8DFLT	Q8DBLR	INTEGER-TO-DOUBLE FUNCTION
ADC Q8Q72I	DEXPR	DOUBLE-TO-INTEGER FUNCTION
ADC Q8Q72F	DEXPR	DOUBLE-TO-SINGLE FUNCTION
ADC Q8QD2D	DEXPR	DOUBLE-TO-DOUBLE FUNCTION
ADC DABS	Q8DABR	DBL. PREC. ABSOLUTE VALUE FUNCTION
ADC DSQRT	DSQRTR	DBL. PREC. SQUARE ROOT FUNCTION
ADC DSIGN	DSIGNR	INTRINSIC FUNCTION DSIGN
ADC SNGL	SNGLR	INTRINSIC FUNCTION SNGL
ADC DFLT	Q8DFLR	INTRINSIC FUNCTION DFLT
ADC DEXP	DEXPR	DBL. PREC. EXTERNAL FUNCTION EXP
ADC DLOG	DLOGR	DBL. PREC. EXTERNAL FUNCTION ALOG
ADC DSIN	DSNCSR	DBL. PREC. EXTERNAL FUNCTION SIN
ADC DCOS	DSNCSR	DBL. PREC. EXTERNAL FUNCTION COS
ADC DATAN	DATANR	DBL. PREC. EXTERNAL FUNCTION ATAN
ADC DFLOT	DFLOTR	DBL. PREC. FLOATING POINT PROCESSOR
FEND		
EQU FEND(*-FLIST-1)		

F O R T R A N R E E N T R A N T I N F O R M A T I O N

THIS ENTRY IS PROVIDED TO ALLOW COMPATIBILITY BETWEEN THE NON-REENTRANT (BACKGROUND) FORTRAN AND REENTRANT FORTRAN

ENT Q8STP

Q8STP NOP 0 FORTRAN STOP

 JMP- (ADISP)

b-4b

The skeleton is modified as described in Article b.4.1 and Article b.4.2. The total length of the reentrant library modules to be added is determined by loading these modules in the background using a *L job processor command. This technique is illustrated in Article b.3.2. The length of the modules is $9E6_{16}$. The modification of SYSDAT increases the start of unprotected by $15=F_{16}$ words. Therefore the effective decrease in the area available for allocatable area 4 and unprotected core is $9E6 + F = 9F5$.

b-4bA

The current system has $18F8_{16}$ words in allocatable area 4 and 2000_{16} words of unprotected core, or a total of $38F8_{16}$ available for area 4 and unprotected together. A decrease of $9F5_{16}$ would leave $2F03_{16}$ words available for these two areas. The Input/Output Library with double precision requires 3000_{16} words of unprotected core {Table b-1}. The minimum value of N4 which permits File Manager requests by background programs is $5DC_{16}$ {Article 1.5.8}. Therefore, this system requires a total of $35DC_{16}$ words for unprotected and area 4 together. The difference between the area needed and the area available is $35DC_{16} - 2F03_{16} = 6D9_{16}$.

The user first considers increasing available core by using the procedures described in Appendix A.

b-4bC

Referring to Appendix A, $C0_{16}$ additional words could be made available by substituting the module PR0TK for BPR0TK, leaving 619_{16} words as the additional space which still must be made available. Again referring to Appendix A, the size of the mass resident driver buffer may be reduced. Its current size is $8Cb_{16}$, as can be seen from Section AcR of SYSDAT, as follows:

M I S C E L L A N E O U S I N F O R M A T I O N

M A S S R E S I D E N T D R I V E R S B U F F E R

THIS BUFFER WILL CONTAIN THE MASS RESIDENT DRIVER(S) WHEN THEY ARE IN CORE. THE SMALLEST ALLOWABLE SIZE IS EQUAL TO THE LARGEST MASS RESIDENT DRIVER IN THE SYSTEM. OPTIMUM THROUGHPUT REQUIRES SIZING EQUAL TO THE TWO LARGEST MASS RESIDENT DRIVERS IN THE SYSTEM.

ENT BUFF, BUFFF
EQU MBFSZ(30806)

BUFF BZS BUFF(MBFSZ)
EQU BUFFE(*)

The size of the largest mass resident driver is approximated by examining the mass resident drivers portion of the system load map. This portion of the current system load map is as follows:

```

*
*      MASS RESIDENT DRIVERS
*
#M
      COSY DRIVER
#S,S00SY,S      02RC      DECK-ID C28      MSCS 4.1      SUMMARY-079
#S,I00SY,P
#M      1731 601 MAG TAPE
      D1731U      02C4      DECK-ID C33      MSCS 4.1      SUMMARY-081
      FRWA      01F5      DECK-ID C34      MSCS 4.1      SUMMARY-079
      FRWB      02C3      DECK-ID C35      MSCS 4.1      SUMMARY-079
      PWBA      03C0      DECK-ID C36      MSCS 4.1      SUMMARY-079
      MAXRVU      0440      DECK-ID C37      MSCS 4.1      SUMMARY-081
#S,S1731U,S
#S,I1731U,P
#M      1740-501/1742 LINE PRINTER
      04421      02D4      DECK-ID C46      MSCS 4.1      SUMMARY-082
#S,S40421,S
#S,I40421,P
#M
      01728      02D9      DECK-ID C48      MSCS 4.1      SUMMARY-082
      CR 26      03R4      DECK-ID C54      MSCS 4.1      SUMMARY-079
      CR 26      03C4      DECK-ID C52      MSCS 4.1      SUMMARY-079
#S,S1728,S
#S,I1728,P
*
*      MASS RESIDENT FILE MANAGER
*
#M
      DEFFIL      02E4      DECK-ID F05      MSCS 4.1      SUMMARY-079
  
```

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The number to the right of the first module of a driver is the sector number where the driver resides on mass memory. The number to the right of a subsequent module is the driver word count at the beginning of the module. In the user's current load map the largest mass resident driver is the 1731-601 magnetic tape driver, requiring between 15 and 16 sectors or between 5A1₁₆ and 600₁₆ words. Thus, reducing the mass resident driver buffer to this size from 8C6₁₆ words would not provide the additional 619₁₆ words needed. In this example the user requires all locations between NXTLOC and MSIAV4. Therefore, no additional core may be made available from that area. 6-46F

In this example the user attains the additional core needed by adding a hardware module of 4K words of core {1000₁₆ words = 4096 words}. The change in core size necessitates changing the value of MSIZV4 in the skeleton from 7FFF₁₆ to 8FFF₁₆. With the addition of 4K of core there are now 3F03₁₆ words available for unprotected core and allocatable area 4 together. In this example the user wishes to maximize the unprotected area. He therefore sets N4 to the value 5DC₁₆. The previous value of BGNMON was 547D₁₆. The new value of BGNMON will be 547D₁₆ + 1000₁₆ - 9E6₁₆ = 5A97₁₆. The new value of ENDOV4 will be 5A96₁₆. 6-46G

6-47

In addition to the above skeleton changes, SYSMON and SYSDAY are updated. Using the revised skeleton, the FORTRAN binaries tape, the current install tape, and the binary cards for the revised SYSDAT, a new install tape is created by use of LIBILD.

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With the new install tape, a new system including double precision in both the foreground and background libraries may be built and verified. The procedure is described in the 1700 MSOS 4 Installation Handbook. An example of a similar installation showing typing on the comment device is found in Article 5.3.5. The first portion of the load map with FORTRAN double precision added is as follows:

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*V
 *S,SYSMCN,\$3131
 *S,SYSDAY,\$3136
 *S,SYSYER,\$3734
 *S,SYSLVL,\$3836
 *S,N4,\$05DC

1700 MASS STORAGE OPERATING SYSTEM - VER. 4.1

ANN#S TEST SYSTEM

*V
 *YM,LIBEDT,1
 *YM,LOADSD,2
 *YM,JOBENT,3
 *YM,JOBPRO,4
 *YM,PROTEC,5
 *YM,JFLOAD,6
 *YM,JFCHGE,7
 *YM,JPT13,8
 *YM,JCRDV4,9
 *YM,JLGOV4,10
 *YM,JPSTV4,11
 *YM,NAMEV4,12
 *YM,JFFLV4,13
 *YM,AFILV4,14
 *YM,RESTOR,15
 *YM,RCOVER,16
 *YM,BRKPT,17
 *YM,ODEBUG,18
 *YM,SYSCOP,19
 *YM,SYSSEG,20
 *YM,MIPRO,21
 *YM,TDFUNC,22
 *YM,EFSTOR,23
 *YM,EFLIST,24
 *YM,SCMM17,25
 *YM,VERIFY,26
 *YM,DUMMY1,27
 *YM,DUMMY2,28
 *YM,DUMMY3,29
 *YM,DUMMY4,30
 *YM,DUMMY5,31
 *YM,DUMMY6,32
 *YM,DUMMY7,33
 *YM,DUMMY8,34
 *YM,DUMMY9,35
 *YM,DUMMY0,36
 *S,MSIZV4,\$8FFF
 *S,ENDOV4,\$5A96
 *S,BGNMCN,\$5A97
 *S,SECTOR,\$7EDA

SYSTEM DATA PROGRAM

SYSDAT 0000 COPYRIGHT CONTRCL DATA CORPORATION 1973
SPACE REQUEST PROCESSOR

SPACE 123A DECK-1D 402 MSOS 4.1 SUMMARY-082

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* SYSTEM CORE RESIDENT PROGRAMS

*LP	MONITOR	DECK-ID	MSCS	FTN	RUNTIME	SUMMARY
	NMONI 5A97	DECK-ID A03	MSCS 4.1			SUMMARY-079
	RCISP 5ADB	DECK-ID A05	MSCS 4.1			SUMMARY-079
	RW 5C75	DECK-ID A06	MSCS 4.1			SUMMARY-079
	T14 5D30	DECK-ID A07	MSCS 4.1			SUMMARY-079
	T16 5D41	DECK-ID A08	MSCS 4.1			SUMMARY-079
	PARAME 5D4C	DECK-ID A09	MSCS 4.1			SUMMARY-079
	COMMON 5DBD	DECK-ID A10	MSCS 4.1			SUMMARY-079
	NIPROC 5DE4	DECK-ID A11	MSCS 4.1			SUMMARY-079
	ALVOL 5E6D	DECK-ID A13	MSCS 4.1			SUMMARY-079
	OFVCL 5E8A	DECK-ID A14	MSCS 4.1			SUMMARY-079
	ALCORE 5E96	DECK-ID A15	MSCS 4.1			SUMMARY-079
	DCORE 5F44	DECK-ID A16	MSCS 4.1			SUMMARY-079
	NFNR 60CE	DECK-ID A18	MSCS 4.1			SUMMARY-079
	NCMPRO 6140	DECK-ID A19	MSCS 4.1			SUMMARY-079
	MAKG 6170	DECK-ID A20	MSCS 4.1			SUMMARY-081
	ADEV 619E	DECK-ID A21	MSCS 4.1			SUMMARY-079
	TMINT 6306	DECK-ID A22	MSCS 4.1			SUMMARY-079
	DTIMER 639D	DECK-ID A23	MSCS 4.1			SUMMARY-079
	TCD 63BF	DECK-ID A24	MSCS 4.1			SUMMARY-079
	MINT 640E	DECK-ID A25	MSCS 4.1			SUMMARY-079
*LP	TRVEC 651A	DECK-ID D01	MSCS 4.1			SUMMARY-079
	DEBUGGING / CHECKOUT					
	SNAPOL 6562	DECK-ID H01	MSCS 4.1			SUMMARY-079
	DNP421 661B	DECK-ID H03	MSCS 4.1			SUMMARY-079
*LP	RCK85X 66E4	DECK-ID H05	MSCS 4.1			SUMMARY-079
	FILE MANAGER					
	FILMGR 672F	DECK-ID F01	MSCS 4.1			SUMMARY-081
	RSPCV4 6954	DECK-ID F02	MSCS 4.1			SUMMARY-079
*LP	SRHFIS 6A44	DECK-ID F03	MSCS 4.1			SUMMARY-079
	CORE RESIDENT DRIVERS					
	EFDATA 6BFD	DECK-ID C01	MSCS 4.1			SUMMARY-079
	DLMY 6D5B	DECK-ID C02	MSCS 4.1			SUMMARY-079
	ALAG 6D7E	DECK-ID C03	MSCS 4.1			SUMMARY-081
	D1711 6DEC	DECK-ID C05	MSCS 4.1			SUMMARY-082
	D1738 6F87	DECK-ID C08	MSCS 4.1			SUMMARY-081
	REWCK 71CC	DECK-ID C13	MSCS 4.1			SUMMARY-079
*LP	MMESEC 71EA	DECK-ID C15	MSCS 4.1			SUMMARY-081
	REENTRANT FORTRAN RUNTIME LIBRARY					
	FCRTR 735F	DECK-ID A01	3.2 FTN RUNTIME			SUMMARY-079
	QBPRMR 7491	DECK-ID B01	3.2 FTN RUNTIME			SUMMARY-079
	PARABR 74A8	DECK-ID B02	3.2 FTN RUNTIME			SUMMARY-081
	QBEXPR 74B9	DECK-ID B03	3.2 FTN RUNTIME			SUMMARY-079
	QBABR 756A	DECK-ID B04	3.2 FTN RUNTIME			SUMMARY-079
	SGRTFR 7581	DECK-ID B05	3.2 FTN RUNTIME			SUMMARY-079
	SIGNR 75DC	DECK-ID B06	3.2 FTN RUNTIME			SUMMARY-079
	FXFLR 7608	DECK-ID B07	3.2 FTN RUNTIME			SUMMARY-079
	FXPRGR 7673	DECK-ID B08	3.2 FTN RUNTIME			SUMMARY-079
	LNPRGR 7715	DECK-ID B09	3.2 FTN RUNTIME			SUMMARY-079
	TANHR 778F	DECK-ID B10	3.2 FTN RUNTIME			SUMMARY-079
	SNCSR 77FB	DECK-ID B11	3.2 FTN RUNTIME			SUMMARY-079
	ARCPGR 78C7	DECK-ID B12	3.2 FTN RUNTIME			SUMMARY-079
	TFALTR 7960	DECK-ID B13	3.2 FTN RUNTIME			SUMMARY-079
	FLOATR 7977	DECK-ID B14	3.2 FTN RUNTIME			SUMMARY-079
	QBQIOR 7B73	DECK-ID C01	3.2 FTN RUNTIME			SUMMARY-082

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BINARR	70D5	DECK-ID	C02	3.2	FTN	RUNTIME	SUMMARY-079
IOCCDR	7D22	DECK-ID	D01	3.2	FTN	RUNTIME	SUMMARY-079
INITLR	7D56	DECK-ID	D02	3.2	FTN	RUNTIME	SUMMARY-079
RSTORR	7D65	DECK-ID	D03	3.2	FTN	RUNTIME	SUMMARY-079
GETCHR	7D75	DECK-ID	D04	3.2	FTN	RUNTIME	SUMMARY-079
IRACKR	7D8F	DECK-ID	D05	3.2	FTN	RUNTIME	SUMMARY-079
UPDATR	7DC6	DECK-ID	D06	3.2	FTN	RUNTIME	SUMMARY-079
DECLPR	7DD3	DECK-ID	D07	3.2	FTN	RUNTIME	SUMMARY-079
INTGRR	7DF9	DECK-ID	D08	3.2	FTN	RUNTIME	SUMMARY-079
SPACER	7E26	DECK-ID	D09	3.2	FTN	RUNTIME	SUMMARY-079
HOLF	7E3E	DECK-ID	D10	3.2	FTN	RUNTIME	SUMMARY-079
DCHXR	7ED2	DECK-ID	D11	3.2	FTN	RUNTIME	SUMMARY-079
HXASCR	7F45	DECK-ID	D12	3.2	FTN	RUNTIME	SUMMARY-079
AFMTOR	7F98	DECK-ID	D13	3.2	FTN	RUNTIME	SUMMARY-079
RFMTOR	7FC2	DECK-ID	D14	3.2	FTN	RUNTIME	SUMMARY-079
AFMTIR	7FDB	DECK-ID	D15	3.2	FTN	RUNTIME	SUMMARY-079
RFMTIR	8009	DECK-ID	D16	3.2	FTN	RUNTIME	SUMMARY-079
ASCHXR	8020	DECK-ID	D17	3.2	FTN	RUNTIME	SUMMARY-079
HXDGR	805B	DECK-ID	D18	3.2	FTN	RUNTIME	SUMMARY-079
FLOTIR	80EB	DECK-ID	D19	3.2	FTN	RUNTIME	SUMMARY-079
FOUFR	8135	DECK-ID	D20	3.2	FTN	RUNTIME	SUMMARY-079
ECUTR	81BE	DECK-ID	D21	3.2	FTN	RUNTIME	SUMMARY-079
EWRTIR	82A6	DECK-ID	D22	3.2	FTN	RUNTIME	SUMMARY-079
INTIIR	82B2	DECK-ID	D23	3.2	FTN	RUNTIME	SUMMARY-079
FCRMTR	82CF	DECK-ID	D24	3.2	FTN	RUNTIME	SUMMARY-081
QBQFIR	84A4	DECK-ID	D25	3.2	FTN	RUNTIME	SUMMARY-079
QBQFLR	84BE	DECK-ID	D26	3.2	FTN	RUNTIME	SUMMARY-079
QBQFXR	84ED	DECK-ID	D27	3.2	FTN	RUNTIME	SUMMARY-079
HEXAR	8524	DECK-ID	D28	3.2	FTN	RUNTIME	SUMMARY-079
HEXDR	853C	DECK-ID	D29	3.2	FTN	RUNTIME	SUMMARY-079
ASCIIR	8559	DECK-ID	D30	3.2	FTN	RUNTIME	SUMMARY-079
DECHXR	856E	DECK-ID	D31	3.2	FTN	RUNTIME	SUMMARY-079
AFORMR	858E	DECK-ID	D32	3.2	FTN	RUNTIME	SUMMARY-079
RFORMR	85AA	DECK-ID	D33	3.2	FTN	RUNTIME	SUMMARY-079
FLOTGR	85C6	DECK-ID	D34	3.2	FTN	RUNTIME	SUMMARY-079
QBDBLR	85E2	DECK-ID	E01	3.2	FTN	RUNTIME	SUMMARY-082
DEXPR	85F7	DECK-ID	E02	3.2	FTN	RUNTIME	SUMMARY-079
QBDABR	8688	DECK-ID	E03	3.2	FTN	RUNTIME	SUMMARY-079
DSQRTR	86A6	DECK-ID	E04	3.2	FTN	RUNTIME	SUMMARY-079
DSIGNR	8726	DECK-ID	E05	3.2	FTN	RUNTIME	SUMMARY-079
SNGLR	8756	DECK-ID	E06	3.2	FTN	RUNTIME	SUMMARY-079
QBDFLR	876C	DECK-ID	E07	3.2	FTN	RUNTIME	SUMMARY-079
DEXPFR	8791	DECK-ID	E08	3.2	FTN	RUNTIME	SUMMARY-079
DLOGR	884D	DECK-ID	E09	3.2	FTN	RUNTIME	SUMMARY-079
DRSTRR	88F6	DECK-ID	E10	3.2	FTN	RUNTIME	SUMMARY-082
DSNCSR	892C	DECK-ID	E11	3.2	FTN	RUNTIME	SUMMARY-079
DATANR	8A3C	DECK-ID	E12	3.2	FTN	RUNTIME	SUMMARY-079
DFLOTR	8B0F	DECK-ID	E13	3.2	FTN	RUNTIME	SUMMARY-079
QBQDFR	8EAB	DECK-ID	E14	3.2	FTN	RUNTIME	SUMMARY-079
DCUTR	8EC7	DECK-ID	E15	3.2	FTN	RUNTIME	SUMMARY-079
NXTLOC	8FD1	NEXT AVAILABLE LOCATION					

*
*
*M
*M

SYSTEM MASS RESIDENT PROGRAMS

LIBEDT	0181	DECK-ID	D02	MSCS	4.1	SUMMARY-081
LOADSD			2			

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6.5 Modification of Background Library

6.5.1 Extracting Q8QI0 from Binaries File So That the Limited Input/Output Library May Be Used Optionally in the Background

This procedure assumes that the current FORTRAN background library includes modules with deck identifiers H01-H20 and J01-J26 as described in Table 2-1. In the strict sense this procedure is not a system customization since the system itself is not modified. The procedure described in this article shows how to exercise the runtime option of running a background FORTRAN program with the FORTRAN Limited Input/Output Library rather than the FORTRAN Input/Output Library. The basic method is to load the module Q8QI0 with the user's program so that when the user's program is linked to the background library routines, the entry points in Q8QI0 will be used first. This has the effect of making the FORTRAN Limited Input/Output Library the library used in executing the user's program.

The binary form of Q8QI0 will be available in the MSOS binaries if a FORTRAN compiler was included in the system at the time of MSOS 4.1 installation. If FORTRAN was added subsequent to MSOS installation the Q8QI0 binary will be found in the FORTRAN binaries file sent to the user at the time FORTRAN was added. If Q8QI0 has been modified since release, it will be included in an update file.

The Q7QI0 binary may be located within the appropriate binaries file by running the LISTR program with the binary file as input. This produces a list of the modules in the file and the number of records in each module. {Refer to the MSOS 4.1 Reference Manual}. Using this information the Q8QI0 binary may be extracted from the binaries file.

For example, suppose the relevant binaries file is the FORTRAN 3.2A binaries file. A portion of the LISTR output of that file is as follows:

6-51A

0329	Q8GGTR	0004	RECORDS	3938	TOTAL RECORDS..
0330	Q8GGTX	0004	RECORDS	3942	TOTAL RECORDS..
0331	Q8G10	0010	RECORDS	3952	TOTAL RECORDS..
0332	Q8GUNI	0006	RECORDS	3958	TOTAL RECORDS..
0333	Q8GX	0007	RECORDS	3965	TOTAL RECORDS..
0334	Q8RWBU	0010	RECORDS	3975	TOTAL RECORDS..
0335	Q8TRAN	0076	RECORDS	4051	TOTAL RECORDS..

The program LIBEDT may be used to extract the Q8Q10 binary from a binary file. If the above file is on tape, logical unit 6, and Q8Q10 is to be punched on cards on logical unit 11 the LIBEDT commands to perform this transfer are as follows:

```
*JOB
*LIBEDT
*T,6,B,2,B,3942
```

The above transfers 3942 record to the dummy device, logical unit 2, thus advancing the tape to the beginning of Q8Q10.

```
*T,6,B,11,B,10
```

The above transfers the 10 records in Q8Q10 to the card punch, logical unit 11.

The job setup for running a FORTRAN background program with the FORTRAN Limited Input/Output Library is shown in Figure B-1.

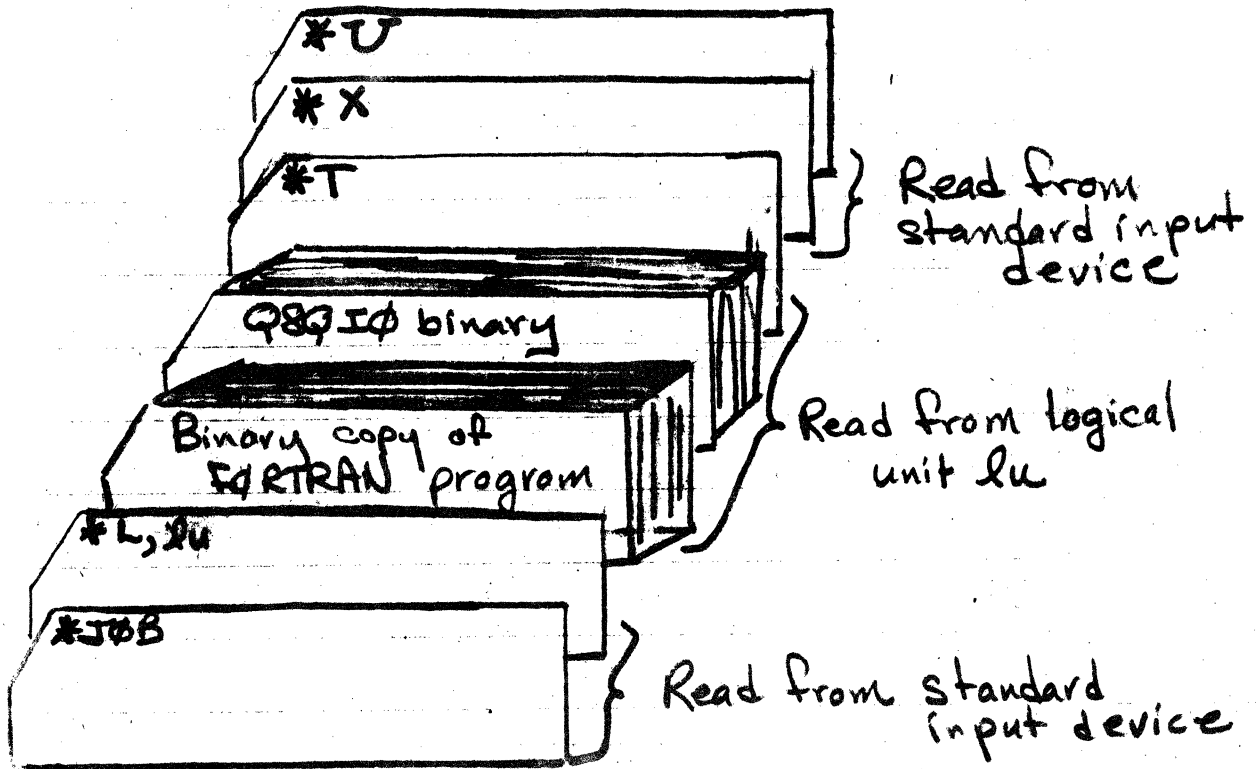


Figure B-1. Job Setup for Running Background FORTRAN program with Limited Input/Output Library.

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6-51C

The setup is the same whether cards or tape is used. The value of `lu` in the second record designates the logical unit from which the binary records and the `*T` record are to be read by the loader. The `*X` and `*U` records are read by the Job Processor. In the figure it is assumed that logical unit `lu` designates the standard input device, but this need not be the case if the binary information and the `*T` record are separated from the other control records. To run with the FORTRAN Input/Output Library, the `Q8QIO` binary is omitted.

6-51D

Consider now an example of a FORTRAN program run with the FORTRAN Input/Output Library and subsequently run with the FORTRAN Limited Input/Output Library. The FORTRAN source program and its compilation assembly list are as follows:

564 (6-51E)

C THIS PROGRAM MAY BE USED TO DEMONSTRATE EXECUTION WITH THE
C FORTRAN INPUT/OUTPUT LIBRARY AND EXECUTION WITH THE FORTRAN

C LIMITED INPUT/OUTPUT LIBRARY
DIMENSION IBUF(100)

C THE CALL TO SETBFR IS NECESSARY WHEN RUNNING WITH THE FORTRAN
C LIMITED INPUT/ OUTPUT LIBRARY

CALL SETBFR (IBUF,100)

C THE VALUE OF PI SHOULD BE PRINTED CORRECTLY BY BOTH THE FORTRAN

C INPUT/ OUTPUT LIBRARY AND THE FORTRAN LIMITED INPUT/ OUTPUT LIBRARY
PI =3.1415927

WRITE (12,100) PI

C THE VALUE OF TESTN WILL BE PRINTED CORRECTLY BY THE FORTRAN INPUT/

C OUTPUT LIBRARY, BUT NOT BY THE FORTRAN LIMITED INPUT/ OUTPUT LIBRARY
TESTN=600000.

WRITE (12,200) TESTN

100 FORMAT (10X,3FPI=,2X,F9.7)

200 FORMAT (* TESTN= *,F8.0)

END

0000	0000		NAM	QBQNAM
0000	186A	00001 QBQNAM	JMP#	.00002
0001	0064	IBUF	BSS	100
0065	0064	0064\$	CGN	100
0066	0002	PI	BSS	2
0068	0002	TESTN	BSS	2
006A	5802	.00002	RTJ#	.00006
006B	FF94		ADC	.00001
006C	0001	.00006	BSS	1
006D	C8FE		LDA#	.00006
006E	88FC		ADD#	.00006
006F	88FC		STA#	.00006
0070	5400		RTJ+	SETEFR
0071	7FFF			
0072	FF8E		ADC	IBUF
0073	FFF1		ADC	0064\$
0074	5400		RTJ+	DFLCT
0075	7FFF			
0076	5B40		CGN	23360
0077	001D		ADC	4164..
0078	5400		RTJ+	RSTCR1
0079	7FFF			
007A	FFEB		ADC	PI
007B	5400		RTJ+	QBQINI
007C	7FFF			
007D	7A00		CGN	31232
007E	0004		CGN	4
007F	000C		CGN	12
0080	8019		ADC	100
0081	5400		RTJ+	QBQX
0082	7FFF			
0083	FFE2		ADC	PI
0084	5400		RTJ+	QBQEND
0085	7FFF			
0086	5400		RTJ+	FLOT
0087	7FFF			

-1

965

6	0088	58D4		CGN	23508
	0089	000E		ADC	4A49.
	008A	7FDD		ADC	TESTN
	008B	5CF0		RTJ*	(Q8GINI)
	008C	7A00		CGN	31232
	008D	0006		CGN	6
	008E	000C		CGN	12
6	008F	8015		ADC	200
	0090	5CF1		RTJ*	(Q8GX)
6	0091	FFD5		ADC	TESTN
	0092	5CF2		RTJ*	(Q8GEND)
	0093	1810		JMP*	.00007
	0094	4164	4164..	CGN	16740
	0095	87ED		CGN	-30738
	0096	69FC		CGN	27132
	0097	4A49	4A49.	CGN	19017
	0098	3E00		CGN	15872
7	0099	000A	100	BSS	10
	0099	0099		ORG	100
	0099	2831		CGN	10289
	009A	3058		CGN	12376
	009B	2C33		CGN	11315
	009C	4850		CGN	18512
	009D	493D		CGN	18749
	009E	2C32		CGN	11314
	009F	582C		CGN	22572
	00A0	4639		CGN	17577
	00A1	2E37		CGN	11831
	00A2	2920		CGN	10528
	00A3	180A	.00007	JMP*	.00008
8	00A4	0009	200	BSS	9
	00A4	00A4		ORG	200
	00A4	2838		CGN	10296
	00A5	4820		CGN	18464
	00A6	5445		CGN	21573
	00A7	5354		CGN	21332
	00A8	4E3D		CGN	20029
	00A9	202C		CGN	8236
	00AA	4638		CGN	17576
	00AB	2E30		CGN	11824
9	00AC	2920		CGN	10528
	00AD	5400	.00008	RTJ+	Q8STP
9	00AE	7FFF			
	0000	0000		END	0

PROGRAM LENGTH \$00AF (175)

OPTS = RPAL

EXTERNALS

FLOT Q8STP Q8GINI Q8GX Q8GEND DFLOT RSTOR1
SETBFP

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Running this program with the FORTRAN Input/Output Library
{i.e., the usual way} produces a load map, entry point table,
and program output as follows:

L,10

X

Q8QNAM	2C9E						
FLOAT	2D4D	DECK-ID	G13	3.2	FTN	RUNTIME	SUMMARY-079
Q8QINI	2F9F	DECK-ID	F04	3.2	FTN	RUNTIME	SUMMARY-081
Q8QEND	3076	DECK-ID	F05	3.2	FTN	RUNTIME	SUMMARY-079
Q8CMP	3097	DECK-ID	F06	3.2	FTN	RUNTIME	SUMMARY-083
Q8RMBL	317B	DECK-ID	F07	3.2	FTN	RUNTIME	SUMMARY-079
Q8FRM	328C	DECK-ID	F08	3.2	FTN	RUNTIME	SUMMARY-079
Q8DFIC	335F	DECK-ID	F09	3.2	FTN	RUNTIME	SUMMARY-079
Q8QX	3425	DECK-ID	F10	3.2	FTN	RUNTIME	SUMMARY-079
Q8QUNI	348D	DECK-ID	F11	3.2	FTN	RUNTIME	SUMMARY-079
Q8FGET	34DB	DECK-ID	F12	3.2	FTN	RUNTIME	SUMMARY-079
Q8MAGT	3548	DECK-ID	F13	3.2	FTN	RUNTIME	SUMMARY-079
TAFCON	359C	DECK-ID	F14	3.2	FTN	RUNTIME	SUMMARY-081
PSSTOP	3644	DECK-ID	F16	3.2	FTN	RUNTIME	SUMMARY-079
Q8PAND	367B	DECK-ID	H17	3.2	FTN	RUNTIME	SUMMARY-083
Q8QGTX	36DD	DECK-ID	F20	3.2	FTN	RUNTIME	SUMMARY-079
DFICTN	36E5	DECK-ID	K12	3.2	FTN	RUNTIME	SUMMARY-079
DUMVOL	3AAF	DECK-ID	K13	3.2	FTN	RUNTIME	SUMMARY-079
DRSTOR	3ADF	DECK-ID	K14	3.2	FTN	RUNTIME	SUMMARY-079
Q8IFRM	3B17	DECK-ID	F01	3.2	FTN	RUNTIME	SUMMARY-079
Q8FS	3B56	DECK-ID	F02	3.2	FTN	RUNTIME	SUMMARY-079
Q8TRAN	3D8A	DECK-ID	F03	3.2	FTN	RUNTIME	SUMMARY-079
Q8EXP1	45D5	DECK-ID	F18	3.2	FTN	RUNTIME	SUMMARY-079
Q8FXP9	4653	DECK-ID	F19	3.2	FTN	RUNTIME	SUMMARY-079
Q8DXP1	4786	DECK-ID	K15	3.2	FTN	RUNTIME	SUMMARY-079
Q8DXP9	47A6	DECK-ID	K16	3.2	FTN	RUNTIME	SUMMARY-079
Q8PRMS	48AC	DECK-ID	G01	3.2	FTN	RUNTIME	SUMMARY-079

ENTRY POINT TABLE-

Q8DXP1	4706	Q8DXP2	4897	Q8QFLE	35E6	Q8QRCK	360C
Q8INTE	317B	Q8DXP9	47B4	Q8MAGT	3548	Q8FGET	34E4
Q8RMBL	31CB	FOF	35F5	Q8IGP	34DF	Q8BINB	3212
Q8CCMI	3665	DSTOP1	3ADF	DSTOP2	3B01	Q8EOTT	3582
Q8PSE	3644	Q8QX	3438	Q8QY	3431	Q8QZ	342B
Q8PAND	367B	Q8IRUF	322B	Q8EXP1	45D5	Q8EXP2	46FA
Q8CLR8	31A6	Q8EXP9	4661	RSTOR1	3AF1	Q8TRAN	4575
Q8PSEN	3649	Q8UNIT	2FF8	Q8EXPT	46F6	Q8STP	365B
FLOT	2D4F	AVOLR	3AB7	Q8MOVE	3476	Q8QTRM	343B
Q8STPN	3662	Q8QUN1	348D	Q8QUN2	3491	Q8QUN3	34A0
Q8QEND	3076	Q8QINI	2F9F	Q8DFIN	33DF	Q8QENS	3143
Q8QWNC	3603	Q8RFGH	31A3	AVOLA	3AAF	Q8DFNF	335F
Q8IFRM	3B46	RECEND	3144	Q8QNAM	2C9E	Q8FPUT	3516
Q8DFAC	3097	Q8RINT	31B5	Q8ERRM	328C	Q8FS	3D59
Q8CMP1	3099	Q8CMP1	30D0	WRFLG	31CA	Q8SKIP	306F
Q8FREM	32E3	Q8QTM	3425	Q8LOCE	319E	Q8FERM	32D6
Q8LCCF	34DC	Q8QGET	36DD	SETBFR	36E1	DFLOT	36F4
Q8DXPT	4894	Q8PREP	48AC	Q8PKUF	48B9		

PI= 3.1415927
 TESTN= 600000.
 STOP

When the same program is loaded with a Q8QIO binary the FORTRAN Limited Input/Output Library is used during execution, and output is as follows:

L,10							
X	QBQAM	2C9E					
	Q8QIO	2D4D	DECK-ID	L01	3.2	FTN RUNTIME	SUMMARY-079
	FLOAT	2E4E	DECK-ID	G13	3.2	FTN RUNTIME	SUMMARY-079
	PSSTOP	30A0	DECK-ID	F16	3.2	FTN RUNTIME	SUMMARY-079
	PRPAND	30D7	DECK-ID	J17	3.2	FTN RUNTIME	SUMMARY-083
	PSUEDC	3139	DECK-ID	J02	3.2	FTN RUNTIME	SUMMARY-079
	FORMIN	315E	DECK-ID	J23	3.2	FTN RUNTIME	SUMMARY-079
	DFLECTN	333A	DECK-ID	K12	3.2	FTN RUNTIME	SUMMARY-079
	DUMVOL	3704	DECK-ID	K13	3.2	FTN RUNTIME	SUMMARY-079
	DKSTOR	3734	DECK-ID	K14	3.2	FTN RUNTIME	SUMMARY-079
	DOUIN	376C	DECK-ID	K18	3.2	FTN RUNTIME	SUMMARY-079
	IGFTCH	3877	DECK-ID	J03	3.2	FTN RUNTIME	SUMMARY-079
	IPACK	388E	DECK-ID	J04	3.2	FTN RUNTIME	SUMMARY-079
	UPDATA	38C8	DECK-ID	J05	3.2	FTN RUNTIME	SUMMARY-079
	DFCEL	38D2	DECK-ID	J06	3.2	FTN RUNTIME	SUMMARY-079
	INTER	38FF	DECK-ID	J07	3.2	FTN RUNTIME	SUMMARY-079
	SPACEN	3915	DECK-ID	J08	3.2	FTN RUNTIME	SUMMARY-079
	HOLRTH	3928	DECK-ID	J09	3.2	FTN RUNTIME	SUMMARY-079
	DCHX	39AB	DECK-ID	J10	3.2	FTN RUNTIME	SUMMARY-079
	HXASC	3A1B	DECK-ID	J11	3.2	FTN RUNTIME	SUMMARY-079
	AFFMOT	3A67	DECK-ID	J12	3.2	FTN RUNTIME	SUMMARY-079
	RFRMOT	3A8A	DECK-ID	J13	3.2	FTN RUNTIME	SUMMARY-079
	AFFMIN	3A9E	DECK-ID	J14	3.2	FTN RUNTIME	SUMMARY-079
	RFRMIN	3ACA	DECK-ID	J15	3.2	FTN RUNTIME	SUMMARY-079
	ASCHX	3ADC	DECK-ID	J16	3.2	FTN RUNTIME	SUMMARY-079
	HXDC	3B10	DECK-ID	J17	3.2	FTN RUNTIME	SUMMARY-079
	FLOTIN	3B99	DECK-ID	J18	3.2	FTN RUNTIME	SUMMARY-079
	FOUT	3BDE	DECK-ID	J19	3.2	FTN RUNTIME	SUMMARY-079
	EOUT	3C64	DECK-ID	J20	3.2	FTN RUNTIME	SUMMARY-079
	FWRITE	3D4D	DECK-ID	J21	3.2	FTN RUNTIME	SUMMARY-079
	Q8QFI	3D5C	DECK-ID	J24	3.2	FTN RUNTIME	SUMMARY-079
	Q8QFL	3D72	DECK-ID	J25	3.2	FTN RUNTIME	SUMMARY-079
	Q8QFX	3D9D	DECK-ID	J26	3.2	FTN RUNTIME	SUMMARY-079
	Q8QDFN	3DCD	DECK-ID	K17	3.2	FTN RUNTIME	SUMMARY-079

E10
BINADY

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NOTE: This message indicates that BINARY is an unpatched external. When running with QBQIO it is necessary at this point to type * and press carriage return on the comment device. This causes the loader to ignore the unpatched external, BINARY, and to continue with the entry point table and program execution.

ENTRY	PC	INIT	TABLE-					
QBQFL	3072		DECPL	38D2	ASCHX	3ADC	QUOTE	3967
AFRMIN	3A9E		ISAVE	3138	QBQCOMI	30C1	DSTOR1	3734
DSTCR2	3756		RFRMOT	3A8A	QBQSE	30A0	QBQX	2DE2
INTGR	38EF		QBQAND	30D7	DGUT	376D	RSTOR1	3746
QBQSEN	30A5		QBQFI	3D5C	QBQSTP	30B7	DCHX	39AB
FLOT	2E50		AVOLR	370C	QBQSTAN	30BE	EWRITE	3D4D
AFRROT	3A67		SPACEX	3915	IPACK	388E	QBQEND	2E31
EOUT	3C65		QBQINI	2D82	HxASC	3A1B	QBQGET	2E3E
FLOTIN	3B99		GETCH	3880	CHCNT	315F	AVOLA	3704
QBQNAM	2C9E		IGETCH	3877	FGRMTR	3160	QBQFX	3D9D
FOUT	3BDF		QBQDFI	3DCD	HGLRTH	3928	DFLOT	3349
UPDATE	38C8		SETBFR	2D4D	RFRMIN	3ACA	IOERR	2E48
COMMON	3139		HXDC	3B10				

TESTN= *****
PI= 3.1415925
STOP

6.5.2 Making the Limited Input/Output Library the Only FORTRAN Background Library

The procedure described in this article is one which would not be performed by most users. The main reason a user might wish to make the Limited Input/Output Library the only FORTRAN background library would be that his only use of FORTRAN in the background would be to simulate the foreground. The reason this procedure is not often used is that while it does provide for simulation of the foreground FORTRAN library, it does not simulate other foreground conditions. The procedure is included for the sake of completeness.

To make the Limited Input/Output Library the only FORTRAN background library available, the skeleton must be modified so that those modules listed in Group V of Article 2.1 together with QBQIO are included in the FORTRAN background library. The modules in Group VI of Article 2.1 may be optionally included. Either Group W or the module DBLDY must be included. For this customization a module included in the FORTRAN background library must be in Group V, Group VI, or Group W or be QBQIO or DBLDY.

An example of that portion of the skeleton including the FORTRAN Limited Input/Output Library follows. In this example Group VI

```

*L,ENGINE is included; Group W. is not.
*B *INCOE# * DECK-ID J01 3.2 FTN RUNTIME#
*L,COMMON
*B *PSUEDO# * DECK-ID J02 3.2 FTN RUNTIME#
*L,IGETCH
*B *IGETCH# * DECK-ID J03 3.2 FTN RUNTIME#
*L,IPACK
*B *IPACK# * DECK-ID J04 3.2 FTN RUNTIME#
*L,UPDATE
*B *UBCATN# * DECK-ID J05 3.2 FTN RUNTIME#
*L,DECL
*B *DECL# * DECK-ID J06 3.2 FTN RUNTIME#
*L,INTGR
*B *INTGR# * DECK-ID J07 3.2 FTN RUNTIME#
*L,SPACEX
*B *SPACEN# * DECK-ID J08 3.2 FTN RUNTIME#
*L,HOLRTH
*B *HOLRTH# * DECK-ID J09 3.2 FTN RUNTIME#
*L,DCHX
*B *DCHX# * DECK-ID J10 3.2 FTN RUNTIME#
*L,HXASC
*B *HXASC# * DECK-ID J11 3.2 FTN RUNTIME#
*L,AERMCT
*B *AERMCT# * DECK-ID J12 3.2 FTN RUNTIME#

```

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*L,RFRMCT
*B #RFRMCT# # DECK-ID J13 3.2 FTN RUNTIME#
*L,AFRMIN
*B #AFRMIN# # DECK-ID J14 3.2 FTN RUNTIME#
*L,RFRMIN
*B #RFRMIN# # DECK-ID J15 3.2 FTN RUNTIME#
*L,ASCHX
*B #ASCHX# # DECK-ID J16 3.2 FTN RUNTIME#
*L,HXDC
*B #HXDC# # DECK-ID J17 3.2 FTN RUNTIME#
*L,FLOTIN
*B #FLOTIN# # DECK-ID J18 3.2 FTN RUNTIME#
*L,FOUT
*B #FOUT# # DECK-ID J19 3.2 FTN RUNTIME#
*L,EOUT
*B #EOUT# # DECK-ID J20 3.2 FTN RUNTIME#
*L,EWRITE
*B #EWRITE# # DECK-ID J21 3.2 FTN RUNTIME#
*L,INITL1
*B #INITL1# # DECK-ID J22 3.2 FTN RUNTIME#
*L,FORMTR
*B #FORMTR# # DECK-ID J23 3.2 FTN RUNTIME#
*L,QBQFI
*B #QBQFI# # DECK-ID J24 3.2 FTN RUNTIME#
*L,QBQFL
*B #QBQFL# # DECK-ID J25 3.2 FTN RUNTIME#
*L,QBQFX
*B #QBQFX# # DECK-ID J26 3.2 FTN RUNTIME#
*L,HEXASC
*B #HEXASC# # DECK-ID J27 3.2 FTN RUNTIME#
*L,HEXDEC
*B #HEXDEC# # DECK-ID J28 3.2 FTN RUNTIME#
*L,ASCTI
*B #ASCTI# # DECK-ID J29 3.2 FTN RUNTIME#
*L,DECHEX
*B #DECHEX# # DECK-ID J30 3.2 FTN RUNTIME#
*L,AFORM
*B #AFORM# # DECK-ID J31 3.2 FTN RUNTIME#
*L,RFORM
*B #RFORM# # DECK-ID J32 3.2 FTN RUNTIME#
*L,FLOATG
*B #FLOATG# # DECK-ID J33 3.2 FTN RUNTIME#

*L,QBQINI
*B #QBQINI#
*L,QBQXFI
*B #QBQXFI# # DECK-ID K19 3.2 FTN RUNTIME#

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The module Q8QI0 is available from the MS0S binaries file if FORTRAN was part of the initial MS0S 4.1 installation. Otherwise Q8QI0 is available from the FORTRAN binaries file. Using the modified skeleton the appropriate binaries file, and the current install file, a new install file may be created by using LIBILD. The new install file may then be used to build and verify a new system, as described in the 1700 MS0S 4 Installation Handbook.

6.5.3 Making the Limited Input/Output Library the Principal FORTRAN Background Library with the Input/Output Library Available Optionally

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For this customization the skeleton must be modified so that the modules with the following deck identifiers form the core of the FORTRAN background library:

- H01-H03
- H06-H09
- H11-H19
- J01-J26
- H21

K01-K18 or K19

In addition the modules with the following deck identifiers optionally:

J27-J33

Q8QI0 is available in the MS0S binaries file if FORTRAN was included in the initial MS0S 4.1 installation. Otherwise it is available in the FORTRAN binaries file sent to the user at the time FORTRAN was added. Using the revised skeleton, the appropriate binaries file, and the current install file, a new install file may be created by the use of LIBILD. The new install file may then be used to build a new system using the procedure described in the 1700 MS0S 4 Installation Handbook. {Refer also to Article 2.1 for more information on this customization}.

CHAPTER 7 - INSTALLATION OF PSR CORRECTIVE CODE

An MSOS corrective code file is a set of programs in binary form such that each program in the file has been modified by Control Data since the initial issuance of MSOS 4. A binary in the update file is the new modified version of the program. Associated with each file is a PSR level. A file for a given PSR level contains the modified version of each program having a PSR at that level as well as the modified version of each program having a PSR which was written subsequent to the corrective code release. For example, the MSOS update file for PSR Level 86 contains those modules with PSR's at levels 82-85 which were written after MSOS 4.1 release, as well as those modules with PSR's at Level 86. An MSOS file may be on cards, magnetic tape, or paper tape. The file media should correspond to the user's installation medium. PSR corrective code is released periodically.

A FORTRAN corrective code file is defined similarly to an MSOS file. A given FORTRAN update file will apply to either the FORTRAN 3A compiler or to the FORTRAN 3B compiler. The user should obtain the FORTRAN file corresponding to the particular compiler he has, if his system includes FORTRAN. This file will include any FORTRAN library module updates as well as the compiler updates. As

for an MSOS file, FORTRAN files are issued periodically by Control Data, and each FORTRAN update file contains preceding as well as current updates. A FORTRAN update file sent to a user may be on cards, magnetic tape, or paper tape.

7.1 Updating a System Using Cards

If the installation medium is cards, the user receives an MSOS file on cards and either a FORTRAN 3.2A update file or a FORTRAN 3.2B update file on cards if his system includes FORTRAN. For each binary program in the received, the user must find the module with the same name in the current install file. He then deletes the binary cards for this module from the current install file, replacing them with the binary cards for the updated module. By running the LISTR program with the update cards as input, a list may be obtained of the name of each updated module and the number of cards in each module. {Refer to the MSOS 4.1 Reference Manual}. This information is helpful in separating the binary decks in the update file. For further information on identifying a binary deck, refer to the MSOS 4.1 Reference Manual, Section 4.3, Card Reader/Punch Drivers.

In addition to replacing current binaries by updated binaries, it is necessary to change the *S system initializer control statements in

the install file which define SYSLVL, SYSMON, SYSDAY, and SYSYER.
{Refer to Article 2.2}

Using the new install file, the updated system may be built according to the procedure described in the MSOS 4.1 Installation Handbook. Should the system initializer print an error message indicating core has been exceeded, this may be due to an increase in size in one or more updated core resident modules. In this case BGNMON and ENDOV4 should be decreased. {Refer to Article 2.2.}

7.2 Updating a System Using Magnetic Tape or Paper Tape

When using tape, a skeleton corresponding to the latest install file must first be obtained. This may be done by using the program SKED, as shown in the example in Article 7.3. The skeleton should be modified to change the *S system initializer control statement defining SYSLVL, SYSMON, SYSDAY, and SYSYER. Again, the program SKED may be used. The procedure is then to use LIBILD to create a new install file in which the updated binaries from the update tape{s} received replace the corresponding modules in the current install file. This is done by first presenting the update tapes as library input to LIBILD followed by the current install tape. {Refer to Article 7.3.} With the new install tape, the updated system may be built by following the procedure in the MSOS Installation Handbook. As noted in Article 7.1, a system initializer error message may mean

that ENDDV4 and BGNMON must be decreased.

7.3 Example of System Update Using Magnetic Tape

In this example magnetic tape is the installation medium. It should be noted that the update procedure for paper tape is essentially the same.

In this example the installed system includes the FORTRAN 3.2B compiler. Therefore, the user received two update tapes, one for MSOS 4.1 and one for FORTRAN 3.2 B.

The procedure is as described in Article 7.2. A skeleton is obtained from the install file and modified by using SKED. The latest install file is mounted on tape unit D, logical unit B. In this example the system has been updated or customized since initial installation and thus the install file is the first and only file on this tape. A scratch tape is mounted on tape unit 1, logical unit 1B. The typing on the teletype is as follows:

```
III  
*BATCH  
L,10 FAILED 02  
ACTION  
CU  
J  
*JOB  
J  
*SKED  
SKED IN  
  
NEXT  
CAT
```


577

The BUILD command builds the skeleton from the install file. The CATLOG command lists the skeleton records. The first ten records of the skeleton are as follows:

```
1 *S,SYSMON,$3036
2 *S,SYSDAY,$3135
3 *S,SYSYER,$3734
4 *S,SYSLVL,$3833
5 *V
6 *V          1700 MASS STORAGE OPERATING SYSTEM - VER. 4.1
7 *V
8 *V          1700 + 3 -- 32K SYSTEM
9 *V
10 *YM,LIBEDT,1
```

The PSR level is to be changed from 83 to 87. The date of system build is to be changed from 6/15/74 to 10/29/74. The following commands to SKED are used to make these changes.

```
NEXT
INSERT,3,4
*S,SYSLVL,$3837
*S,SYSMON,$3130
*S,SYSDAY,$3239
```

```
NEXT
DELETE,1,2
```

```
NEXT
DELETE,4,4
```

```
NEXT
DUMP,16
```

```
NEXT
EXIT
```

```
J
```

578

The dump command results in the revised skeleton being listed on the line printer and being dumped on tape unit one. The first ten records of the revised skeleton are as follows:

```
1 *S,SYSYER,$3734
2 *S,SYSLVL,$3837
3 *S,SYSMON,$3130
4 *S,SYSDAY,$3239
5 *V
6 *V      1700 MASS STORAGE OPERATING SYSTEM - VER. 4.1
7 *V
8 *V      1700 ↑ 3 -- 32K SYSTEM
9 *V
10 *YM,LIBEDT,1
```

The second step is to use LIBILD to generate an updated install file. A scratch tape is mounted on tape unit 1, logical unit 16. There are three input library tapes, the MSOS 4.1 update tape, the FORTRAN 3.2B update tape, and the most recent install file. The update tapes must be read first. This is because when LIBILD encounters more than one binary copy of a program specified by the skeleton, it is the first binary that is placed in the install file. The MSOS 4.1 library tape originally sent to the user as a part of the installation materials may be used as LIBILD input instead of the current install file, but then a fourth input library, a binary of the current SYSDAT, must also be input to LIBILD. The three input library tapes are to be read from tape unit 0, logical unit 6. Typing on the teletype proceeds as follows:

579

*LIBILD
CONTROL LU =
DEFS LU =
INSTALL LU = 16
NEWLIB LU =
LIB 01 LU = 6
LIB 02 LU = 6
LIB 03 LU = 6
LIB 04 LU =
SKELETON LU = 6

LOAD LIBRARY INPUT 02 ON LU 06. CR WHEN READY.

LOAD LIBRARY INPUT 03 ON LU 06. CR WHEN READY.

LOAD SKEL/INSTAL. CR WHEN READY

LIBRARY BUILD COMPLETE
TYPE *Z TO TERMINATE OR
TYPE *C TO CONTINUE WITH CURRENT SKELETON AND/OR
OUTPUT LIBRARY LU'S *Z

J
*REW.16.6
J

The third step is to use the updated install file to build a new system including the updates. The procedure is as described in the 1700 MS0S 4 Installation Handbook. Typing on the teletype continues as follows:

*SILP
THE INITIALIZER WILL BE MOVED TO LOCATION 50FF AND EXECUTED

TURN OFF PROTEC SWITCH AND TYPE CARRIAGE RETURN

580

At this point the disk pack on disk unit 0 is removed. A scratch pack is mounted. The new system will be built on the scratch pack. After entering a carriage return typing on the teletype continues.

MSOS 4.1 SYSTEM INITIALIZER
FWA OF CONTRL = 5CFF

DATE MM/DD/YY
10/29/74

Q
*C,7
*C,7

Q
*V
INITIALIZATION COMPLETED - YOU MAY AUTOLOAD

MSOS 4.1--PSR LEVEL 87 10/29/74

SET PROGRAM PROTECT

1700 # 3 -- 32K SYSTEM

65K MODE

CHECKING FILES - OK

ENTER DATE/TIME MMDDYYHHMM
1029741050
DATE: 29 OCT 74 TIME: 1050:00

581

MI

*BATCH

*CTO, ISOS 4.1 INSTALLATION COMPLETED - YOU MAY AUTOLOAD

ISOS 4.1--PSR LEVEL 87 10/29/74

SET PROGRAM PROTECT

1700 # 3 -- 32K SYSTEM

65K MODE

CHECKING FILES - OK

ENTER DATE/TIME INDDYYHHMM

1029741118

DATE: 29 OCT 74 TIME: 1118:00



Chapter 8. Addition of a User-Written Core Resident Application Program.

In general it is not necessary to modify SYSDAT when adding a core resident application program. The skeleton must be modified to include the necessary control records to load the core resident program. The program to be added may be loaded in either Part 0 or Part 1. If loaded in Part 0 and unprotected core is also in Part 0, the program to be added must be loaded between SYSDAT and SPACE. This insures that protected allocatable core is contiguous with unprotected core, thus permitting core swaps when necessary. If loaded in Part 1, the program to be added should be inserted just before the dummy module NXTLOC. If the new program is loaded in Part 1 it will usually be necessary to modify the skeleton records which define ENDOV4 and BGNMON. This will necessitate changing the value of N4 in some cases.

If the core resident program is to be a system library program a *Y record must be added to the skeleton so that the added program will be included in the system library directory. The possible advantages of including a core resident program in the system library are {1} the program may be accessed by a monitor schedule request and {2} LIBEDT may be used to replace the core resident program by a new version of the program so long as the length of the revised program does not exceed the length of the original ordinal loaded into core.

The following example illustrates the addition of a core resident program to the system library. The base system in this example is the system built in Article 6.4.3. The source and assembly list of the program to be added are as follows:

583

```

1      PROGRAM CORESO
2      C THIS PROGRAM MAY BE USED TO DEMONSTRATE THE LOADING OF A CORE
3      C RESIDENT ORDINAL INTO THE SYSTEM LIBRARY
4      DIMENSION IBUF (38)
5      CALL SETBFR(IBUF,38)
6      WRITE (4,100)
100   FORMAT ( * HELLO THIS IS CORESO * )
      END

```

Address	Hex	Label	Value
0000	0000	NAM	CORESO
0000	1828	CORESO	JMP# .00001
0001	0026	IBUF	BSS 38
0027	0026	0026\$	CGN 38
0028	5400	.00001	RTJ+ SETBFR
0029	7FFF		
002A	0001	P	ADC IBUF
002B	0027	P	ADC 0026\$
002C	5400		RTJ+ QBQINI
002D	7FFF		
002E	7200		CGN 29184
002F	0004		CGN 4
0030	0004		CGN 4
0031	0033	P	ADC 100
0032	180F		JMP# .00004
0033	000E	100	BSS 14
0033	0033		ORG 100
0033	2832		CGN 10290
0034	3248		CGN 12872
0035	2048		CGN 8264
0036	454C		CGN 17740
0037	4C4F		CGN 19535
0038	2054		CGN 8276
0039	4849		CGN 18505
003A	5320		CGN 21280
003B	4953		CGN 18771
003C	2043		CGN 8259
003D	4F52		CGN 20306
003E	4553		CGN 17747
003F	4F20		CGN 20256
0040	2920		CGN 10528
0041	5400	.00004	RTJ+ QBSTP
0042	7FFF		
0000	0000		END 0

PROGRAM LENGTH \$0043 (67)

END = PAL

EXTERNALS
QBSTP QBQINI SETBFR

The user wishes to load this program in Part 1.

The first part of the revised skeleton is shown in Figure 8-1. Skeleton records 165 and 166 are included to load CORES0 as a Part 1 core resident program.

The length of this program is 43_{16} words, meaning that the new value of BGNMON will be $5A97_{16} - 43_{16} = 5A54_{16}$, and the new value of ENDOV4 is $5A96_{16} - 43_{16} = 5A53_{16}$. The decrease of 43_{16} words available to unprotected and allocatable area 4 together still allow area 4 to remain at $5DC_{16}$ words, since a decrease of 43_{16} in unprotected core leaves $3927_{16} - 43_{16} = 38E4_{16}$ words of unprotected. This is ample for the needs of the system. Skeleton records 49 and 50 show the revised definitions of ENDOV4 and BGNMON.

The program added is to be a system library program. Therefore a *Y system initializer record must be added for this program. This record must precede the *YM records for mass memory resident system library programs. The *Y record will have the form *Y,CORES0,n. The value of n defined as the total number of *L and *LP records preceding the *B record for CORES0. This includes the *LP record immediately preceding the *B record for CORES0. Referring to Figure 8-1, the proper value for n in this example is 8. The *Y record for CORES0 is skeleton record 11. The core resident ordinals and mass resident ordinals are numbered independently. Therefore, there is no need to modify the *YM records in the skeleton. The LIBEDT *S control records, skeleton records 393 through 428, need not be modified since they only pertain to the setting of core request priorities for mass resident ordinals. If, however, the user plans to schedule the core resident ordinal by means of a monitor indirect scheduler request, pointing to the system library directory entry as the parameter list, he must set up the request priority within the directory.

(595)

```

1  #S,SYSMON,$3131
2  #S,SYSDAY,$3233
3  #S,SYSYER,$3734
4  #S,SYSLVL,$3836
5  #S,N4,$05DC
6  #V
7  #V      1700 MASS STORAGE OPERATING SYSTEM - VER. 4.1
8  #V
9  #V      ANNAS TEST SYSTEM
10 #V
11 #Y,CORES0,8
12 #YM,LIBEDT,1
13 #YM,LOADSD,2
14 #YM,JOCHENT,3
15 #YM,JOHFRQ,4
16 #YM,PROTEC,5
17 #YM,JPLCAD,6
18 #YM,JPCFGE,7
19 #YM,JPT13,8
20 #YM,JCRDV4,9
21 #YM,JLGOV4,10
22 #YM,JPSTV4,11
23 #YM,NAMEV4,12
24 #YM,JPFLV4,13
25 #YM,AFILV4,14
26 #YM,RESTOR,15
27 #YM,PCOVER,16
28 #YM,BRKET,17
29 #YM,ODEBUG,18
30 #YM,SYSCOP,19
31 #YM,SYSSEG,20
32 #YM,MIPK0,21
33 #YM,TOFLNC,22
34 #YM,FFSTOR,23
35 #YM,FFLIST,24
36 #YM,SCMM17,25
37 #YM,VERIFY,26
38 #YM,DUMMY1,27
39 #YM,DUMMY2,28
40 #YM,DUMMY3,29
41 #YM,DUMMY4,30
42 #YM,DUMMY5,31
43 #YM,DUMMY6,32
44 #YM,DUMMY7,33
45 #YM,DUMMY8,34
46 #YM,DUMMY9,35
47 #YM,DUMMY0,36
48 #S,MSIZV4,$8FFF
49 #S,FND0V4,$5A53
50 #S,PGNMGN,$5A54
51 #S,SECTOR,$7FCA
52 *
53 #L      SYSTEM DATA PROGRAM
54 #P #SYSDAT# # COPYRIGHT CONTROL DATA CORPORATION 1973#
55 #L      SPACE REQUEST PROCESSOR
56 #E #SPACE# # DECK-ID A02 MSOS 4.1#
57 *
58 *      SYSTEM CORE RESIDENT PROGRAMS

```

Figure B-1 Partial List of Skeleton for Addition of Core Resident ordinal.

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59	*								
60	*LP	MONITOR							
61	*E	*NMONI*	*	DECK-ID	A03	MSOS	4.1*		
62	*E	*RDISP*							
63	*E	*RW*	*	DECK-ID	A06	MSOS	4.1*		
64	*E	*T14*	*	DECK-ID	A07	MSOS	4.1*		
65	*E	*T16*	*	DECK-ID	A08	MSOS	4.1*		
66	*E	*PARAM*	*	DECK-ID	A09	MSOS	4.1*		
67	*E	*COMMON*	*	DECK-ID	A10	MSOS	4.1*		
68	*E	*NIPROC*	*	DECK-ID	A11	MSOS	4.1*		
69	*E	*ALVOL*	*	DECK-ID	A13	MSOS	4.1*		
70	*E	*OFVOL*	*	DECK-ID	A14	MSOS	4.1*		
71	*E	*ALCORE*	*	DECK-ID	A15	MSOS	4.1*		
72	*E	*DCORE*	*	DECK-ID	A16	MSOS	4.1*		
73	*E	*NFNR*	*	DECK-ID	A18	MSOS	4.1*		
74	*E	*NCONPRQ*	*	DECK-ID	A19	MSOS	4.1*		
75	*E	*MAKQ*	*	DECK-ID	A20	MSOS	4.1*		
76	*E	*ADEV*	*	DECK-ID	A21	MSOS	4.1*		
77	*E	*TMINT*	*	DECK-ID	A22	MSOS	4.1*		
78	*E	*DTIMER*	*	DECK-ID	A23	MSOS	4.1*		
79	*E	*TOD*	*	DECK-ID	A24	MSOS	4.1*		
80	*E	*MINT*	*	DECK-ID	A25	MSOS	4.1*		
81	*E	*TRVEC*	*	DECK-ID	D01	MSOS	4.1*		
82	*LP	DEBUGGING / CHECKOUT							
83	*E	*SNAPOL*	*	DECK-ID	F01	MSOS	4.1*		
84	*E	*DMP421*	*	DECK-ID	F03	MSOS	4.1*		
85	*E	*RDK85X*	*	DECK-ID	F05	MSOS	4.1*		
86	*LP	FILE MANAGER							
87	*E	*FILMGR*	*	DECK-ID	F01	MSOS	4.1*		
88	*E	*RSPCV4*	*	DECK-ID	F02	MSOS	4.1*		
89	*E	*SRFIS*	*	DECK-ID	F03	MSOS	4.1*		
90	*LP	CORE RESIDENT DRIVERS							
91	*E	*EFDATA*	*	DECK-ID	C01	MSOS	4.1*		
92	*E	*DUMMY*	*	DECK-ID	C02	MSOS	4.1*		
93	*E	*ALAQ*	*	DECK-ID	C03	MSOS	4.1*		
94	*E	*D1711*	*	DECK-ID	C05	MSOS	4.1*		
95	*E	*D1738*	*	DECK-ID	C08	MSOS	4.1*		
96	*E	*REWCK*	*	DECK-ID	C13	MSOS	4.1*		
97	*E	*MMEXEC*	*	DECK-ID	C15	MSOS	4.1*		
98	*LP	REENTRANT FORTRAN RUNTIME LIBRARY							
99	*E	*FORTR*	*	DECK-ID	A01	3.2	FTN	RUNTIME*	
100	*E	*Q8PRMR*	*	DECK-ID	B01	3.2	FTN	RUNTIME*	
101	*E	*PARABR*	*	DECK-ID	B02	3.2	FTN	RUNTIME*	
102	*E	*Q8EXPR*	*	DECK-ID	B03	3.2	FTN	RUNTIME*	
103	*E	*Q8ABR*	*	DECK-ID	B04	3.2	FTN	RUNTIME*	
104	*E	*SQRTFR*	*	DECK-ID	B05	3.2	FTN	RUNTIME*	
105	*E	*SIGNR*	*	DECK-ID	B06	3.2	FTN	RUNTIME*	
106	*E	*FXFLR*	*	DECK-ID	B07	3.2	FTN	RUNTIME*	
107	*E	*EXPRGR*	*	DECK-ID	B08	3.2	FTN	RUNTIME*	
108	*E	*LNPRGR*	*	DECK-ID	B09	3.2	FTN	RUNTIME*	
109	*E	*TANHR*	*	DECK-ID	B10	3.2	FTN	RUNTIME*	
110	*E	*SNGSR*	*	DECK-ID	B11	3.2	FTN	RUNTIME*	
111	*E	*AREPGR*	*	DECK-ID	B12	3.2	FTN	RUNTIME*	
112	*E	*IFALTR*	*	DECK-ID	B13	3.2	FTN	RUNTIME*	
113	*E	*FLOATR*	*	DECK-ID	B14	3.2	FTN	RUNTIME*	
114	*E	*Q8QIOR*	*	DECK-ID	C01	3.2	FTN	RUNTIME*	
115	*E	*BINARR*	*	DECK-ID	C02	3.2	FTN	RUNTIME*	
116	*E	*IOCODR*	*	DECK-ID	D01	3.2	FTN	RUNTIME*	
117	*E	*INITLR*	*	DECK-ID	D02	3.2	FTN	RUNTIME*	
118	*E	*RSTORR*	*	DECK-ID	D03	3.2	FTN	RUNTIME*	

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119	*E	*GETCHR*	* DECK-ID D04	3.2	FTN	RUNTIME*
120	*E	*IPACKR*	* DECK-ID D05	3.2	FTN	RUNTIME*
121	*E	*UPDATR*	* DECK-ID D06	3.2	FTN	RUNTIME*
122	*E	*DEGPLR*	* DECK-ID D07	3.2	FTN	RUNTIME*
123	*E	*INTGRR*	* DECK-ID D08	3.2	FTN	RUNTIME*
124	*E	*SPACER*	* DECK-ID D09	3.2	FTN	RUNTIME*
125	*E	*HOLR*	* DECK-ID D10	3.2	FTN	RUNTIME*
126	*E	*DCFXR*	* DECK-ID D11	3.2	FTN	RUNTIME*
127	*E	*HXASCR*	* DECK-ID D12	3.2	FTN	RUNTIME*
128	*E	*AFMTR*	* DECK-ID D13	3.2	FTN	RUNTIME*
129	*E	*RFMTR*	* DECK-ID D14	3.2	FTN	RUNTIME*
130	*E	*AFMTR*	* DECK-ID D15	3.2	FTN	RUNTIME*
131	*E	*RFMTR*	* DECK-ID D16	3.2	FTN	RUNTIME*
132	*E	*ASGHXR*	* DECK-ID D17	3.2	FTN	RUNTIME*
133	*E	*HXDCR*	* DECK-ID D18	3.2	FTN	RUNTIME*
134	*E	*FLOTIR*	* DECK-ID D19	3.2	FTN	RUNTIME*
135	*E	*FOLTR*	* DECK-ID D20	3.2	FTN	RUNTIME*
136	*E	*EOLTR*	* DECK-ID D21	3.2	FTN	RUNTIME*
137	*E	*EWRTTR*	* DECK-ID D22	3.2	FTN	RUNTIME*
138	*E	*INTIR*	* DECK-ID D23	3.2	FTN	RUNTIME*
139	*E	*FORMTR*	* DECK-ID D24	3.2	FTN	RUNTIME*
140	*E	*Q8GFIR*	* DECK-ID D25	3.2	FTN	RUNTIME*
141	*E	*Q8QFLR*	* DECK-ID D26	3.2	FTN	RUNTIME*
142	*E	*Q8GFXR*	* DECK-ID D27	3.2	FTN	RUNTIME*
143	*E	*HEXAR*	* DECK-ID D28	3.2	FTN	RUNTIME*
144	*E	*HEXDR*	* DECK-ID D29	3.2	FTN	RUNTIME*
145	*E	*ASCIIR*	* DECK-ID D30	3.2	FTN	RUNTIME*
146	*E	*DECHXR*	* DECK-ID D31	3.2	FTN	RUNTIME*
147	*E	*AFORMR*	* DECK-ID D32	3.2	FTN	RUNTIME*
148	*E	*RFORMR*	* DECK-ID D33	3.2	FTN	RUNTIME*
149	*E	*FLOTGR*	* DECK-ID D34	3.2	FTN	RUNTIME*
150	*E	*Q8CBLR*	* DECK-ID E01	3.2	FTN	RUNTIME*
151	*E	*DEXPR*	* DECK-ID E02	3.2	FTN	RUNTIME*
152	*E	*Q8CABR*	* DECK-ID E03	3.2	FTN	RUNTIME*
153	*E	*DSGRTTR*	* DECK-ID E04	3.2	FTN	RUNTIME*
154	*E	*DSIGNR*	* DECK-ID E05	3.2	FTN	RUNTIME*
155	*E	*SNGLR*	* DECK-ID E06	3.2	FTN	RUNTIME*
156	*E	*Q8CFLR*	* DECK-ID E07	3.2	FTN	RUNTIME*
157	*E	*DEXPR*	* DECK-ID E08	3.2	FTN	RUNTIME*
158	*E	*DLGGR*	* DECK-ID E09	3.2	FTN	RUNTIME*
159	*E	*DRSTRR*	* DECK-ID E10	3.2	FTN	RUNTIME*
160	*E	*DSNCSR*	* DECK-ID E11	3.2	FTN	RUNTIME*
161	*E	*DATANR*	* DECK-ID E12	3.2	FTN	RUNTIME*
162	*E	*DFLOTR*	* DECK-ID E13	3.2	FTN	RUNTIME*
163	*E	*Q8GDFR*	* DECK-ID E14	3.2	FTN	RUNTIME*
164	*E	*DOLTR*	* DECK-ID E15	3.2	FTN	RUNTIME*
165	*LP	*CORF	RESIDENT SYSTEM LIBRARY PROGRAM,			ORDINAL 1
166	*E	*CORESO*				
167	*E	*NXTLOC*	* NEXT AVAILABLE LOCATION*			
168	*					
169	*		SYSTEM MASS RESIDENT PROGRAMS			
170	*					
171	*N		LIBEDT	1		
172	*E	*LIBEDT*	* DECK-ID D02	MSOS	4.1*	
173	*N		LOADSC	2		
174	*E	*LOAD1*	* DECK-ID D03	MSOS	4.1*	
175	*E	*BRNCHI*	* DECK-ID D04	MSOS	4.1*	
176	*E	*LIDRV1*	* DECK-ID D05	MSOS	4.1*	
177	*E	*LCDRV1*	* DECK-ID D06	MSOS	4.1*	
178	*E	*LMDRV1*	* DECK-ID D07	MSOS	4.1*	

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179	*B	*LLDRV1*	*	DECK-ID	D08	MSOS	4.1*
180	*B	*ADJOF1*	*	DECK-ID	D09	MSOS	4.1*
181	*B	*CNVRT1*	*	DECK-ID	D10	MSOS	4.1*
182	*B	*LSTOT1*	*	DECK-ID	D11	MSOS	4.1*
183	*B	*LINK11*	*	DECK-ID	D12	MSOS	4.1*
184	*B	*LOADR1*	*	DECK-ID	D13	MSOS	4.1*
185	*B	*NAMPR1*	*	DECK-ID	D14	MSOS	4.1*
186	*B	*RBDZ1*	*	DECK-ID	D15	MSOS	4.1*
187	*B	*ENTEX1*	*	DECK-ID	D16	MSOS	4.1*
188	*B	*XFRPR1*	*	DECK-ID	D17	MSOS	4.1*
189	*B	*STBASE*	*	DECK-ID	D18	MSOS	4.1*
190	*B	*LNKENT*	*	DECK-ID	D19	MSOS	4.1*
191	*B	*LNKCR1*	*	DECK-ID	D20	MSOS	4.1*
192	*B	*PATCH*	*	DECK-ID	D21	MSOS	4.1*
193	*B	*TBSCH1*	*	DECK-ID	D22	MSOS	4.1*
194	*B	*HASH*	*	DECK-ID	D23	MSOS	4.1*
195	*B	*TRSTR1*	*	DECK-ID	D24	MSOS	4.1*
196	*B	*PAGE*	*	DECK-ID	D25	MSOS	4.1*
197	*B	*PROGLD*	*	DECK-ID	D26	MSOS	4.1*
198	*B	*SCAN1*	*	DECK-ID	D27	MSOS	4.1*
199	*B	*CHPUI*	*	DECK-ID	D28	MSOS	4.1*
200	*B	*ADJOV2*	*	DECK-ID	D29	MSOS	4.1*
201	*B	*ADRPR1*	*	DECK-ID	D30	MSOS	4.1*
202	*N			JOBERT		3	
203	*B	*JOBENT*	*	DECK-ID	D31	MSOS	4.1*
204	*B	*T11*	*	DECK-ID	D32	MSOS	4.1*
205	*B	*T7*	*	DECK-ID	D33	MSOS	4.1*
206	*B	*T5*	*	DECK-ID	D34	MSOS	4.1*
207	*B	*T3*	*	DECK-ID	D35	MSOS	4.1*
208	*S,N1,P						
209	*N			JOBPRC		4	
210	*B	*JOBPRO*	*	DECK-ID	D36	MSOS	4.1*
211	*B	*ONE*	*	DECK-ID	D37	MSOS	4.1*
212	*B	*TWO*	*	DECK-ID	D38	MSOS	4.1*
213	*B	*THREE*	*	DECK-ID	D39	MSOS	4.1*
214	*S,N2,P						
215	*N			PROTEC		5	
216	*B	*BPROTK*	*	DECK-ID	D41	MSOS	4.1*
217	*B	*JRKILL*	*	DECK-ID	D42	MSOS	4.1*
218	*N			JLOAD		6	
219	*B	*JPLOAD*	*	DECK-ID	D43	MSOS	4.1*
220	*N			JPCHGE		7	
221	*B	*JPEHGE*	*	DECK-ID	D44	MSOS	4.1*
222	*B	*ASGHEX*	*	DECK-ID	D45	MSOS	4.1*
223	*N			JPT13		8	
224	*B	*T13*	*	DECK-ID	D46	MSOS	4.1*
225	*N			JCRDV4		9	
226	*B	*JCRDV4*	*	DECK-ID	D47	MSOS	4.1*
227	*N			JLGOV4		10	
228	*B	*JLGOV4*	*	DECK-ID	D48	MSOS	4.1*
229	*N			JPSTV4		11	
230	*B	*JPSTV4*	*	DECK-ID	D49	MSOS	4.1*
231	*N			NAMEV4		12	
232	*B	*NAMEV4*	*	DECK-ID	D50	MSOS	4.1*
233	*N			JPFLV4		13	
234	*B	*JPFLV4*	*	DECK-ID	D51	MSOS	4.1*
235	*N			AFILV4		14	
236	*B	*JPF2V4*	*	DECK-ID	D52	MSOS	4.1*
237	*N			RESTOR		15	
238	*B	*RESTOR*	*	DECK-ID	D53	MSOS	4.1*

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	*N		RECOVER	16			
4	#B	#RCCVER#	# DECK-ID	F09	MSOS	4.1#	
1	#B	#OUTSEL#	# DECK-ID	F10	MSOS	4.1#	
	#B	#RDMPV4#	# DECK-ID	F11	MSOS	4.1#	
	#B	#MASDMP#	# DECK-ID	F12	MSOS	4.1#	
	*N		BRKPT	17			
5	#B	#BRKPTD#	# DECK-ID	F13	MSOS	4.1#	
	#B	#SIFT#	# DECK-ID	F14	MSOS	4.1#	
	#B	#RIASCI#	# DECK-ID	F15	MSOS	4.1#	
	#B	#REJUMP#	# DECK-ID	F16	MSOS	4.1#	
	#B	#JUMPTO#	# DECK-ID	F17	MSOS	4.1#	
	#B	#ENTER#	# DECK-ID	F18	MSOS	4.1#	
	#B	#ENTCOR#	# DECK-ID	F19	MSOS	4.1#	
	#B	#PRTRREG#	# DECK-ID	F20	MSOS	4.1#	
	#B	#TERMIN#	# DECK-ID	F21	MSOS	4.1#	
	#B	#RESUME#	# DECK-ID	F22	MSOS	4.1#	
	#B	#DPCORB#	# DECK-ID	F23	MSOS	4.1#	
	#B	#MSDMPH#	# DECK-ID	F24	MSOS	4.1#	
	#B	#SETBRP#	# DECK-ID	F25	MSOS	4.1#	
	*N		ODEBUG	18			
	#B	#ODEBUG#	# DECK-ID	F26	MSOS	4.1#	
	*N		SYSCOP	19			
	#B	#SYSCOP#	# DECK-ID	F27	MSOS	4.1#	
	*N		SYSSEG	20			
	#B	#COLIST#	# DECK-ID	F28	MSOS	4.1#	
	#B	#CO2ND#	# DECK-ID	F29	MSOS	4.1#	
	#B	#CO3RD#	# DECK-ID	F30	MSOS	4.1#	
	#B	#COLAST#	# DECK-ID	H31	MSOS	4.1#	
	*N		MIPRO	21			
	#B	#MIPRO#	# DECK-ID	A26	MSOS	4.1#	
	*N		TDFUNC	22			
7	#B	#TDFUNC#	# DECK-ID	A27	MSOS	4.1#	
	*N		EFSTOR	23			
	#B	#EFSTOR#	# DECK-ID	A28	MSOS	4.1#	
	*N		EFLIST	24			
	#B	#EFLIST#	# DECK-ID	A29	MSOS	4.1#	
	*N		SCMM17	25			
	#B	#SCMEXC#	# DECK-ID	E01	MSOS	4.1#	
	*N		VERIFY	26			
	#B	#VERIFY1#	# DECK-ID	A30	MSOS	4.1#	
	*N		DUMMY1	27			
8	*N		DUMMY2	28			
	*N		DUMMY3	29			
	*N		DUMMY4	30			
	*N		DUMMY5	31			
	*N		DUMMY6	32			
	*N		DUMMY7	33			
	*N		DUMMY8	34			
	*N		DUMMY9	35			
	*N		DUMMY0	36			

MASS RESIDENT DRIVERS

	*N		CCSY DRIVER				
	#B	#DCOSY#	# DECK-ID	C28	MSOS	4.1#	
	#S,	SCOSY,S					
	#S,	LCOSY,P					
	*N		1731 601 MAG TAPE				
	#B	#D1731U#	# DECK-ID	C33	MSOS	4.1#	
	#B	#FRWA#	# DECK-ID	C34	MSOS	4.1#	

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30 *E *FRWR# * DECK-ID C35 MSOS 4.1#
   *E *RWBA# * DECK-ID C36 MSOS 4.1#
   *E *MAXRVU# * DECK-ID C37 MSOS 4.1#
   *S,S1731U,S
   *N
   *S,L1731U,P
   *N
   *E *D40421# * DECK-ID C46 MSOS 4.1#
   *S,S40421,S
   *S,L40421,P
   *N
   *E *D1728# * DECK-ID C48 MSOS 4.1#
   *E *CP026# * DECK-ID C54 MSOS 4.1#
   *E *CR026# * DECK-ID C52 MSOS 4.1#
   *S,S1728,S
   *S,L1728,P
   *
   *
   *          MASS RESIDENT FILE MANAGER
   *
   *N
   *E *DEFFIL# * DECK-ID F05 MSOS 4.1#
   *E *FILSPC# * DECK-ID F06 MSOS 4.1#
   *E *RPEND# * DECK-ID F07 MSOS 4.1#
   *S,FMRP01,S
   *N
   *E *RELFIL# * DECK-ID F08 MSOS 4.1#
   *E *RELSPC# * DECK-ID F09 MSOS 4.1#
   *E *RPEND# * DECK-ID F07 MSOS 4.1#
   *S,FMRP02,S
   *N
   *E *DEFID# * DECK-ID F10 MSOS 4.1#
   *E *SORTFM# * DECK-ID F11 MSOS 4.1#
   *E *FILSPC# * DECK-ID F06 MSOS 4.1#
   *E *RPEND# * DECK-ID F07 MSOS 4.1#
   *S,FMRP03,S
   *N
   *E *LOKFIL# * DECK-ID F12 MSOS 4.1#
   *E *RPEND# * DECK-ID F07 MSOS 4.1#
   *S,FMRP04,S
   *N
   *E *UNLFIL# * DECK-ID F13 MSOS 4.1#
   *E *RPEND# * DECK-ID F07 MSOS 4.1#
   *S,FMRP05,S
   *N
   *E *STGSEQ# * DECK-ID F14 MSOS 4.1#
   *E *FILSPC# * DECK-ID F06 MSOS 4.1#
   *E *RPEND# * DECK-ID F07 MSOS 4.1#
   *S,FMRP06,S
   *N
   *E *STCDIR# * DECK-ID F15 MSOS 4.1#
   *E *RPEND# * DECK-ID F07 MSOS 4.1#
   *S,FMRP07,S
   *N
   *E *STCIDX# * DECK-ID F16 MSOS 4.1#
   *E *HASHCD# * DECK-ID F17 MSOS 4.1#
   *E *GETKID# * DECK-ID F18 MSOS 4.1#
   *E *FILSPC# * DECK-ID F06 MSOS 4.1#
   *E *RPEND# * DECK-ID F07 MSOS 4.1#
   *S,FMRP08,S
   *N
   *E *RTVSEQ# * DECK-ID F19 MSOS 4.1#

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359 *B *RTNSPC* * DECK-ID F20 MSOS 4.1*
360 *B *RPEND* * DECK-ID F07 MSOS 4.1*
361 *S,FMRP09,S
362 *M
363 *B *RTVDIR* * DECK-ID F21 MSOS 4.1*
364 *B *RTNSPC* * DECK-ID F20 MSOS 4.1*
365 *B *RPEND* * DECK-ID F07 MSOS 4.1*
366 *S,FMRP10,S
367 *M
368 *B *RTVIDX* * DECK-ID F22 MSOS 4.1*
369 *B *HASHCD* * DECK-ID F17 MSOS 4.1*
370 *B *GETKID* * DECK-ID F18 MSOS 4.1*
371 *P *RTNSPC* * DECK-ID F20 MSOS 4.1*
372 *B *RPEND* * DECK-ID F07 MSOS 4.1*
373 *S,FMRP11,S
374 *M
375 *B *RTVIDO* * DECK-ID F23 MSOS 4.1*
376 *B *GETKID* * DECK-ID F18 MSOS 4.1*
377 *B *RTNSPC* * DECK-ID F20 MSOS 4.1*
378 *B *RPEND* * DECK-ID F07 MSOS 4.1*
379 *S,FMRP12,S
380 *M
381 *B *FMDUMY* * DECK-ID F04 MSOS 4.1*
382 *S,FMREND,S
383 *S,BEGFMS,S SPECIFY THE SYSTEM FILE SPACE
384 *M,BEGFMS+1000
385 *M
386 *B *FMDUMY* * DECK-ID F04 MSOS 4.1*
387 *T END OF SYSTEM
388 *JOB,INSTAL,SYSTEM
389 *K,I6
390 *LIBEDT
391 *K,I6
392 *V DEFINE REQUEST PRIORITIES
393 *S,001,03,M
394 *S,002,00,M
395 *S,003,01,M
396 *S,004,02,M
397 *S,005,03,M
398 *S,006,02,M
399 *S,007,02,M
400 *S,008,02,M
401 *S,009,02,M
402 *S,010,02,M
403 *S,011,02,M
404 *S,012,03,M
405 *S,013,03,M
406 *S,014,03,M
407 *S,015,02,M
408 *S,016,03,M
409 *S,017,03,M
410 *S,018,04,M
411 *S,019,04,M
412 *S,020,04,M
413 *S,021,04,M
414 *S,022,04,M
415 *S,023,04,M
416 *S,024,04,M
417 *S,025,04,M
418 *S,026,04,M

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419 *S,027,04,M
420 *S,028,04,M
421 *S,029,04,M
422 *S,030,04,M
423 *S,031,04,M
424 *S,032,04,M
425 *S,033,04,M
426 *S,034,04,M
427 *S,035,04,M
428 *S,036,04,M

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Using the program LIBILD, the revised skeleton, the binary cards for CORES0, and the current system install tape, a new install file incorporating CORES0 may be built. With this new install tape a new system is built and verified following the procedure in the 1700 MS0S 4 Installation Handbook. When the new system is built the user gets a listing of the system library directory using the LIBEDT *DM command. This listing is as follows:

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1	4200	8F8E	0000	0000			
1	0030	1838	0000	1F64	04A5	0000	0181
2	0000	0000	0000	0000	0CB2	0000	018E
3	0010	1EDA	0000	646A	0297	0000	01E0
4	0020	1CC2	0000	000E	0210	0000	01E7
5	0030	1838	0000	1CD7	0488	0000	01ED
6	0020	1CC2	0000	0000	0192	0000	01FB
7	0020	0000	0000	0000	014D	0000	0200
8	0020	0000	0000	0000	018C	0000	0204
9	0020	1CC2	0000	0000	0148	0000	0209
10	0020	0000	0000	0000	00DB	0000	020D
11	0020	0000	0000	0000	0062	0000	0210
12	0030	1838	0000	000E	02E2	0000	0212
13	0030	0000	0000	0000	0270	0000	021A
14	0030	0000	0000	0000	0367	0000	0221
15	0020	0000	0000	0000	00C9	0000	022B
16	0030	0000	0000	0000	034D	0000	022E
17	0030	0000	0000	0000	0568	0000	0237
18	0044	0000	0000	0000	11B3	0000	0246
19	0040	0000	0000	0000	0192	0000	0276
20	0040	0000	0000	0000	14A0	0000	027B
21	0043	0000	0000	0000	01E1	0000	0282
22	0044	125A	0000	0001	0160	0000	0288
23	0044	125A	0000	6C71	0184	0000	028C
24	0040	0000	0000	0000	07A1	0000	02C1
25	0040	0000	0000	0000	05A4	0000	02D6
26	0040	0000	0000	0000	001A	0000	02E6
27	0040	0000	0000	0000	0000	0000	02E7
28	0040	0000	0000	0000	0000	0000	02E7

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29	0040	0000	0000	0000	0000	0000	02E7
30	0040	0000	0000	0000	0000	0000	02E7
31	0040	0000	0000	0000	0000	0000	02E7
32	0040	0000	0000	0000	0000	0000	02E7
33	0040	0000	0000	0000	0000	0000	02E7
34	0040	0000	0000	0000	0000	0000	02E7
35	0040	0000	0000	0000	0000	0000	02E7
36	0040	0000	0000	0000	0000	0000	02E7

FINI

The first entry is for CORES0. The second directory entry is for the first mass memory ordinal, LIBEDT.

The following is a portion of the system load map-including CORES0.

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*L SYSTEM DATA PROGRAM

*L SYSDAT 0000 COPYRIGTH CONTRCL DATA CORPORATION 1973
SPACE REQUEST PRCESSOR

SPACE 123E DECK-ID A02 MSCS 4.1 SUMMARY-082

DATE 11/23/74

*M	LIREDT	1			
*M	LIREDT 0181	DECK-ID D02	MSCS 4.1		SUMMARY-081
	LOADSD 2				
	LOAD1 018E	DECK-ID D03	MSCS 4.1		SUMMARY-081
	RRNCH1 0236	DECK-ID D04	MSCS 4.1		SUMMARY-081
	LIDRV1 0393	DECK-ID D05	MSCS 4.1		SUMMARY-079
	LCORV1 03E4	DECK-ID D06	MSCS 4.1		SUMMARY-079
	IMDRV1 0411	DECK-ID D07	MSCS 4.1		SUMMARY-079
	LLDRV1 0430	DECK-ID D08	MSCS 4.1		SUMMARY-079
	ADJCF1 043E	DECK-ID D09	MSCS 4.1		SUMMARY-079
	CNVRT1 044A	DECK-ID D10	MSCS 4.1		SUMMARY-079
	I STCT1 0462	DECK-ID D11	MSCS 4.1		SUMMARY-079
	LINK11 0485	DECK-ID D12	MSCS 4.1		SUMMARY-079
	LCADR1 04F8	DECK-ID D13	MSCS 4.1		SUMMARY-079
	NAMPRI 056C	DECK-ID D14	MSCS 4.1		SUMMARY-081
	RRDEZ1 0605	DECK-ID D15	MSCS 4.1		SUMMARY-079
	ENTEX1 06FE	DECK-ID D16	MSCS 4.1		SUMMARY-079
	XFRFR1 0734	DECK-ID D17	MSCS 4.1		SUMMARY-079
	STBASE 0745	DECK-ID D18	MSCS 4.1		SUMMARY-081
	LNKENT 081F	DECK-ID D19	MSCS 4.1		SUMMARY-079
	LNKCR1 083R	DECK-ID D20	MSCS 4.1		SUMMARY-079
	DATCH 087C	DECK-ID D21	MSCS 4.1		SUMMARY-079
	TESCH1 08BE	DECK-ID D22	MSCS 4.1		SUMMARY-079
	HASH 0906	DECK-ID D23	MSCS 4.1		SUMMARY-079
	TESTR1 091F	DECK-ID D24	MSCS 4.1		SUMMARY-079
	PAGE 0973	DECK-ID D25	MSCS 4.1		SUMMARY-079
	FROGLD 0A63	DECK-ID D26	MSCS 4.1		SUMMARY-079
	SCAN1 0B65	DECK-ID D27	MSCS 4.1		SUMMARY-079
	CFPL1 0C2R	DECK-ID D28	MSCS 4.1		SUMMARY-079
	ADJCV2 0C38	DECK-ID D29	MSCS 4.1		SUMMARY-079
	ADRPRI 0C51	DECK-ID D30	MSCS 4.1		SUMMARY-079
*M	JCRENT	3			
	JCRENT 01F0	DECK-ID D31	MSCS 4.1		SUMMARY-079
	T11 00CC	DECK-ID D32	MSCS 4.1		SUMMARY-079
	T7 0103	DECK-ID D33	MSCS 4.1		SUMMARY-081
	T5 0213	DECK-ID D34	MSCS 4.1		SUMMARY-079
	T3 0267	DECK-ID D35	MSCS 4.1		SUMMARY-079
*S,N1,P	JCRPRC	4			
*M	JCRPRO 01E7	DECK-ID D36	MSCS 4.1		SUMMARY-081
	ONE 020D	DECK-ID D37	MSCS 4.1		SUMMARY-081
	TWO 0210	DECK-ID D38	MSCS 4.1		SUMMARY-081
	THREE 0213	DECK-ID D39	MSCS 4.1		SUMMARY-081
*S,N2,P					

DATE 11/23/74



*
* SYSTEM CORE RESIDENT PROGRAMS
*

*LP	MONITOR					
	NMONI	5A54	DECK-ID A03	MSCS 4.1		SUMMARY-079
	RDISP	5A98	DECK-ID A05	MSCS 4.1		SUMMARY-079
	RW	5C32	DECK-ID A06	MSCS 4.1		SUMMARY-079
	T14	5CED	DECK-ID A07	MSCS 4.1		SUMMARY-079
	T16	5CFE	DECK-ID A08	MSCS 4.1		SUMMARY-079
	PARAME	5D09	DECK-ID A09	MSCS 4.1		SUMMARY-079
	COMMON	5D7A	DECK-ID A10	MSCS 4.1		SUMMARY-079
	NIPROC	5DA1	DECK-ID A11	MSCS 4.1		SUMMARY-079
	ALVCL	5E2A	DECK-ID A13	MSCS 4.1		SUMMARY-079
	OFVCL	5E47	DECK-ID A14	MSCS 4.1		SUMMARY-079
	ALCCRE	5E53	DECK-ID A15	MSCS 4.1		SUMMARY-079
	DCORE	5F01	DECK-ID A16	MSCS 4.1		SUMMARY-079
	NFNR	608B	DECK-ID A18	MSCS 4.1		SUMMARY-079
	NCMPRQ	60FD	DECK-ID A19	MSCS 4.1		SUMMARY-079
	MAKG	612D	DECK-ID A20	MSCS 4.1		SUMMARY-081
	ADEV	615B	DECK-ID A21	MSCS 4.1		SUMMARY-079
	TMINT	62C3	DECK-ID A22	MSCS 4.1		SUMMARY-079
	DTIMER	635A	DECK-ID A23	MSCS 4.1		SUMMARY-079
	TCD	637C	DECK-ID A24	MSCS 4.1		SUMMARY-079
	MINT	63CB	DECK-ID A25	MSCS 4.1		SUMMARY-079
	TRVEC	64D7	DECK-ID D01	MSCS 4.1		SUMMARY-079
*LP	DEBUGGING / CHECKOUT					
	SNAPOL	651F	DECK-ID H01	MSCS 4.1		SUMMARY-079
	DMP421	65D8	DECK-ID H03	MSCS 4.1		SUMMARY-079
	BCK85X	66A1	DECK-ID H05	MSCS 4.1		SUMMARY-079
*LP	FILE MANAGER					
	FILMGR	66EC	DECK-ID F01	MSCS 4.1		SUMMARY-081
	RSPCV4	6911	DECK-ID F02	MSCS 4.1		SUMMARY-079
	SRHFIS	6A01	DECK-ID F03	MSCS 4.1		SUMMARY-079
*LP	CORE RESIDENT DRIVERS					
	EFDATA	68BA	DECK-ID C01	MSCS 4.1		SUMMARY-079
	DUMMY	6D18	DECK-ID C02	MSCS 4.1		SUMMARY-079
	ALAG	6D3B	DECK-ID C03	MSCS 4.1		SUMMARY-081
	D1711	6DA9	DECK-ID C05	MSCS 4.1		SUMMARY-082
	D1738	6F44	DECK-ID C08	MSCS 4.1		SUMMARY-081
	REWCK	7189	DECK-ID C13	MSCS 4.1		SUMMARY-079
	MMEXEC	71A7	DECK-ID C15	MSCS 4.1		SUMMARY-081
*LP	REENTRANT FORTRAN RUNTIME LIBRARY					
	FCRTR	731C	DECK-ID A01	3.2 FTN RUNTIME		SUMMARY-079
	Q8PRMR	744E	DECK-ID B01	3.2 FTN RUNTIME		SUMMARY-079
	PARABR	7465	DECK-ID B02	3.2 FTN RUNTIME		SUMMARY-081
	Q8EXPR	7476	DECK-ID B03	3.2 FTN RUNTIME		SUMMARY-079
	Q8ABR	7527	DECK-ID B04	3.2 FTN RUNTIME		SUMMARY-079
	SGRTR	753E	DECK-ID B05	3.2 FTN RUNTIME		SUMMARY-079
	SIGAR	7599	DECK-ID B06	3.2 FTN RUNTIME		SUMMARY-079
	FXFLR	75C5	DECK-ID B07	3.2 FTN RUNTIME		SUMMARY-079
	EXPRGR	7630	DECK-ID B08	3.2 FTN RUNTIME		SUMMARY-079
	LINPRGR	76D2	DECK-ID B09	3.2 FTN RUNTIME		SUMMARY-079
	TANFR	774C	DECK-ID B10	3.2 FTN RUNTIME		SUMMARY-079
	SACSR	778B	DECK-ID B11	3.2 FTN RUNTIME		SUMMARY-079
	ARCPGR	7884	DECK-ID B12	3.2 FTN RUNTIME		SUMMARY-079
	IFALTR	791D	DECK-ID B13	3.2 FTN RUNTIME		SUMMARY-079
	FLOATR	7934	DECK-ID B14	3.2 FTN RUNTIME		SUMMARY-079

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QBQIOR	7B30	DECK-ID C01	3.2	FTN	RUNTIME	SUMMARY-082
RINARR	7C92	DECK-ID C02	3.2	FTN	RUNTIME	SUMMARY-079
TOCODR	7CDF	DECK-ID D01	3.2	FTN	RUNTIME	SUMMARY-079
INITLR	7D13	DECK-ID D02	3.2	FTN	RUNTIME	SUMMARY-079
RSTCRR	7D22	DECK-ID D03	3.2	FTN	RUNTIME	SUMMARY-079
GETCHR	7D32	DECK-ID D04	3.2	FTN	RUNTIME	SUMMARY-079
IPACKR	7D4C	DECK-ID D05	3.2	FTN	RUNTIME	SUMMARY-079
UPDATR	7D83	DECK-ID D06	3.2	FTN	RUNTIME	SUMMARY-079
DECFLR	7D90	DECK-ID D07	3.2	FTN	RUNTIME	SUMMARY-079
INTGRR	7DB6	DECK-ID D08	3.2	FTN	RUNTIME	SUMMARY-079
SPACER	7DE3	DECK-ID D09	3.2	FTN	RUNTIME	SUMMARY-079
HOLE	7DEF	DECK-ID B10	3.2	FTN	RUNTIME	SUMMARY-079
DCHXR	7E8F	DECK-ID B11	3.2	FTN	RUNTIME	SUMMARY-079
HXASCR	7E02	DECK-ID B12	3.2	FTN	RUNTIME	SUMMARY-079
AFMTOR	7E55	DECK-ID B13	3.2	FTN	RUNTIME	SUMMARY-079
RFMTOR	7E7F	DECK-ID D14	3.2	FTN	RUNTIME	SUMMARY-079
AFMTIR	7E98	DECK-ID D15	3.2	FTN	RUNTIME	SUMMARY-079
RFMTIR	7FC6	DECK-ID D16	3.2	FTN	RUNTIME	SUMMARY-079
ASCHXR	7FDD	DECK-ID D17	3.2	FTN	RUNTIME	SUMMARY-079
HXDGR	8018	DECK-ID D18	3.2	FTN	RUNTIME	SUMMARY-079
FLOTIR	80A8	DECK-ID D19	3.2	FTN	RUNTIME	SUMMARY-079
FCUIR	80F2	DECK-ID D20	3.2	FTN	RUNTIME	SUMMARY-079
FCUIR	8178	DECK-ID D21	3.2	FTN	RUNTIME	SUMMARY-079
EWRITR	8263	DECK-ID D22	3.2	FTN	RUNTIME	SUMMARY-079
INTIIR	826F	DECK-ID D23	3.2	FTN	RUNTIME	SUMMARY-079
FCRMTR	828C	DECK-ID D24	3.2	FTN	RUNTIME	SUMMARY-081
QBQFIR	8461	DECK-ID D25	3.2	FTN	RUNTIME	SUMMARY-079
QBQFLR	847B	DECK-ID D26	3.2	FTN	RUNTIME	SUMMARY-079
QBQFXR	84AA	DECK-ID D27	3.2	FTN	RUNTIME	SUMMARY-079
HEXAR	84E1	DECK-ID D28	3.2	FTN	RUNTIME	SUMMARY-079
HEXDR	84F9	DECK-ID D29	3.2	FTN	RUNTIME	SUMMARY-079
ASCIIR	8516	DECK-ID D30	3.2	FTN	RUNTIME	SUMMARY-079
DECHXR	852B	DECK-ID D31	3.2	FTN	RUNTIME	SUMMARY-079
AFORMR	854B	DECK-ID D32	3.2	FTN	RUNTIME	SUMMARY-079
RFORMR	8567	DECK-ID D33	3.2	FTN	RUNTIME	SUMMARY-079
FLOTGR	8583	DECK-ID D34	3.2	FTN	RUNTIME	SUMMARY-079
QBDELR	859F	DECK-ID E01	3.2	FTN	RUNTIME	SUMMARY-082
DEXPR	85B4	DECK-ID E02	3.2	FTN	RUNTIME	SUMMARY-079
QBDABR	8645	DECK-ID E03	3.2	FTN	RUNTIME	SUMMARY-079
DSQTR	8663	DECK-ID E04	3.2	FTN	RUNTIME	SUMMARY-079
DSIGNR	86E3	DECK-ID E05	3.2	FTN	RUNTIME	SUMMARY-079
SANGLR	8713	DECK-ID E06	3.2	FTN	RUNTIME	SUMMARY-079
QBDFLR	8729	DECK-ID E07	3.2	FTN	RUNTIME	SUMMARY-079
DEXPFR	874E	DECK-ID E08	3.2	FTN	RUNTIME	SUMMARY-079
DLOGR	880A	DECK-ID E09	3.2	FTN	RUNTIME	SUMMARY-079
DESTRR	88B3	DECK-ID E10	3.2	FTN	RUNTIME	SUMMARY-082
DSNCSR	88E9	DECK-ID E11	3.2	FTN	RUNTIME	SUMMARY-079
DATNR	89F9	DECK-ID E12	3.2	FTN	RUNTIME	SUMMARY-079
NFLCTR	8ACC	DECK-ID E13	3.2	FTN	RUNTIME	SUMMARY-079
QBQDFR	8E68	DECK-ID E14	3.2	FTN	RUNTIME	SUMMARY-079
DCUIR	8E84	DECK-ID E15	3.2	FTN	RUNTIME	SUMMARY-079

*LP CORE RESIDENT SYSTEM LIBRARY PROGRAM, ORDINAL 1
 CCRESO 8F8E
 NXTLOC 8FD1 NEXT AVAILABLE LOCATION

*
 * SYSTEM MASS RESIDENT PROGRAMS
 *

599

Following system installation the user autoloads the system. As a test case he runs CORES0 by pressing STEP, clearing the P, X, and I registers, setting P=8F8E₁₆, and pressing RUN. Typing on the comment device is as follows:

```
_____  
MSOS 4.1--PSR LEVEL 86  11/23/74  
_____  
ANN'S TEST SYSTEM  
_____  
65K MODE  
_____  
CHECKING FILES -  OK  
_____  
ENTER DATE/TIME  MMDDYYHHMM  
123741317  
TE: 23 NOV 74  TIME: 1317:00  
110 THIS IS CORES0  
_____
```

Note that CORES0 cannot be accessed via MIPRO since it is a core resident ordinal.

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APPENDIX A

INCREASING AVAILABLE CORE WITHOUT ADDING HARDWARE CORE MODULES

In a system with mass resident drivers the size of the mass resident driver buffer may be reduced. In a standard released system this buffer is large enough to accommodate the two largest mass resident drivers. Although throughput may be slowed, this buffer may be decreased in size, perhaps to accommodate the two smallest mass resident drivers, or perhaps to accommodate only the largest mass resident driver. The mass resident buffer size is specified by the parameter MBFSZ in Section ACR of SYSDAT. {Refer to Article 1.70.}

In a system which includes the buffered protect processor, BPROTK, the unbuffered protect processor, UPROTK, may be substituted. The difference between the two processors is described in Article 2.1, under Group M. The length of BPROTK is 488₁₆. The length of UPROTK is 3C8₁₆. Thus, this substitution saves C0₁₆{=192} words of core. To make this change, the system skeleton must be changed. If the skeleton is on cards, the appropriate card is modified. If on tape, SKED, the skeleton editor program, may be used to make the change. {Refer to Article 4.3.3 for examples of the use of SKED.}

It is assumed in this manual that the user wishes to save the words from NXTLOC to MSIZV4 for some purpose of his own. If, however, he finds that he does not need all these locations, BGNMON and ENDLLOC may be increased accordingly, thereby making more core available.



(604)

Appendix B

Listing of FORTRAN 3.2B
Skeleton Records

602

*V FORTRAN COMPILER 3.2 B
*K,I6
*L,FTN
*B 'FTNB' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
*K,P8
*P,F
*B 'FTNB' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
*B 'GOA' ' DECK-ID 02F FORTRAN 3.2B'
*B 'CFIVOC' ' DECK-ID 34A FORTRAN 3.2B'
*B 'CKNAME' ' DECK-ID 36A FORTRAN 3.2B'
*B 'CNVT' ' DECK-ID 01A FORTRAN 3.2B'
*B 'CONV' ' DECK-ID 03F FORTRAN 3.2B'
*B 'DIAG' ' DECK-ID 04F FORTRAN 3.2B'

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*B 'DXP9' ' DECK-ID 05F FORTRAN 3.2B'
*B 'DFLOT' ' DECK-ID 06F FORTRAN 3.2B'
*B 'DUMVOL' ' DECK-ID 35F FORTRAN 3.2B'
*B 'GETC' ' DECK-ID 14F FORTRAN 3.2B'
*B 'GETF' ' DECK-ID 04A FORTRAN 3.2B'
*B 'GETSYM' ' DECK-ID 07F FORTRAN 3.2B'
*B 'GPUT' ' DECK-ID 02A FORTRAN 3.2B'
*B 'IGETCF' ' DECK-ID 15F FORTRAN 3.2B'
*B 'IOPRBA' ' DECK-ID 08F FORTRAN 3.2B'
*B 'PACK' ' DECK-ID 09F FORTRAN 3.2B'
*B 'Q8PRMS' ' DECK-ID 10F FORTRAN 3.2B'
*B 'RDLABL' ' DECK-ID 10A FORTRAN 3.2B'
*B 'STORE' ' DECK-ID 11F FORTRAN 3.2B'
*B 'SYMBOL' ' DECK-ID 03A FORTRAN 3.2B'
*B 'ENDDO' ' DECK-ID 29A FORTRAN 3.2B'
*B 'GNST' ' DECK-ID 05A FORTRAN 3.2B'
*B 'OPTION' ' DECK-ID 16F FORTRAN 3.2B'
*B 'OUTENT' ' DECK-ID 06A FORTRAN 3.2B'
*B 'PHASEA' ' DECK-ID 07A FORTRAN 3.2B'
*B 'PLABEL' ' DECK-ID 08A FORTRAN 3.2B'
*B 'STCHAR' ' DECK-ID 11A FORTRAN 3.2B'
*B 'TYPE' ' DECK-ID 12A FORTRAN 3.2B'
*B 'SAVEID' ' DECK-ID 13A FORTRAN 3.2B'
*B 'LOCLA1' ' DECK-ID 12F FORTRAN 3.2B'
*B 'DUMYA1' ' DECK-ID 13F FORTRAN 3.2B'
*B 'Q8QBDS' ' DECK-ID 09A FORTRAN 3.2B'
*B 'ENDLOC' ' DECK-ID 17F FORTRAN 3.2B'

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*T
*K,I8
*N,FORTA1,,,8
*K,I6
*K,P8

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*P,F,,MARKER
*B 'FTNB' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
*B 'GOA' ' DECK-ID 02F FORTRAN 3.2B'
*B 'CFIVOC' ' DECK-ID 34A FORTRAN 3.2B'
*B 'CKNAME' ' DECK-ID 36A FORTRAN 3.2B'
*B 'CNVT' ' DECK-ID 01A FORTRAN 3.2B'
*B 'CONV' ' DECK-ID 03F FORTRAN 3.2B'
*B 'DIAG' ' DECK-ID 04F FORTRAN 3.2B'
*B 'DXP9' ' DECK-ID 05F FORTRAN 3.2B'
*B 'DFLOT' ' DECK-ID 06F FORTRAN 3.2B'
*B 'DUMVOL' ' DECK-ID 35F FORTRAN 3.2B'
*B 'GETC' ' DECK-ID 14F FORTRAN 3.2B'
*B 'GETF' ' DECK-ID 04A FORTRAN 3.2B'
*B 'GETSYM' ' DECK-ID 07F FORTRAN 3.2B'
*B 'GPUT' ' DECK-ID 02A FORTRAN 3.2B'
*B 'IGETCF' ' DECK-ID 15F FORTRAN 3.2B'
*B 'IOPRBA' ' DECK-ID 08F FORTRAN 3.2B'
*B 'PACK' ' DECK-ID 09F FORTRAN 3.2B'
*B 'Q8PRMS' ' DECK-ID 10F FORTRAN 3.2B'
*B 'RDLABL' ' DECK-ID 10A FORTRAN 3.2B'
*B 'STORE' ' DECK-ID 11F FORTRAN 3.2B'
*B 'SYMBOL' ' DECK-ID 03A FORTRAN 3.2B'
*B 'ENDDO' ' DECK-ID 29A FORTRAN 3.2B'
*B 'GNST' ' DECK-ID 05A FORTRAN 3.2B'
*B 'OPTION' ' DECK-ID 16F FORTRAN 3.2B'
*B 'OUTENT' ' DECK-ID 06A FORTRAN 3.2B'
*B 'PHASEA' ' DECK-ID 07A FORTRAN 3.2B'
*B 'PLABEL' ' DECK-ID 08A FORTRAN 3.2B'

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```
*B 'STCHAR' ' DECK-ID 11A FORTRAN 3.2B'  
*B 'TYPE' ' DECK-ID 12A FORTRAN 3.2B'  
*B 'SAVEID' ' DECK-ID 13A FORTRAN 3.2B'  
*B 'LOCLA2' ' DECK-ID 18F FORTRAN 3.2B'  
*B 'DUMYA2' ' DECK-ID 19F FORTRAN 3.2B'  
*B 'BYEQPR' ' DECK-ID 19A FORTRAN 3.2B'  
*B 'CHECKF' ' DECK-ID 20A FORTRAN 3.2B'  
*B 'COMNPR' ' DECK-ID 15A FORTRAN 3.2B'  
*B 'CONSUB' ' DECK-ID 30A FORTRAN 3.2B'  
*B 'DATAPR' ' DECK-ID 31A FORTRAN 3.2B'  
*B 'DIMPR' ' DECK-ID 16A FORTRAN 3.2B'  
*B 'EXRLPR' ' DECK-ID 24A FORTRAN 3.2B'  
*B 'FGETC' ' DECK-ID 21A FORTRAN 3.2B'  
*B 'FORK' ' DECK-ID 22A FORTRAN 3.2B'  
*B 'PEQVS' ' DECK-ID 25A FORTRAN 3.2B'  
*B 'PRNTNM' ' DECK-ID 26A FORTRAN 3.2B'  
*B 'SUBPPR' ' DECK-ID 23A FORTRAN 3.2B'  
*B 'SYMSCN' ' DECK-ID 28A FORTRAN 3.2B'  
*B 'TYPEPR' ' DECK-ID 18A FORTRAN 3.2B'  
*B 'ENDLOC' ' DECK-ID 17F FORTRAN 3.2B'  
*T  
*K,I8  
*N,FORTA2,,,B  
*K,I6  
*K,P8  
*P,F,,MARKER  
*B 'FTNB' ' COPYRIGHT CONTROL DATA CORPORATION 1973'  
*B 'GOA' ' DECK-ID 02F FORTRAN 3.2B'  
*B 'CFIVOC' ' DECK-ID 34A FORTRAN 3.2B'  
*B 'CKNAME' ' DECK-ID 36A FORTRAN 3.2B'  
*B 'CNVT' ' DECK-ID 01A FORTRAN 3.2B'  
*B 'CONV' ' DECK-ID 03F FORTRAN 3.2B'  
*B 'DIAG' ' DECK-ID 04F FORTRAN 3.2B'  
*B 'DXP9' ' DECK-ID 05F FORTRAN 3.2B'  
*B 'DFLOT' ' DECK-ID 06F FORTRAN 3.2B'  
*B 'DUMVOL' ' DECK-ID 35F FORTRAN 3.2B'  
*B 'GETC' ' DECK-ID 14F FORTRAN 3.2B'  
*B 'GETF' ' DECK-ID 04A FORTRAN 3.2B'  
*B 'GETSYM' ' DECK-ID 07F FORTRAN 3.2B'  
*B 'GPUT' ' DECK-ID 02A FORTRAN 3.2B'  
*B 'IGETCF' ' DECK-ID 15F FORTRAN 3.2B'  
*B 'IOPRBA' ' DECK-ID 08F FORTRAN 3.2B'  
*B 'PACK' ' DECK-ID 09F FORTRAN 3.2B'  
*B 'Q8PRMS' ' DECK-ID 10F FORTRAN 3.2B'  
*B 'ROLABL' ' DECK-ID 10A FORTRAN 3.2B'  
*B 'STORE' ' DECK-ID 11F FORTRAN 3.2B'  
*B 'SYMBOL' ' DECK-ID 03A FORTRAN 3.2B'  
*B 'ENDDO' ' DECK-ID 29A FORTRAN 3.2B'  
*B 'GNST' ' DECK-ID 05A FORTRAN 3.2B'  
*B 'OPTION' ' DECK-ID 16F FORTRAN 3.2B'  
*B 'OUTENT' ' DECK-ID 06A FORTRAN 3.2B'  
*B 'PHASEA' ' DECK-ID 07A FORTRAN 3.2B'  
*B 'PLABEL' ' DECK-ID 08A FORTRAN 3.2B'  
*B 'STCHAR' ' DECK-ID 11A FORTRAN 3.2B'  
*B 'TYPE' ' DECK-ID 12A FORTRAN 3.2B'  
*B 'SAVEID' ' DECK-ID 13A FORTRAN 3.2B'  
*B 'LOCLA3' ' DECK-ID 20F FORTRAN 3.2B'  
*B 'DUMYA3' ' DECK-ID 21F FORTRAN 3.2B'  
*B 'ARAYSZ' ' DECK-ID 42A FORTRAN 3.2B'  
*B 'ASEMPR' ' DECK-ID 40A FORTRAN 3.2B'
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605

*B 'ASGNPR' ' DECK-ID 32A FORTRAN 3.2B'
*B 'BDOPR' ' DECK-ID 33A FORTRAN 3.2B'
*B 'CHECKF' ' DECK-ID 20A FORTRAN 3.2B'
*B 'CKIVC' ' DECK-ID 35A FORTRAN 3.2B'
*B 'CONSUB' ' DECK-ID 30A FORTRAN 3.2B'
*B 'CPL00P' ' DECK-ID 43A FORTRAN 3.2B'
*B 'FGETC' ' DECK-ID 21A FORTRAN 3.2B'
*B 'FORK' ' DECK-ID 22A FORTRAN 3.2B'
*B 'ERBPR' ' DECK-ID 38A FORTRAN 3.2B'
*B 'MODMXR' ' DECK-ID 39A FORTRAN 3.2B'
*B 'PUNT' ' DECK-ID 27A FORTRAN 3.2B'
*B 'ENDLOC' ' DECK-ID 17F FORTRAN 3.2B'

*T
*K,I8
*N,FORTA3,,,B
*K,I6
*K,P8
*P,F,,,MARKER

*B 'FTNB' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
*B 'GOA' ' DECK-ID 02F FORTRAN 3.2B'
*B 'CFIVOC' ' DECK-ID 34A FORTRAN 3.2B'
*B 'CKNAME' ' DECK-ID 36A FORTRAN 3.2B'
*B 'CNVT' ' DECK-ID 01A FORTRAN 3.2B'
*B 'CONV' ' DECK-ID 03F FORTRAN 3.2B'
*B 'DIAG' ' DECK-ID 04F FORTRAN 3.2B'
*B 'DXP9' ' DECK-ID 05F FORTRAN 3.2B'
*B 'DFLOT' ' DECK-ID 06F FORTRAN 3.2B'
*B 'DUMVOL' ' DECK-ID 35F FORTRAN 3.2B'
*B 'GETC' ' DECK-ID 14F FORTRAN 3.2B'
*B 'GETF' ' DECK-ID 04A FORTRAN 3.2B'
*B 'GETSYM' ' DECK-ID 07F FORTRAN 3.2B'
*B 'GPUT' ' DECK-ID 02A FORTRAN 3.2B'
*B 'IGETCF' ' DECK-ID 15F FORTRAN 3.2B'
*B 'IOPRBA' ' DECK-ID 08F FORTRAN 3.2B'
*B 'PACK' ' DECK-ID 09F FORTRAN 3.2B'
*B 'Q8PRMS' ' DECK-ID 10F FORTRAN 3.2B'
*B 'RDLABL' ' DECK-ID 10A FORTRAN 3.2B'
*B 'STORE' ' DECK-ID 11F FORTRAN 3.2B'
*B 'SYMBOL' ' DECK-ID 03A FORTRAN 3.2B'
*B 'ENDDO' ' DECK-ID 29A FORTRAN 3.2B'
*B 'GNST' ' DECK-ID 05A FORTRAN 3.2B'
*B 'OPTION' ' DECK-ID 16F FORTRAN 3.2B'
*B 'OUTENT' ' DECK-ID 06A FORTRAN 3.2B'
*B 'PHASEA' ' DECK-ID 07A FORTRAN 3.2B'
*B 'PLABEL' ' DECK-ID 08A FORTRAN 3.2B'
*B 'STCHAR' ' DECK-ID 11A FORTRAN 3.2B'
*B 'TYPE' ' DECK-ID 12A FORTRAN 3.2B'
*B 'SAVEID' ' DECK-ID 13A FORTRAN 3.2B'
*B 'LOCLA4' ' DECK-ID 22F FORTRAN 3.2B'
*B 'DUMYA4' ' DECK-ID 23F FORTRAN 3.2B'
*B 'ARITH' ' DECK-ID 14A FORTRAN 3.2B'
*B 'SUBSCR' ' DECK-ID 17A FORTRAN 3.2B'
*B 'TREE' ' DECK-ID 41A FORTRAN 3.2B'
*B 'ENDLOC' ' DECK-ID 17F FORTRAN 3.2B'

*T
*K,I8
*N,FORTA4,,,B
*K,I6
*K,P8
*P,F,,,MARKER

606

```

*B 'FTNB' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
*B 'GOA' ' DECK-ID 02F FORTRAN 3.2B'
*B 'CFIVOC' ' DECK-ID 34A FORTRAN 3.2B'
*B 'CKNAME' ' DECK-ID 36A FORTRAN 3.2B'
*B 'CNVT' ' DECK-ID 01A FORTRAN 3.2B'
*B 'CONV' ' DECK-ID 03F FORTRAN 3.2B'
*B 'DIAG' ' DECK-ID 04F FORTRAN 3.2B'
*B 'DXP9' ' DECK-ID 05F FORTRAN 3.2B'
*B 'DFLOT' ' DECK-ID 06F FORTRAN 3.2B'
*B 'DUMVOL' ' DECK-ID 35F FORTRAN 3.2B'
*B 'GETC' ' DECK-ID 14F FORTRAN 3.2B'
*B 'GETF' ' DECK-ID 04A FORTRAN 3.2B'
*B 'GETSYM' ' DECK-ID 07F FORTRAN 3.2B'
*B 'GPUT' ' DECK-ID 02A FORTRAN 3.2B'
*B 'IGETCF' ' DECK-ID 15F FORTRAN 3.2B'
*B 'IOPRBA' ' DECK-ID 08F FORTRAN 3.2B'
*B 'PACK' ' DECK-ID 09F FORTRAN 3.2B'
*B 'Q8PRMS' ' DECK-ID 10F FORTRAN 3.2B'
*B 'RDLABL' ' DECK-ID 10A FORTRAN 3.2B'
*B 'STORE' ' DECK-ID 11F FORTRAN 3.2B'
*B 'SYMBOL' ' DECK-ID 03A FORTRAN 3.2B'
*B 'ENDDO' ' DECK-ID 29A FORTRAN 3.2B'
*B 'GNST' ' DECK-ID 05A FORTRAN 3.2B'
*B 'OPTION' ' DECK-ID 16F FORTRAN 3.2B'
*B 'OUTENT' ' DECK-ID 06A FORTRAN 3.2B'
*B 'PHASEA' ' DECK-ID 07A FORTRAN 3.2B'
*B 'PLABEL' ' DECK-ID 08A FORTRAN 3.2B'
*B 'STCHAR' ' DECK-ID 11A FORTRAN 3.2B'
*B 'TYPE' ' DECK-ID 12A FORTRAN 3.2B'
*B 'SAVEID' ' DECK-ID 13A FORTRAN 3.2B'
*B 'LOCLA5' ' DECK-ID 24F FORTRAN 3.2B'
*B 'DUMYA5' ' DECK-ID 25F FORTRAN 3.2B'
*B 'BOOPR' ' DECK-ID 33A FORTRAN 3.2B'
*B 'CKIVC' ' DECK-ID 35A FORTRAN 3.2B'
*B 'IOSPR' ' DECK-ID 37A FORTRAN 3.2B'
*B 'ENDLOC' ' DECK-ID 17F FORTRAN 3.2B'

```

```

*T
*K,I8
*N,FORTA5,,,B
*K,I6
*K,P8
*P,F

```

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*B 'FTNB' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
*B 'GOB' ' DECK-ID 26F FORTRAN 3.2B'
*B 'CNVT' ' DECK-ID 01A FORTRAN 3.2B'
*B 'DUMMY' ' DECK-ID 01B FORTRAN 3.2B'
*B 'FCMSTK' ' DECK-ID 02B FORTRAN 3.2B'
*B 'GETSYM' ' DECK-ID 07F FORTRAN 3.2B'
*B 'IOPRBB' ' DECK-ID 27F FORTRAN 3.2B'
*B 'KCPART' ' DECK-ID 03B FORTRAN 3.2B'
*B 'KOUTPT' ' DECK-ID 04B FORTRAN 3.2B'
*B 'KPCSTK' ' DECK-ID 05B FORTRAN 3.2B'
*B 'KPC3PR' ' DECK-ID 06B FORTRAN 3.2B'
*B 'KSYMGN' ' DECK-ID 07B FORTRAN 3.2B'
*B 'LABKPC' ' DECK-ID 08B FORTRAN 3.2B'
*B 'LABLER' ' DECK-ID 09B FORTRAN 3.2B'
*B 'PUNT' ' DECK-ID 10B FORTRAN 3.2B'
*B 'CONV' ' DECK-ID 03F FORTRAN 3.2B'
*B 'Q8PRMS' ' DECK-ID 10F FORTRAN 3.2B'
*B 'STOREB' ' DECK-ID 34F FORTRAN 3.2B'

```


607

*B 'SYMBOL' ' DECK-ID 11B FORTRAN 3.2B'
*B 'TSALOC' ' DECK-ID 12B FORTRAN 3.2B'
*B 'ARAYSZ' ' DECK-ID 42A FORTRAN 3.2B'
*B 'ASSEM' ' DECK-ID 13B FORTRAN 3.2B'
*B 'BANANA' ' DECK-ID 14B FORTRAN 3.2B'
*B 'BGINDO' ' DECK-ID 15B FORTRAN 3.2B'
*B 'ENDJ' ' DECK-ID 16B FORTRAN 3.2B'
*B 'ENTCOD' ' DECK-ID 17B FORTRAN 3.2B'
*B 'HELEN' ' DECK-ID 18B FORTRAN 3.2B'
*B 'INXRST' ' DECK-ID 19B FORTRAN 3.2B'
*B 'NOPROC' ' DECK-ID 20B FORTRAN 3.2B'
*B 'PHASEB' ' DECK-ID 21B FORTRAN 3.2B'
*B 'READIR' ' DECK-ID 22B FORTRAN 3.2B'
*B 'SUBFUN' ' DECK-ID 23B FORTRAN 3.2B'
*B 'SYMSCN' ' DECK-ID 28A FORTRAN 3.2B'
*B 'ACP' ' DECK-ID 24B FORTRAN 3.2B'
*B 'AFIDL' ' DECK-ID 25B FORTRAN 3.2B'
*B 'ASUPER' ' DECK-ID 26B FORTRAN 3.2B'
*B 'CGOTO' ' DECK-ID 27B FORTRAN 3.2B'
*B 'FINK' ' DECK-ID 28B FORTRAN 3.2B'
*B 'INTRAM' ' DECK-ID 29B FORTRAN 3.2B'
*B 'PARTSB' ' DECK-ID 30B FORTRAN 3.2B'
*B 'SUBPR1' ' DECK-ID 31B FORTRAN 3.2B'
*B 'SUBPR2' ' DECK-ID 32B FORTRAN 3.2B'
*B 'SUBPR3' ' DECK-ID 33B FORTRAN 3.2B'
*B 'ARITHR' ' DECK-ID 34B FORTRAN 3.2B'
*B 'ENDLOC' ' DECK-ID 17F FORTRAN 3.2B'

*T
*K,I8
*N,FORTB1,,,B
*K,I6
*K,P8
*P,F

*B 'FTNB' ' COPYRIGHT CONTROL DATA CORPORATION 1973'

*B 'GOC' ' DECK-ID 28F FORTRAN 3.2B'
*B 'BKDOWN' ' DECK-ID 01C FORTRAN 3.2B'
*B 'BLDUP' ' DECK-ID 02C FORTRAN 3.2B'
*B 'BSS' ' DECK-ID 03C FORTRAN 3.2B'
*B 'CHKWD' ' DECK-ID 04C FORTRAN 3.2B'
*B 'CHOP' ' DECK-ID 05C FORTRAN 3.2B'
*B 'CL12' ' DECK-ID 06C FORTRAN 3.2B'
*B 'CON' ' DECK-ID 07C FORTRAN 3.2B'
*B 'COUNT' ' DECK-ID 08C FORTRAN 3.2B'
*B 'DATAST' ' DECK-ID 09C FORTRAN 3.2B'
*B 'GETSYM' ' DECK-ID 10C FORTRAN 3.2B'
*B 'INOUT' ' DECK-ID 11C FORTRAN 3.2B'
*B 'IOPRBC' ' DECK-ID 29F FORTRAN 3.2B'
*B 'IXOPT' ' DECK-ID 12C FORTRAN 3.2B'
*B 'LABEL' ' DECK-ID 14C FORTRAN 3.2B'
*B 'LABIN' ' DECK-ID 15C FORTRAN 3.2B'
*B 'PHASEC' ' DECK-ID 13C FORTRAN 3.2B'
*B 'Q8PRMS' ' DECK-ID 10F FORTRAN 3.2B'
*B 'QXLD' ' DECK-ID 16C FORTRAN 3.2B'
*B 'REED' ' DECK-ID 17C FORTRAN 3.2B'
*B 'SKIP' ' DECK-ID 18C FORTRAN 3.2B'
*B 'SYMSCN' ' DECK-ID 19C FORTRAN 3.2B'
*B 'ENDLOC' ' DECK-ID 17F FORTRAN 3.2B'

*T
*K,I8
*N,FORTC1,,,B

608

*K,I6

*K,P8

*P,F

*B 'FTNB' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
*B 'GOOD' ' DECK-ID 30F FORTRAN 3.2B'
*B 'AMOUT' ' DECK-ID 01D FORTRAN 3.2B'
*B 'ADMAX' ' DECK-ID 02D FORTRAN 3.2B'
*B 'BEGINO' ' DECK-ID 03D FORTRAN 3.2B'
*B 'BKDWN' ' DECK-ID 04D FORTRAN 3.2B'
*B 'COUNT' ' DECK-ID 05D FORTRAN 3.2B'
*B 'FINISH' ' DECK-ID 06D FORTRAN 3.2B'
*B 'GETSYM' ' DECK-ID 10C FORTRAN 3.2B'
*B 'IACON' ' DECK-ID 07D FORTRAN 3.2B'
*B 'IHCON' ' DECK-ID 08D FORTRAN 3.2B'
*B 'INDEX' ' DECK-ID 09D FORTRAN 3.2B'
*B 'IOPRBD' ' DECK-ID 31F FORTRAN 3.2B'
*B 'LABOUT' ' DECK-ID 10D FORTRAN 3.2B'
*B 'NP2OUT' ' DECK-ID 11D FORTRAN 3.2B'
*B 'NPUNCH' ' DECK-ID 12D FORTRAN 3.2B'
*B 'NWRITE' ' DECK-ID 13D FORTRAN 3.2B'
*B 'PACK' ' DECK-ID 09F FORTRAN 3.2B'
*B 'PHASE6' ' DECK-ID 14D FORTRAN 3.2B'
*B 'Q8PRMS' ' DECK-ID 10F FORTRAN 3.2B'
*B 'RBDX' ' DECK-ID 15D FORTRAN 3.2B'
*B 'RBPk' ' DECK-ID 16D FORTRAN 3.2B'
*B 'SYMScN' ' DECK-ID 17D FORTRAN 3.2B'
*B 'TABDEC' ' DECK-ID 18D FORTRAN 3.2B'
*B 'UNPUNC' ' DECK-ID 19D FORTRAN 3.2B'
*B 'CONV' ' DECK-ID 33F FORTRAN 3.2B'
*B 'ENDLOC' ' DECK-ID 17F FORTRAN 3.2B'

*T

*K,I8

*N,FORTD1,,,B

*K,I6

*K,P8

*P,F

*B 'FTNB' ' COPYRIGHT CONTROL DATA CORPORATION 1973'
*B 'GOE' ' DECK-ID 32F FORTRAN 3.2B'
*B 'AMOUT' ' DECK-ID 01E FORTRAN 3.2B'
*B 'ADMAX' ' DECK-ID 02E FORTRAN 3.2B'
*B 'BEGINO' ' DECK-ID 03E FORTRAN 3.2B'
*B 'BKDWN' ' DECK-ID 04E FORTRAN 3.2B'
*B 'CONV' ' DECK-ID 33F FORTRAN 3.2B'
*B 'COUNT' ' DECK-ID 05E FORTRAN 3.2B'
*B 'FINISH' ' DECK-ID 06E FORTRAN 3.2B'
*B 'GETSYM' ' DECK-ID 10C FORTRAN 3.2B'
*B 'IACON' ' DECK-ID 07E FORTRAN 3.2B'
*B 'IHCON' ' DECK-ID 08E FORTRAN 3.2B'
*B 'INDEX' ' DECK-ID 09E FORTRAN 3.2B'
*B 'IOPRBD' ' DECK-ID 31F FORTRAN 3.2B'
*B 'LABOUT' ' DECK-ID 10E FORTRAN 3.2B'
*B 'NP2OUT' ' DECK-ID 11E FORTRAN 3.2B'
*B 'NPUNCH' ' DECK-ID 12E FORTRAN 3.2B'
*B 'NWRITE' ' DECK-ID 13E FORTRAN 3.2B'
*B 'PACK' ' DECK-ID 09F FORTRAN 3.2B'
*B 'PHASE6' ' DECK-ID 14E FORTRAN 3.2B'
*B 'Q8PRMS' ' DECK-ID 10F FORTRAN 3.2B'
*B 'RBDX' ' DECK-ID 15E FORTRAN 3.2B'
*B 'RBPk' ' DECK-ID 16E FORTRAN 3.2B'
*B 'SETPRT' ' DECK-ID 17E FORTRAN 3.2B'

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*B 'SYMSCN' ' DECK-ID 17D FORTRAN 3.2B'
*B 'TABDEC' ' DECK-ID 18E FORTRAN 3.2B'
*B 'UNPUNC' ' DECK-ID 19E FORTRAN 3.2B'
*B 'ENDLOC' ' DECK-ID 17F FORTRAN 3.2B'
*T
*K, I8
*N, FORTE1,,,8

