

DESIGN AND IMPLEMENTATION OF AN INPUT/OUTPUT
SCHEDULER FOR THE TIME-SHARING SYSTEM OF THE
GENERAL ELECTRIC CORPORATE RESEARCH AND DEVELOPMENT CENTER

by

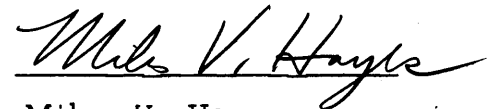
MICHAEL B. RUBENS

Bachelor of Engineering Project Report

June 1972

THAYER SCHOOL OF ENGINEERING
DARTMOUTH COLLEGE
HANOVER, NEW HAMPSHIRE

APPROVED:



Miles V. Hayes

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ABSTRACT

The problem is to design and implement an Input/Output Scheduler for the General Electric Corporate Research and Development Center. Given the Center's current time-sharing environment of master and slave modes, a slave mode scheduling system is proposed. This system is composed of two distinct levels: a monitor, which handles all external input/output and scheduling, comprises the upper level; the lower level contains all the peripheral driver modules, which, while also operating in slave mode, transfer the data to/from such peripheral devices as line printers and card punches. Just such a system has been successfully written and is operating on the Center's computer system.

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INTRODUCTION

The purpose of this paper is to provide an overview of an Input/Output scheduling system which was designed and implemented at the General Electric Research and Development Center during the period June 1971 to September 1971. The object of this work was to build a software operating system which while running under the Center's current time-sharing system would:

- (1) decrease the amount of code that must be core resident;
- (2) increase absolute number and types of I/O devices that could be brought on-line simultaneously;
- (3) use the existing interfaces;
- (4) allow the operator via a command language to bring I/O devices arbitrarily on/off line;
- (5) and allow for experimentation and debugging of new I/O devices.

This project report is divided into three major sections. The first provides an overview of the hardware, the current operating executive system, and the old Listener structure. The second section deals with the overall design of the new Listener structure (i.e. the Input/Output Scheduler). Lastly, the third part gives a detailed view of the internal structure of the Monitor and an I/O driver prototype.

CHAPTER I
SYSTEM OVERVIEW

- I.1 Hardware
- I.2 Executive System
- I.3 Old Listener Structure

I.1 Hardware

The hardware configuration is as shown in Figure 1. The basic system consists of a single processor, a real time - I/O controller, and two 64K memory modules. The central processor is a GE 605, which has four base address registers, (BAR), instead of the one as in the GE 635. These provide memory protection, automatic relocation, and optional write inhibit. Each register has two fields, one which denotes the origin of the current program in core and the other which denotes its length. The hardware automatically checks all logical addresses produced by the processor (when operating in slave mode). A process (program in execution) can thus consist of up to 4 physically disjoint segments (which all must be in core when the process is in execution). Since the address field is 18 bits long and the 2 high order bits are used to designate the BAR, a segment has a maximum length of 2^{16} words (65,536). The write inhibit bit, which is associated with each base register, can be used to prevent modification of a segment. This is particularly useful in implementation of pure procedure. The processor operates in either of two modes: master or slave. In addition, certain privileged instructions (for performing I/O and manipulating BAR's) can only be executed in master mode.

The RT-IOC serves as the input-output interface for the system. This device is capable of transmitting data in an asynchronous manner between core memory and up to 32 peripheral devices.

The memory is composed of two memory controllers and 64 K of 36-bit (plus 1 parity bit) magnetic core storage with a 1.0 microsecond read-restore memory cycle time per controller.

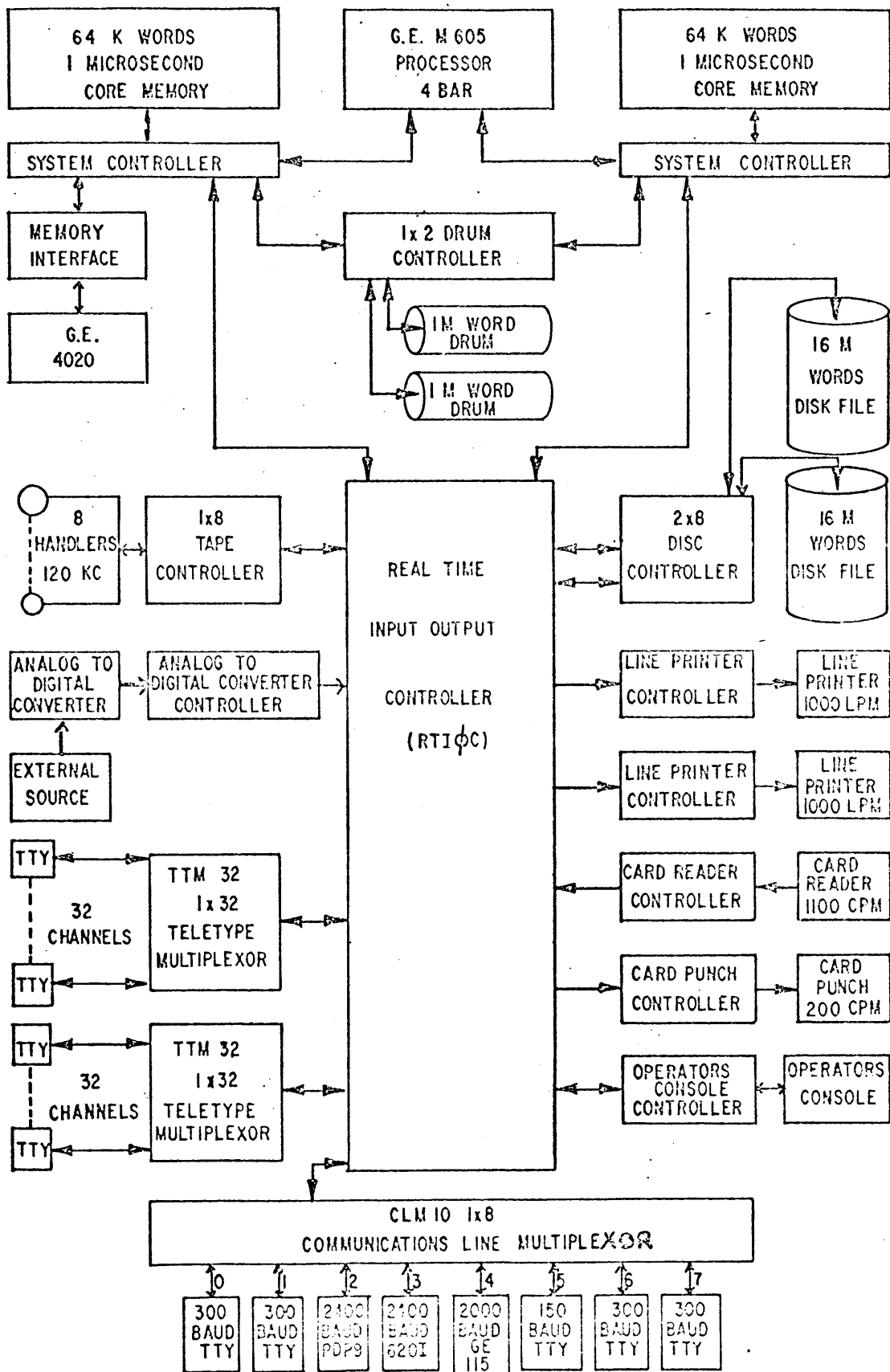


Figure 1

I.2 Executive system

The exec is designed to perform two major functions (see Appendix I) [6]. First of all, it provides a set of primitives accessible to all users which serve to enhance the hardware instruction repertoire of the machine. Thus the user sees a pseudo machine with extended capabilities. It is convenient to divide the primitive commands into the following three categories:

1. File manipulation (I/O commands) -- primitives for the reading, writing, appending, and scratching of either sequential or random access files. These enable the user to perform input/output operations which are expressed in terms of logical file parameters. Thus the user is shielded from the intricacies of the actual hardware device being dealt with and the actual physical location of the file.

2. Process manipulation (control commands) -- primitives for creating, terminating, and blocking processes, setting up fault handling modes, for making memory requests and readjusting base address register settings.

3. Directory manipulation (file and event commands) -- primitives for creating and destroying directories and entries within directories and for modifying and reading information within entries. The file commands provide the user with an interface to the file system. They allow him to open cataloged and scratch files, to catalog a file or directory, to unsave a file or directory. The user is thus allowed to create a file structure and to manipulate it within this structure.

The second major function performed by the exec is the allocation of resources to the active processes in the system demanding service. These resources include central processor time, core space, space on mass storage devices and use of peripherals. The exec takes into account the current utilization of the system resources as well as the priority and history of the various processes demanding service in making decisions in this area.

The exec itself is divided into two sections: the Master Mode Executive and the Slave Mode Executive (see Figure 2). The Master Mode Executive is the only portion of the entire Executive that executes code in master mode. The Slave Mode Executive operates in slave mode. The Master Mode Executive can be categorized by its three principal functions:

1. The execution of system input/output primitive commands -- the code for initiating data transfers is contained in the master exec. It translates these primitive commands, which are expressed in terms of logical parameters, into commands recognized by the hardware. The major reason for this is that I/O transactions must use absolute core addresses. Such information is invisible to code which is executed in slave mode where addresses are logical, relative to some BAR. A further reason is that the instruction for initiating a transfer can only be executed in master mode. This prevents slave processes from directly initiating their own transactions, a prohibition which is indispensable when storage on peripheral devices is shared by many users. In addition to servicing the interrupts generated, the Master Mode Executive returns to the user the physical and logical status of the operation.

2. The running of processes -- code in the Master Exec is also responsible for setting up and transferring control to slave processes. The reason for this is that the instructions for loading BAR's can only be executed in master mode. Such a restriction is necessary in a system in which several processes must coexist in core.

3. Fault handling, disk and drum allocation, and hardware malfunction servicing -- master mode is entered in one of two ways: either by interrupts or by faults. In contrast to interrupts, which are caused by signals from peripheral devices, faults are caused by the process in control when the fault occurred (e.g. accumulator overflow, illegal instruction, illegal memory reference, etc.). If the process has previously indicated its desire to handle such faults,

control is returned by the master exec to the fault handling code of the slave process responsible for the fault. If the process has not set up its own fault handling mechanisms, the process is terminated when such a fault occurs.

A fault may also be caused by a slave process by executing a master mode entry (MME) instruction. Such an occurrence indicates that the slave process wishes the exec to execute a primitive on its behalf. The exec determines the identity of the primitive as well as its parameter by examining the contents of the registers at the time the MME occurred. Those primitives dealing with file manipulations are handled by the exec's primitive handler. All other primitives are passed on to the slave exec.

The Slave Mode Executive can likewise be categorized by its three principal functions:

1. The allocation of system resources -- it is its responsibility to allocate the central processor and memory to the slave processes. The slave exec must also make decisions concerning the allocation of certain peripheral devices (e.g. magnetic tapes) to slave processes. The only exception to the allocation of system resources is the space on the shared mass storage device, which is allocated by the master exec.
2. Scheduling and swapping -- the slave exec determines core utilization and process swapping. This also includes (process termination).
3. Directory and process primitives -- directory and process manipulation primitives are passed on to the slave exec by the master exec. The slave exec then executes the process and file system primitives and it maintains the file system catalog (directory). It is the slave exec that has the real control over the operating system.

CONCEPTUAL MULTILEVEL OPERATING SYSTEM

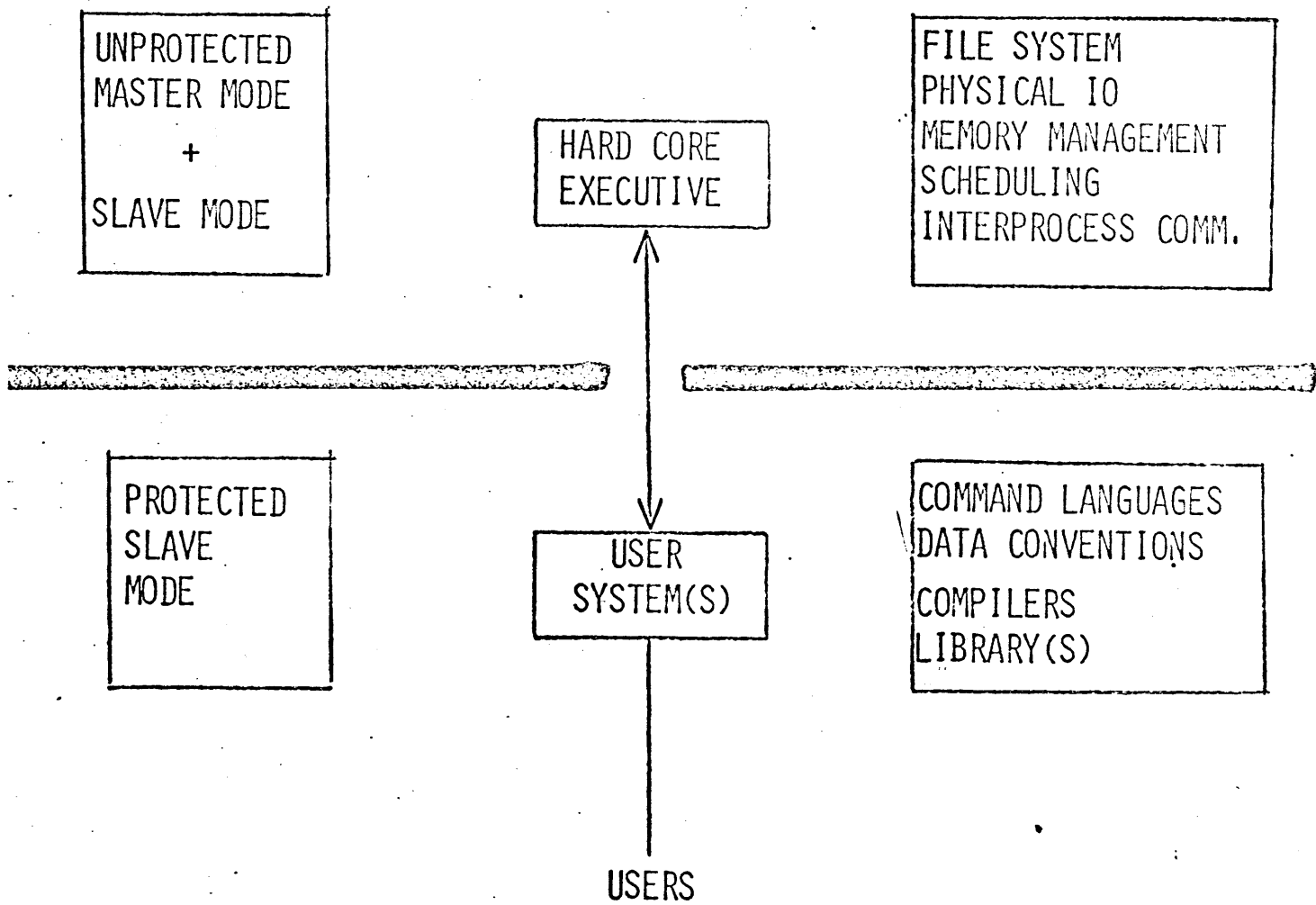


Figure 2

I.3 Old Listener Structure

The Listener is the one slave process that is always in the system and it plays a rather unique role [8]. As can be seen from Figure 3, the Listener is the most important slave process of the multi-level job structure. In some senses, it is an extension of the Executive. Because of its functional responsibilities, which include the handling of all teletypes and communications files, the validation of all user numbers, the spawning of all other slave processes, the collection of system statistics, and the handling of the system output to such devices as the line printer and card punch, the Listener must be "booted-in" with the Executive as part of the system start-up deck.

In order to better describe the relative importance of the Listener, it is most convenient to discuss the Listener by dividing it into the following four functional categories:

1. The handling of teletypes and communications files -- this category or functional area is itself composed of three sub-areas. On the physical level, the Listener has the responsibilities of answering a ringing phone; hanging-up the phone after a sign-off; and taking care of a disconnect. On the message level, the Listener handles all panic stops (i.e. breaks) such editing as character or line deletions for teletype input; and the transferring of data between teletype and the slave process wishing to communicate via the teletype.

On the operator level, the Listener is in charge of sending all operator messages and warnings to the teletype users. Due to the Listener's control overall teletype, the operator can selectively enable or disable any and all teletypes.

2. System statistics and accounting -- by the very nature of its position in the job structure hierarchy, the Listener is the most logical process to collect system statistics (e.g. the number of teletype connections, the number of characters input and output,

the maximum number of users, the number of communication file reads and writes, and the number of processes spawned). Since the Listener handles the validation of user numbers, it likewise creates the accounting blocks for user billing.

3. Spawns other slave processes -- it spawns other slave processes as they are required and passes on to them the parameters they need. Hence all processes are descendents of the Listener.

4. SYSOUT processing -- SYSOUT (i.e. System Output) processing includes the printing and punching of output generated by any slave process. A slave process which has created data to be outputted, can do so by copying that data into a cataloged file (or by copying the data into a scratch file and then cataloging the file [2]). Next the slave process opens the system file 'PRINT-FILE-QUEUE' if the data is to be printed on the high-speed printer or opens the system file 'PUNCH-FILE-QUEUE' if the data are to be punched. After a successful open, the slave process appends a 64 word descriptor to the appropriate file. The descriptor contains the complete tree-name of the file to be outputted and identifier bits (e.g. the bits describe the format of the data and whether or not a header has been supplied). Via the event structure [1], the slave process 'causes' the appropriate system event (either PRINT-FILE-EVENT or PUNCH-FILE-EVENT). The Listener is then 'notified' via the event mechanism. It responds by reading the appropriate 'FILE-QUEUE', opening the data file, and outputting it to the proper device. This assumes that the device is currently inactive. If the peripheral device is busy, the Listener simply does nothing for the moment. When it finishes the current data file, it will check to see if there are any more des-

criptors since it last looked. If so, it will start the outputting of the next data file. Although it is possible for any slave process to have a data file punched or printed, the slave process may never access the peripheral device which receives the data. The Listener in addition to the one line printer and card punch also owns the card reader.

OLD LISTENER STRUCTURE

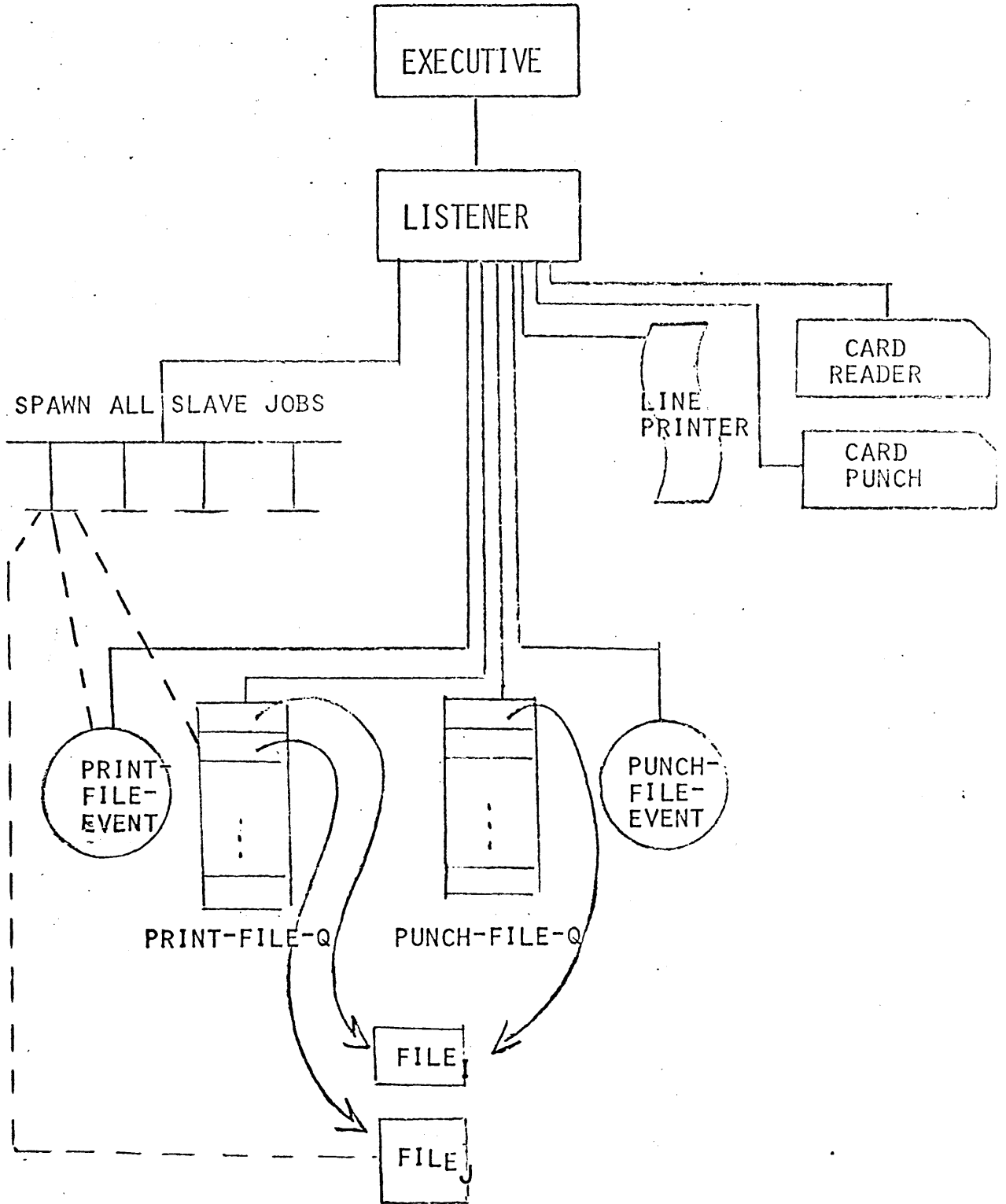


Figure 3

CHAPTER II
NEW LISTENER DESIGN

- II.1 Problem Statement
- II.2 Limitations of the Old Listener Design
- II.3 Design Criteria
- II.4 System Design

II.1 Problem Statement

Given the General Electric Research and Development Center's time-sharing system with its hierarchial job and file structure as the programming environment, the problem is to design and then implement in assembly language an Input/Output Scheduler system, which would assume the Listener's current functional duties of card punching, card reading and line printing, and which would increase the utilization of the system resources (e.g. line printer, card punch, core) while leaving as much of the existing support interfaces unaffected.

II.2 Limitations of the Old Listener Design

The Listener, as originally designed, probably served well in its apprentice years. But just as the Executive system has grown to expand its capabilities, the Listener has been forced to grow. Growth in an assembly language program usually means modifications of the existing code and especially additional code to handle the new or unthought of situations. Thus growth (or attempted extra added responsibilities) points out the Listener's three major inter-related limitations with regard to peripheral device I/O scheduling:

1. Core resident -- the Listener, as mentioned earlier, is a core resident module. It is currently 18 1/2 K in size (45000 octal words). Any additions which would obviously increase its size would do so only at the expense of the core designated for slave processes since the additional code would also become core resident. Since the Listener must be core resident it must be made as small as possible by removing non-essential code. The code which handles the line printer, card punch and reader is non-essential.

2. Coded for one peripheral device of each type -- the Listener was coded to handle only one line printer and one card reader and one card punch. Although the Center has two line printers, only one can truly be on-line. This is an obvious waste of system resources. The Listener could be rewritten to handle the second line printer, but this would just increase the amount of code that would then become core resident. This would not answer the problem if yet another line printer was purchased or another card punch.

3. Debugging and experimentation extremely limited -- since the Listener controls all teletypes and spawns all slave processes, any debugging must be done on a dedicated system.

This necessarily limits the amount of time that can be spent trying out modifications. For to crash the Listener is to crash the entire system.

II.3 Design Criteria

The software system to be designed is defined by the problem statement. The assembly language program(s) must run under the General Electric R & D time-sharing environment. The new system must handle Input/Output scheduling formerly handled by the Listener and it should utilize the existing interfaces as much as possible. The criteria that this system must meet are the following:

1. By decreasing a functional responsibility of the Listener, the amount of code that must be core resident must be likewise decreased. And if possible, the total amount of code to handle the peripheral devices should be kept to a minimum, thus freeing core for the other slave processes.
2. The number and type of I/O devices that could be brought on-line simultaneously should be considered arbitrary. The code must be general enough to handle such additions as a third line printer or graphics plotter.
3. The existing software interfaces should be used as much as possible in order to minimize the amount of new code that need be written. This also minimizes the amount of time spent debugging the new system.
4. The operator through some command language must be able to bring I/O devices arbitrarily on/off line. This is necessary in order to handle hardware failures. Also through this command language, the operator must be allowed to re-start or stop the output on any of the I/O devices.
5. The new system must allow for experimentation and debugging of new I/O devices without danger of crashing the entire system. If possible all debugging should be able to be done on-line in order to give the programmer the greatest amount of on time.

II.4 System Design

The development of this Input/Output Scheduling system was evolutionary (rather than a selection of one from a number of alternatives), and only the final result will be described here (see Figure 4), although justifications will be given where possible. To satisfy the first criterion stated above, it was decided that the system should be separated into two major functional areas. The first included such functional duties as the handling of all external input/output, which is composed of slave process and operator requests, the pre- and post-processing of a request, billing, and the scheduling of requests. The second was comprised largely of the transferring of data from the file to the appropriate I/O device. This separation was accomplished by coding the two areas as two distinct prototype modules -- a Monitor and a peripheral driver prototype. This method minimizes at all times the amount of code that must be in core at any given time. For the Monitor need be in core only for a few milliseconds to handle a request. On the other hand, a particular peripheral driver, such as the line printer module, may be transferring data for a length of time as small as a few seconds to as much as an hour or more. Since the two functional areas are independent, the amount of core that is tied up is minimized if they are coded separately. Since any module that is not busy is legible to be swapped out of core.

The second criterion is easily met. The fact that the entire system was modularized allows for an arbitrary number and type of I/O devices that could be brought simultaneously on/off line. The Monitor has a complete set of assembly time parameters (i.e. MACROS) which allow for the definition of an arbitrary number and type of I/O devices. It has a command language via which the operator may selectively bring a particular device on or off line. Also the peripheral driver prototype may be modified (e.g. character conversion tables) to handle the new device or through an assembly time parameter handle an additional device of an already defined type or new type.

The third criterion simply requires that the old interfaces and conventions are retained. Since the author had no objections to or improvements for the old methods of interfacing, these were retained unmodified. The fourth criterion has already been explained. There is a command language via which the operator is able to bring I/O devices arbitrarily on/off line.

It is the fifth criterion that sums up the appropriateness of the modular system design. Since the Monitor is just another slave process spawned by the Listener, the programmer is allowed to spawn more than one of these at a time. Through the use of the command language, the programmer can arbitrarily assign the various I/O devices to the currently running Monitors. Thus while the current version of the Monitor is running, an experimental version may be spawned to test out some new feature. If the regular or experimental version of the Monitor should crash, the rest of the system and users would be unaffected. Likewise new peripheral driver prototypes may be tested in a similar fashion.

NEW LISTENER STRUCTURE

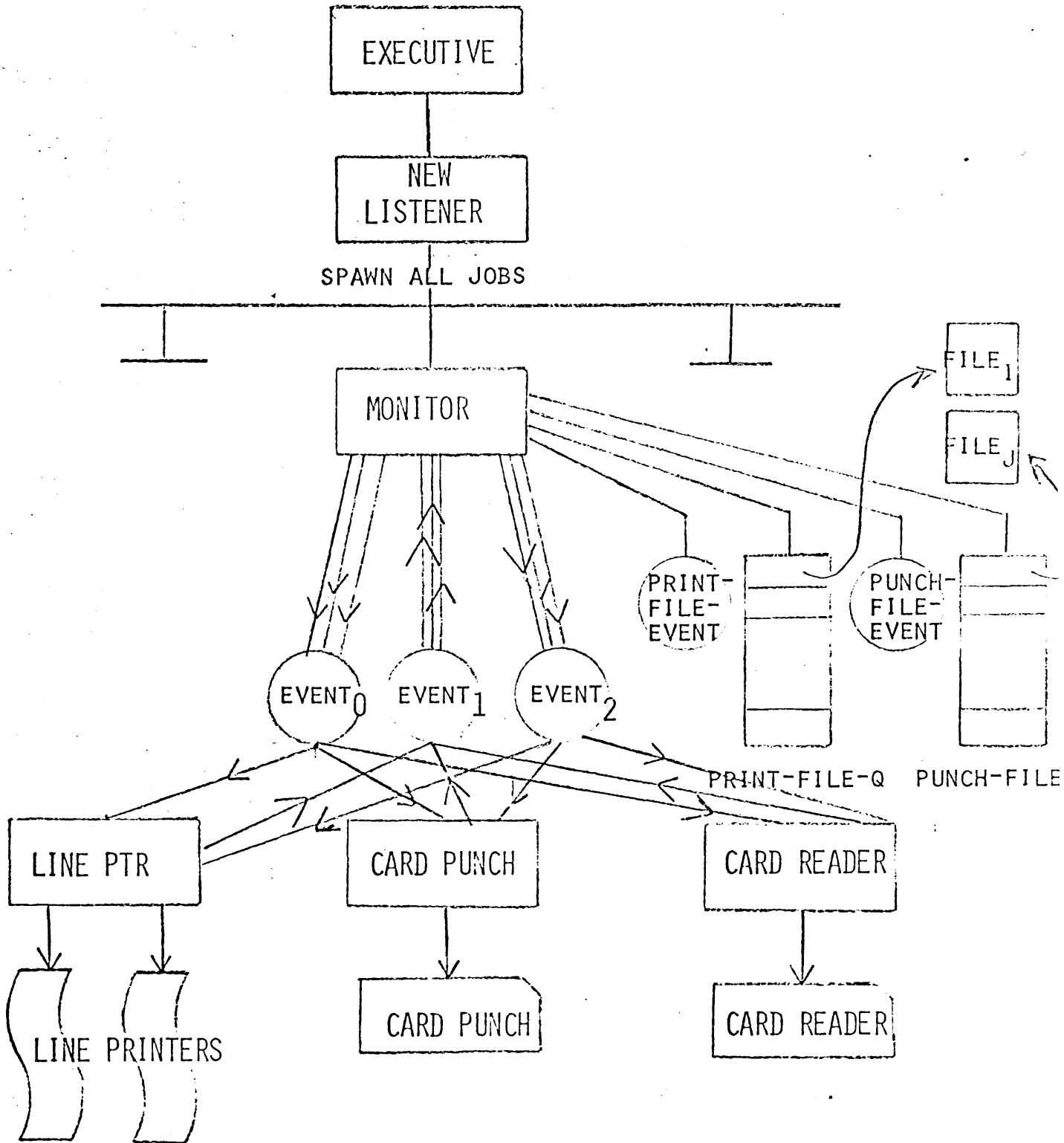


Figure 4

Before discussing the coding in detail of the new Listener structure, it is felt that a more general discussion of the organization of both the Monitor and the peripheral driver prototype would be most enlightening. Although not a design criterion, all modules written were designed to be multi-programmed [3]. Each module is therefore internally organized around a set of queues. Generally speaking, each resource has associated with it a queue on which are put all tasks currently requesting that resource. In a more general sense, the queues are used for synchronizing and communicating between routines.

These routines are in essence a set of algorithms. The Monitor is composed of five major algorithms (see Figure 5); the peripheral driver prototype, four algorithms (see Figure 11). An algorithm, as defined by Trakhtenbrot in Algorithms and Automatic Computing Machines, is a list of instructions specifying a sequence of operations which will give the answer to any problem of a given type. The important point to note is that the list of instructions is never modified. Hence two or more persons or processes can be executing the same algorithm simultaneously (i.e. multi-programming). The only thing that can be modified is the individual data area of each process. This now allows for the definition of a task (sometimes referred to as a process) [5]. A task is represented by a pair of words. The first is called the instruction pointer, or IP, which points to the next instruction in the algorithm list to be executed. The second word points to the task's data area. It is called the environment pointer, or EP.

What follows is now that generalized discussion of the major algorithms first in the Monitor and then in the peripheral driver prototype. The algorithms are described in a pseudo-algol-like language to help the reader conceptualize the process.

MAJOR ALGORITHMS OF MONITOR

- 1) EXTERNAL INPUT
- 2) SCHEDULER
- 3) RUN-SERVICE
- 4) NOTIFY-SERVICE
- 5) TERMINATION

Figure 5

MONITOR

1) External input algorithm

The External Input algorithm is executed on every interrupt from either a slave process requesting a data file to be outputted or the operator via the command language. At initialization time for the Monitor, a task is created for each system event (e.g. PRINT-FILE-EVENT, PUNCH-FILE-EVENT, or OPERATOR-EVENT) to execute this code. The normal state of each task is to be blocked waiting for an interrupt, or wake-up, signal (see Figure 6, L1). Upon receipt of a signal, the task receiving the interrupt resumes at line L2. The conditional statement tests whether or not the Monitor owns any resources associated with the interrupt request. If not, there is no need to read the request because it cannot be handled now -- no resources at all -- so the task goes blocked again. Hence there is no reason to start the processing of the request, which would only tie up core. This is a safety feature to handle mechanical failures.

If the Monitor does own at least one of the resources requested, the execution proceeds. A job control block is allocated, as well as a read buffer, to hold the slave process 64 word descriptor. The descriptor is then read into core. If the file containing the descriptor was not empty (not End-Of-File), then the job control block is filled in as described by the descriptor. A statistics counter is incremented. The read buffer is released, and the request is then placed on the appropriate waiting, or input, queue. Since a single slave process may place more than one request at a time, the whole list of instructions is repeated until the end of the request file is reached. When this condition occurs, both the read buffer and the job control block are released.

The BRANCH statement is the most interesting statement of the entire algorithm. It is through this instruction that the currently executing task creates a second task. The second task has its IP pointing to the Scheduler algorithm and its EP pointing to some allocated memory. This

1) EXTERNAL INPUT (A PERPETUAL PROCESS)

```

L1: BLOCK(EVENTI, _____)
L2: IF RESOURCEI = 0 THEN L1 ELSE
      BEGIN
          ALOC(JOB CONTROL BLOCK)
          ALOC(READ BUFFER)
          READ(FILEI, (ALOC(READ BUFFER)), STATUS)
          IF STATUS = EOF THEN
              BEGIN
                  DALOC(READ BUFFER)
                  DALOC(JOB TASK BLOCK)
                  BRANCH(SCHEDULER)
                  GO TO L1
              END
          ELSE
              BEGIN
                  CREATE(JCB)
                  COUNTERI := COUNTERI + 1
                  DALOC(READ BUFFER)
                  QUEUE(JCB, INPUT-QI)
              END
          GO TO L2
      END
END

```

Figure 6

task is then placed on the queue associated with the processor. When the currently executing task blocks, the next waiting task on the processor queue is then started. After creating the new task, the current task blocks waiting for another interrupt signal.

2) Scheduler algorithm

The scheduler algorithm is executed by a created task (see Figure 7). The task simply checks each waiting task in all input queues to determine if that waiting task may be started. If not, it merely steps to the next item. If so, the peripheral resources required by that task are allocated to it; the task is removed from the queue; its IP is set to the Run-service algorithm; and it is placed on the processor queue. After stepping through all input queues, this task simply terminates (i. e. transfers control of the processor to the next waiting task without scheduling itself for a restart).

3) Run-service algorithm

Likewise the run-service algorithm is executed by a created task (see Figure 8). All this task need do is pass the proper information to the correct peripheral driver submodule. It is accomplished via the event mechanism. The conditional tests whether or not the submodule received the message. If not, it is re-transmitted. This created task in turn creates another task to wait for the reply from the submodule (IP points to notify-service algorithm). Hence the current task terminates after the BRANCH statement.

4) Notify-service algorithm

This algorithm specifies that the executing task alter its restart address (future IP) to point to the termination algorithm and to wait for an interrupt signal from the submodule which just got passed the information mentioned above (see Figure 9). Thus when the submodule signals a completion of the data transferring, the task will then execute the termination code for that request.

```
2) SCHEDULER (A CREATED PROCESS)

  FOR I = 1 STEP 1 UNTIL (# OF QUEUES) DO
  BEGIN
    FOR J = 1 STEP 1 UNTIL LEN(Q-LISTI) DO
    BEGIN
      IF RUNABLE(Q-LISTI, ELEMENTJ) THEN
      BEGIN
        ALOCRES(Q-LISTI, ELEMENTJ)
        DEQ(Q-LISTI, ELEMENTJ)
        RESTART = RUN-SERVICE
        QUEUE(Q-LISTI, Q$TASK)
      END
    END
  END
END
EXIT
```

Figure 7

3) RUN-SERVICE (A CREATED PROCESS)

```
L1: CAUSE(EVENTI, STATEJ, MESSAGEK, FILEL, NUMBER, ACCESS, STATUS)
    IF STATUS(NUMBER) = 0 THEN L1 ELSE
    BEGIN
    BRANCH(NOTIFY-SERVICE, EVENTI, STATEJ, TERMINATION)
    END
    EXIT
```

Figure 8

5) Termination algorithm

The execution of this algorithm is contingent upon the interrupt signal sent from a submodule via the event mechanism. (See Figure 10). The signal contains coded information. If the status of the signal is bad, then some error processing must proceed. Otherwise a read buffer is allocated; the disposition as specified in the 64 word descriptor is then acted upon; the buffer released. The counter is decremented by one and tested. If zero, then a task is created to attempt to scratch the request file. The resources allocated to this task are then released; another task is created. Since some resources have been released, a task is created to execute the scheduler algorithm. And finally the current task terminates.

4) NOTIFY-SERVICE (A CREATED PROCESS)

```
RESTART = TERMINATION  
NOTIFY(EVENTI, STATEJ)  
EXIT
```

Figure 9

```
5)  TERMINATION  (A CREATED PROCESS)
      IF STATUS = BAD THEN ERRORCHECK ELSE
      BEGIN
      ALOC(READ BUFFER)
      DISPOSITION(FILEI ,JCB)
      DALOC(READ BUFFER)
      IF (COUNTERI := COUNTERI - 1) = 0 THEN BRANCH(SCRATCH, FILEI)
      DALOCRES(JCB)
      BRANCH(SCHEDULER)
      EXIT
      END
```

Figure 10

PERIPHERAL DRIVER PROTOTYPE

1) External input algorithm (see Figure 12)

This algorithm is not to be confused with the external input algorithm of the Monitor. It is called external because the inputs come external to the module, but they come from the Monitor only. At initialization time for the submodule, a task is created to execute this code. Its normal state is blocked waiting for a message from the Monitor. Upon receipt of any message, it must differentiate the information between a true message and a command. Commands deal with the acquiring and relinquishing of a peripheral device. Messages announce that new data are to be handled. If it is new data, a job control block is allocated and filled in. Then two asynchronous tasks are created to read the data from the data file and transfer it to the I/O device. Then the current task goes blocked waiting for another message from the Monitor.

2) Read task algorithm (see Figure 13)

The algorithm is composed of two other algorithms -- P and V (see Figure 15)[3]. It locates an empty buffer; fills it from the data file; and marks it full and ready to be written out to the I/O device. The filling of empty buffers continues until the data file has been exhausted. The task then simply terminates (EXIT).

3) Write task algorithm (see Figure 14)

This algorithm is the complement of the read task algorithm. It locates full buffers; empties them to the peripheral device; marks them empty and continues until there are no more buffers to empty. At that point it releases all resources allocated to this task and creates a task to send a termination message back up to the Monitor. The task itself terminates.

MAJOR ALGORITHMS OF PERIPHERAL DRIVER PROTOTYPE

- 1) EXTERNAL INPUT
- 2) READ TASK
- 3) WRITE TASK
- 4) TERMINATION

Figure 11

```
1) EXTERNAL INPUT (A PERPETUAL PROCESS)
   BLOCK(EVENTI, STATEJ)
   IF MESSAGE = COMMAND THEN GO TO COMMAND-SERVICE ELSE
   BEGIN
       ALOC(JCB)
       CREATE(JCB DESCRIPTOR)
       BRANCH(READTASK, JCB)
       BRANCH(WRITETASK, JCB)
   END
   GO TO EXTERNAL INPUT
```

Figure 12

```
2) READ TASK (A CREATED PROCESS)
   P(EMPTY,JCB)
   FILL(ALOC(READ BUFFER),READ,STATUS)
   V(FULL,JCB)
   IF STATUS = MORE THEN GO TO READ TASK ELSE EXIT
```

Figure 13

```
3) WRITE TASK (A CREATED PROCESS)
   P(FULL,JCB)
   EMPTY(WRITE,DALOC(READ BUFFER),STATUS)
   V(EMPTY,JCB)
   IF STATUS = MORE THEN GO TO WRITE TASK ELSE
   BEGIN
       DALOCRES(JCB)
       BRANCH(TERMINATION,STATEI,MESSAGEJ)
   END
   EXIT
```

Figure 14

P(SEMAPHORE)

```
SEMAPHORE = SEMAPHORE - 1
IF SEMAPHORE < 0 THEN SUSPEND PROCESS
RETURN
```

V(SEMAPHORE)

```
SEMAPHORE = SEMAPHORE + 1
IF SEMAPHORE <= 0 THEN AWAKEN PROCESSES
RETURN
```

Figure 15

4) Termination algorithm (see Figure 16)

The task executing the termination algorithm simply sends a message back up to the Monitor via the event mechanism. The conditional is to test to see whether or not the message was received by the Monitor. The message is re-transmitted until it is received. At this time the task terminates.

4) TERMINATION (A CREATED PROCESS)

```
CAUSE(EVENT1, STATEJ, MESSAGEK, , 1, 1, STATUS)
```

```
IF STATUS(NUMBER) = 0 THEN GO TO TERMINATION ELSE EXIT
```

Figure 16

CHAPTER III
CODE & STRUCTURES

III. Introduction

III.1 Common code & Structures

III.2 Monitor code & Structures

III.3 Peripheral driver prototype code & Structures

III. Introduction

This chapter deals primarily with the internal structures implemented in both the Monitor and the peripheral driver prototype. Since the amount of code written totals close to 400 pages (See APPENDICES II AND III), it would be impractical to detail all of the coding styles, techniques, and structures employed. In order to give the reader the proper flavor and scope of the work, only the most important areas will be discussed. For the super inquisitive, the listings with comments are included in the Appendix.

The chapter is broken down into three sections. The first deals with the universally common structures (e.g. core, queue, and task management). The second and third sections describe particular portions of the Monitor and the peripheral driver prototype, respectively.

III. 1 Common Code & Structures

III.1.1 Memory map

All modules can be separated into four distinct sections (see Figure 17). The first is the code of the algorithms. It is the set of instructions that is never modified. It is followed by the set of constants, lists, and tables that are likewise never modified. The third section contains all the queue heads. And the last section is the dynamic storage, or buffer, area.

These four sections describe the memory map for any module. Since the first two sections are never modified, a simple debugging trick is to compare the first two sections of a module after it has run to its original self. If the two don't match, then the programmer knows that there is a bug in the pure procedure -- modifying an algorithm.

LOCATION-COUNTERS		MEMORY MAP	
	97	HEAD	
	98	*	
	99	*	
	100	*	LOCATION COUNTERS
	101	*	
	102	*	
	103	*	THE FOLLOWING PREDEFINES THE ORDER IN WHICH THE LOCATION
	104	*	COUNTERS WILL OCCUR, INDEPENDENT OF THE ORDER IN WHICH
	105	*	THEY ARE USED WITHIN THE PROGRAM.
	106	*	
000000	107	USE	CODE MAIN PROGRAM SEGMENT
000000	108	ZCODE BSS	0
	109	*	
	110	*	
004620	111	USE	CONST STORAGE FOR CONSTANTS, TABLES
004620	112	ZCONS BSS	0
	113	*	
	114	*	
005140	115	USE	QSTOR FOR ALL QUEUES
005140	116	ZQSTR BSS	0
	117	*	
	118	*	
005300	119	USE	STORE FOR ALL DYNAMIC STORAGE
005300	120	ZSTOR BSS	0
	121	*	
	122	*	
000000	123	USE	CODE CODE LOCATION COUNTER, INITIALLY
	124	*\$*	DISK MMEDEFS

Figure 17

III.1.2 Task control Blocks (TCB)

A task as defined earlier is an EP, IP word pair -- the EP, environment pointer, points to the task's modifiable, or work, area and the IP, instruction pointer, points to the next instruction of some algorithm that the task wishes to execute. As pointed out earlier, this allows for the 'simultaneous' execution of multiple tasks, i.e. multi-programming or parallel processing. In actuality, since there is only one processor on the G.E. system, there can be only one task in execution. All other ready-to-run tasks are queued on the processor queue waiting for the currently executing task to either terminate or block. As soon as this happens, the processor is assigned to the next (top) task of the processor queue. The effect is seemingly parallel processing. While several tasks may be blocked waiting for I/O to complete, another task is executing code.

Since there can be many tasks in various states of execution, the task's EP was standardized (see Figure 18). Each data area is 24 words long with the last half reserved for temporary storage. The first three words deal with Executive status return after the issuance of a system primitive. Control is transferred to word three of the block after the status has been returned. It contains an execute double statement, 'XED', which links this task block onto the processor queue. This moves the task from the blocked state to the ready-to-run state. The remaining four words contain support pointers -- pointers to additional information.

The macro GETT creates a task. The macro will return in symbolic index register T--T for task--a pointer to an allocated block 24 words long. To terminate a task, the RELT macro is called. It returns the 24 word block pointed to by the contents of symbolic index register T to the core management routines where the contents of T are destroyed (i.e. made to point out of core bounds).

	457	*				TCB
	458	*				
	459	*				
	460	*	THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS			
	461	*	IN THE TRAP BLOCK (TBLOCK).			
	462	*				
000000	463	SRW1	EQU	0		FIRST STATUS RETURN WORD FROM EXEC
000001	464	SRW2	EQU	1		SECOND STATUS RETURN WORD
000002	465	RET	EQU	2		SAVED IC/IR WHEN EXEC SPRINGS TRAP
000003	466	XED	EQU	3		CONTROL IS TRANSFERRED HERE WHEN EXEC
	467	*				SPRINGS THE TRAP. IT CONTAINS AN XED
	468	*				OF A CHAIN WHICH LINKS THE TRAP TO THE
	469	*				MASTER TASK QUEUE.
000004	470	TRA	EQU	4		(UPPER) RESTART ADDRESS FOR TASKS ON
	471	*				ON A QUEUE (SUCH AS THE QSTASK)
	472	*				(LOWER) MAY BE USED TO SAVE RETURN
	473	*				FROM A REENRANT ROUTINE
000005	474	LINK	EQU	5		(UPPER) LINK TO PREVIOUS TCB
000006	475	NCB	EQU	6		(UPPER) POINTER TO NCB
000006	476	JCB	EQU	NCB		(LOWER) POINTER TO JCB
000007	477	SPARE	EQU	7		SPARE
000030	478	LEN	EQU	24		LENGTH OF TCB (NICE IF MULTIPLE OF 8)
	479	*				
	480	*				
000027	481	TEMP1	EQU	LEN-1		TEMPORARY STORAGE AT END OF BLOCK
000026	482	TEMP2	EQU	TEMP1-1		MORE TEMPORARY STORAGE
000025	483	TEMP3	EQU	TEMP2-1		
000024	484	TEMP4	EQU	TEMP3-1		
000023	485	TEMP5	EQU	TEMP4-1		
000022	486	TEMP6	EQU	TEMP5-1		
000021	487	TEMP7	EQU	TEMP6-1		
000020	488	TEMP8	EQU	TEMP7-1		
	489	*				
	490	*	NO ONE EXCEPT R\$GETC SHOULD USE TEMP9 - TEM16			
	491	*				
000017	492	TEM9	EQU	TEMP8-1		
000016	493	TEM10	EQU	TEM9-1		
000015	494	TEM11	EQU	TEM10-1		
000014	495	TEM12	EQU	TEM11-1		
000013	496	TEM13	EQU	TEM12-1		
000012	497	TEM14	EQU	TEM13-1		
000011	498	TEM15	EQU	TEM14-1		

Figure 18

III.1.3 Queue Control Block (QCB)

All modules are internally organized around a set of queues. Each resource has associated with it a queue on which are put all tasks currently requesting that resource. As seen from the description of the major algorithms, the queues are used for synchronizing and communicating between algorithms. This synchronization and communication is of paramount importance in a system where many tasks are competing for a few limited resources.

To ease coding problems, all queues used in all modules are of the same structure -- a 16 word block (see Figure 19). A queue consists of a possibly empty linked list of task control blocks. The pointers point to word 4 (Q\$OFFST) of a block. The link pointers are stored in word 3 (Q\$LINK) of a block. The word at location Q\$FIRST points to Q\$OFFST of the first block of the queue. The location Q\$LAST points to Q\$OFFST of the last block of the queue. The empty queue is denoted by the word Q\$LAST pointing to Q\$FIRST+1 (i.e. pointing to itself).

Accompanying the queue structure is a set of macros that will manipulate any queue. For example, task blocks may be enqueued via the ENQ macro and dequeued via the DEQ macro.

 QUEUE-MANAGEMENT DEFINITIONS

```

656 HEAD EQU 0
657 *
658 *
659 *
660 * QCB
-----
661 *
662 * THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS
663 * IN A QBLOCK GENERATED BY THE QUEUE MACRO.
664 *
000000 665 FIRST EQU 0 POINTER TO FIRST BLOCK OF QUEUE
000001 666 LAST EQU FIRST+1 POINTER TO LAST BLOCK OF QUEUE
000002 667 XADD EQU LAST+1 INSTRUCTION PAIR FOR ADDING A BLOCK
000004 668 XENQ EQU XADD+2 INSTRUCTION PAIR FOR ENQUEUEING
000006 669 XDEQ EQU XENQ+2 INSTRUCTION PAIR FOR DEQUEUEING
000010 670 XINV EQU XDEQ+2 INSTRUCTION PAIR FOR INVERTING
000012 671 BUSY EQU XINV+2 RESPONSIBLE BLOCK IF QUEUE IS BUSY
672 ZERO OTHERWISE
000013 673 MAX EQU BUSY+1 MAXIMUM NUMBER OF ITEMS ASSOCIATED WITH Q
000014 674 AVAIL EQU MAX+1 NUMBER OF ITEMS CURRENTLY AVAILABLE
000015 675 SPAR1 EQU AVAIL+1 SPAR1
000016 676 SPAR2 EQU SPAR1+1 SPAR2
000017 677 ABBR EQU SPAR2+1 ASCII ABBREVIATION OF QUEUE
000020 678 LEN EQU ABBR+1 LENGTH OF QUEUE (WISE TO KEEP EVEN)
679 *
680 *
000004 681 OFFST EQU 4 OFFSET FOR QUEUE POINTER
000003 682 LINK EQU OFFST-1 FORWARD LINK POINTER
  
```

Figure 19

III.1.4 Core Control Block (CCB)

As mentioned in the memory map discussion, the fourth section of all modules in the dynamic storage area. This area is organized into the free memory list. The list consists of a possibly empty linked list of blocks. The forward/backward pointers of a block point to the first word of the succeeding/preceding blocks, respectively. The link pointers are stored in words 0 and 1, respectively, of the block (see Figure 20). Hence the minimal theoretical size of a block is two words; the practical size is eight. The total length of the block is also kept in word 0. By design conventions, the pointers are upper half quantities and the length is a lower half quantity. The empty list is denoted by the forward link of R\$FIRST pointing to R\$LAST and the backward link of R\$LAST pointing to R\$FIRST (see Figure 21).

There is a set of macros, RELC and GETC, at the programmers disposal. RELC releases a block of core back to the free memory list. As a safety feature, each block released is first checked to see if it is out of bounds of the dynamic buffer area and if it has already been released. If either condition holds, the module is halted and copied out to a dump file. GETC returns to the caller a block of memory. The size requested is rounded up to the next multiple of eight. If there is no block big enough on the free list to satisfy the request, a system request is made to expand the size of the dynamic buffer.

----- CORE MANAGEMENT DEFINITIONS -----

	685	HEAD	R		
	686	*			
	687	*			
	688	*		CCB	
	689	*			
	690	*			
	691	*	THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS IN		
	692	*	A BLOCK ON THE FREE MEMORY LIST.		
	693	*			
000000	694	LINKF	EQU	0	POINTER TO SUCCESSOR (UPPER)
000000	695	LEN	EQU	LINKF	TOTAL LENGTH (IN WORDS) OF BLOCK (LOWER)
000001	696	LINKB	EQU	LINKF+1	POINTER TO PREDECESSOR
	697	*S*	DISK	HEAD	

Figure 20

		2670 *	FREE MEMORY LIST			
		2671 *				
		2672 *				
	005344	2673	USE	STORE		
006400	000000	2674	FIRST	ZERO	\$NEXTF,0	FORWARD LINK/ LENGTH OF BLOCK
000000	000000	2675		ZERO	0,	BACKWARD LINK/ <NOT USED>
		2676 *				
		2677 *				
000000	000000	2678	LAST	ZERO	0,0	FORWARD LINK/ LENGTH OF BLOCK
006400	000000	2679		ZERO	\$NEXTB,	BACKWARD LINK/ <NOT USED>
	001152	2680	USE	PREVIOUS		

Figure 21

III.1.5 MACROS

Programming applications frequently involve (1) the coding of a repeated pattern of instructions that within themselves contain variable entries at each iteration of the pattern and (2) basic coding patterns subject to conditional assembly at each occurrence. The macro-operation gives the programmer a shorthand notation for handling (1) and (2) through the use of a special type of pseudo-operation referred to in the GE-625/635 Macro Assembler as a MACRO [4]. Having once determined the iterated pattern, the programmer can, within the MACRO, designate selectable fields of any instruction of the pattern as variable. Thereafter, by coding a single MACRO instruction, the programmer can use the entire pattern as many times as needed, substituting different parameters for the selected subfields on each use.

When the programmer defines the iterated pattern, he gives it a name, and this name then becomes the operation code of the MACRO instruction by which he subsequently uses the macro-operation.

What follows is a discussion of the more representative MACROS employed. Specifically, MACROS dealing with (1) system calls for executive services, such as opening a file or reading/writing an opened file, (2) parallel tasking for multiprogramming capabilities, and (3) diagnostic aids, which greatly facilitated the debuggng of the module are treated in detail.

All trapping system calls for executive services are done through MACROS. Essentially they supply a mechanism whereby a slave task can issue requests to the EXECUTIVE for services. Each of these MACROS is coded to the following specific conventions: the first instruction is a 'TSXO', a transfer and save the current Instruction Counter plus one in Index Register XO, to the subroutine with the same name as the MACRO; it is then followed by the argument list for that particular executive service.

The READ macro (See Figure 22) typifies a system call for an executive service. Figure 23 points out the importance of MACROS in the overall design. The READ subroutine, which is called only by the READ macro, in turn employs the three following macros:

1) `SETUP` (see Figures 24 and 25). This macro and subroutine combination initializes the Task Control Block for the task currently wishing to make a system call for any executive service. It zeroes out the first three status words of the TCB and places the execute double instruction in the link word of the block. This instruction links the task unto the processor queue after the executive service call is completed. `SETUP` also places the proper IP in the TCB such that the task can continue executing its current algorithm.

2) `CKPT` (see Figure 26) -- Checkpoint is a diagnostic tool. If the assembly time debugging flag, `$DBG`, is on, this macro will cause the registers to be stored in 8-word blocks in a circular queue. This is very useful for backtracking of an error.

3) `EXIT` (see Figure 27) -- The `EXIT` macro terminates a thread of control by returning to the task distributor.

Thus the `READ` subroutine works as follows:

It saves the pointer to the caller, but bumps the return past the argument list. It sets up the Task Control Block. Next it picks up the parameters from the argument list and for debugging purposes saves the registers in a circular debugging queue. The MME instruction notifies the Executive of the system call. Instead of waiting in an idle loop for the completion of the call, the task relinquishes control of the processor by executing the `EXIT` macro. This is how the module is multiprogrammed.

The last major important common macro is the `BRANCH` macro (see Figure 28). Any task that executes the `BRANCH` macro creates another asynchronous task. This is the final part of the multi-programming system.

READ MACRO				
000536	1268	USE	CODE	
	1269	HEAD		
	1270	*		
	1271	*		
	1272	*	READ	
	1273	*		
	1274	READ	MACRO	FRN, CORELOC, N, MODE
	1275	TSX	C, SREAD	
	1276	ARG	#1	FRN ADDRESS
	1277	ARG	#2	ADDRESS OF CORE LOC
	1278	ARG	#3	NUMBER OF ELEMENTS
	1279	ARG	#4	MODE
	1280	ENDM	READ	

Figure 22


```

1282 *
1283 *          READ -- SUBROUTINE
1284 *
1285 *          THIS SUBROUTINE IS CALLED BY THE READ MACRO.  IT ISSUES THE
1286 *          COMMAND TO READ THE NEXT N ELEMENTS OF FRN IN A PARTICULAR MODE
1287 *
1288 *          CALL WITH
1289 *          C(XT) = TBLOCK-ADDRESS
1290 *          C(XJ) = JBLOCK-ADDRESS
1291 *          ENTER BY
1292 *          TSX 0,3READ
1293 *          ARG ADDRESS OF FRN
1294 *          ARG ADDRESS OF CORELOC
1295 *          ARG N
1296 *          ARG MODE
1297 *          RETURNS TO FIRST LOC AFTER MACRO EXPANSION
1298 *          RETURNS WITH
1299 *          C(XT) = TBLOCK-ADDRESS
1300 *          C(XJ) = JBLOCK-ADDRESS
1301 *          C(XL) = RESTART-ADDRESS
1302 *          USES LOCAL TEMPORARY ONLY
1303 *
1304 *

```

```

BINARY CARD 10S00011
00536 005321 7400 00 1305 READ STX 0,READT POINTER TO ARGUMENT LIST
00537 000004 0200 03 1306 ADLX 0,4,DU RESTART ADDRESS
      000540 1307 SETUP
00540 000510 7170 00 XED $SETUP
00541 005321 2220 57 1308 LDX 2,READT,IDC LOAD FRN
00542 005321 2240 57 1309 LDX 4,READT,IDC LOAD CORE LOC
00543 005321 2250 57 1310 LDX 5,READT,IDC LOAD N
00544 005321 2260 57 1311 LDX 6,READT,IDC LOAD MODE
00545 000004 2200 03 1312 LDX 0,READ,DU LOAD MME NUMBER
      000546 1313 CKPT CHECKPOINT
00546 000474 7170 00 XED XCKPT
00547 000000 0010 00 1314 MME READ
      000550 1315 EXIT
0550 003074 7100 00 TRA $EXIT
      1316 *
      005321 1317 USE STORE
5321 000000 0000 20 1318 READT ARG 0,* POINTER TO ARGUMENT LIST
      000551 1319 USE PREVIOUS

```

```

----- TRAP SETUP MACRO -----
000510 1134 USE CODE
        1135 HEAD
----- 1136 * -----
        1137 * -----
----- 1138 * ----- SETUP MACRO -----
        1139 * -----
----- 1140 SETUP MACRO ----- NO ARGUMENTS -----
        1141 XED SSETUP
----- 1142 ENDM SETUP -----
        1143

```

Figure 24

```

-----
1144 *
1145 *          SETUP -- SUBROUTINE TO SET UP A TRAP
1146 *
1147 *          CALL WITH
1148 *          C(XT) = TBLOCK-ADDRESS
1149 *          C(XJ) = JBLOCK ADDRESS
1150 *          C(XO) = TRANSFER ADDRESS FOR JSTRA
1151 *          ENTER BY
1152 *          XED T$SETUP
1153 *          DESTROYS C(A), C(Q), C(XO)
1154 *          USES NO TEMPORARIES
1155 *
1156 *
1157 *          000510      EVEN
1158 *          000510      SETUP BSS      0
1159 *          000510 000004 7400 11      STX      0,T$TRA,T      SET T$TRA = RESTART ADDRESS
1160 *          000511 000512 7000 00      TSX      0,++1      BREAK XED
1161 *          000512 000000 4310 03      FLD      0,DU      ZERO OUT A AND C
1162 *          END OF BINARY CARD 10$00010
1163 *          000513 000000 7570 11      STAQ     T$SRW1,T      ZERO STATUS WORDS
1164 *          000514 000520 2370 00      LDAQ     TRAP-1      GET ZERO, XED WORDS
1165 *          000515 000002 7570 11      STAQ     T$XED-1,T      SAVE ZERO, XED
1166 *          000516 000000 7100 10      TRA      0,0      RETURN
1167 *
1168 *          TRAP -- XED SEQUENCE TO PUT BLOCK ON Q$TASK
1169 *
1170 *          000520      EVEN
1171 *          000521 000000 000000      ZERO
1172 *          000521 000522 7170 00      TRAP     XED      ++1      CAN BE USED FOR CLEARING RET WORDS
1173 *          000522 005161 5540 54      STC1     Q$LAST+Q$TASK,DI  *UPDATE PREVIOUS LAST POINTER
1174 *          000523 000524 7170 00      XED      ++1      CONTINUE WITHOUT AFFECTING IC
1175 *          000524 005161 5540 00      STC1     Q$LAST+Q$TASK  *UPDATE POINTER TO LAST
1176 *          000  5  777777 6300 04      RET      -1,IC      RETURN TO POINT OF INTE OPTION
1177 *          *$*      DISK      SYSCALLS

```

Figure 25

```

----- CHECKPOINT- MACRO -----
1073          USE      CODE
1074          HEAD    X
1075 *
1076 *
1077 *          CHECKPOINTS
1078 *
1079 *          THIS MACRO CAUSES THE REGISTERS TO BE STORED IN 8-WORD
1080 *          BLOCKS IN A CIRCULAR QUEUE FOR DEBUGGING USE.  INFORMATION
1081 *          IS STORED IN THE FOLLOWING FORMAT:
1082 *
1083 *
1084 *          C(0) = C(X0) (UPPER)
1085 *          C(1) = C(X1) (LOWER)
1086 *          C(1) = C(X2) (UPPER)
1087 *          C(2) = C(X3) (LOWER)
1088 *          C(2) = C(X4) (UPPER)
1089 *          C(3) = C(X5) (LOWER)
1090 *          C(3) = C(X6) (UPPER)
1091 *          C(4) = C(X7) (LOWER)
1092 *          C(4) = C(A)
1093 *          C(5) = C(D)
1094 *          C(6) = C(E) (0-7 BITS)
1095 *          C(7) = C(TR) (0-23 BITS)
1096 *
1097 *
1098 *          CKPT
1099 *
1100 CKPT  MACRO  <NO ARGUMENTS>
1101          IFE    SDBG,SON,1
1102          XED    X$CKPT
1103          ENDM   CKPT

```

Figure 26

-----EXIT-MACRO-----

000473	960	USE	CODE
	961	HEAD	
	962	*	
	963	*	
	964	*	EXIT
	965	*	
	966	*	EXIT TERMINATES A THREAD OF CONTROL BY RETURNING TO THE
	967	*	TASK DISTRIBUTOR.
	968	*	
	969	EXIT	MACRO NO ARGUMENTS
	970	TRA	SEXIT
	971	ENDM	EXIT

Figure 27

```

2530 *
2531 * BRANCH MACRO
2532 *
2533 * THIS MACRO CREATES AN ASYNCHRONOUS TASK TO BE PERFORMED AT A
2534 * LATER TIME. IT CAN GIVE THE CREATED TASK THE CURRENT TBLOCK
2535 * OR GIVE IT A NEW TBLOCK. EITHER WAY FOUR PARAMETERS MAY BE
2536 * BETWEEN THE TWO TBLOCKS.
2537 *
2538 * PAST INFORMATION IS PLACED IN TEMP1 THRU TEMP4 OF
2539 * THE TASK PLACED ON THE QSTASK QUEUE.
2540 *
2541 * CALLS
2542 * TSGETT
2543 * CLOBBERS C(XX), C(X0)
2544 *
2545 *
2546 BRANCH MACRO PASS,XFER ADD,C(TEMP1),C(TEMP2),C(TEMP3),C(TEMP4)
2547 TSX 0,TSGETT GET A NEW TBLOCK
2548 EAX X,0,T C(XX) POINTS TO NEW TBLOCK
2549 LDX T,T$LINK,X C(XT) POINTS TO OLD TBLOCK
2550 INE '#3',,,2
2551 LDQ #3 SAVE FIRST PARAMETER
2552 STQ T$TEMP1,X
2553 INE '#4',,,2
2554 LDQ #4 SAVE SECOND PARAMETER
2555 STQ T$TEMP2,X
2556 INE '#5',,,2
2557 LDQ #5 SAVE THIRD PARAMETER
2558 STQ T$TEMP3,X
2559 INE '#6',,,2
2560 LDQ #6 SAVE FOURTH PARAMETER
2561 STQ T$TEMP4,X
2562 INE '#1',,PASS',1 T IS THE BLOCK THAT IS PASSED
2563 EAX T,0,X PASS NEW BLOCK
2564 EAX 0,#2 POINT TO TRANSFER ADDRESS
2565 STX 0,T$TRA,T INTO QUEUE BLOCK
2566 EAX 0,Q$OFFST,T PREPARE TO QUEUE
2567 XED Q$XADD+Q$TASK GET ON THE TASK QUEUE
2568 IFE '#1',,PASS',1 WHICH BLOCK TO WE GIVE BACK AS CURRENT
2569 EAX T,0,X GIVE BACK NEW BLOCK
2570 INE '#1',,PASS',1
2571 LDX T,T$LINK,X NO. GIVE BACK OLD BLOCK
2572 BUGXR (0,X)
2573 ENDM BRANCH

```

Figure 28

III.1.6 Communications Network

There exists a private communications network among the Monitor and all of its submodules (i.e. the peripheral drivers). At start up time for the Monitor, it creates the network by opening three scratch events and passing a file reference number, FRN, of each to each submodule spawned. For the submodules, these events are referenced by canonical numbers:

- 1) FRNO -- This is the command event for the drivers. Each driver is allowed notify access only. Commands are channeled to the specified submodule by the STATE when caused.
- 2) FRN1 -- This is the command reply event for the drivers. Each driver is allowed cause access only. In order to inform the Monitor, a peripheral driver simply causes this event with the proper STATE.
- 3) FRN2 -- As implied above, FRNO and FRN1 are an input/output pair. FRN2, however, is not paired at all. The monitor uses this event as a pass event sending files to be processed and devices down to its sons. The sons (peripheral drivers) never pass anything back to the Monitor. They simply close the files.

See Figure 29 for the message formats used in the communication network.

```

5226 *
5227 * MESSAGE FORMATS: RETURNED IN T&SRW2,T (UPPER)
5228 *
5229 * FOR FRN2 --
5230 * BITS 0 - 3 = JOB NUMBER (WITH 0 ILLEGAL)
5231 * BITS 4 = BANNER (ON MEANS SUPPLY BANNER)
5232 * BITS 5 = OUTPUT MODE: 512/ 320 (ON MEANS 320)
5233 * BITS 6 -17 = START ADDRESS (IN ELEMENTS)
5234 *
5235 * FOR FRN1 --
5236 * BITS 0 - 3 = MUST BE ZERO
5237 * BITS 4 - 7 = COMMAND
5238 * BITS 8 -14 = <NOT USED>
5239 * BITS 15-17 = DEVICE UNIT NUMBER (0-7)
5240 *
5241 *
5242 * FOR FRN0 --
5243 * BITS 0 - 3 = JOB NUMBER (MUST BE ZERO)
5244 * BITS 4 - 7 = COMMAND
5245 * BITS 8 -14 = <NOT USED>
5246 * BITS 15-17 = DEVICE UNIT NUMBER (0 THRU 7)

```

Figure 29

III.2 Code and Structures of the Monitor

III.2.1 Job Control Block (JCB)

The Job Control Block (see Figure 30) is merely an extension of the Task Control Block. The JCB contains a coded description of the request submitted by a slave process. The task executing the request examines the JCB for the coded instructions. The JCB also contains additional information (e.g. the user number to bill, a unique number for job identification purposes and lots of debugging aids -- the extra pointers to the TCB, etc.).

JOB CONTROL BLOCK DESCRIPTION

	502	HEAD	J	
	503	*		
	504	*		
	505	*	JCB	
	506	*		
	507	*		
	508	*		
	509	*	THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS	
	510	*	IN THE JOB CONTROL BLOCK (HEREAFTER CALL JCB).	
	511	*		
000000	512	QFRN	EQU	0 (UPPER) FRN OF ASSOCIATED INPUT FILE
000001	513	QFLOC	EQU	QFRN+1 R/W PTR POSITION OF INPUT FILE IN "QFRN"
000002	514	FRN	EQU	QFLOC+1 FRN OF FILE TO BE PROCESSED
000003	515	TYPE	EQU	FRN+1 (UPPER) TYPE
000003	516	DISP	EQU	TYPE (LOWER) DISPOSITION
000004	517	ACODE	EQU	DISP+1 ACODE FOR ACCOUNTING
000005	518	NCB	EQU	ACODE+1 (UPPER) PTR TO NCB
000005	519	TCB	EQU	NCB (LOWER) PTR TO TCB
000006	520	STATI	EQU	NCB+1 INITIATE STATE FOR COMMUNICATIONS
000007	521	JOB	EQU	STATI+1 JOB NUMBER
000007	522	STATT	EQU	JOB TERMINATE STATE FOR COMMUNICATIONS
000010	523	MESS	EQU	STATT+1 MESSAGE FOR COMMUNICATIONS
000011	524	BUF	EQU	MESS+1 WORKING BUFFER
000012	525	SIZE	EQU	BUF+1 AMOUNT OF DATA TO BE PROCESSED
000013	526	RES	EQU	SIZE+1 START OF RESOURCE REQUIREMENT LIST
000026	527	TT	SET	RES+3+1-QFRN+7 ROUND TO MULTIPLE OF 8
000020	528	TT	SET	TT/8*8 ROUND
000020	529	LEN	EQU	TT LENGTH OF JCB (MINIMUM LENGTH = 16.)
	530	*S*	DISK	NCB

Figure 30

III.2.2 Notify Control Block (NCB)

A Notify Control Block (see Figure 31) is nothing more than a TCB with frills. The perpetual tasks which execute the external input algorithms (see Figures 6 and 12) have an NCB instead of just a plain TCB. The extra is for identification of the request and what and how to handle a request after it is received.

```

535 *
536 *
537 * THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS
538 * IN THE NOTIFY BLOCK (ALIAS NCB).
539 *
540 * A NCB IS A TCB WITH EXTRAS.
541 *
000000 542 SRW1 EQU TSSRW1
000001 543 SRW2 EQU TSSRW2
000002 544 RET EQU TSRET
000003 545 XED EQU TSXED
000004 546 TRA EQU TSTRA
000005 547 LINK EQU TSLINK
000005 548 RLINK EQU LINK (LOWER) RESTART AFTER NOTIFY
000006 549 NCR EQU TGNCR
000006 550 JCB EQU TBJCB
000007 551 ABBR EQU TSSPARE ASCII ABBREVIATION OF NCB
000027 552 TEMP1 EQU TSTEMP1
000026 553 TEMP2 EQU TSTEMP2
000025 554 TEMP3 EQU TSTEMP3
000024 555 TEMP4 EQU TSTEMP4
000023 556 TEMP5 EQU TSTEMP5
000022 557 TEMP6 EQU TSTEMP6
000021 558 TEMP7 EQU TSTEMP7
000020 559 TEMP8 EQU TSTEMP8
000017 560 TEM9 EQU TSTEM9
000016 561 TEM10 EQU TSTEM10
000015 562 TEM11 EQU TSTEM11
000014 563 TEM12 EQU TSTEM12
000013 564 TEM13 EQU TSTEM13
000012 565 TEM14 EQU TSTEM14
000011 566 TEM15 EQU TSTEM15
000010 567 TEM16 EQU TSTEM16
000024 568 ERN EQU TEMP4 ERN FOR NOTIFY ***BENE NOTA***
000025 569 STATE EQU TEMP3 STATE FOR NOTIFY ***BENE NOTA***
000030 570 QFRN EQU TQLEN (UPPER) INPUT Q FILE
000031 571 QFLOC EQU QFRN+1 R/W PTR FOR "QFRN"
000032 572 BUSY EQU QFLOC+1 NO. OF FILES CURRENTLY ACTIVE FROM THIS
573 INPUT QUEUE FILE
000033 574 RUN EQU BUSY+1 PTR TO RSOMAX FOR THE TYPE REOURCES
575 NEEDED BY THIS JOB. IF RSOMAX = 0, THEN
576 SHOULD IGNORE HIM FOR NOW (SAVVY?)
000034 577 QUEUE EQU RUN+1 PTR TO WAIT Q-LIST TO PLACE JOB ON
000035 578 RES EQU QUEUE+1 RESOURCE LIST (MUST BE LAST)
579 THIS WAY WE TEST FIRST TO SEE IF WE

```

III.2.3 Peripheral Management

The peripherals are managed exclusively by the Monitor. It is responsible for the allocation of all resources. The peripheral drivers never have to worry with scheduling and managing per se. The Monitor has all the peripheral I/O devices organized into a hierarchical structure. At the head of this structure is the peripheral type table. It has an entry for each type of system resource (e.g. line printer, mag tape, etc.). If the entry is empty, then there are no resources of that type ever available (e.g. invalid types). If it is not empty, it contains a pointer to peripheral header table.

The peripheral header table is composed of device headers. A device header is the item pointed by an entry in the peripheral type table.

A device header contains:

- 1) a pointer to a device table,
- 2) information regarding the configuration of the system (e.g. the maximum number of devices of a certain type, the number the Monitor owns, the number currently free, accesses allowed on device, etc.).

The entries of a device table have a one-to-one correspondence between a 'device' and the real physical I/O device in the machine room. The device contains the name of the device, the FRN when opened, flag bits telling of the current status, and if busy, who is responsible.

All of these tables are generated by Macros. There is a pair of macros GETP and RELP that seize and release a peripheral of a given type.

III.2.4 Operator Interface

The operator interface is a set of routines that allows for conversation between the monitor and the operator of the system. The conversation takes form in the set of the following five commands:

- 1) GET <peripheral-name >; ...; <peripheral-name >
(where < peripheral-name > is the 4 letter generic name for any peripheral device).

It reopens the peripherals identified by the four letter abbreviations. After a successful open, the peripheral is passed down to the appropriate peripheral driver via the communications network.

- 2) RELEASE <peripheral-name >; ...; < peripheral-name >

The Monitor sends a command via the communications network to the appropriate submodule telling it to close the specified peripheral as soon as possible. The submodule when finished will send back a corresponding message.

- 3) KILL < peripheral-name > ; ...; < peripheral-name >

This is an immediate RELEASE command. The output to the peripheral is stopped immediately.

- 4) START <peripheral-name >; ...; <peripheral-name >

It is a command to restart from the beginning the output to the specified peripheral.

- 5) EXIT

The command to terminate the conversation with the monitor.

The conversation takes place on the operator's console. Thus there will be a record of the commands for later debugging if necessary. The conversation is initiated by the operator. By running a certain program, the operator can "call up" the Monitor for a conversation.

III.2.5 Initialization

The Monitor initialization routine is a very simple procedure. It initializes the registers and location zero in order to catch wild transfers to the beginning. Through a system primitive, it sets up the fault vector. It then opens the various system files (e.g. PRINT-FILE-QUEUE, PUNCH-FILE-QUEUE, PRINT-FILE-EVENT, etc.); it then opens the peripheral driver modules. The next step creates the internal communications network between Monitor and submodule. It opens three scratch events ($EVENT_0$, $EVENT_1$, $EVENT_2$ (see Figure 4)). It completes the system by spawning the peripheral drivers. At spawn time, the Monitor passes a link to the communications network to each submodule. The set of predefined I/O devices are opened and passed to the appropriate peripheral driver. Lastly, the Monitor allows slave process requests to be accepted by putting out notifies on all events. It then goes blocked waiting for any request (see Figure 6).

III.3 Code and Structures of the Peripheral Driver Prototype

III.3.1 Job Control Block (JCB)

There is a one-to-one correspondence between a JCB and a peripheral. Therefore the number of JCBs equals the number of peripheral devices a module can have at the maximum. Hence the JCBs are all pre-allocated for the following reasons:

- 1) Since a JCB is allocated at job initialization and is not released until the job completes, we don't want memory tied down that long.
- 2) Also we don't want to create holes in the dynamic buffer area.
- 3) There are only as many JCBs as real devices (currently 3 -- 2 for the line printer module and 1 for the card punch module).

There is the following pair of macros that manage the JCBs:

GETJ gets a JCB for the calling routine
RELJ releases a JCB (validity checks are made, of course)

III.3.2 Notify Control Block (NCB)

For the sake of symmetry, the NCB's of the peripheral drivers were coded identical to those of the Monitor. For a full description, see the discussion in section III.2.2 and see section III.1.6 discussing the communications network.

III.3.3 Peripheral Management

There is no peripheral management per se for a peripheral driver prototype. All managing is done by the Monitor. That is, a driver never has more jobs to process than it has I/O devices to send the data.

III.3.4 Initialization

The peripheral driver initialization routine is very simple. It initializes the registers and location zero similar to the Monitor. Likewise it sets up the fault vector. The last step is to create a set of tasks to talk to the Monitor via the communications network -- event structure. These tasks execute the external input algorithm. The first statement of it is to go blocked waiting for an interrupt signal. (See Figure 12).

CONCLUSION

During the summer of 1971, the following portions of the Input/Output Scheduler system were implemented:

- 1) The Monitor was fully implemented. All five major algorithms were implemented and have run. Also, the operator's command language was coded to allow the operator control over the I/O peripheral devices.
- 2) Of the peripheral driver prototypes, the line printer and card punch modules have been fully implemented. Only the card reader module lacks to be coded. The coding and debugging of such a card punch module, given all the standard macros, structures, etc., would take approximately two, maybe three, man-weeks of work.

As currently implemented, the Monitor is roughly 3 1/2 to 4 K of code, including the dynamic buffer area (see Appendix II). The line printer module and card punch module, due to striking similarities of the devices that they control, were coded together in a single module (see Appendix III). Together they are roughly 3 K big, including 1 K of dynamic buffer area. Hence the overall system is approximately 7 K. Yet the Monitor is not core resident. Thus the effective size of the system drops to 3 K. Although the peripheral drivers are allowed to be core resident (in order to increase their efficiency when transferring data), they are swappable. When there is nothing for them to do, the whole I/O system is swapped out of core, unto the drum, waiting for a request.

This new system, when fully implemented, would remove approximately 3 1/2 K of permanently core resident code from the present Listener. This is the same order of magnitude of core that would be used by the new I/O Scheduler system. Yet the new system includes the double-buffering of data. The old Listener was single-buffered. Moreover, the new system handles the two line printers; the Listener handles only one. This IOS system has been running experimentally since September of '71. Although some "bugs" have appeared, they have been corrected. The output efficiency of the system has noticeably increased.

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APPENDICES

- I. System Programmer's Manual for the R & DC 600
Operating System
- II. Listing of Monitor
- III. Listing of Peripheral driver prototype - Line Printer
& Card Punch Module

APPENDIX I

This is the G.E. R & DC systems programmer's Manual. It was furnished through courtesy of the G.E. Research and Development Center.

SYSTEM PROGRAMMER'S MANUAL
FOR THE R&DC 600 OPERATING SYSTEM

SYSTEM PROGRAMMER'S MANUAL FOR THE R&DC 600 OPERATING SYSTEM

Revised August '70

PREFACE

Who This Manual Is Intended For

This manual is primarily intended as a tutorial and reference document for system programmers who are developing assembly language code which is to be run under the R&DC 600 operating system. A general familiarity with the GMAP assembly language and the fault and interrupt features of 600 machines is assumed.

The overview portions in the manual can also be used to provide an introduction to some of the features of the operating system. Additional information can be found in separate documents.¹

Structure of the Manual

Section I gives a general overview of the operating system. The structure of the Executive is described together with the basic terminology which is utilized in describing its actions.

Section II gives a general description of the primitive commands which are available with the operating system. The technique for initiating a primitive command and handling the associated trap return is described in some detail.

Sections III through V describe the I/O primitives, the File System and File Primitives, and the Event Primitives.

Section VI provides a detailed description of each of the system primitives.

The Appendices summarize information which is useful for reference purposes.

How To Use This Manual

Programmers should read the introductory and overview sections before attempting to utilize the primitive descriptions in Section VI. The detailed description of the parameters which appear in these overviews can, however, be omitted on a first reading.

-
1. "Overview of the R&DC Operating System", TIS Report No. 69-C-355, October 1969.
"Process Control and Communication in a General Purpose Operating System." TIS Report No. 69-C-357.
"The Design and Implementation of a Directory Hierarchy For A General Purpose Operating System." TIS Report No. 69-C-356.

Non-Programmers should concentrate on the first few pages in Section II and Sections I and III-V.

Suggestions and Criticisms

Comments concerning this publication are solicited for use in improving future additions. Please send any recommended additions, deletions, corrections, or other information you deem necessary for improving this manual to: J.E. Kapitula, 4C26, Building K-1, General Electric Research and Development Center; P.O. Box 8; Schenectady, New York 12301.

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Section I

Operating System Overview

OPERATING SYSTEM OVERVIEW

Introduction

The following is a brief description of the 600 Series Time Shared Computer System which has been implemented at the Research and Development Center.

The system provides teletype computing, remote batch and conventional batch processing capabilities. The system has been designed to be sufficiently flexible to allow the incorporation of additional software and hardware developments with minimum effort and disruption of existing service.

The implementation is based on multilevel executive structure, a generalized device independent file system, and a set of primitive commands issued to the executive by running programs.

System Capabilities

The computer system design offers flexibility both in terms of new and/or unusual peripheral devices and in terms of programming ease. The following broad service capabilities are provided:

- 1) Teletype time sharing for (64) users
- 2) Large program capability
- 3) Extensive file system
- 4) Remote and conventional batch processing
- 5) An interface to remote and directly coupled computers
- 6) Ability to utilize unusual peripheral devices.

The teletype time-shared system presents the user with an interface similar to the Desk Side or Mark 2 Computer System. In addition, the teletype user is able to exercise the file system, run machine language programs, and use peripheral I/O devices and remote computers. The initial hardware configuration allows for -64- teletypes. However, the system design will allow this number to be extended with suitable hardware additions.

The system has the capability of running large or small programs intermixed. The running time of a program is generally proportional to the amount of system resources used and to the load on the system.

The system has an extensive file system. Users are able to access files by logical name, rather than by physical device. For most files the user has no control over the physical device allocation. The user is able to access, by name, certain peripheral devices, such as magnetic tapes, in a general manner.

OPERATING SYSTEM OVERVIEW (Continued)

A batch processing capability exists, as a person can initiate the running of a program from a card reader or magnetic tape unit in the same manner as from a teletype.

Several remote computers are connected to the RT-IOC* via high-speed phone lines. A GE115 computer is used for remote entry and output of batch jobs. A PDP-9 computer is used to drive a large cathode ray oscilloscope display unit. A Varian 620-i computer and a GEPAC-30 computer are also connected and are used for special applications.

A directly coupled computer, the GE 4020 is employed for experimental data acquisition, control, and data reduction.

A Varian 522-i computer is being implemented as a disk controller for both the GE 500 and the GE 4020 computers.

The system is able to handle new and unusual peripheral devices which may arrive from time to time with a minimum of reprogramming both in the executive and in the user programs.

* Real Time Input Output Controller

System Hardware Overview

The following is a summary of the hardware configuration. The basic unit of the system is the 4 Base Address Register 600 computer which is composed of three major modules: memory, processor, and real time input/output controller (RT-IOC).

The 600 system consists of a single processor module, an RT-IOC, and two 64K memory modules. The memory is composed of two memory (system) controllers and 64K of 36-bit (plus 1 parity bit) magnetic core storage with a 1.0 microsecond read-restore memory cycle time per controller.

The processor has been modified to include four base address registers for address relocation and protection.

The RT-IOC serves as the input-output interface for the system. This device is capable of transmitting data, in an asynchronous manner, between core memory and up to 32 peripheral devices. The following devices are attached to the RT-IOC:

- 1) Two -32- channel teletype multiplexors
- 2) 2 by 8 disk controller with two disk files.
- 3) 1 by 8 tape controller with eight tape handlers
- 4) Card reader (1100 CPM)
- 5) Card Punch (200 CPM)
- 6) Two line printers (1000 LPM)
- 7) Console teletypewriter
- 8) 1 by 8 high speed communications line multiplexor (CLM)
(interface for remote computers, etc.)
- 9) Interface for GE 4020 (Memory to memory interface)
- 10) High speed analog to digital converter

A diagram of the configuration is shown in Figure I-1.

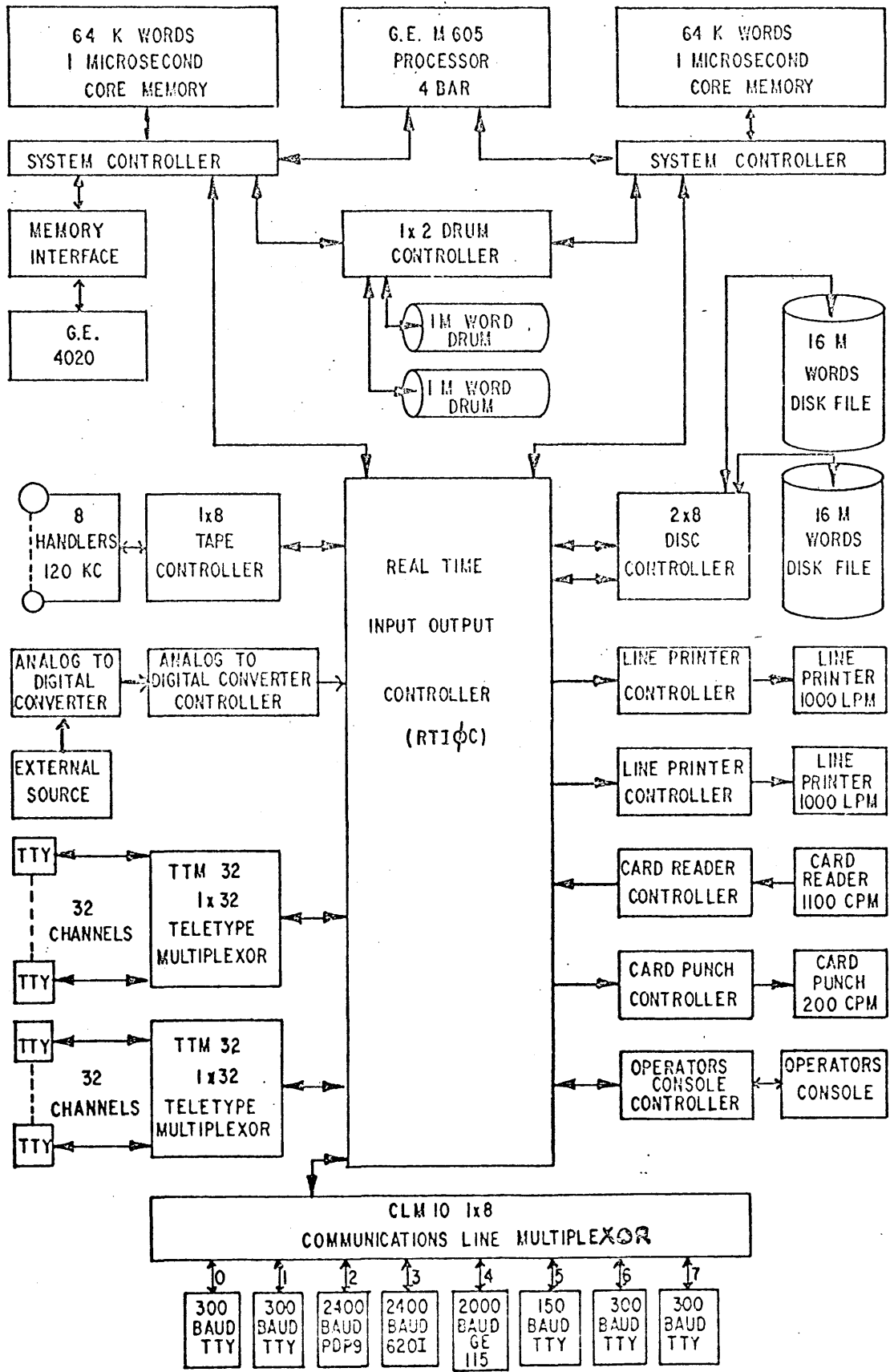


FIG. I-1

Master and Slave Modes of Operation

The R&DC operating system utilizes both the Master and Slave modes of operation which are available with 600 machines.

Programs which run in Master Mode have access to the entire memory, may initiate peripheral and internal control functions, and do not have base address register relocation applied. Only the Master Mode Executive is allowed to execute in Master Mode.

Programs run in Slave Mode have access to a limited portion of memory and cannot generate peripheral control functions. USER PROGRAM'S WILL ONLY BE ALLOWED TO EXECUTE IN SLAVE MODE.

Structure of the Operating System

The operating system can logically be divided into three categories:

- The Multi-Level Executive
- The File System
- The Primitive Commands

A brief discussion of each of these categories is given in the following paragraphs.

The Multi-Level Executive

The System Executive consists of three levels

- 1) The Master Mode Executive
- 2) The Slave Mode Executive and Listener
- 3) Sub-Operating Systems

The Master Mode Executive is that portion of the executive which executes in master mode.

Its principle function is the execution of system input/output primitive commands. The master mode translates these primitive commands, expressed in terms of logical parameters, into commands recognized by the hardware. Master mode services the interrupts generated and returns to the user the physical and logical status of the operation.

Another function of master mode is the running of processes. It sets the BARS to the squeeze or normal settings, returns all traps outstanding to the process, and sets the process into execution at the appropriate location.

Other Master Mode functions are fault handling, disk and drum allocation, and hardware malfunction servicing.

The Multi-Level Executive (Continued)

The Slave Mode Executive directs the allocation of processor(s) and memory to processes and has the real control over the system. The slave mode executive decides process scheduling, determining when a process should be swapped out and the next process to be swapped in. It also executes the control and file system primitives and maintains the file system catalog.

The Sub-Operating Systems provide direct interfaces to the user. These systems will provide the teletype user with interfaces similar to the Desk Side Computer System or Mark 2 and the batch user with a system similar to the GECOS Operating System.

The File System

The File System allows the user to store and retrieve data and programs from a mass storage file by logical name rather than by physical device and address. The File System provides protection against improper use of saved information and also allows simultaneous access to the same file by several users. The File System also provides input/output capabilities with conventional peripheral devices in a similar manner to accessing a mass storage file. The user also is able to access unusual peripherals and remote computers through the file system. Finally, the File System provides a mechanism, the event, by which different processes can communicate with each other.

The Primitive Commands

The Primitive Commands may be viewed as a set of macro calls, available to slave mode processes, which effectively extend the capabilities provided by the hardware. In particular, they relieve the user of the need for dealing directly with peripheral devices.

Executive Overview (Continued)

Processes, Segments, and the State Vector

The process is the basic unit of activity in the system. The notion of a process can perhaps best be described by enumerating its characteristics.

A process (a son) is created by another process (its father) by the system Spawn Primitive. All processes are descendents of a system process called the Listener which is the only process running when the system is first initialized.

A process has the following entities associated with it:

- 1) From 1 to 4 segments
- 2) A set of items (files and events) which the process may access.
- 3) A state segment

When a process is spawned, the father can specify the contents of from 1 to 4 contiguous areas in core, each of which is to have a Base Address Register set around it. The contents of the contiguous areas of core associated with the process in this manner are called the segments of the process. A segment may be either a core image of a father's file or a segment of the father. When a process is in execution, all of its segments must be in core. The manner in which a slave mode program addresses its segments is described in the discussion which follows on Base Address Register usage.

The files and events which are accessible to the spawned process, and the type of access allowable, are also specified by the father when the process is spawned.

The state segment is a unique block of data, associated with each process, which is accessible only to the executive. The state segment is created when the process is spawned and is used to store the information needed by the master and slave executives to run the process.

A more detailed description of the state segment is given in the discussion on Basic System Tables in Section IV.

Base Address Register Usage and Slave Mode Addressing

All addresses for programs running in slave mode are relocated relative to an address which is specified in one of the systems 4 Base Address Registers (BARS). Before this relocation occurs, the slave address is compared with a maximum length setting which is also contained in the BAR. (See Fig. I-2A). If the slave program attempts to address a location which falls outside of this maximum length, a memory fault occurs. A BAR also provides a bit which when set prohibits the slave mode program from doing a write operation in the area defined by the BAR. The BAR selection is determined by bits 0-1 in the slave address as summarized below.

<u>Slave Address (Bits 0-1)</u>	<u>Address Range</u>	<u>Associated BAR</u>	<u>Associated Segment</u>
00	000000-177777	B0	0
01	200000-377777	B1	1
10	400000-577777	B2	2
11	600000-777777	B3	3

The segments associated with a process correspond to the 4 BAR settings. That is, segment zero is associated with the memory bounded by BAR 0, segment 1 with the memory bounded by BAR 1, etc. The slave program can reference its segments by specifying an address associated with the corresponding BAR. For example, to reference segment 3 one would specify an address in the range $600000_8-777777_8$.

Note that the 18 bits which are allocated for a slave address enable the slave mode program to address a virtual memory of 2^{18} words (or approximately 262k). Although the slave mode program can utilize addresses in the full range of the virtual memory, the total length of the program will still be restricted by the maximum amount of core memory which the system will allocate to his process.

The interpretation of the 18 bit relative slave address and the BAR format is shown in Fig. I-2A. A further illustration of the mapping of addresses in the user's virtual memory to actual core locations (via the BAR's) is shown in Fig. I-2B. Note that for BAR utilization core is conceptually divided into blocks of 512 words. Both the BAR relocation origin and the maximum length associated with a BAR is specified in terms of these 512 word blocks.

CORRESPONDENCE BETWEEN BASE REGISTER MAPPING AND ADDRESSING
IN THE USER'S SLAVE PROGRAM

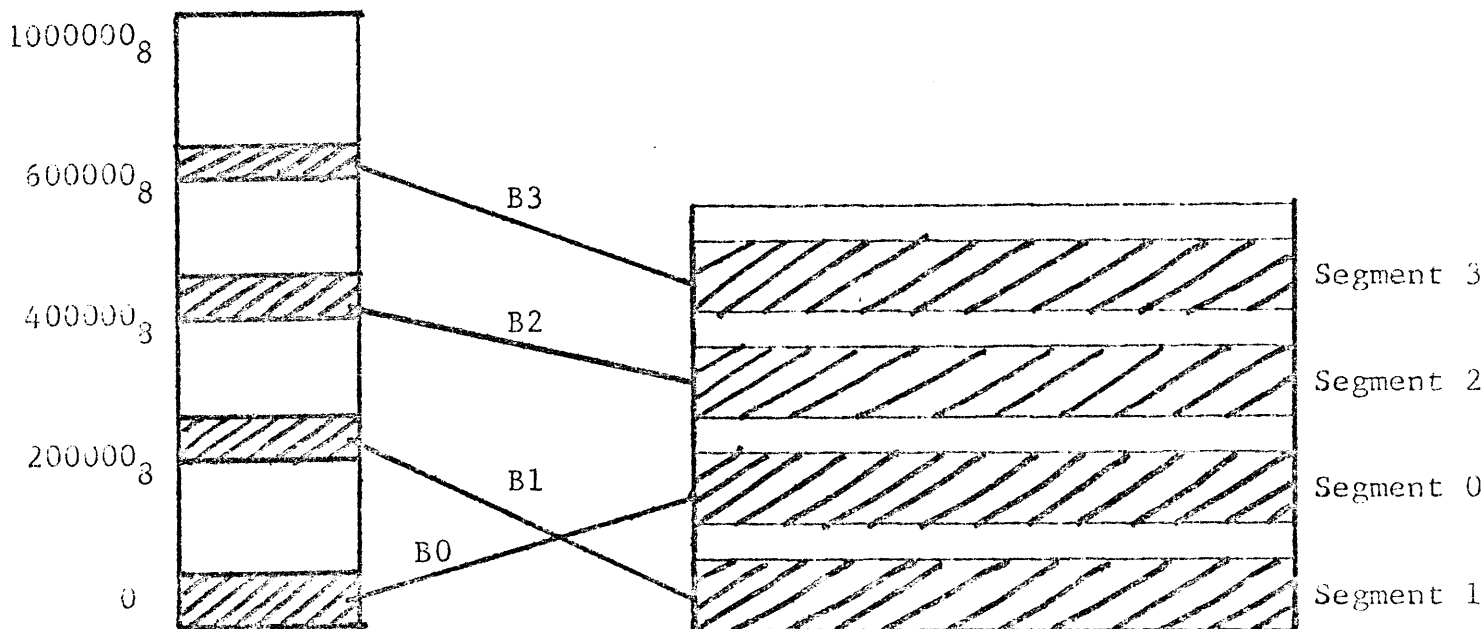
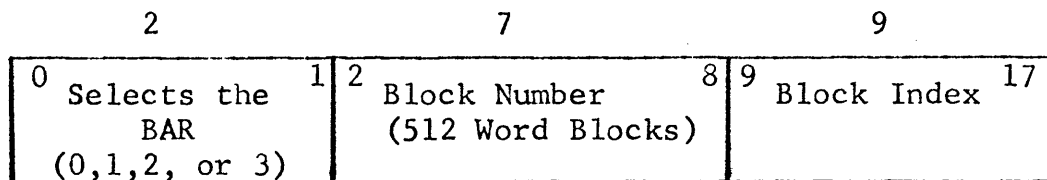
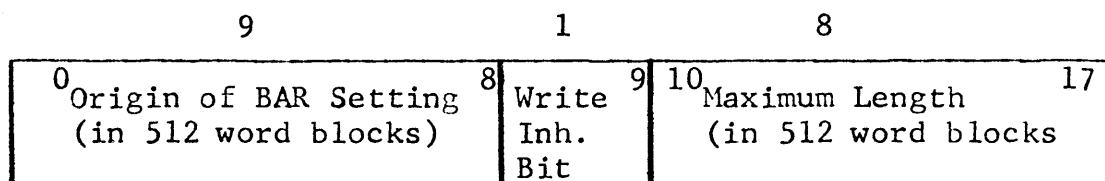


FIG. I-2B



INTERPRETATION OF THE 18 BIT RELATIVE SLAVE MODE ADDRESS



FORMAT FOR A BASE ADDRESS REGISTER (BAR)

FIG. I-2A

FAULT AND INTERRUPT HANDLING

Fault Handling

When a fault occurs, control is automatically transferred by hardware from the process in execution to the appropriate location in the hardware fault vector. An execute double is done on the two instructions at this location. The further action taken by the Master Mode Executive depends upon whether the process in execution was in default, normal, or squeeze mode. The base location of the hardware fault vector is defined by a panel setting and consists of 32 consecutive locations in core.

Default Mode

A process is said to run in default mode when it has not declared a fault vector.* For any fault other than Master Mode Entry or Timer Runout, control is passed to an abort routine in the Master Mode Executive which terminates the process.

Normal Mode

A process is said to run in normal (unsqueezed) mode if a slave fault vector has been declared and the squeeze primitive has not been issued. In normal mode all faults except Master Mode Entry (MME) and Timer Runout (TRO) are passed back to the slave mode process at the fault vector previously designated by the slave mode process.

This fault vector has a two word entry pair for each fault. The instruction counter and indicators are stored in the first word and control is transferred to the second word. The order of the faults is the same as in the hardware fault vector (See Set Fault Vector Primitive).

Handling -MME- and -TRO- Faults

The MME fault is used to call System Primitives, in normal mode it is not returned to the user fault vector. Control is, instead, transferred to a MME Fault Processor within master mode exec. The MME Fault Processor identifies the MME and then transfers control to the appropriate routine in the master or slave mode executive to process the MME. The return mechanism for MME faults is described in the discussion on Primitive Initiation in Section II.

* A slave program declares its fault vector by issuing the Set Fault Vector Primitive. For additional information on the slave fault vector see the write-up of the Set Fault Vector Primitive in Section VI.

Fault and Interruption Handling (Continued)

If the Timer Runout occurs in the slave exec, the timer is reset and control is returned to the slave exec. If the TRO occurs in a normal slave process, control is passed to the slave exec. The slave exec, schedules and runs the next process (which may be the same one).

Interrupt Handling

When an interrupt occurs, control is transferred, by hardware, to an appropriate location in the hardware interrupt vector, where an execute double is performed in Master Mode. The Master Mode Executive then saves the process registers, timer register, instruction counter and indicators, and invokes an appropriate interrupt handling routine. All outstanding interrupts are processed before control is returned to slave mode. Unless an interrupt has occurred for a special priority ("preemptive") process, control is returned to the original slave process. If a trap for this process has occurred, control is returned to the entry point of the trap. Otherwise, control is returned to the point of interrupt. In either case, the process registers, timer register, and indicators at the point of interrupt are restored. (Note that the interrupts which occur, may or may not be related to an activity of the process which was interrupted.) A more detailed discussion of interrupt handling is given in "Trap Routines and Trap Handling" in Section II.

Squeeze Mode

Processes which have declared a slave fault vector can enter a special mode known as squeeze mode. The addressing space of the squeezed process represents a subset of the addressing space of the unsqueezed program and is specified by a SETSQUEZE primitive in unsqueezed (normal) mode.

Processes are created in the default mode. They enter normal mode by issuing a Set Fault Vector Primitive. After issuing a Set Squeeze Primitive to define the BARs of the squeezed program, they can issue an "ENTER SQUEZE MODE" primitive to enter squeeze mode. The Master Mode Executive resets the BARs to the squeezed values and runs the squeeze program. If a fault (except Timer Runout) or an interrupt for the process occurs, the Master Mode Executive resets the BARs to the unsqueezed values and transfers to the fault vector or trap word in the unsqueezed program. All faults (except Timer Runout) which occur in squeeze mode, including MMEs, are passed back to the fault vector in the unsqueezed mode. However, a MME issued in the unsqueezed mode is treated as a system primitive. The unsqueezed process can reenter squeeze mode by issuing another "ENTER SQUEZE MODE" primitive.

Squeeze Mode and Its System Applications

In the discussion that follows, we shall elaborate upon the squeeze and normal modes of execution of a slave process, and then indicate applications in which the squeeze mode may profitably be employed. As we shall see, these applications are those in which a slave process desires to execute a restricted portion of its segments in a controlled manner.

Recall that a slave process consists of from 1 to 4 segments each having a corresponding BAR set around it. When the process executes in normal (unsqueezed) mode, the BAR's are set to their full values, whereas when the process executes in squeeze mode some of these BAR settings may be reduced or interchanged (See Fig. I-3). A process goes from normal to squeeze mode when it issues the ENTER SQUEZE MODE primitive, having previously specified the new BAR settings for the process with a set squeeze primitive.

A squeeze mode process returns to normal mode (and the BAR's reset to their unsqueezed value) whenever a fault or trap occurs. Note that squeeze mode processes are distinguished from processes executing in normal mode in that the MME fault is handled differently. A MME fault which occurs when a process is in squeeze mode is returned to the slave fault vector of the unsqueezed process (as are all other faults of the squeeze mode process) rather than being passed on to the executive as a primitive call. The process may then handle the MME fault in whatever manner it deems appropriate. Traps are also returned to locations in the unsqueezed process. The utility for this feature will become more apparent in the examples which follow.

Example 1: Slave Mode DDT

We shall first illustrate the operation of the squeeze mode by describing its action for a single base register system in which a Slave Mode Program is to be debugged using a Slave Mode Debugging package (Slave Mode DDT). The DDT program is to intercept all faults which occur in the Slave Mode Program and take an appropriate action.

Let us assume that control is initially in DDT. The single BAR is set around both DDT and the user program and the processor mode is "normal". Before returning control to the user program, DDT issues the SQUEZE primitive which resets the BAR around the user program, and in addition sets a flag which indicates that the process is now running in "squeeze" mode. Control is then transferred to the user program which runs in the normal manner until a fault occurs. Whenever a fault occurs, the system fault processing routine notes the flag which indicates that squeeze mode is in effect.

The system fault routine, therefore, resets the BAR about both DDT and the user program, clears the squeeze mode flag to indicate that the process is now in "normal" mode, and returns control to the appropriate location in the DDT fault vector. The DDT routine proceeds to service the fault. If the fault was not a MME, DDT reissues the squeeze primitive and transfers to the fault vector in the squeezed program. If the fault was a MME, DDT validates it and converts certain arguments (such as addresses) to their unsqueezed values. It then reissues the MME to the exec with the new arguments. When DDT has finished, it reissues the SQUEZE primitive which returns control to a specified location in the user program, resetting the BAR and squeeze mode flag. The user program continues in the manner indicated above until the next fault occurs, etc.

The principal steps in the squeeze mode procedure are summarized in Fig. I-4.

Example 2: Implementation of GECOS Interface

Consider the problem of developing an interface for code, such as GECOS, whose MME conventions do not conform to those employed by the R&DC system. The squeeze mode feature enables such already existing code to be run under the R&DC system with a minimum of reprogramming.

Essentially the interface runs GECOS in squeeze mode with the BAR's appropriately set about the GECOS system. Whenever a MME fault occurs, control is transferred to the interface which interprets the GECOS MME and then reissues it in the correct format for the operating system. Control is then returned to the GECOS program (by means of the SQUEZE

BAR Settings for Normal and Squeeze Modes of Operations for a Slave Process

Normal Mode:



- 1) BAR's set to full value
- 2) MME's interpreted as primitive commands and passed to the executive

Squeeze Mode:



- 1) BAR's set to reduced value
- 2) All faults (including the MME) returned to the fault vector of the slave process for handling in unsqueezed mode
- 3) All traps returned to locations in unsqueezed mode

Transition from Normal Mode Execution to Squeeze Mode Execution

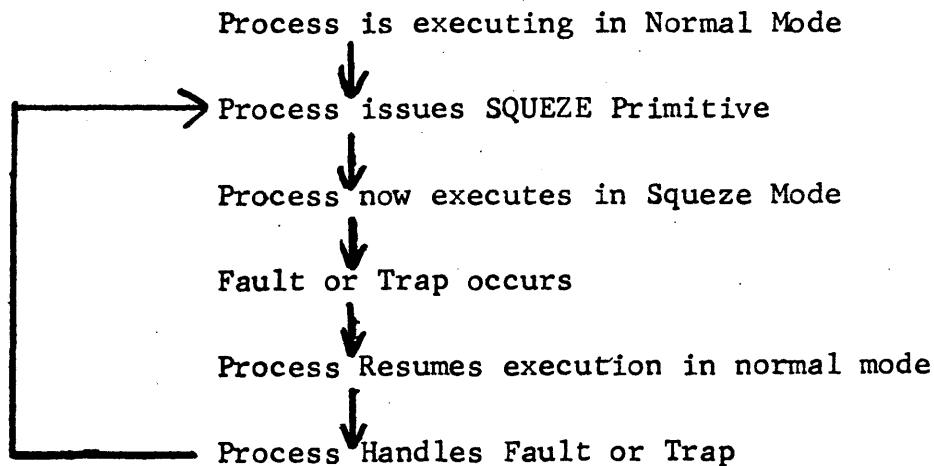
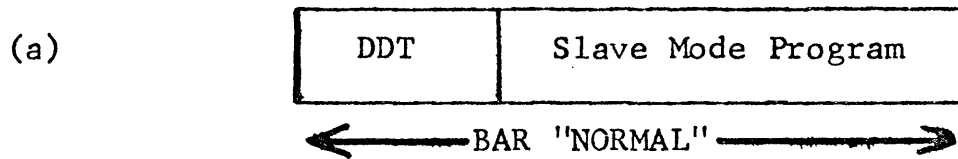


FIG. I-3

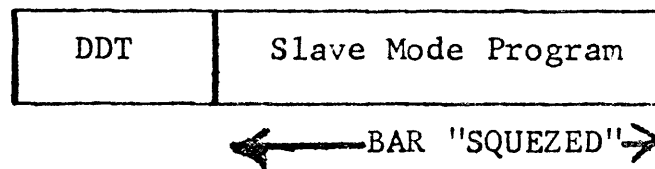
primitive) which resumes execution in squeeze mode. The same procedure is repeated for each MME fault which GECOS generates.

The important point to be observed is that most of the required programming is for the interface. Only a minimal change to the existing code, in this case GECOS, is required.



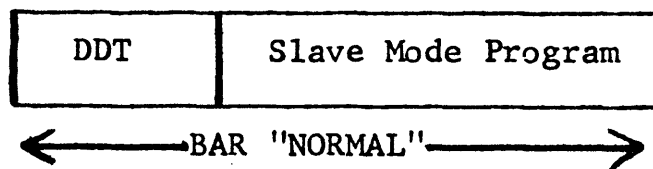
1. Control is in DDT
2. Process Mode in "NORMAL"
3. BAR is set to unsqueezed value.

(b) The SQUEZE primitive is issued:



1. Control is in Slave Mode Program
2. Process Mode is "SQUEZE"
3. BAR is set to squeezed value.

(c) A fault occurs in slave mode program:



1. Control is returned to DDT
2. Process Mode is reset to "NORMAL"
3. BAR is reset to unsqueezed value

(d) After DDT has processed the fault, step (b) is performed and the procedure repeats itself.

Fig. I-4

Section II

Primitive Commands and Trap Handling

PRIMITIVE COMMANDS AND TRAP HANDLING

Introduction

This section describes the considerations involved in using the primitive commands which are available with the R&DC 600 operating system. These commands, which have been implemented as a set of system macros, provide the user with services beyond those provided directly by the hardware. Essentially they supply a mechanism whereby slave programs can issue requests to the executive for services. The macros which initiate the system primitives are available for GMAP assemblies and can be employed with any slave program designed to run under the operating system.

It is convenient to divide the primitive commands into the following categories:

1) I/O Commands

These commands, such as the reading and writing of sequential and random files, enable the user to perform input/output operations which are expressed in terms of logical file parameters.

2) File and Event Commands

The file commands provide the user with an interface to the file system. They allow him to open cataloged and scratch files, to catalog a file or directory, to unsave a file or directory, etc. In short, they enable a user to create a file structure and manipulate the files within this structure.

The Event commands provide a mechanism for interprocess communication. They allow a process to cause an -event-, to be notified when an -event- occurs and to create and catalog -events-.

3) Control Commands

These commands provide the user with the ability to create, terminate, and block processes, re-adjust base register settings to accommodate non-standard software packages, make memory requests, etc.

A summary of the primitive commands is given at the beginning of Section VI.

Primitive Commands and Trap Handling (Continued)

Primitive Initiation

A typical macro for a primitive call consists of:

- 1) A set of instructions which load parameters required by the primitive into appropriate registers. (The primitive is identified by a code number which is loaded into X0).
- 2) The MME instruction. The address portion of this instruction is ignored by the executive.

The MME (Master Mode Entry) instruction is reserved for primitive initiation and causes control to be transferred from the slave process to the master mode portion of the executive. Associated with each primitive call is generally (but not always) a trap address. The trap address defines the starting location of a corresponding trap routine, which is a block of code that the programmer must specify with the primitive call. Subsequent to the completion of a primitive, the executive stores status in, and returns control to, the associated trap routine. Note that in general, a primitive operation may not be completed when control is returned to the slave process. Hence, the slave program is free to continue its execution while awaiting the primitive completion. When the primitive is eventually completed or aborted due to an error, the slave program is interrupted and the associated trap routine is entered. Upon exiting the trap routine, control may either return to the location at which the slave program was previously interrupted or to an arbitrary location in the slave program. The location to which a trap routine exits depends upon the coding for the trap routine and will be discussed in detail in the following sections.

Illustrations showing the flow of control when a primitive command is issued and when an interrupt occurs are shown in Figs. II-1 and II-2. A detailed discussion of these diagrams is deferred to subsequent paragraphs in this section.

Primitive Commands and Trap Handling (Continued)

Trap Routines and Trap Handling

The trap routine is located at the trap address specified by the primitive call and consists of a block of code (of at least four words) defined within the slave program. The fourth word is the entry location with the three preceding words set aside for information to be stored by the executive. The format for a trap routine is shown in Fig. II-3, and is summarized in Fig. II-4.

The first two words are reserved for status information which the executive returns. A logical status code, which is returned in the lower half of Word 0, indicates either the success (=0) of the primitive operation or a possible error code. The remainder of the first two words is reserved for optional return information which is relevant to the particular primitive. The logical status code is either specified in the discussion of the primitive in Section VI or in Appendix A.

The third word is used by the executive to store the exit location for the trap routine. By returning through the exit location (e.g., by the instruction RET WORD_3) the trap routine enables the slave program to ultimately resume execution at the point it was interrupted. The location which the executive stores in the exit may be either

- a) The instruction counter plus one and indicators at the time the slave program was last interrupted (i.e. the executive effectively does an "STC1 WORD_3" at the point of interrupt).
- b) The entry location of another trap routine.

The information so stored is related to the manner in which the operating system handles interrupts, and requires some elaboration. Whenever a primitive operation is completed, the process currently in execution (which is not necessarily the process which issued the primitive) is interrupted and a trap occurs. An entry for the completed primitive is then added to a trap queue which is associated with the process that issued the primitive. The entries on this queue (the outstanding trap queue) consist of all traps which have occurred since the issuing process was last interrupted. Whenever a process with traps outstanding resumes execution, control is first transferred to a trap routine which corresponds to one of the entries on the outstanding trap queue. If there are no traps, control resumes at the interrupt location. The trap routines which correspond to the remaining entries on the outstanding trap queue are linked via the mechanism of the exit location which the executive has stored in Word 3. (See Fig. II-5). The exit location for each trap routine

Primitive Commands and Trap Handling (Continued)

points to the entry location for the next. The exit location for the last trap routine contains the instruction and indicators at the time that the slave program was last interrupted. It should be noted that a trap routine need not exit to the location which the executive has stored. It can for example transfer to any executable location in the slave program. Timing considerations can, however, cause difficulties in the use of this option, especially when there is more than one trap on the outstanding trap queue. It is therefore advised that the programmer carefully read the remaining information in this chapter before employing other than the normal exit.

The fourth word is the first executable instruction of the trap routine and is the entry location to which the executive transfers control when a trap routine is invoked. The remaining words in the trap routine are optional and can be employed to test status returns, set a flag indicating the trap has returned, etc.

It is possible for several primitive commands to share a common trap routine. However, this is only feasible if the traps for these commands do not occur within the same time interval. (otherwise, status and return information stored by the executive for one primitive command would be over-written by the information for the other). A clever programmer may share the same procedure code of a trap routine while allocating distinct four word blocks for status information.

Primitive Commands and Trap Handling (Continued)

Flow of Control for a Primitive Operation

Let us now summarize the flow of control when a primitive command is issued (See Figs. II-1 and II-2). In order to focus our attention on the essential points we shall first assume that the command is issued after all other primitive commands have been completed. We shall also assume that the primitive command is one with an associated trap routine, and that this trap routine has been coded to exit to the location which the executive stores in Word 3. After a slave process issues a primitive with a MME instruction, the process is interrupted and control is transferred to the Master Mode Executive. The Master Mode Executive may either service the primitive request completely, simply initiate the servicing (by placing the request on an appropriate queue), or pass the request on to the Slave Executive.

Control is returned to the slave process at one of two possible entry points depending upon whether the primitive operation has been completed or not. If the primitive has not been completed, control returns to the location of the MME plus one. If it has been completed, control is returned to the trap routine, with the location of the MME plus one stored as the trap routine exit.

Note the entry point which the executive returns control to is dependent upon timing considerations and also upon the state of the operating system at the time the primitive command is issued. Hence, the slave program should generally be coded to accept either entry location without error.

If control is returned to the slave program before the completion of the primitive (the more usual situation) the slave process has the option of either going blocked or continuing in execution. If the process goes blocked it is taken off the list of processes which are to be scheduled for shots at the processor. The blocked process is reawakened when the trap for the primitive operations occurs with execution resuming at the trap routine.

If a process does not go blocked it will continue execution in parallel with the primitive operation. When the trap for the primitive occurs, the process will be interrupted and control transferred to the trap routine. Upon exiting the trap routine control will return the location of previous interrupt. If the trap occurs when the process under consideration is not in execution, the trap routine will be executed the first time the process regains control. Upon exiting the trap routine, control is again transferred to the location of previous interrupt.

Primitive Commands and Trap Handling (Continued)

Let us now drop the assumption that the primitive command is issued after all other primitive commands have been completed. In this case several traps may occur during an interval in which the process is interrupted. Control is returned to the process at one of the corresponding trap routines and is transferred successively to the others.

Upon exiting the last of the trap routines control is returned to the location of previous interrupt. Note that this can only occur if each trap routine is coded to exit to the return location which the executive stores in Word 3. (See Fig. II-5).

The fact that the order in which the above trap routines are processed is not mentioned is not an oversight. Generally this order need not correspond to the order in which the corresponding primitive commands are issued. Hence, assumptions relating to the order in which the trap routines are executed should not be built into the program. Programmers who anticipate having multiple traps outstanding at one time should carefully read the remaining paragraphs in this section for additional details in this regard.

Primitive Commands and Trap Handling (Continued)

Some Considerations in Programming Primitives and Trap Routines

There are several factors that a programmer must bear in mind while programming the primitives and their corresponding trap routines.

- A. The contents of all registers are restored after a primitive is initiated by a MME call. Information which is required for later processing can be kept in registers. However, two primitives (request date & time and request elapsed run time) return results in the AQ registers.
- B. The primitive may not be completed when control returns to the issuing process. Since several primitives may be issued before any traps return, more than one trap may be outstanding at any one time. A separate trap address is, therefore, required for each trap that may be outstanding during any time interval. Trap routines may, however, share common codes since only the first four words need be distinct.
- C. Primitive commands are not necessarily completed in the order in which the primitive commands are issued. For example, successive commands to read the disc and then print from a data area in core might well be completed in the reverse order. There are two basic reasons why this may be so: 1) It may take more time to execute one primitive command than another, 2) Some primitive requests are placed on queues and the queue lengths will generally be different. Assumptions relating to the order of completion of primitive commands should, therefore, not be built into the programming. Note that execution of the trap routine for a primitive command gives a positive indication that the particular primitive command has been successfully or unsuccessfully completed.
- D. The order in which trap routines are executed does not necessarily correspond to the order in which the corresponding primitives were completed. (This is a consequence of the trap linking procedure of the Executive and is unavoidable). Hence, one should not assume that the execution of one trap routine presupposes the execution of some other.
- E. The programmer should beware of reissuing a primitive within the trap routine for that primitive, since the second trap might occur before the trap routine has been exited. The routine must either be reentrant or must contain a lock to prevent being reentered.

Primitive Commands and Trap Handling (Continued)

- F. The executive stores the exit location and indicators into WORD_3 of a trap. This cannot be zero. Therefore, the programmer can use WORD_3 as a flag to indicate whether the trap has occurred by zeroing WORD_3 before issuing the MME, and then testing it for non-zero.

Conventions for Programming Trap Routines

Recall that several traps may be on the trap queue waiting for processing when an interrupted process resumes execution. In order that all of the corresponding trap routines be executed and control then returned to the process at the point of previous interrupt, the following conventions in writing trap routines must generally be adhered to:

1. Each trap routine must be terminated by a return to the location stored in the third word of the routine (i.e., via a RET WORD_3). (However, see last paragraph on page 9 in case of squeeze mode.)
2. All registers which are utilized by a trap routine must be saved in a user data area upon entering the trap routine and restored upon exiting.

Some clarification of these requirements is in order (See Fig. II-5). The third word in a trap routine contains return information and is supplied by the system executive. After a process is interrupted by the occurrence of a trap, the instruction counter and indicators are stored in WORD 3 of the trap routine and control is transferred to the trap routine. The terminating instruction, RET WORD_3, restores control to the process at the point it was interrupted.

If several traps are to be processed, a pointer to the next trap routine in the linking will instead be stored in WORD_3. The terminating instruction, RET WORD_3, then transfers control to the next trap routine in the linking. In order that all such trap routines be executed with control finally returning to the process, ALL trap routines must be terminated with a RET WORD_3. (Recall from the previous section that the trap routines are not executed in an order which the user can predetermine.)

Let us now examine briefly why a trap routine must save and restore the registers which it modifies. When a process is interrupted, the system automatically saves the registers. The system ultimately restores these registers when the process resumes its execution. The restored register settings are, however, those which were in effect

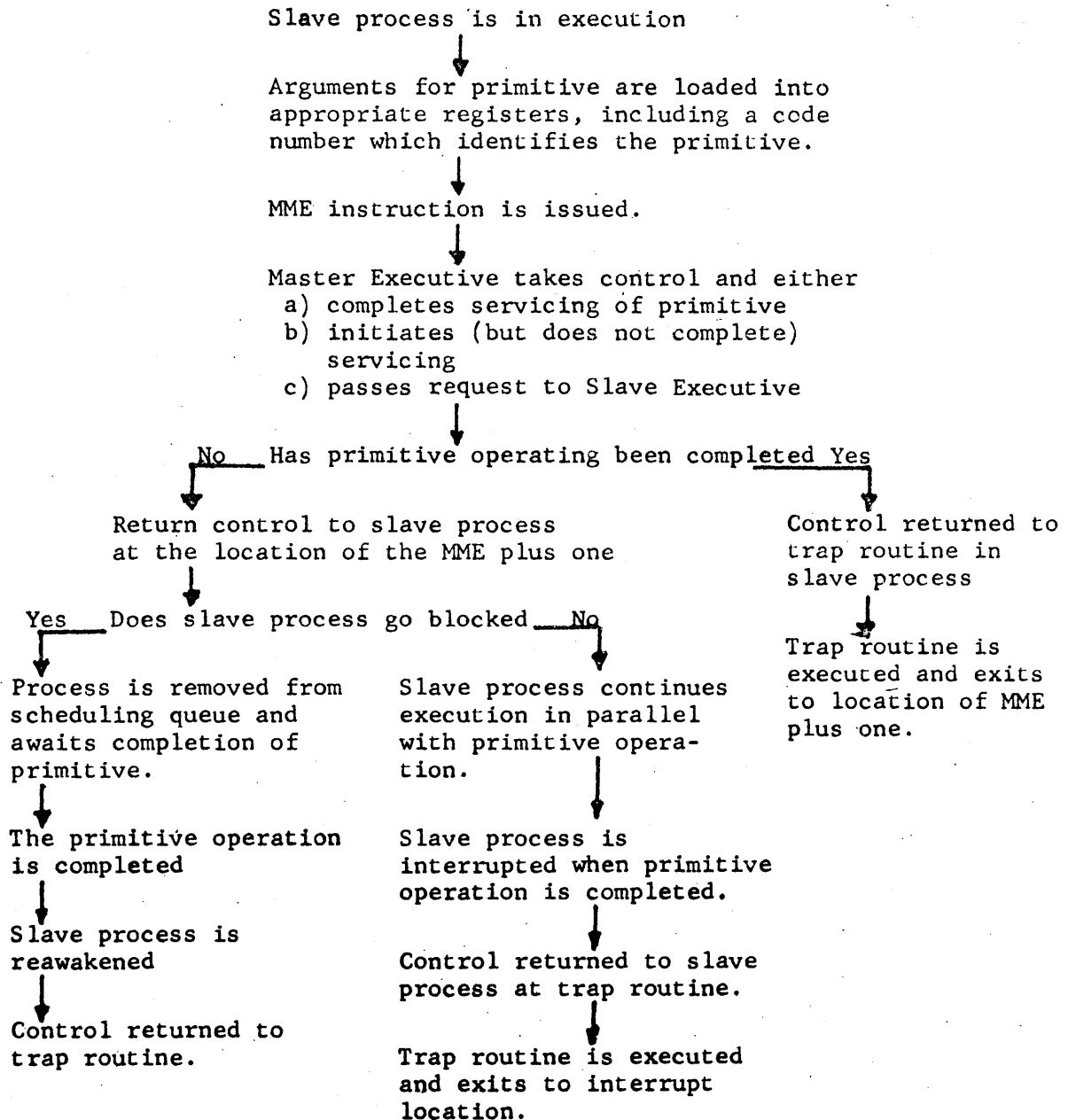
Primitive Commands and Trap Handling (Continued)

when the process was last interrupted. However, if any traps have since returned, the process resumes its execution, not at the point it was interrupted, but in a trap routine. It is, therefore, the responsibility of each trap routine to restore the initial settings upon exit, so that ultimately the interrupted process resumes with the correct register settings. (This is not necessary if the trap routine does not modify any registers).

If an interrupt occurred in squeeze mode, bit 35 is set to 1 in WORD_3 of the last trap in the chain. A process which utilizes squeeze mode must check this bit in every trap before performing a REI WORD_3. If the bit is found on, the process must instead squeeze to the location in WORD_3.

FIG. II-1

Flow of Control for a Primitive Command

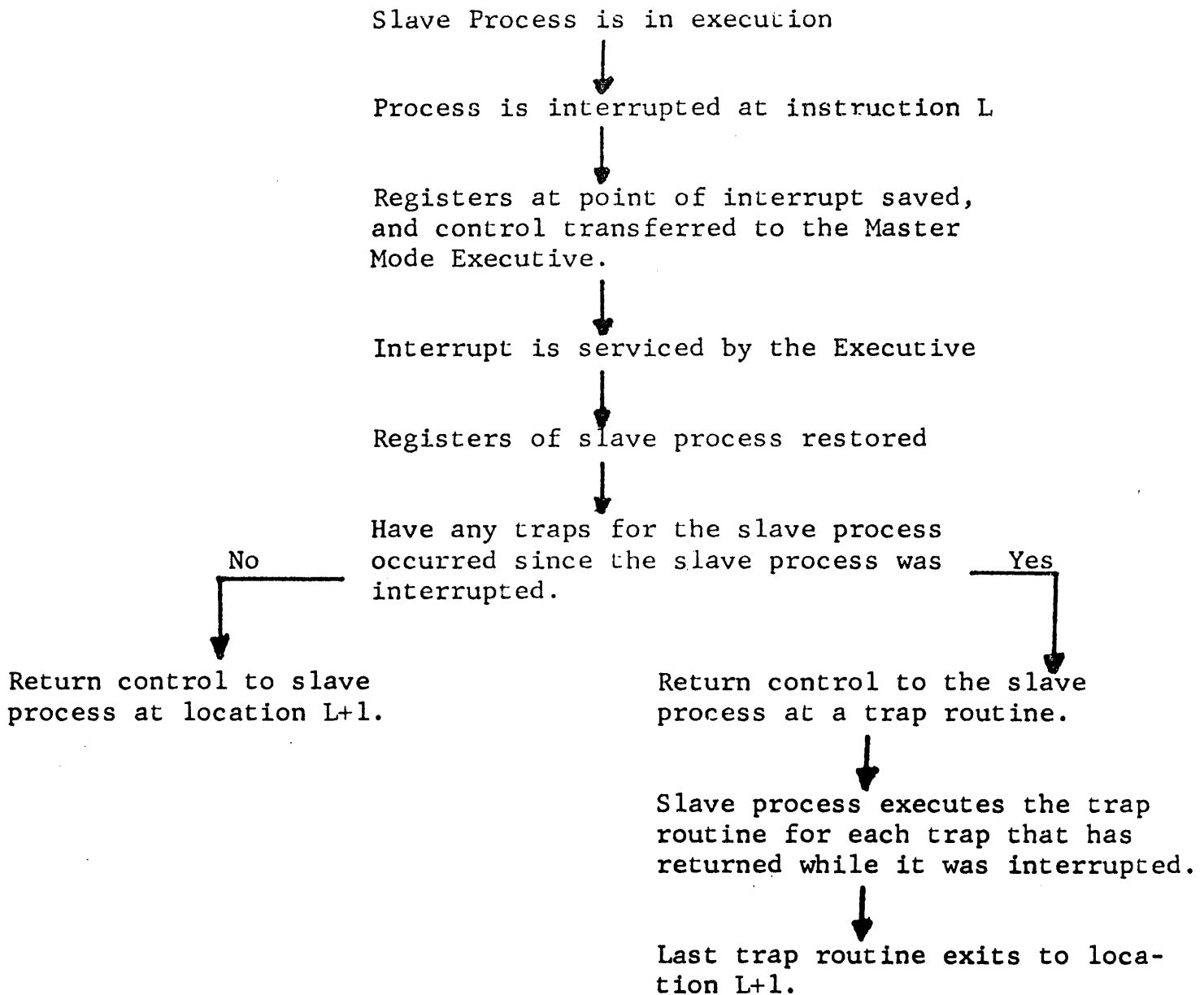


Note:

For the flow of control shown it is assumed:

- 1) that the primitive command is one which has an associated trap routine
- 2) that the trap routine is coded to exit to the location which the executive stores in the exit word.
- 3) There are no other primitive commands which have not been completed.

Flow of Control when a Slave Process is Interrupted



Note:

- 1) For the flow of control shown, it is assumed that all trap routines exit through their exit location.
- 2) The flow of control shown holds equally well for an interrupt caused when a MME instruction is issued.

FIG. II-2

FORMAT FOR A TRAP ROUTINE

	0	17 18	35	
WORD 1	(Optional) status information returned by executive		Logical Status Code	STATUS WORD1
WORD 2	(Optional) status information returned by executive			STATUS WORD2
WORD 3	Exit Location (returned by executive)		Indicators at point of interrupt (or zero)	EXIT
WORD 4	Entry location - the first instruction of the trap routine			ENTRY
WORD 5	<div style="text-align: center;"> Remaining Instructions (optional) </div>			
.				
.				
.				
WORD N				

where WORD 1 is located at the trap address.

FIG. II-3

Format for a Trap Routine

WORD 1	STATUS RETURN WORD 1
Bits 0 -17	Optional primitive dependent information, returned by the executive.
Bits 18-35	Logical Status Code - the code is zero if the primitive operation was successful. Otherwise, a primitive dependent error code number is given. The I/O primitives share a common set of error codes, as do the File and Event primitives.
WORD 2	STATUS RETURN WORD 2
Bits 0 -35	Optional primitive dependent information, returned by the executive.
WORD 3	EXIT LOCATION
	The executive stores an exit location which the trap routine may utilize for returning to the slave program.
Bits 0 -17	The following exit locations may be stored here: a) The interrupt (or MME) location plus one b) The first instruction of another trap routine
Bits 18-35	Dependent upon (a) or (b) above a) The indicator settings at the point of interrupt. If the interrupt occurred in squeeze mode, the interrupt location is the squeezed mode address, and Bit 35 is set on. Otherwise Bit 35 is set off (zero). b) Zero
WORD 4	ENTRY LOCATION
	The executive transfers control to this location when the trap routine is entered. This is the first executable instruction of the trap routine.
WORDS 5-N	REMAINING INSTRUCTIONS OF TRAP ROUTINE (OPTIONAL)
	These instructions constitute the body of the trap routine. The trap routine may return control to the body of the slave program by doing a RET WORD_3.

FIG. II-4

Fig. II-5

TRAP LINKING IN A USERS PROGRAM

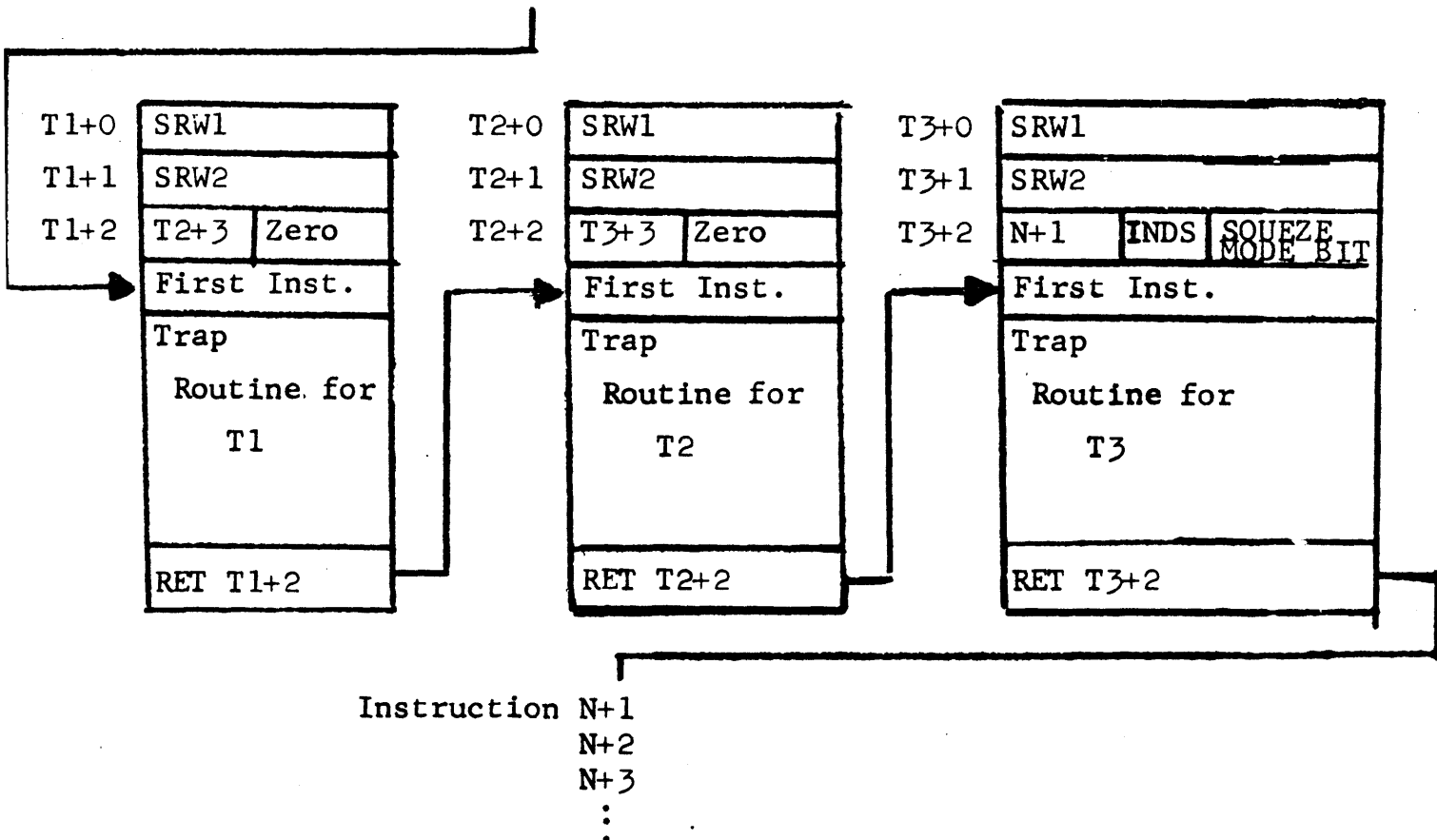
User Process in Execution

⋮
 INSTR. N-2
 N-1
 N

⋮
 Process is interrupted after instruction N (e.g., by a timer runout) and loses control of processor.

⋮
 While process is not in execution, traps with trap routines located at T1, T2, T3 occur.

⋮
 Control is returned to process at entry instruction of T1, the routines for T1, T2, and T3 executed, after which control is returned to instruction N+1 of the interrupted process, i.e.,



Section III

I/O Primitive Overview

I/O PRIMITIVES OVERVIEW

Summary of Macro Calls for I/O Primitives

The primitives classified as I/O, their code identification, and their macro calls are summarized below:

<u>Code</u>	<u>Macro Call</u>	<u>Description</u>
04	READ TRAP,FRN,CORELOC,N,MODE	Sequential Read
05	APEND TRAP,FRN,CORELOC,N,MODE	Sequential Write
06	RRF TRAP,FRN,FILELOC,CORELOC,N	Random File Read
07	WRF TRAP,FRN,FILELOC,CORELOC,N	Random File Write
08	SCR TRAP,FRN, FILEOC	Scratch File
09	SPTR TRAP,FRN,N	Set File Pointer
10	RQST TRAP,RN	Request Status

These primitives supply a slave program with an interface to both random and sequential devices as indicated.

Random Devices - Drum, Disc

Sequential Devices - Card Reader, Card Punch, Line Printer,
Magnetic Tapes, Teletypewriters,
Operators Console, Communication Lines.

Element Size and Maximum Transmission for I/O Operations

Each input-output device type has associated with it a unit size, which characterizes the number of bits (or words) employed in an actual data transfer. Primitive commands, however, transfer data in terms of an element size which the slave program specifies for a file at the time it is opened. The element size must be some integral multiple of the unit size.

For each device type there is also a maximum number of data units that can be transferred in response to a single primitive command. This maximum number is determined both by the device and by the software.

The unit sizes and maximum amount of data transmission for each of the I/O device types are summarized in Tables III-A and III-B.

The Addressing Mechanism for Sequential and Random Operations

For all I/O commands, files are referenced by a File Reference Number (FRN) which is returned to the slave program when the file is opened. Associated with each file is an element size which is also specified when the file is opened.

A file can, therefore, be regarded as a linear array of elements, each element of the file having the same specified element size. The file element represents the basic unit in which all input-output operations are expressed. The manner in which an element is referenced depends upon whether the primitive operation is random or sequential.

For random operations, successive elements of a file are associated with consecutive integers, the first element of the file being associated with the integer 0. The starting location for a data transmission can, therefore, be specified by an integer corresponding to the relative position of some element in the file. For example, one can perform a random read of N elements from file "FRN" starting at element M, where M and N are integers.

For sequential operations, the starting location for a read is specified by a current read pointer, while the starting location for an append is specified by an end of file pointer. The read and end of file pointers are discussed further in the write-up of the READ and APEND Primitives.

Treatment of Mass Storage and Physical Device Files

The I/O primitives can be employed with either mass storage or physical device files. The mass storage files are those located on the drum or disc and can be accessed by both the sequential and random commands.

The physical device files are associated with devices such as Card Reader, Card Punch, Line Printer, Magnetic Tapes, and Teletypewriters. Physical device files can also be regarded as consisting of a linear array of elements. The element size will, as previously noted, be some integral multiple of the basic data unit which a device can transmit. Unlike the mass storage files, reads and writes on physical device files can only be done sequentially.

In order to issue an I/O primitive for a physical device file, the device type corresponding to the file must first be opened. This is performed by the OPEN primitive; this primitive will return a file reference number for one of the available devices of the type specified by a treename. All future references to this physical device file will be made in terms of this file reference number. Provision is made to enable a particular unit of a device type to be opened, e.g. a particular line printer or teletype. (See Fig. III-1).

Note that the same sequential I/O primitives are used with both physical devices and mass storage files. For the physical device files, a physical interpretation (such as the current position of a magnetic tape head) is given to the current read and end of file pointers. This interpretation is different for each device type and is summarized in the remarks in Tables III-A and III-B.

Mode Parameter for I/O Primitives

I/O primitives for certain device types require a device dependent mode parameter to further specify the data transaction which is to occur. For example, the Card Reader can read cards in binary, bcd, or mixed, depending upon the setting of the mode. The mode parameter is also used in specifying a particular operation on magnetic tapes.

A summary of the valid mode values for each device type is given in Appendix B.

Status Returns for I/O Primitives

The I/O primitives return device independent logical status (Word 1) and device dependent physical status (Word 2). The same logical status code is employed for all of the I/O primitives and is summarized in Appendix A-1.

The physical status is dependent upon the particular device file which is being referenced and is summarized in the 600 manual for the device in question.

Device Identification Definition

<u>Identification Number</u>	<u>Device Type</u>
1	Disc
2	Drum
3	Operator's Console
4	Card Punch
5	Line Printer
6	Card Reader
7	TTM32-1
8	CLM-10
9	Tape Units
10	Analog to Digital Converter
13	Mem. Interface
14	Events
15	TTM32-2

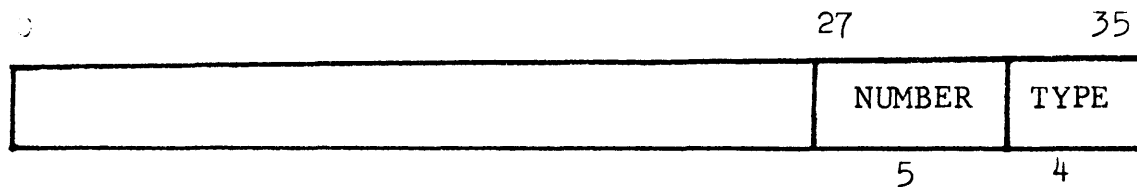


FIG. III-1

Device Identification Number

The following information on the device identification number is primarily for the use of programmers working directly on the executive.

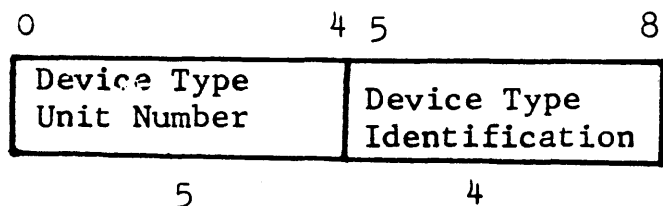
Each physical device in the hardware configuration is identified by a 7 bit device identification number.

Device ID

Bits 0-4 Device type unit number
Bits 5-8 Device type identification number

The device type identification number specifies the number to be assigned to a given device type and is summarized above.

The device type unit number specifies the particular physical unit of that device type (i.e. the 22nd teletype).



MASS STORAGE FILE SUMMARY

<u>Device</u>	<u>Unit Size</u>	<u>Maximum Units per Transmission</u>	<u>Remarks on Starting Location for a Data Transmission</u>
Drum	32 words	64K words	For the drum and the disc, both the sequential and random primitives can be employed.
Disc	32 words	64K words	<p>Random operations can begin at an arbitrary element of the file. The element is specified by an integer index which gives the element's position relative to the beginning of the file.</p> <p>For sequential operations, the starting location for a read is specified by a current read pointer, while the starting location for a write is specified by an end of file pointer.</p>

TABLE III-A

PHYSICAL DEVICE FILE SUMMARY

Device	Access		Unit Size	Maximum Units per Transmission	Remarks on Starting Location for a Data Transmission
	READ	APEND			
Card Reader	X		6-bit characters	Binary Mode: 160 characters Hollerith Mode: 80 characters Mixed Mode: 160 characters	The READ starts from the first character of the next card to be read and continues for the number of characters which correspond to the specified number of elements. If less than the maximum number of characters are specified, the remaining characters on the card are ignored.
Card Punch		X	6-bit characters	Binary Mode: 160 characters Hollerith Mode: 80 characters	The APEND starts at the first character of the next card to be punched and continues for the number of characters which correspond to the specified number of elements.
Line Printer		X	6-bit characters	4,096 characters	The APEND starts at the left margin of the current print line. Necessary slow and control characters must be supplied by the user. See Printer Manual for detailed characteristics.
Magnetic Tapes	X	X	1 word	61,440 words	Both the READ and APEND begin at the current position of the magnetic tape head. The set pointer primitive can be employed to modify this starting location.
Teletype-writers Operator's Console	X	X	9-bit characters	128 characters	These devices are not initialized to the left margin so that READ and APEND proceed from the typewriter current carriage position.
Communication Line Multiplex-or	X	X	36 bit words	4096 words	The read starts after the last SYNC character and continues until the subchannel dependent end of record character. The append transmits the words specified.
A to D Converter	X		36-bit words	4096 words	

TABLE III-B

Section IV

File System and File Primitive Overview

FILE SYSTEM AND FILE PRIMITIVE OVERVIEW

Logical Structure

The R and DC file system is, in formal terms, a tree structure of indefinite length whose origin is the system Root Directory. The primary nodes of the tree are the user's highest level directories, referred to as the user's Main Directories. The lower level nodes, if they exist, are subdirectories. The terminal points of the structure are the files (or events). A schematic representation of the file system's hierarchical structure is shown in Figure IV-1.

Files

Files may be classified as mass storage or physical device. The file system views both of these files as linear arrays of elements and is not concerned with the contents. The element may be either a character, word, or block of words depending upon the nature of the file.

Mass storage files are located on the disk and/or drum, and are paged. Storage for a file is allocated dynamically on a -when needed- basis in multiples of either the drum or disc page size. Files located on the disk or drum may be accessed in either a random or a sequential manner.

Physical device files permit access to external storage media such as unit record devices, magnetic tapes, teletypes, and remote computers.

The Directory File

The directory file is a system file which maintains information about all of the catalogued entities. The directory file consists of a hierarchy of directories and is accessible only to the EXECUTIVE.

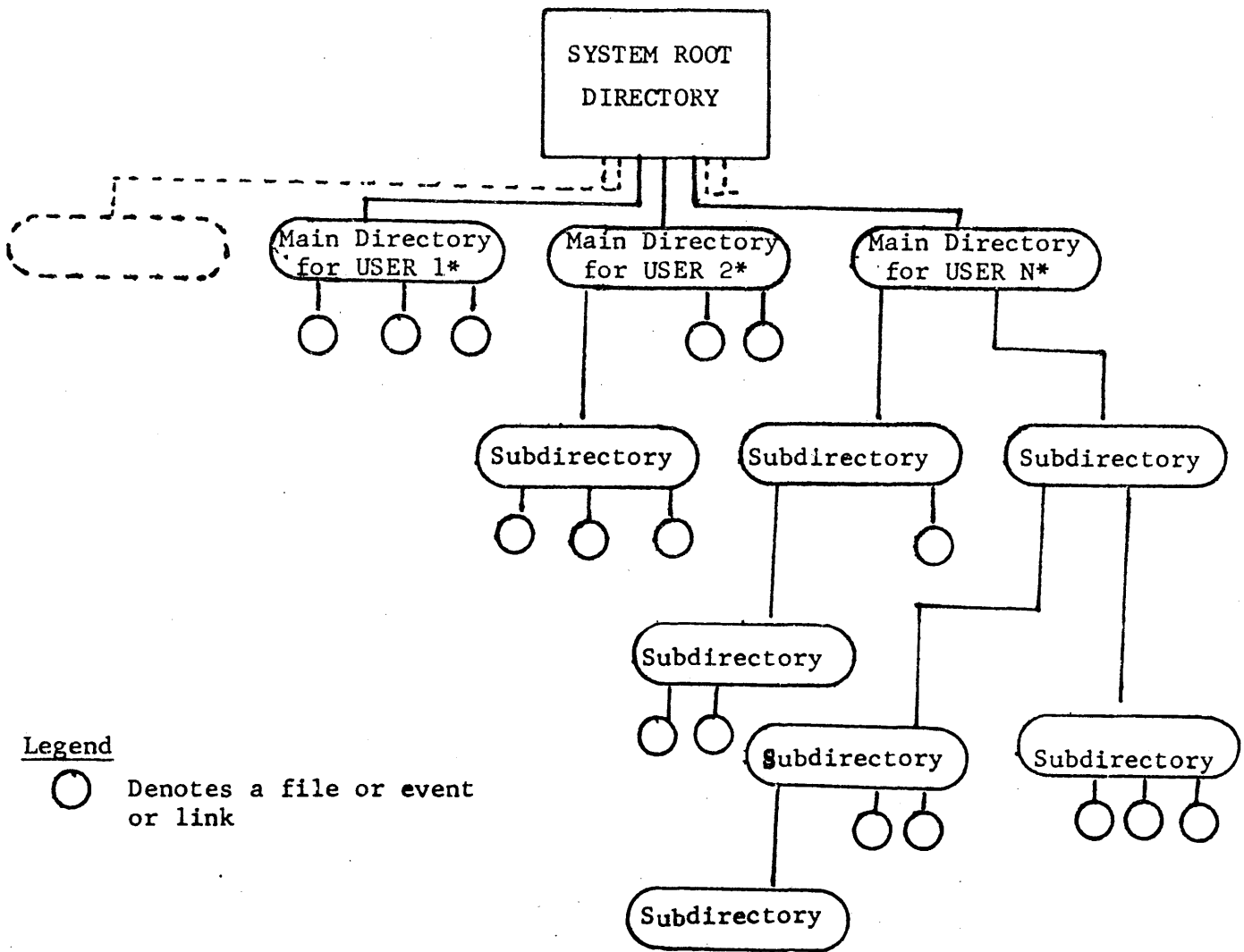
Directory

A directory consists of a collection of entries called branches. Each branch describes either a file, event, directory, or link. Included in the information for a branch entry is

- 1 - the symbolic name of the entity referred to
- 2 - the access granted to the entity for system users (except for link)
- 3 - the password associated with the entity

A detailed description of the information in a directory branch is given in the discussion on the READ BRANCH and READ DIRECTORY primitives.

LOGICAL STRUCTURE FOR THE R&DC FILE SYSTEM



Legend

○ Denotes a file or event or link

* Identified by the name used for the system root directory entry. All such names must be unique within the system; all subdirectories and file names are qualified by the user's name and the names of any intermediate subdirectories. The system root directory cannot be accessed by the user.

FIG. IV-1

Events

Events are data structures which are used for interprocess communication. They can be catalogued and, in general, handled much in the same manner as files. A more complete description of events will be given in Section V on Event Primitives.

Identifying a File - Tree Name

File or directory names need not be unique in the file system, except that all files emanating directly from a given directory must be uniquely named among themselves. To uniquely identify an individual file in the system, a string of names is given, beginning with the creator's Main Directory name and ending with the file name.

Each successive directory name in the string qualifies the file name, thereby uniquely defining the file. This string is called the tree name. The tree name of any file or directory must include the names of all of the higher-level directories that must be traversed in order to arrive at the desired point.

Note that events and links (to be described) are also referenced by the same tree name conventions as files.

For example FILA in Fig. IV-2 has the tree name USERNO1/DIRA/FILA. In this representation, successive levels of the tree name are separated by a slash. A password can also be associated with the name at a given level. This is represented notationally by appending the dollar symbol and the password to the name component of the level. For example, to associate passwords with USERNO1 and FILA, one would write the tree name as

USERNO1\$PASSWORD1/DIRA/FILA\$PASSWORD2

Note that this representation which is the same as that used in GECOS 3, is used only for describing tree names. It is similar to, but not the same as, the representation which the programmer will employ in specifying tree names when coding the file primitives.

Links

A link contains the tree name of an entity being pointed to. The entity may be a file, directory, event, or another link. In the latter case, the link chain must eventually terminate with a non-link entry. The tree name specified by the link must begin from the level of a user's main directory. Note that link chains are not allowed to close back upon themselves. For example, the tree name of the link shown in Fig. IV-2, is USERNO2/LINKA. A possible content of this link entry would be the tree name USERNO1/DIRA/FILA.

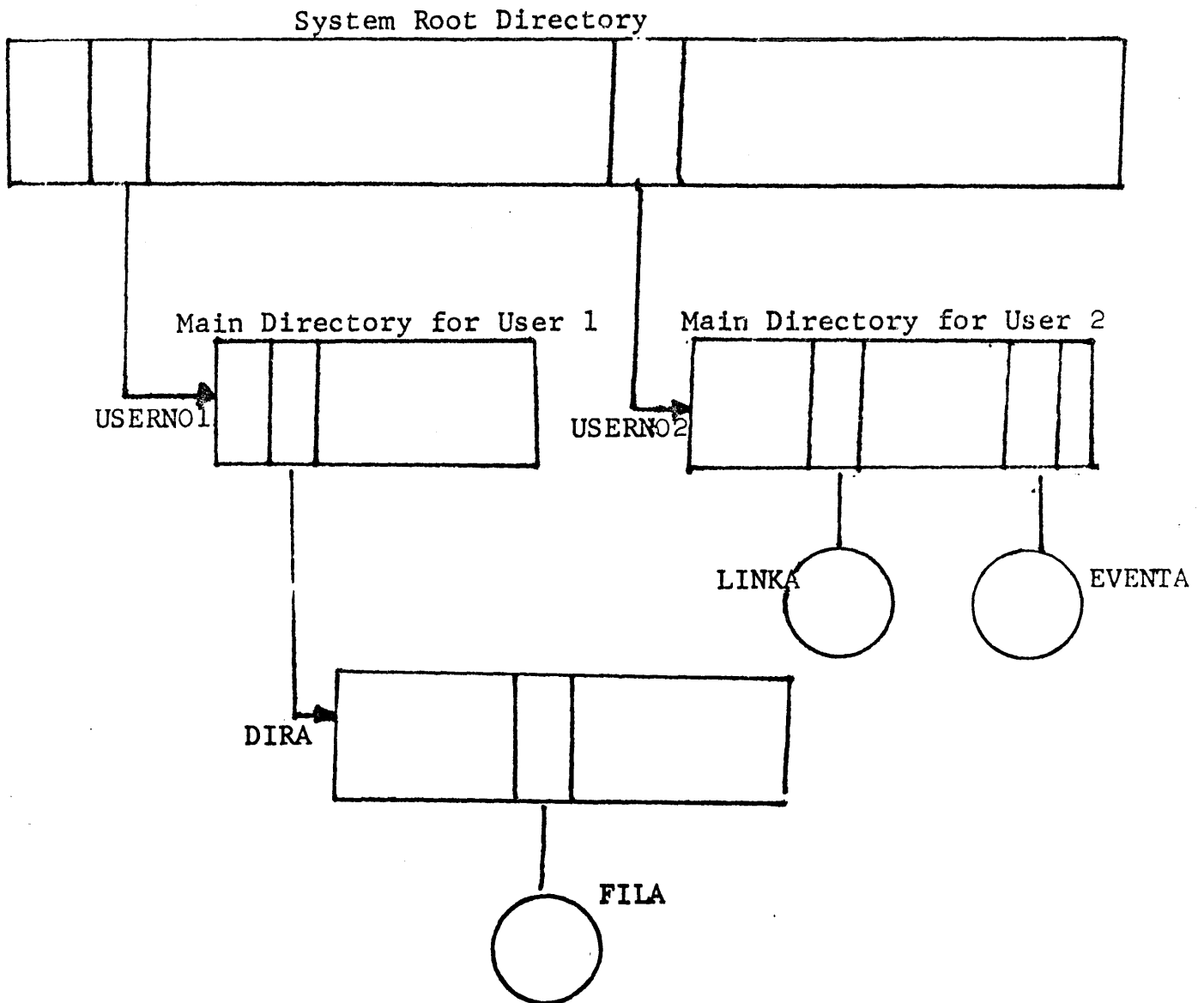
Working Directory

Each process has a current working directory associated with it with respect to which the process can specify tree names. For example, if in Fig. IV-2, the DIRA is the working directory, then the tree name of FILA relative to the working directory is */FILA. (The symbol * denotes that referencing is from the working directory instead of the root directory).

ACCESS

Permission may be granted to other users by the file creator for one or more kinds of access to his catalogued entities. A discussion on access is given in "Parameter Summary for File Primitives", which appears in a subsequent paragraph in this section.

FIG IV-2



Entity in Directory Structure

Tree Name of Entity

Main Directory for User 1

USERNO1

The directory, DIRA, catalogued in the Main Directory for User 1

USERNO1/DIRA

The file, FILA, catalogued in DIRA

USERNO1/DIRA/FILA

The link, LINKA, catalogued in the main directory for User 2

USERNO2/LINKA

The event, EVENTA, catalogued in the main directory for User 2

USERNO2/EVENTA

The Basic System Tables - The AIT, KIT, and State Segment

The following sections describe the basic system tables which the Executive utilizes in performing its functions. These sections are intended primarily for reference and are included to clarify the meaning of the terms 'AIT' and 'KIT' which are employed in the description of certain primitives.

In the discussion that follows, the term 'item' is used to signify a file or event.

The Active Item Table (AIT)

The Active Item Table maintains the global information, required by the executive, for the manipulation of the files, segments, or events which are currently active. There is a single entry in the Active Item Table (AIT) for each such active item.

A file or event becomes active the first time it is opened by a slave process. At this time an entry is made for the file or event in the Active Item Table (AIT). The file or event remains active until it is closed with an attachment count (entry hold count) of zero. At this time the corresponding entry is removed from the AIT.

A file or event may be opened more than once or by more than one process. In this case there is still only one AIT entry, but the attachment count for the entry is updated. In addition, an appropriate pointer to the AIT entry (to be described in discussing the Known Item Table) is set up. (See Fig. IV-3)

A segment becomes active the first time it is specified in the spawning of some process or upon being opened by a process. It remains active until all processes which utilize it have terminated. Segments may also be shared by more than one process. But again there is only a single AIT entry for each segment.

Information Contained in a File or Segment AIT Entry

1) Location of file or segment

Segment Entry - Core location of segment or the location of the page table for the corresponding segment swap file.

File Entry - location of page table for file.

2) Maximum and current length (if file)

The Active Item Table (Continued)

- 3) Number of processes using the segment or file
- 4) Statistical information on use of segment or file
- 5) Pointer to a branch in the system directory which points to the page table of the file (catalogued file only).
- 6) Owner's identification
- 7) Lock indicator (files only)

Information Contained in an Event AIT Entry

- 1) Pointer to queue of processes awaiting notification
- 2) Current and maximum allowable size of queue
- 3) Number of processes using the event
- 4) Pointer to corresponding branch (catalogued events only)
- 5) Owner's identification
- 6) Lock
- 7) Maximum time an entry can remain on queue
- 8) Parameters describing operation of event

State Segment and Known Item Table

The State Segment

Associated with each process is a unique state segment which is created when the process is first spawned. The state segment is used to store the information which is needed by the master and slave executives to run the process. The information in the state segment is local (that is, it applies only to the particular process), as contrasted with the information in the Active Item Table which is global to all processes.

A principal data item contained in the state segment is the Known Item Table (KIT). The KIT contains information concerning the files and events that the process has knowledge of. Because of its significance, the contents of a KIT entry will be discussed separately.

Information in the State Segment

- 1) Register storage
- 2) Location of slave process fault vector
- 3) Timing information
- 4) Pointer to AIT entry for each of the process' 4 segments
- 5) Squeeze mode information
- 6) Process ID and father's process ID
- 7) Count of I/O and file operations underway
- 8) Count of number of events process is awaiting
- 9) The Known Items Table (summarized below)
- 10) Trap information
- 11) Other Measurement Information

The Known Items Table (KIT)

The Known Items Table has at least one entry for each file and event that a process has knowledge of. Entries are made in the KIT when a process is initially spawned, for each item (if any) passed to it, and subsequently whenever the process opens a file or event (item). The items in the KIT are accessed by means of their -reference number- which has been returned to the process when the item was opened, or which was passed to the process when it was spawned.

The Known Item Table (Continued)

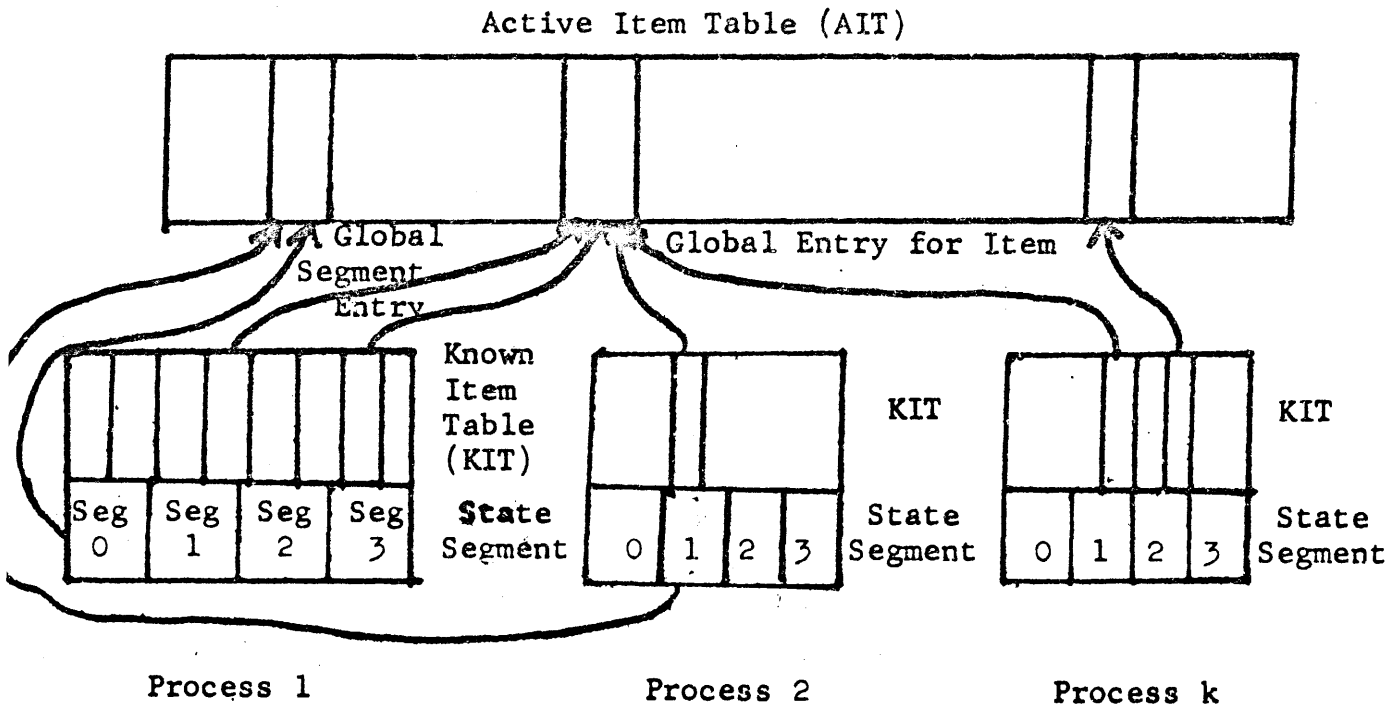
Since a process can open an item more than once, there may be several entries in the KIT which represent the same item. Similarly an item which has a reference number in the KIT of one process may also have a reference number (usually different) in the KIT of some other process. (This occurs, for example, when a father specifies the reference numbers in the father's KIT for items in the son's KIT).

Note, however, that regardless of the number of times an item is referenced by entries from various KIT's, it has but a single AIT entry. Each KIT entry has a pointer to the global entry for the item in the AIT. Hence, different KIT entries which correspond to the same item will all point to a common AIT entry. This mechanism aids in the manipulation and maintenance of items which are common to several processes. (See Fig. IV-3).

Information in a Known Item Table Entry

- 1) Access permitted to item
- 2) Pointer to AIT entry for item
- 3) Flag to indicate whether process has locked the item
- 4) Count of number of I/O operations underway (file) or requests for notification outstanding (event).
- 5) Read pointer (for reading files sequentially).

Relationship Between the Active Item Table,
Known Item Table, and State Segment



* NOTE: Entries in the KIT are referenced by their Item Reference Number, RN, which is used as an index in the KIT. The reference number for the KIT in each process is private to the process.

FIG. IV-3

Summary of Macro Calls for File Primitives

<u>Code</u>	<u>Macro Call</u>
20	OPEN TRAP, TREE_NAME, TREE_SIZE, BEHALF, ELSIZE, ACCESS
21	CLOSE TRAP, RN
22	CATALOG TRAP, RN, TREE_NAME, TREE_SIZE, SWITCH, UACCESS, OACCESS
23	DESTRO TRAP, TREE_NAME, TREE_SIZE, BEHALF
24	OPENS TRAP, DEVID, MAXLEN, ELSIZE
25	UPDATE TRAP, RN
26	CATDIR TRAP, TREE_NAME, TREE_SIZE, BEHALF, UACCESS, OACCESS
27	WRACL TRAP, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, BUFSIZE
28	RDACL TRAP, INDEX, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, NUMBER
29	RDDIR TRAP, INDEX, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, BUFSIZE
30	OPENW TRAP, TREE_NAME, TREE_SIZE, BEHALF
31	RDBRN TRAP, SYSID, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF
32	RDLNK TRAP, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, BUFSIZE
33	WSINF TRAP, SYSID, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, DELETE
34	CATLK TRAP, LINK_NAME, LINK_SIZE, TREE_NAME, TREE_SIZE, BEHALF
35	WTBRN TRAP, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF
36	LOCK TRAP, RN
37	UNLOCK TRAP, RN

Description of Parameters for File Primitives

Summarized are those parameters which are common to several of the file (and event) primitives. The remaining parameters are described in the individual write up for each primitive.

TREE_NAME

Catalogued items are initially referenced by a tree name which specifies the location of the item in the 600 system directory structure. The parameter 'TREE_NAME' is a pointer to an area in the slave program where the characters in the tree name are stored.

Tree names which consist of more than one level must occupy a contiguous block of storage, with six words reserved for each level. For each level, the first 4 words specify the name and the last two words the password.

The characters of the name and pass word are left justified and all characters must be specified for each level. Trailing blanks (octal 040) must be supplied if the names and passwords are to be compatible with those catalogued by the System Loader from the card reader.

If a password is not desired, a default password of eight ASCII blanks (octal 040) should be specified. Note that the name and password of a given level of the tree name are conveniently defined by the ACI Pseudo Op in the GMAP assembler.

Examples of Tree Name Specification with Passwords

An example showing the default passwords for device files

ACI 6,DEVICE	Name and default password for device
ACI 6,MTAA	Name and default password for any magnetic tape

The above file has the tree name DEVICE/MTAA.

An example of a file with a non default password

ACI 4,ZBROWN	Name of user's main directory
ACI 2,SESAME	Password for main directory
ACI 6,DOCCAT	Name of directory in above with default password
ACI 4,FILE1	Name of file in DOCCAT
ACI 2,SHAZAM	Password for FILE1

The above file has the tree name ZBROWN\$SESAME/DOCCAT/FILE1\$SHAZAM.

Parameter Summary for File Primitives (Continued)

- TREE_SIZE** The number of words in the storage area where the characters of the tree name are stored (i.e. the storage area whose starting location is given by 'TREE_NAME'). The number of words specified by 'TREE_SIZE' must be a multiple of 6.
- If 'TREE_SIZE'=6 it implies the operation is being performed at the root level of the directory.
- RN** The item (file or event) reference number, 'RN', is returned when either a scratch or catalogued item is opened. Most subsequent references to the item will be in terms of this reference number. The reference number, 'RN', is an index to the entry in the Known Item Table of the process which describes the item. The value of 'RN' has meaning only to the process which opened the item. An item which is shared by several processes will generally have a different reference number in each process.
- BEHALF** The BEHALF indicator specifies the user identification number which is to be checked in determining whether the requested access to the directory structure will be granted. The BEHALF indicator settings are as follows:
- Bit 17 (in behalf word)
- 1 = ORIGINATOR's ID is to be used (normal setting)
- 0 = OWNER's ID is to be used
- The BEHALF should almost always be set for the ORIGINATOR. The use of OWNER's behalf should be restricted only to system programs accessing proprietary subroutines and then used with CAUTION. An explanation of this terminology is given below.
- Each process may have two distinct identification numbers stored in its state vector, one of which is referred to as the ORIGINATOR's ID, the other the OWNER's ID. The ID is a number which the system assigns to each user of the system when the user's main directory is initially catalogued in the system root directory.

Parameter Summary for File Primitives (continued)

The user who signs on via the "HELLO" sequence is called the ORIGINATOR, and the ID corresponding to this user is the ORIGINATOR's ID. When a process is spawned by the LISTENER, the ORIGINATOR's ID is stored in the state vector of the process. All processes which are subsequently spawned by this spawned process will also have the same ORIGINATOR's ID specified. Hence if any such process issues a file or event primitive with the ORIGINATOR's behalf specified, the ID of the user signing on via the "HELLO" sequence is the one which will be checked by the file system in determining whether access is to be granted. Note that a process will always have a non-null entry in its state vector for the ORIGINATOR's ID.

When a process is spawned, a non-null OWNER's ID may also be stored in its state vector. The OWNER's ID is that of the user in whose main directory the file (or segment) spawned as segment zero is cataloged. A NON-NULL OWNER'S ID IS STORED ONLY IF THIS FILE WAS CATALOGED WITH THE OWNER'S ACCESS PRIVILEGE SWITCH SET ON (see catalog primitive). If the owner's access privilege switch is set OFF, or if the file spawned as segment zero is a scratch file, a null OWNER's ID is set in the state vector. A file or event primitive issued with the OWNER's behalf specified will be rejected unless the OWNER's ID is non-null and the access for the non-null ID is valid.

In general files are rarely catalogued with the OWNER's ACCESS PRIVILEGE SWITCH set ON, since any process which can spawn this file as segment zero will be allowed to use OWNER's ID and hence have all access to the directory structure that the owner has. The OWNER's ID is generally useful only for system programs accessing proprietary subroutines.

Parameter Summary for File Primitives (Continued)

ACCESS, UACCESS, OACCESS

This parameter specifies the access to be requested or assigned in performing a manipulation of the file structure. The access attributes are represented by a mask, the format of which is summarized in Fig. IV-4.

Whenever a user issues a primitive which involves a tree search on the directory structure, a check is made to determine whether he possesses the requested access. Access privileges are specified for three classes of users:

- 1) The Owner - the individual in whose main directory the referenced entity is catalogued.
- 2) A set of users, specifically enumerated, that appear on the Access Control List for the entity.
- 3) The Universe - all other valid users (excluding the owner and users on access control list)

The OWNER's and UNIVERSAL access (OACCESS and UACCESS) for an entity are defined when the entity is initially catalogued. Entries on the Access Control List are defined by the Write Access Control List Primitive.

The BEHALF indicator (discussed previously) determines whether the ORIGINATOR's or OWNER's ID is to be used for the access check. (Normally the ORIGINATOR's ID will be specified). The ID specified by BEHALF is first checked against the ID of the file owner. If they are the same, then the access used is that granted to the owner. Otherwise, a search for the ID is made on the Access Control List. If the ID is specified on the Access Control List, then the access there defined is the one granted. If the ID is neither that of the owner nor on the Access Control List, then the access is that granted to the Universe.

Note that the above comments on the employment of the 'BEHALF' indicator apply only to those primitive requests which involve a tree search on the entity. Primitive requests which reference an entity by its reference number use the access specified in the associated Known Item Table entry.

A more detailed description of the manner in which the access at a particular level in a tree search is checked is summarized in "Access Checking in the Directory File" on the following pages.

SUMMARY OF USAGE ACCESS ATTRIBUTES

Bit	Attribute	Entity	Description
31	READ	File	Can read the file or spawn it into any base register, except zero
		Directory	Can read a directory to get information about any or all of the entries including access control lists
32	WRITE	File	Can truncate or rewrite existing contents of file without adding to its length
		Directory	Can delete or modify specifically named entry
33	APPEND	File	Can add to a file without changing its original contents
		Directory	Can add entries without changing existing entries
	NOTIFY	Event	Can issue a notify or delete to the event
34	EXECUTE	File	Can spawn the file into base register zero
		Directory	Can use the directory for a tree search on a symbolic name
	CAUSE	Event	Can issue a CAUSE or UNCAUSE to the event
35	LOCK	File	Can prevent other users from accessing the file
		Event	Can prevent other users from accessing the event

The usage attributes are referenced by the macro parameters ACCESS, UACCESS, and OACCESS. The access mask is loaded to the -A- or -Q- register (as specified by the primitive) with a bit set ON if the access attribute is to be granted.

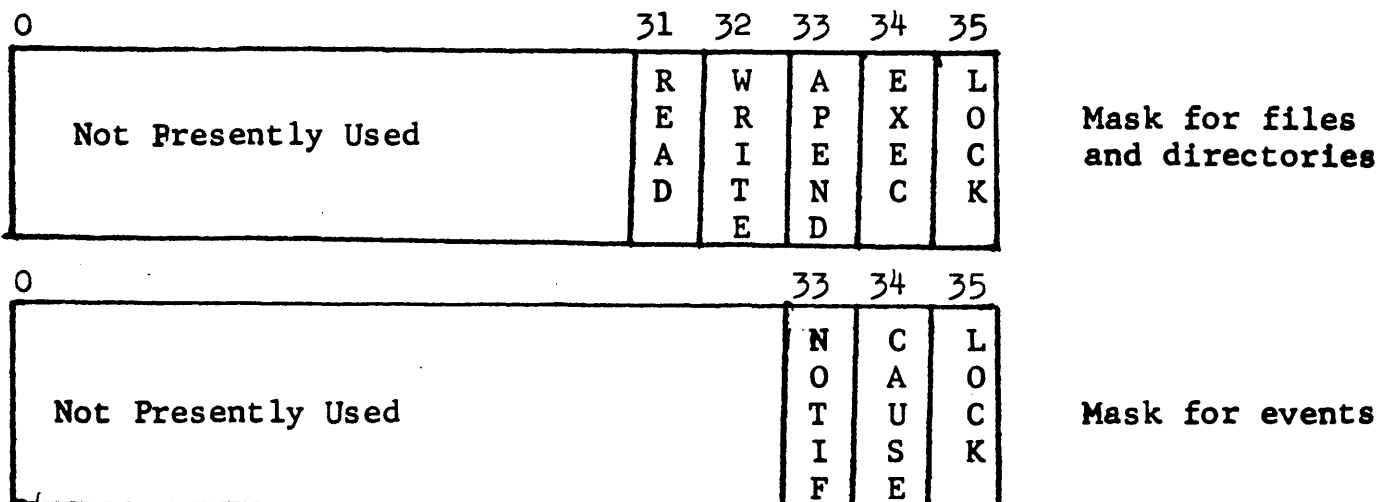


Fig. IV-4

Access Checking in the Directory File

By access checking at a particular stage of a tree search we mean both a password check and, if that is successful, a check on the user's access permission at that stage. For the purpose of describing access checking we divide the primitives which involve a tree search into two categories.

<u>Category I</u>	OPEN	requested access
	OPEN WORKING DIR	execute access
	READ DIR	read access
<u>Category II</u>		
A	CATALOG	append access
	CATDIR	append access
	CATLINK	append access
B	DESTROY	write access
	READ BRANCH	read access
	READ LINK	read access
	READ ACL	read access
	WRITE ACL	write access
	WRITE SYSTEM INF	write access
	WRITE BRANCH	write access

If a primitive in category I has as an argument a tree name $N_1/N_2/\dots/N_k$, then the item actually being manipulated is the one whose local name is N_k . As a result access will only be checked at level k and only the password at level k need be supplied. The access permission which the user must have to that item is specified opposite the corresponding primitive.

Primitives of category II are manipulating the items whose local name is N_{k-1} and as a result access will only be checked at level $k-1$.

In establishing a link the link name may have the form $N_1/N_2/\dots/N_k$ or $N_1/N_2/\dots/N_k P_k$. If the user intends to manipulate the item named by the link name the latter form must be used. If, however, the link is to be used as simply a pointer to a directory and the items to be manipulated lie below that directory then the link name need not include P_k .

Access Checking in the Directory File (Continued)

The following conventions will be established with respect to error returns which result from a failure during the tree search.

<u>Error Return</u>	<u>Circumstance</u>
Invalid Name	Name not found in directory being searched and searcher has execute permission in that directory.
Access Denied	Access check on some item in a directory fails and searcher has execute permission in that directory.
No Search	All other cases

PRESENT PASSWORD SYSTEM

A password is always checked on the directory above the item being accessed.

AND on the root directory when the root directory is the item being accessed.

AND on the item being accessed when the command is an OPEN, OPWD or RDIR.

EXCEPT on catalogs in which case the password is checked only on the directory in which the item is to be cataloged.

Section V

Event Primitives Overview

EVENT PRIMITIVES OVERVIEW

Overview of Event Structure

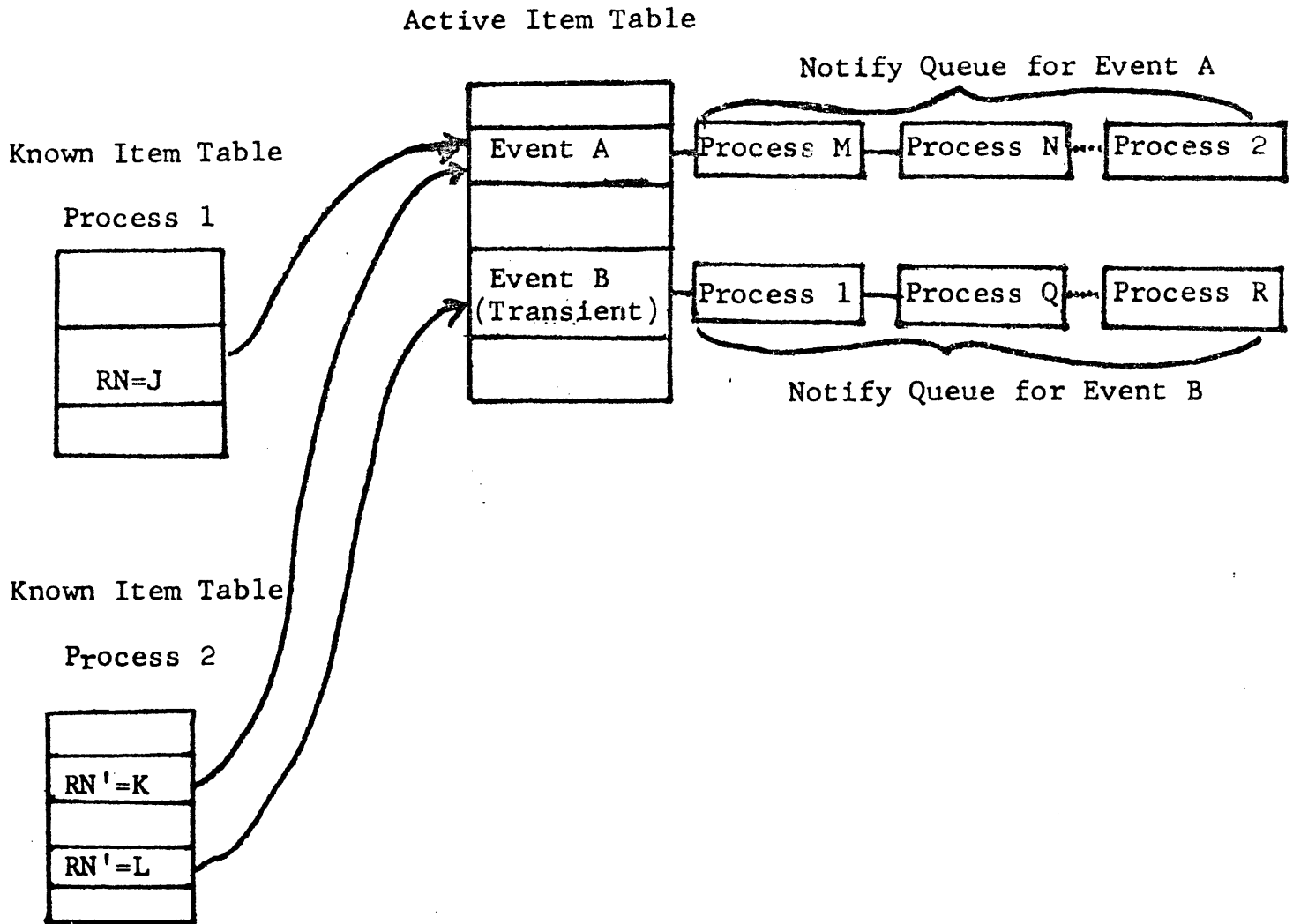
Events are used as a means of interprocess communication, and are handled much in the same manner as files. Events can be either scratch or catalogued (in which case they have a tree name). They have attributes (lock, notify, and cause). They have an owner, and they can be passed on in a spawn.

Events are referenced by an event reference number 'RN' which is either returned when a scratch or catalogued event is first opened, or which is passed to the process when it is spawned.

The system maintains a global event queue for each event which is currently active. A notify primitive to an event 'E' by a process 'P' results in an entry for 'P' being made on E's event queue. The corresponding cause primitive to event 'E' results in the trapping of the process which issued the notify to that event (See fig. V-1). The next time the process regains control, the trap routine (specified by the notify) is executed. If the process which issued the notify is blocked, the occurrence of the cause reawakens it.

Events are catalogued by means of the catlog primitive and a catalogued event is opened by the open primitive. These primitives are described in section IV on File Primitives, and are used for events in a manner similar to that indicated for files.

FIG. V-1



Summary of the action of the principle Event Primitives

OPEN SCRATCH EVENT - A global entry for the event is made in the Active Item Table (AIT) and a private entry for the process is made in its Known Item Table (KIT). For example, if Process 1 opens a scratch event, globally denoted as Event A, the corresponding reference number returned for its KIT entry is RN=J. When the scratch event is opened, its notify queue has length zero.

NOTIFY

- An entry for the process issuing the notify is made on the event's notify queue in the AIT. For example, if Process 2 issues a notify with ERN'=K, an entry for Process 2 is made on the event queue for A.

CAUSE

- A portion of the entries on the notify queue for the event caused will be trapped. For example, if Process 2 issues a CAUSE with ERN'=L, NUMBER=5, and STATE=3, then the first 5 entries on Event Queue B whose state equals 3 will be trapped.

Summary of Macro Calls for Event Primitives

The following calls are applicable for files and events

<u>Code</u>	<u>Macro Call</u>
20	OPEN TRAP, TREE_NAME, TREE_SIZE, BEHALF, ELSIZE, ACCESS
21	CLOSE TRAP, RN
22	CATLOG TRAP, RN, TREE_NAME, TREE_SIZE, SWITCH, UACCESS, OACCESS
23	DESTRO TRAP, TREE_NAME, TREE_SIZE, BEHALF
36	LOCK TRAP, RN
37	UNLOCK TRAP, RN

The following calls apply only to events

<u>Code</u>	<u>Macro Call</u>
38	NOTIF TRAP, ERN, CTRAP, STATE
39	CAUSE TRAP, ERN, STATE, MESSAGE, NUMBER
40	DELET TRAP, ERN, STATE
41	UNCAU TRAP, ERN, NUMBER
42	OPSCE TRAP, TIMLIM, MODE, MAXLEN

Parameters Associated with the Event Primitives

MODE

Events have either a transient or steady state mode associated with them. The mode is specified when the event is initially opened by the Open Scratch Event primitive.

For a transient mode event, only those processes which issue a NOTIFY prior to the time that the event is CAUSEd can be trapped as a result of that CAUSE.

For a steady state event, processes which issue a NOTIFY either prior or subsequent to the time that the event is CAUSEd can be trapped as a result of the CAUSE (See also CURRENT COUNT).

CURRENT COUNT

For each event, the system maintains a running count of the number of processes yet to be trapped. This parameter, the CURRENT COUNT, is incremented by the parameter 'NUMBER' whenever a CAUSE to the event is issued. The current count is decremented by one each time a steady state event is CAUSEd, and is reset to zero each time a transient event is CAUSEd. The current count must be either zero or positive.

Note that for a steady state event, the current count may remain non-zero after the occurrence of a CAUSE to that event. This results when the number of entries on the event queue is less than the parameter 'NUMBER' specified by the CAUSE. A process which subsequently issues a notify to a steady state event with non-zero current count will be immediately trapped.

The current count is therefore the implementation mechanism whereby a NOTIFY to a steady state event can be acknowledged subsequent to the CAUSE to that event. The current count and the current length of an event's queue can be obtained by issuing the Request Status primitive with the event of concern specified by its reference number

Parameters Associated with Event Primitives (Continued)

- NUMBER** The number of entries on the particular event queue which are to be notified (i.e., trapped) when the CAUSE primitive is issued or which are to be deleted when the UNCAUSE primitive is issued. If 'NUMBER' is set to zero, all of the events on the event queue are notified (or deleted). Note, only those entries with the appropriate 'STATE' (see below) are included in those notified or deleted).
- STATE** A parameter specified by the NOTIFY and CAUSE primitives which provides a mechanism for distinguishing between processes awaiting notification. Only those processes whose event queue entries have states matching the state issued with the CAUSE will be trapped.
- The state 7777777777⁸ for transient mode events will match all states: that is, a CAUSE issued with this universal state will cause a process to be notified regardless of the state specified in the NOTIFY.
- Note: States can only be defined for transient mode events. The 'STATE' must be set to zero for a steady state event. If an event primitive is issued to a steady state event with state other than zero, it will be rejected.
- A fixed state event is an event which has been passed on a spawn or pass event and designated 'fixed state' by the passer. He must specify a state. When the receiver of the event performs a NOTIFY or CAUSE on it, the 'STATE' argument from the MME is ignored and the state designated by the father is used instead. If the receiver passes the event to someone else, it will remain fixed state.
- MESSAGE** When an event is caused it also transmits a one word message. The message is returned to the notified process in Status Return Word 2.
- ACCESS** A user may have NOTIFY, CAUSE, or LOCK access to an event. The access to a catalogued event for the owner and the universe is specified when the event is catalogued and checked when the event is opened. Access is granted when the indicated bit is set on.
- | | |
|--------|--------|
| NOTIFY | Bit 33 |
| CAUSE | Bit 34 |
| LOCK | Bit 35 |
- Access for scratch events is established by the open scratch call.

Parameters Associated with Event Primitives (Continued)

- MAXLEN** The maximum number of entries which can be placed on the queue for a particular event. This number is specified in the Open Scratch Event Primitive and may be from 1 to 16 as presently implemented. If the 'MAXLEN' parameter is set to zero, a default number of 16 is assigned.
- TIMLIM** The maximum amount of time a process will be allowed to wait for an event for which it has issued a NOTIFY. This parameter is supplied in the Open Scratch Event Primitive and is expressed in seconds up to a maximum of 1/2 hour. If this parameter is set equal to zero, a default option of 1/2 hour is assumed.
- TRAP** The trap address corresponding to the issuing of the event primitive. The logical status (Status Word 1) employs the same error code as the File Primitives and is summarized in Appendix A-2.
- TRAPC** The trap address where control is returned to a process when an event is caused. (The process specifies this trap address in the Notify Primitive). A message may be returned in Status Word 2 of this trap.
- Status Return Word 1 will contain zero if the trap is for a successful return (i.e., the CAUSE for this NOTIFY has been issued) and will contain an appropriate error code if the trap is the result of the time limit being exceeded.
- RN** Reference number of a file or an event
- ERN** Reference number of an event

SYSTEM EVENTS

The system events are events catalogued by the system which may be used by any slave process. At present there are three system events, the TIME, UNLOCK, and DEALLOCATE events. System events are catalogued with a root directory name of SYSEVNTS. The tree name of the system events are respectively SYSEVNTS/TIME, EVNTS/UNLOCK, and SYSEVNTS/DEALL. A description of these events and their use follows.

Time Event

A process can request notification (i.e. being trapped) at a time N/64 millisecond after it issues a NOTIFY to the time event, where N is specified as the state of the NOTIFY.

To use the time event:

- 1) OPEN the time event with the tree name specified below and request notify access.

ACI 6,SYSEVNTS

ACI 6,TIME

- 2) Issue a NOTIFY to the time event with the following parameters

TRAP Trap associated with issuance of notify

ERN Reference number of time event which was returned by the open.

TRAPC Trap location to which the system is to return control when the system causes the time event.

STATE The number of 64th of a millisecond, N, after the NOTIFY in which the process is to be trapped.

See the OPEN and NOTIFY primitives for additional details.

Unlock Event

A process can request notification when a particular item is unlocked. The item in question is identified by its reference number which is used as the state of the notify.

To use the unlock event:

- 1) OPEN the unlock event with the tree name specified below and request notify access.

ACI 6,SYSEVNTS

ACI 6,UNLOCK

System Events (Continued)

Unlock Event (cont'd)

2) Issue a NOTIFY to the unlock event with the following parameters

TRAP	Trap associated with the issuance of the notify
ERN	Reference number of unlock event which was returned by the open
TRAPC	Trap location to which the system is to return control when the item referenced is unlocked.
STATE	The reference number of the item which is tested for being unlocked.

See the OPEN and NOTIFY primitives for additional details.

Deallocate Event

If a busy status is returned after an OPEN primitive is issued on a device the process can request notification of the device deallocation. When the process is trapped back with logical status 0, the device is then open to it and the status returned is specified below:

Status return word 1:

Bits 0 -17	reference # of open device
18-35	logical status code (See Appendix A-2)

Status return word 2:

Bits 0 -19	not used
20-25	access mask (right justified)
26	not used
27-35	device ID (see fig. III-1).

To use the deallocate event:

1) OPEN the deallocate event with the tree name specified below and request notify access.

ACI 6,SYSEVNTS

ACI 6,DEALL

System Events (Continued)

Deallocate Event (cont'd)

- 2) Issue a NOTIFY to the deallocate event with the following parameters

NOTIFY TRAP,ERN,TRAPC,STATE,MESSAGE

TRAP Trap associated with the issuance of the notify

ERN Reference # of deallocate event which is returned by the open

TRAPC Trap location to which the system is to return control when the device referenced is deallocated.

STATE The tree name of the device which is tested for being deallocated.

ex. ACI 1,MTAA

MESSAGE The message is to be loaded into the Q register. The upper half of Q must contain the element size, expressed in bits, in which subsequent data transactions involving this device are to be expressed. The lower half must have the access requested for the device:

Bit 31	Read
Bit 32	Write
Bit 33	Append
Bit 34	Execute
Bit 35	Lock

If Q lower is set to zero, the process will receive all the access to the device that is allowed.

Possible bad status on NOTIFY:

<u>DEC</u>	<u>OCT</u>	
2	2	Access denied
12	14	Illegal element size
31	25	Device not allocated - reissue OPEN

SPECIAL EVENTS

Process Event

The process event is a scratch event, associated with each process, which occurs (i.e. is CAUSEd) when the process is terminated. The reference number for this event is returned to the father in the Upper Half of Status Word 1 when the process is spawned. (The process event reference number is also referred to as the son's reference number).

The process event may be passed to other processes or closed by the father. In the latter case the son becomes unknown to the father. A CAUSE issued to this event results in the termination of the process. The event is a steady state mode event with NUMBER = ∞ . The father has full access to the event. There are six ways a process can terminate:

- 1) execution of a CAUSE on the process event
- 2) execution of a final CLOSE on the process event
- 3) execution of terminate primitive
- 4) committing a fault, other than MME, while in default mode
- 5) using all the time allocated to process
- 6) process becoming too large to fit into core

Pass Event

A pass event is a transient mode event which enables one process (Process A) to pass the reference number of an item to another process (Process B). State differently, Process A can, by CAUSING the pass event, make an item known to Process B.

To create a pass event, bit 16 is set ON in the MODE parameter for the Open Scratch Event Primitive. The MODE parameter, which is loaded into index register -X4- will be interpreted as follows:

<u>Bits 16,17</u>	<u>Interpretation of Mode Parameter</u>
00	Steady state mode, regular event
01	Transient mode, regular event
11	Transient mode, event of type pass
10	Error. Pass events are defined only in transient mode.

CAUSE TRAP,ERN,STATE,MESSAGE,FRN,ACCESS

The parameters for the CAUSE of the pass event have the following meaning:

TRAP	Trap address associated with issuance of CAUSE
ERN	Reference number of the pass event being CAUSEd.

Pass Event (Continued)

ERN	This reference number is obtained by opening either a scratch or catalogued event of type 'pass'.
STATE	State associated with the CAUSE primitive.
FRN	The reference number of the item being passed.
ACCESS	The access granted on the item being passed. This access is and'ed with the passer's access on the item. (See Fig. IV-4 for format).
MESSAGE	Only 18 bits can be sent as a message with a pass event. QU - Message QL - Ignored
NUMBER	The number of processes to receive the item passed.

The following discussion illustrates how the LISTENER might use the pass event to allow system processes to pass items to it:

- 1) The LISTENER opens a scratch pass event, and then catalogs it with universal access of CAUSE and owner's access of NOTIFY and CAUSE.
- 2) The LISTENER issues a NOTIFY to the pass event.
- 3) To pass an item to the LISTENER, a process must first open the catalogued pass event and then issue a CAUSE to this event.

The format of the status return words for a notify on a pass event is as follows:

TRAP	WORD 1	BITS 0 -17 BITS 18-35	Not specified Logical status code (See Appendix A-2)
TRAPC	WORD 1	BITS 0 -17 BITS 18-35	FRN of passed item Logical status code (See Appendix A-2)
	WORD 2	BITS 0 -17 BITS 18-35	Message ACODE of passer. Accounting ID.

Pass Event (Continued)

The ACODE is a code set up when a process is created by the LISTENER (via the LOGIN sequence) and passed along to all descendents of that process. It is unique to each family of processes (generally one user.)

Section VI

Description of System Primitive Commands

DESCRIPTION OF SYSTEM PRIMITIVE COMMANDS

Introduction

In this section a description of each of the system primitive commands is given. Each description is given in the following general format.

FORMAT	EXPLANATION
Name of Primitive Command	Self Explanatory
Primitive Command Macro Call	Macro call for system macro which initiates the primitive action. The macro expansions are given in Appendix D.
Registers	Specifies the contents of the registers prior to the issuance of the MME for the primitive command.
Status Return Words	Specifies the information which the executive returns to the first two words at the primitive trap location when the primitive operation is completed.
Remarks	Self explanatory

Information On The Use of Primitive Command Descriptions

The primitive command descriptions should initially be used in conjunction with the overview summaries presented in the previous sections. After the basic terminology is understood the descriptions themselves should suffice. It is suggested that the programmer have a clear understanding of the system trap handling procedure (Section II) before attempting to utilize the primitives.

SUMMARY OF PRIMITIVE COMMANDS AND THEIR CODE NUMBERS

Primitives Executed Directly by the Master Mode Executive

Primitive Code and Name	Primitive Classification
00--Privileged Command From Slave Exec	CONTROL
01--Set Up Fault Vector	CONTROL
02--Set Up Squeeze Mode	CONTROL
03--Enter Squeeze Mode	CONTROL
04--Read	I/O
05--Append	I/O
06--Random Read	I/O
07--Random Write	I/O
08--Scratch File	I/O
09--Set Pointer	I/O
10--Request Status	I/O
11--Request Date and Time	CONTROL
12--Request Elapsed Run Time	CONTROL
45--System Status Measurements	CONTROL
46--Measure READ Me	CONTROL
48--Write Me	CONTROL
49--Who Am I	CONTROL
51--Request Working Directory	CONTROL

Primitives Executed by the Slave Mode Executive

13--Spawn	CONTROL
14--Terminate	CONTROL
15--Pause	CONTROL
16--Open Segment	CONTROL
17--Close Segment	CONTROL
18--Change Segment Length	CONTROL
19--Exchange Segments	CONTROL
20--Open	FILE AND EVENT
21--Close	FILE AND EVENT
22--Catalog	FILE AND EVENT
23--Destroy	FILE AND EVENT
24--Open Scratch	FILE
25--Update	FILE
26--Catalog Directory	FILE
27--Write Access Control List	FILE
28--Read Access Control List	FILE
29--Read Directory	FILE
30--Open Working Directory	FILE
31--Read Branch	FILE
32--Read Link	FILE
33--Write System Information	FILE
34--Catalog Link	FILE
35--Write Branch	FILE
36--Lock	FILE AND EVENT
37--Unlock	FILE AND EVENT
38--Notify	EVENT

Summary of Primitive Commands and Their Code Numbers (cont'd)

Primitives Executed by the Slave Mode Executive (cont'd)

39--Cause	EVENT
40--Delete Entry	EVENT
41--Uncause	EVENT
42--Open Scratch Event	EVENT
47--Create Segment	CONTROL

Privileged Primitive (Slave Executive Only)

Code = 0

This primitive is a privileged command issued to the master mode executive by the slave executive only.

Only the slave executive is authorized to issue this primitive. If other processes attempt to use this primitive, the call will be rejected with a command fault.

There are four micro commands, specified by a micro command number, which are associated with this primitive.

<u>Register</u>	<u>Description</u>
X0	0 = Privileged Primitive code number
X1	Micro command number
	00 -- Run micro command
	01 -- Set BAR micro command
	02 -- Destroy scratch file micro
	03 -- Update micro
	04 -- I/O clean-up routine micro
	05 -- Deallocate Drum File micro

The micro commands are discussed on the following pages.

Run Micro Primitive (Slave Executive only)

The Run Micro Primitive (privileged) starts up the execution of the specified slave mode process. Only the slave mode executive is authorized to issue this primitive. This primitive sets up the Run-A-Program working storage and the indirect fault vector. Control is then transferred to the slave mode process via the R\$RETRN routine.

This routine also sets up the BAR1 setting in the Run-A-Program storage for the slave executive to point to the state vector of the current running process. When the slave executive is re-entered for either a timer runout fault or primitive call, BAR1 will be set around the state vector of the current process.

<u>Register</u>	<u>Description</u>
X0	0 = Privileged Primitive
X1	00 = Run Micro Primitive
X2	AIT entry address for state vector
A	Process ID

Set BAR Micro Primitive (Slave Executive Only)

The Set BAR Micro Primitive sets a specified base register around a specified state vector, and returns directly.

This primitive is accessed by the privileged primitive command and is allowed for the slave mode exec only.

<u>Register</u>	<u>Description</u>
X0	0 = Privileged Primitive code number
X1	01 = Set BAR Primitive
X2	AFT pointer for segment
X3	BAR to be set

Destroy Scratch File Micro Primitive (Slave Executive Only)

This Micro Primitive (privileged) releases any disc space or page table space allocated to the file. The file length, allocation and the disc address fields in the AFT are cleared and the disc and page table bits in the flag word are also cleared. The AFT entry is not destroyed by this primitive - that is done by the Slave Exec.

<u>Register</u>	<u>Description</u>
X0	00 = Privileged primitive
X1	02 = Destroy Scratch File Micro Primitive
X2	Slave Trap address
X6	Slave Address of AFT of file

Update Micro Primitive (Slave Executive Only)

The Update Micro Primitive is used in the course of the non-privileged update primitive (25). If the file has a page table and it is in core and has been changed, the page table is written out to the page table file. In any case, at the close of the primitive the file will not have a page table in core. If the file has a new page table and the global switch G\$G2 is set, then the bit map of the page table file is written out. If the file has new disc allocation and the global switch G\$G1 is set, then the disc bit map is written out. In any case, G\$G1 and G\$G2 will be clear after the primitive has been completed. Some of the bits of the flag word are cleared but the AFT entry is otherwise untouched.

<u>Register</u>	<u>Description</u>
X0	00 = Privileged Primitive Code Number
X1	03 = Update Micro Primitive
X2	Slave Trap Address
X6	Slave Address of AFT of file

I/O Cleanup Micro Primitive

This routine is called by the I/O Cleanup Routine of the process terminator. It transfers control to the appropriate device cleanup routine.

<u>Register</u>	<u>Description</u>
X0	0 = Privileged Primitive code number
X1	04 = I/O Cleanup Micro Primitive
X2	Pointer to process table entry
X3	Slave address of outstanding operation entry
X4	Slave address of AIT entry

Deallocate Drum File Micro Primitive (Slave Executive Only)

The Deallocate Drum File primitive deallocates a logically contiguous part of a drum file beginning at the specific logical file address and continuing for as many units as specified. If the unit count is negative all of the file past the logical file address will be deallocated.

<u>Register</u>	<u>Description</u>
X0	00 = Privileged Primitive code number
X1	05 = Deallocate Drum File Micro Primitive
X2	Slave trap address
X6	Slave address of AFT of file

SETFV TRAP,CORELOC

The Setup Fault Vector Primitive is used by a slave process to declare the location of it's slave fault vector. If the declared location, 'CORELOC', is out of bounds then the process is trapped at 'TRAP' with an error return. Otherwise, the new slave fault vector location is established in the state vector and the indirect fault vector is setup to point to the new fault vector location.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	1 = Setup Fault Vector Primitive
X1	TRAP	Trap location
X2	CORELOC	Location of slave fault vector in slave program

STATUS RETURN WORDS

Word 1

18 - 35

Logical Status Code

0 successful operation

1 location of slave fault vector out of bounds

2 upper portion of slave fault vector out of bounds

Format for the Slave Process Fault Vector

The slave process fault vector has a word pair for each of the 16 faults. The order of the faults are the same as in the hardware fault vector.

The format for a word pair is:

Word 1

Storage for the IC and indicators which are in effect when the fault occurs.

Word 2

First instruction of the slave process fault handling routine.

Default mode

If the slave process runs in default mode (no slave fault vector declared), the process is aborted if a fault occurs, except for a MME fault or Timer Runout.

Setup Fault Vector Primitive (Continued)

Example of Coding for Fault Vector

FLOC NULL

DEC	0	
TRA	SDFFT	0 = Shutdown Fault
DEC	0	
TRA	MEMFT	1 = Memory Fault
DEC	0	
TRA	MME	2 = Master Mode Entry
DEC	0	
TRA	FTGFT	3 = Fault Tag Fault
DEC	0	
TRA	TROFT	4 = Timer Runout Fault
DEC	0	
TRA	CMDFT	5 = Command Fault
DEC	0	
TRA	DRLFT	6 = Derail Fault
DEC	0	
TRA	LUPFT	7 = Lockup Fault
DEC	0	
TRA	CONFT	8 = Connect Fault
DEC	0	
TRA	PARFT	9 = Parity Fault
DEC	0	
TRA	IOCFT	10 = Illegal Op Code Fault
DEC	0	
TRA	ONCFT	11 = Operation not Complete Fault
DEC	0	
TRA	SUPFT	12 = Startup Fault
DEC	0	
TRA	OVFFT	13 = Overflow Fault
DEC	0	
TRA	DCKFT	14 = Divide Check Fault
DEC	0	
TRA	EXEFT	15 = Execute Fault

Setup Fault Vector Primitive (Continued)

Note on the Indirect Fault Vector

The Indirect Fault Vector is a block of code maintained by the system executive for passing control from the hardware fault vector to the appropriate entry in the slave process fault vector.

The indirect fault vector is maintained in the Run-A-Program working area and is modified to point to the slave process fault vector when the Set Fault Vector Primitive is issued.

The indirect fault vector contains a word pair for each fault. The first word is a pointer to a corresponding word in the slave process fault vector. The second word is a -TSS- to the corresponding second word in the slave fault vector.

The indirect fault vector, therefore, points to the location in the slave fault vector where the IC and indicators are to be stored by the master mode executive. It also enables control to be returned in slave mode to the user program.

Example:

The system macro which calls the Set Fault Vector Primitive is

```
SETFV  MACRO
        LDX0      1,DU      Set Fault Vector Code Number
        LDX1      #1       TRAP
        LDX2      #2       CORELOC
        MME       0        Issue the Primitive
        ENDM
```

The macro call

```
SETFV (TRAP,DU), (FLOC,DU)
```

establishes a fault vector at FLOC (see previous page for typical coding at FLOC). The trap routine is located at TRAP.

SETSQ TRAP,LOCTBL

The Setup Squeeze Mode Primitive initializes the Squeeze Mode Table in the state vector of the process and computes the effective base register settings for the process when it enters squeeze mode. 'LOCTBL' is the location of the squeeze mode mapping table. If the parameters are in range, a successful trap to location 'TRAP' is made. Otherwise, an unsuccessful trap is returned. The process remains in normal mode until the squeeze primitive is issued.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	2 = Set Squeeze code number
X1	TRAP	Trap location
X2	LOCTBL	Location of Squeeze Mapping Table

STATUS RETURN WORDS

Word 1

18 - 35

Logical status code

0 successful operation

1 squeeze table out of bounds

1 error in segment specification for squeeze mode

Word 2

0 - 17

Number of segment in error (if this was cause of error)

The Squeeze Mapping Table has the following form

ZERO	0,MAP0
ZERO	0,MAP1
ZERO	0,MAP2
ZERO	0,MAP3
VFD	18/ORG0,1/WP,1/0,16/LENO
VFD	18/ORG1,1/WP,1/0,16/LEN1
VFD	18/ORG2,1/WP,1/0,16/LEN2
VFD	18/ORG3,1/WP,1/0,16/LEN3

where:

MAPI = Base register mapping for segment I. (Note that in the squeeze mode table, the index in the table gives the base register in normal mode and MAPI the base register that it maps into in squeeze mode).

Setup Squeeze Mode Primitive (Continued)

ORGI = Origin in Segment I for squeezed bar (relative to segment starting address)

LENI = Length (in words) or the bar setting for Segment I in squeezed mode.

WP = Write protect bit (1 = no writes allowed)

Example of initial settings for squeeze mapping table:

DEC	2	Seg 0 normal mode maps to seg 2 squeeze mode
DEC	3	Seg 1 normal mode maps to seg 3 squeeze mode
DEC	0	Seg 2 normal mode maps to seg 0 squeeze mode
DEC	1	Seg 3 normal mode maps to seg 1 squeeze mode

The remainder of the table sets up the origin and length of each segment in squeeze mode.

OCT	000000400000	SEG 0, ORG = 0, LEN = 0, WP
OCT	000000400000	SEG 1, ORG = 0, LEN = 0, WP
OCT	000000000000	SEG 2, ORG = 0, LEN = 0, No WP
OCT	000000000000	SEG 3, ORG = 0, LEN = 0, No WP

SQUEZ TRAP, REGS, IC

The Enter Squeeze Mode Primitive (also called SQUEZE) will reset the base address registers to those previously established by the SETSQ Primitive, load the registers from 'REGS', and transfer to 'IC'. ('IC' contains the IC and indicators to be used on entry to the squeezed program).

The process will trap at location 'TRAP' on completion. If squeeze mode is not set up, then the process is trapped with error return of four (4). If the register load area is out of bounds, then the process will be trapped with error return of one (1). If the transfer address is out of bounds, then a memory fault will occur.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	3 = Enter Squeeze Mode Primitive code number
X1	TRAP	Trap location
X2	REGS	Location of register settings
A	IC	Instruction counter and indicators

STATUS RETURN WORDS

Word 1

18 - 35

Logical status code

- 0 successful operation
- 4 squeeze mode not set up
- 1 register load area out of bounds

Trap Handling for Squeeze Mode Programs

Consider the sequence of events that occurs when a process running in squeeze mode is interrupted. If no traps have returned during the time of interrupt, control is returned to the slave process which continues its execution in squeeze mode. If, however, a trap has returned (the trap must be for the unsqueezed process) a squeeze mode bit (bit 35) is set in the indicators which are stored in the trap exit location. In addition, the process mode is changed from squeeze to normal and the base address registers set to their unsqueezed values. The outstanding traps are linked in the manner described in section II and control is returned to a trap routine in the unsqueezed program. The unsqueezed program utilizes the squeeze bit, which will be set in the exit location of the last trap routine in the linked sequence of trap routines, to identify the return address in the squeezed program. The standard return from a trap routine, RET to the exit location, cannot be used in this case. Instead, a SQUEZE to the return address must be issued.

READ TRAP,FRN,CORELOC,N,MODE

The Read Primitive (Sequential Read) transfers the next 'N' elements from the file 'FRN' to the area in the slave program which starts at 'CORELOC'. Data is transferred to core from the file element located by the Current Read Pointer.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	4 = Read Primitive code number
X1	TRAP	Trap location
X2	FRN	File reference number
X4	CORELOC	Core memory location indicating the first word in a user slave area for a data transmission.
X5	N	Number of elements in the referenced file which are to be transmitted. The actual element size is specified when the file is opened. (See I/O Primitive Overview for definition of element).
X6	MODE	An indicator specifying what mode the transmission is to be made in. This parameter varies from device to device and is summarized in Appendix B.

STATUS RETURN WORDS

Word 1

0 - 17

The number of units transferred in a successful I/O operation

18 - 35

Logical Status Code for I/O Primitives (See Appendix A-1).

Word 2

0 - 35

Device dependent physical status. See appropriate device manual for interpretation.

Read Primitive (Continued)

The Updating and Determination of the Current Read Pointer For Disk or Drum Files

The starting location for the (Sequential) Read Primitive is specified by a current read pointer which is initialized to zero whenever a file is opened. If a file is shared by several users, each user has his own read pointer.

The current read pointer is updated each time a successful sequential read is executed. However, the pointer may or may not be updated if the read is in error. (To determine the read pointer setting in this case, issue the Set Pointer Primitive for the file in question with the number parameter set to zero. The read pointer setting will be in the lower half of Status Word 2).

If the number of elements read exceeds the current file length, a successful return will be given with the read pointer being set at the end of the file. (That is, the read pointer is not extended past the end of the file, although the read itself may so extend.) This condition can be detected by comparing the number of units specified for transfer with the number of units actually transferred (returned in the upper half of Status Word 1).

The issuing of a read when the current pointer is set to the end of the file will result in an -end of file- error return.

Note that the read pointer is not modified when a random read is issued. For disc and drum files the read pointer can be shifted by the set pointer primitive. For magnetic tape files the pointer can be shifted by issuing the read with an appropriate mode. (See Appendix B for usage of mode parameter with magnetic tapes).

APPEND TRAP,FRN,CORELOC,N,MODE

The Append Primitive (Sequential Write) writes 'N' elements from core memory starting at 'CORELOC' into the file 'FRN'. The element location in the file at which writing starts is specified by the end of file pointer.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	5 = Append Primitive code number
X1	TRAP	Trap location
X2	FRN	File reference number
X4	CORELOC	Core memory location indicating the first word in a user slave area for a data transmission
X5	N	Number of elements in the referenced file which are to be transmitted. The actual element size is specified when the file is opened. (See I/O Primitive Overview for definition of element).
X6	MODE	An indicator specifying what mode the transmission is to be made in. This parameter varies from device to device and is summarized in Appendix B.

STATUS RETURN WORDS

Word 1

- 0 - 17 The number of units transferred in a successful I/O operation
- 18 - 35 Logical Status Code for I/O Primitives (See Appendix A-1).

Word 2

- 0 - 35 Device dependent physical status. See appropriate device manual for interpretation.

Append Primitive (Continued)

End of File Pointer (Current length of file)

The -End of File Pointer- locates the starting element in a file for the next Append (Sequential Write). When a scratch file is first opened the End of File Pointer locates the beginning of the file; that is, element zero.

When a catalogued file is opened the End of File Pointer locates the element following the last one which had been written into the file. There is only one End of File Pointer for a given file, so that Appends by several users sharing a file always start at the location determined by the current setting of the End of File Pointer.

The End of File Pointer is updated whenever a successful Append is executed and also by successful random writes if the last element of the random write exceeds the current pointer setting.

The End of File Pointer may also be updated by an unsuccessful Append if a hardware error occurs. The current position of the End of File Pointer may be obtained by issuing the Request Status Primitive, the position in elements being returned in the status word. The End of File Pointer may be reset to zero by issuing the Scratch Primitive.

RRF TRAP,FRN,FILELOC,CORELOC,N

The RRF (Read Random File) Primitive transfers the 'N' elements from location 'FILELOC' in file 'FRN' to the area in core memory which starts at 'CORELOC'. This primitive can only be used with drum or disk files.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	6 = Read Random File Primitive code number
X1	TRAP	Trap location
X2	FRN	File reference number
X3	FILELOC	An integer, representing the element number within the file at which a data transmission begins. This parameter is only employed for random operations.
X4	CORELOC	Logical core memory location indicating the first word in a user slave area for a data transmission.
X5	N	Number of elements in the referenced file which are to be transmitted. The actual element size is specified when the file is opened. (See I/O Primitive Overview for definition of element).

STATUS RETURN WORDS

Word 1

0 - 17

The number of units transferred in a successful I/O operation

18 - 35

Logical Status Code for I/O Primitives (See Appendix A-1).

Word 2

0 - 35

Device dependent physical status. See appropriate device manual for interpretation.

WRF TRAP,FRN,FILELOC,CORELOC,N

The WRF (Write Random File) Primitive writes 'N' elements from core memory starting at 'CORELOC' into the file 'FRN'. The element in the file at which writing starts is specified by 'FILELOC'. This primitive can only be used with drum or disk files.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	7 = Write Random File Primitive code number
X1	TRAP	Trap location
X2	FRN	File reference number
X3	FILELOC	An integer, representing the element number within the file at which a data transmission begins. This parameter is only employed for random operations.
X4	CORELOC	Logical core memory location indicating the first word in a user slave area for a data transmission.
X5	N	Number of elements in the referenced file which are to be transmitted. The actual element size is specified when the file is opened. (See I/O Primitive Overview for definition of element).

STATUS RETURN WORDS

Word 1

0 - 17

The number of units transferred in a successful I/O operation.

18 - 35

Logical Status Code for I/O Primitives (See Appendix A-1).

Word 2

0 - 35

Device dependent physical status. See appropriate device manual for interpretation.

Scratch Primitive

Code = 8

SCR TRAP,FRN,FILELOC

The Scratch Primitive destroys all data in the file 'FRN' by releasing all storage owned by the file and setting the current Read and End of File Pointers to the start of the file if FILELOC = 0. The file is not closed by this operation. Otherwise, the file is scratched starting from the element specified by FILELOC.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	8 = Scratch Primitive code number
X1	TRAP	Trap location
X2	FRN	File reference number
X3	FILELOC	Element in file to scratch from

STATUS RETURN WORDS

Word 1

0 - 17

Not specified

18 - 35

Logical Status Code for I/O Primitives
(See Appendix A-1).

Word 2

0 - 35

Device dependent physical status. See
appropriate device manual for interpretation.REMARKS

This primitive can be used only on disk or drum files.

Set Pointer Primitive

Code = 9

SPTR TRAP,FRN,N

The Set Pointer Primitive shifts the current Read Pointer by 'N' element for file 'FRN' which is located on the disc or drum. (For files corresponding to magnetic tapes, positioning is accomplished by using the Read Primitive with an appropriate mode).

The process is trapped at location 'TRAP' upon completion with the new setting of the Read Pointer, expressed in elements, in the lower half of Status Word 2. Error returns will be made if the process does not have read access for the file or if the pointer is shifted beyond the maximum file length or file beginning.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	9 = Set Pointer Primitive code number
X1	TRAP	Trap location
X2	FRN	File reference number
X3	N	The number of elements which are added to or subtracted from the current setting of the read pointer. (For subtraction the number is expressed in two's complement form).

STATUS RETURN WORDS

Word 1

0 - 17

Not specified

18 - 35

Logical Status Code for I/O Primitives (See Appendix A-1).

Word 2

0 - 17

Not specified

18 - 35

New setting of Read Pointer (in elements)

Request Status Primitive

Code = 10

RQST TRAP,RN

The Request Status Primitive returns information relating to file (or event) 'RN' in the Status Return Words. The Process traps at location trap upon completion with the status words containing the information summarized below.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	10 = Request Status Primitive code number
X1	TRAP	Trap location
X2	RN	File or event reference number

STATUS RETURN WORDS

Word 1

0 - 17

Number of bits/element (files)
Current count (events)

18 - 35

Logical status code for I/O primitives
(See Appendix A-1).

Word 2

0 - 17

File length in units (files)
Queue length (events)
e.g. 32 words for disk

18 - 20

Not Used

21 - 25

Access mask (files) (RWAEL)

23 - 25

Access mask (events) (NCL)

26

Not Used

27 - 35

Device ID

Request Date and Time Primitive

Code = 11

RQDT No arguments

The Date and Time Request Primitive returns the date in the A-register, the time of day in the Q-register, and returns control to the location of the MME plus one.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	11 = Request Date and Time Primitive code number

STATUS RETURN WORDS

None since no trap routine.

Format for Date and Time

DATE Expressed as BCD characters (i.e., 0007010606108 for 7/16/68).

TIME 33 bit number representing time since midnight in 64's of a millisecond. (Note: this can exceed 24 hours).

Request Elapsed Run Time Primitive

Code = 12

RQERT No arguments

The Request Elapsed Run Time Primitive returns the elapsed run time of the process, right justified in the Q-reg. The units are 1/64 of milliseconds. It returns the count of resources used by the process in the A-reg. This primitive has no trap routine. Upon completion control is returned to the location of the MME plus one.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	12 = Request Elapsed Time Primitive Code Number

STATUS RETURN WORDS

None (since no trap routine)

Other Return Information

- A Resource count in resource units
- Q Elapsed run time (1/64 milliseconds), right justified.

SPAWN TRAP, PLOC, LENGTH, ORIG

The Spawn Primitive creates a new process (a son) which will be executed in parallel with its creator (the father). The father specifies the information pertaining to the son in a parameter list starting at 'PLOC'. The number of words in the list is given by 'LENGTH'. When the son has been created, the father is trapped at 'TRAP' with the son's reference number in the upper half of Status Return Word 0.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	13 = Spawn primitive code number
X1	TRAP	Trap location
X4	PLOC	Location of parameter list
X5	LENGTH	Length of parameter list
Q	ORIG	Originator (for special SPAWN)

STATUS RETURN WORDS

Word 0

0-17

Event reference number for process event just spawned (i.e., son's reference number).

18-35

Logical status code

0 successful operation

12 argument list must be at least 14 words long

4 segment zero (0) must be specified

5 illegal segment specification

6 invalid access on segment specification

7 illegal file reference number

8 only a file or event may be passed

9 resources not available

REMARKS

The number of files and events passed to the spawned process will appear in X-register zero (0) on initial entry to the process.

If a user is trapped with status code = 9, he should retry the command.

The son's reference number is also referred to as the process event number (See 'Special Events' in Section V).

Spawn Primitive (Continued)

Format of Parameter List

<u>Words</u>	<u>Function</u>
0-7	Initial register settings for son
8	Time limit for process in 64's of a msec. A negative number gives an infinite time limit
9	Option switches
Bits 14-17	Priority curve (0=default, same priority as father)
Bit 32	Indicates core end segments exist
Bit 33	On if process is to know about itself, as reference number one greater than the number handed on by the father, (N+1).
Bit 35	If on, process will be given a timer runoff fault. If it runs out of time, it will get 16 secs. more.
10	Son segment zero (0) specification
11	Son segment one (1) specification
12	Son segment two (2) specification
13	Son segment three (3) specification
14-N	Files and events to be passed to the son (i.e., the specification of the son's KIT).

Segment Specification

One (1) word per segment

Bits 0-17	File Reference or Segment Number
Bit 18	Write Protect
Bit 32	Core end segment
Bits 34-35	=0 for void segment =1 for spawn from parent's segment =2 for spawn from parent's file

KFT Specification for File or Event

One (1) word per file or event or 2 words for fixed state

Bits 0-17	File or event reference number in father's KIT
Bit 20	Fixed state event - if on next word contains state
Bits 31-35	Access granted to son for file (Read, Write, Append, Execute, Lock)
or	
Bits 33-35	Access granted to son for event (Notify, Cause, Lock)

Spawn Primitive (Continued)

KFT Specification for File or Event (Continued)

The order of the word in the KIT specification will determine the item reference number seen by the son. (i.e., the item specified by Word 14 will be reference number 0 in the son's KIT, that specified by word 15 will be reference number 1, etc.) The access mask will be ANDed with the father's access mask to determine the son's access to the file.

In the case of a special spawn by a privileged process, Q will contain the originator's ID returned by the open working directory primitive. Six words, originator's name, are stored after the buffer, but they are not counted in the length.

Additional Information on SPAWN

- 1) All RN's which are specified in the parameter list are with respect to the father process.
- 2) The parameter list must be at least 14 words to specify the initial register settings and the contents of all four segments. The remaining words which specify the son's files and events are optional.
- 3) The son's process starts execution at location zero of segment zero. Hence, segment zero must be specified and correspond to an executable procedure.
- 4) Segments other than segment zero may be void. Void segments are specified by setting bits 34-35 zero in the corresponding segment specification word.
- 5) The father must have execute access on a file which he spawns as segment zero for his son and read access for files which are spawned as segments one, two, or three.
- 6) The father can make a segment write inhibit by setting the write protect bit in the corresponding segment parameter word. If the segment is spawned from a write-inhibit file or from a father's segment which is write inhibit, the bit will be set on regardless.
- 7) The access mask for a son's file will be -ANDED- with the father's access to determine the son's resultant access to the file. Hence, the son is not allowed an access to a file that the father does not have.
- 8) If the file that is spawned in segment zero has the owner's access privilege ON then the son will be allowed to use the owner's ID associated with that file in performing file operations. Note that the owner's access switch is set when the file is catalogued.

Terminate Primitive

Code = 14

TERM no arguments

The Terminate Primitive terminates the execution of a process and liquidates the resources used by the process. The following things happen:

- Event trap blocks are returned to AIF free list
- Segments are closed
- State vector is closed
- Process event is activated

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	14 = Terminate Primitive identifier

PAUSE No arguments

The Pause Primitive blocks the further execution of the current process if:

- 1) There are no traps on the outstanding trap queue for the process, and
- 2) If there is at least one trap for an I/O, file, or event primitive which has not returned.

If these conditions are satisfied, the process will remain blocked until one of the I/O, file, or event traps return. At this time, the process resumes execution at the trap location.

If neither condition is satisfied, the corresponding Pause results in the simulation of a timer runout fault. That is, the process is rescheduled. In such a case, the process will resume execution at the instruction following the Pause MME the next time the scheduler runs the process.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	15 = Pause Primitive code number

Notes on the Use of 'PAUSE'

When a process no longer has useful work to do (i.e., it is awaiting the completion of some operation by the executive) it should issue the 'PAUSE' Primitive instead of sitting in a wait loop. This places the process on a queue of blocked processes and enables another process to utilize the processor. The process which issued the 'PAUSE' will be rescheduled when the trap which is being awaited is returned.

The following code illustrates how the Pause Primitive can be incorporated in a 'WAIT' macro, which would be issued whenever a process has no useful work to perform.

Pause Primitive (Continued)

WAIT	MACRO		
	REM		
	INHIB	SAVE,ON	Prevent interrupts in middle of test loop
	SZN	TRAP+2	Test exit word to see if trap has returned
	TNZ	*+4	Trap has returned-Exit and continue processing
	LDX0	15,DU	Otherwise issue PAUSE primitive
	MME	0	
	TRA	*-4	Go back if control reaches here
	INHIB	RESTORE	
	ENDM		
	STZ	TRAP+2	Set exit word to zero as a flag
	OPEN	(),...	Issue Open primitive
	WAIT		Pause and wait for trap to return
	TRA	RESUME	Continue processing

A Trap routine, located at 'TRAP' which can be used with the above macro is:

TRAP	NULL		
	DEC	0	Storage for Status Word 1
	DEC	0	Storage for Status Word 2
	DEC	0	Storage for trap routine exit location
	RET	TRAP+2	Return to process via standard trap return

Further Notes on the Use of PAUSE

It is important to note that the Pause Primitive will cause a process to go in a blocked state only for a subset of the I/O, File, and Event primitives.

For the remaining primitives, which include all of the control primitives, the PAUSE results in the process being rescheduled. If a user desires to go blocked in this case, he should precede the PAUSE by a Notify Event primitive.

Note that when a process is blocked it becomes active again after the occurrence of the I/O or event trap. Control returns to the process at the trap routine and continues in a manner determined by the trap routine coding. If each trap routine is terminated by a -RET WORD3-, control eventually transfers to the instruction following the PAUSE MME. Otherwise, control transfers to the indicated termination location.

Open Segment Primitive

Code = 16

OPSEG TRAP,SEGNUM,LENGTH

This primitive opens a segment for use by the process. The process specifies the segment number and length desired.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	16 = Open Segment Primitive code number
X1	TRAP	Trap location
X2	SEGNUM	Segment number (0, 1, 2, or 3)
X3	LENGTH	Desired segment length, in words. The segment length must be less than 200000 octal or 65536 decimal.

STATUS RETURN WORDS

Word 1

18 - 35

Logical status code

- 0 successful operation
- 1 segment number out of range
- 7 segment length out of range
- 8 segment already opened

The logical addresses associated with the starting location of each of the four segments are

Segment 0	Octal 0
Segment 1	Octal 200000
Segment 2	Octal 400000
Segment 3	Octal 600000

Example:

OPSEG (TRAP,DU),(3,DU),(2048,DU)

Since SEGNUM = 3, the logical starting address for the segment is 600000 (octal).

The segment opened contains 2048 (Mod 512) blocks of 512 words.

Close Segment Primitive

Code = 17

JLSEG TRAP,SEGNUM

This primitive routine closes a segment for a process. The segment is no longer available to the process, however, it may be available to other processes.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	17 = Close Segment Primitive code number
X1	TRAP	Trap location
X2	SEGNUM	Segment number (0, 1, 2, or 3)

STATUS RETURN WORDS

Word 1

18 - 35

Logical status code

- 0 successful operation
- 1 segment number out of range
- 2 segment not known
- 4 I/O activity in progress

Change Segment Length Primitive

Code = 18

CHSEG TRAP, SEGNUM, LENGTH

The Change Segment Length Primitive changes the length of the segment specified by 'SEGNUM' to 'LENGTH'.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	18 = Change Segment Length Primitive code number
X1	TRAP	Trap location
X2	SEGNUM	Segment number (0, 1, 2, or 3)
X3	LENGTH	Desired length (words)

STATUS RETURN WORDS

Word 1

18 - 35

Logical status code

- 0 successful operation
- 1 segment number out of range
- 2 segment not known
- 7 segment length out of range
- 4 I/O activity in progress
- 5 segment is write inhibit
- 6 requested segment length equals zero

Remarks

Segment lengths are defined in blocks of 512 words. The executive will round off the requested length to the next 512 word block, if this length is not a multiple of 512.

Exchange Two Segments Primitive

Code = 19

EXSEG TRAP,SEG1NUM,SEG2NUM

This primitive exchanges the segment numbers of two specified segments:

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	19 = Exchange Segments Primitive identifier
X1	TRAP	Trap location
X2	SEG1NUM	Segment 1 number (0, 1, 2, or 3)
X3	SEG2NUM	Segment 2 number (0, 1, 2, or 3)

STATUS RETURN WORDS

Word 1

18 - 35

Logical status code

- 0 successful operation
- 1 number for segment 1 is out of range
- 1 number for segment 2 is out of range

OPEN TRAP, TREE_NAME, TREE_SIZE, BEHALF, ELSIZE, ACCESS

The OPEN primitive makes the catalogued item specified by 'TREE_NAME' active and known to the process. The reference number (RN) and other information relevant to the item is returned in the status words. Most subsequent references to the item will be in terms of its reference number.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	20 = Open Primitive code number
X1	TRAP	Trap location
X4	TREE_NAME	Location of the tree name of the item being opened
X5	TREE_SIZE	Number of words in the tree name (a multiple of six)
X6	BEHALF	Behalf indicator - Bit 17 1 = Originator (normal setting) 0 = Owner
X7	ELSIZE	The element size, expressed in bits, in which subsequent data transactions involving this file are to be expressed (not required for events).
Q	ACCESS	Requested access for item Bit 31 - Read (File) Bit 32 - Write (File) Bit 33 - Append (File) Notify (Event) Bit 34 - Execute (File) Cause (Event) Bit 35 - Lock (File, Event) If this word is set to zero, the program will receive all the accesses to the item that it is allowed. The accesses granted are returned in Status Word 2.

Open Primitive (Continued)

STATUS RETURN WORDS

Word 1

0-17

File or Event reference number

18-35

Logical Status Code (See Appendix A-2)

Word 2

0-17

File length in elements (File)
Number of queue entries (event)

20-25

Access mask (right justified)

26

Not used

27-35

Device ID (See Fig. III-1).

Tree Name Conventions for Opening Device Files

The tree name of a device file contains two levels. These levels correspond to the identifier 'DEVICE', and the name code of the device type and number of the device. (If the identifier 'AA' is used, the next available free device will be supplied). The two words assigned to the password at each level are filled with ASCII blanks.

Device Name Code

Device Type

DI

Disc

DR

Drum

OP

Operator's Console

CP

Card Punch

LP

Line Printer

CR

Card Reader (see "Communication With Card Reader From the Listener")

TT

Teletypewriter

CL

Communication Line Multiplexer Channel

MT

Magnetic Tape

Example of treename coding for any magnetic tape (tree name = DEVICE/MTAA)

ACI 6,DEVICE

Device Identifier

ACI 6,MTAA

Code Name for any Card Reader

Example of treename coding for tape number 5 (tree name = DEVICE/MT05)

ACI 6,DEVICE

Device Identifier

ACI 6,MT05

Code Name for Tape Handler 5

Open Primitive (Continued)

REMARKS

- 1) Each time the OPEN primitive is issued, a new reference number for the specified file is returned. Hence, a given process may open the same file more than once.
- 2) Several processes may open the same file during a given time interval. The reference number returned by the OPEN primitive is private to the process which issued the OPEN; that is, the reference number is an entry in the Known Item Table for the process. Global information about the file is maintained in a single Active Item Table (AIT) entry to which all of the KIT entries point.
- 3) Each device, except the operator's console, can only be opened by one process at a time. Subsequent processes trying to open it before the first process has closed will receive logical error status 3 (software busy). They can use the deallocate event to wait for its release.

Close Primitive

Code = 21

CLOSE TRAP, RN

The item whose reference number is 'RN' is made unknown to the process. When the item is closed, its attachment count is decremented by one. The global entry for the item (in the Active Item Table) is not removed until the attachment count reaches zero.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	21 = Close Primitive code number
X1	TRAP	Trap address
X2	RN	File or event reference number

STATUS RETURN WORDS

Word 1

0-17

Undefined

18-35

Logical Status Code (See Appendix A-2)

REMARKS

If a scratch file is closed with attachment count zero, the storage allocated to the scratch file is returned to the free storage pool.

CATALOG TRAP, RN, TREE_NAME, TREE_SIZE, SWITCH, UACCESS, OACCESS

The scratch item whose reference number is 'RN' is catalogued with the tree name specified by 'TREE_NAME'. A segment spawned from a catalogued file will be write inhibit if the write inhibit indicator in 'SWITCH' is set on.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	22 = Catalog Primitive code number
X1	TRAP	Trap location
X2	RN	Reference number of the file or event being catalogued
X4	TREE_NAME	Location of the tree name of the catalogued item
X5	TREE_SIZE	Number of words in tree name (a multiple of six)
X6	SWITCH	Bit 17 - Behalf Indicator 1 = Originator's behalf (normal setting) 0 = Owner's behalf Bit 16 - Owner's Access Privilege Indicator (Files Only). See Remarks. 0 = Access to owner's ID not granted (normal setting) 1 = Access granted to owner's ID Bit 15 - Write Inhibit Indicator (Files Only) 0 = Segment spawned from file will not be write inhibit 1 = Segment spawned from file will be write inhibit (See Remarks)
A	UACCESS	Universal Access
Q	OACCESS	Owner's Access Bit 31 - Read (File) Bit 32 - Write (File) Bit 33 - Append (File) Notify (Event) Bit 34 - Execute (File) Cause (Event) Bit 35 - Lock (File, Event)

Catalog Primitive (Continued)

STATUS RETURN WORDS

Word 1

0-17

Undefined

18-35

Logical Status Code (See Appendix A-2)

REMARKS

1) If the owner's access privilege switch is set on, any user with execute access on this file will be allowed the use of the file owner's id when the user spawns this file as segment zero of a process, (i.e. when the file is spawned as segment zero the user doing the spawn will have all access privileges that the file owner has including access to the owner's entire directory structure). This switch should rarely be set on if the owner desires to protect his files.

If owner's access is granted for a file, the owner will be protected only if control cannot be transferred outside of the procedure that this file spawns into.

2) If the write inhibit indicator is set on, the segment which is spawned from this file will be write inhibit, and the file itself will have its WRITE and APPEND access bits set off. That is, neither write ~~nor~~ append operations to the file will be allowed.

Destroy Primitive

Code = 23

DESTRO TRAP,TREE_NAME,TREE_SIZE,BEHALF

The item named by 'TREE_NAME' is uncatalogued (i.e. its directory entry is destroyed). If the item is currently active, it is transformed into a scratch item.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	23 = Destroy Primitive code number
X1	TRAP	Trap location
X4	TREE_NAME	Location of tree name of item to be destroyed
X5	TREE_SIZE	Number of words in tree name
X6	BEHALF	1 = Originator's behalf (normal setting) 0 = Owner's behalf

STATUS RETURN WORDS

Word 1

0-17

Undefined

18-35

Logical Status Code (See Appendix A-2)

PENS TRAP, DEVID, MAXLEN, ELSIZE

The Open Scratch Primitive opens a zero length, scratch (uncatalogued) file on the device specified by 'DEVID' (disc or drum). The File Reference Number of the file is returned in the status word. Subsequent data transactions involving this file will occur in units specified by 'ELSIZE'. The maximum file length is given by 'MAXLEN' and the type of error recovery by 'STATUS'. The scratch file is granted all of the access attributes.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	24 = Open Scratch Primitive code number
X1	TRAP	Trap Location
X4	DEVID	Device Identification of device on which scratch file is to be opened 1 = Disc 2 = Drum (access restricted to privileged users)
X5	MAXLEN	The maximum file length in elements. Attempts to access file outside this length will give an error return.
X7	ELSIZE	Element size, expressed in Bits, in which all subsequent data transactions for the file are to be expressed. The number of bits must be an integral multiple of the unit size for the disc or drum (i.e. a multiple of 32*36=1152). If this field is set to zero, a default element size of 1152 bits (32 words) is set up.

STATUS RETURN WORDS

Word 1

0 - 17	File Reference Number
18 - 35	Logical status code (See Appendix A-2).

Update Primitive

Code = 25

UPDATE TRAP, FRN

The Update Primitive writes current information to the branch. It can be called previous to any close. A final close is no longer required to update information in the branch.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	25 = Update Primitive code number
X1	TRAP	Trap location
X2	FRN	File Reference Number

STATUS RETURN WORDS

0-17	Undefined
18-35	Logical status code

REMARKS

An invalid file reference number or an attempt to update a scratch file will return with an error message.

JATDIR TRAP, TREE_NAME, TREE_SIZE, BEHALF, UACCESS, OACCESS

An empty directory with name specified by an N level 'TREE_NAME' is catalogued in the directory specified by the previous N-1 levels of 'TREE_NAME'. Only privileged users are allowed to catalog with N=1, since this corresponds to making an entry at the root level of the directory structure.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	26 = Catalog Directory Primitive code number
X1	TRAP	Trap location
X4	TREE_NAME	Location of tree name of directory being catalogued
X5	TREE_SIZE	Number of words in tree name
X6	BEHALF	1 = Originator's behalf (normal setting) 0 = Owner's behalf
A	UACCESS	Universal Access
Q	OACCESS	Owner's Access
		Bit 31 READ - Read directory entries including access control lists
		Bit 32 WRITE - Delete or modify specifically named entries
		Bit 33 APPEND - Add entries without changing existing entries
		Bit 34 EXECUTE - Can use directory for a tree search on a symbolic name

STATUS RETURN WORDS

Word 1

0 - 17

Undefined

18 - 35

Logical Status Code (See Appendix A-2)

'RACL TRAP, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, BUFSIZE

The Write Access Control List Primitive modifies the access control list which is associated with a given file, directory, or event. The primitive either adds a new user entry to the list, modifies an existing entry, or deletes an existing entry.

Several entries may be added with a given command. The name and access associated with each entry is defined in a five word block of information which is contained in a buffer of size 'BUFSIZE' which starts at 'BUFLOC'. The format for the five word entry is specified below.

The primitive adds a new entry for a user if none exists previously. Otherwise, it modifies the existing entry. The executive stores status in each five word block if the operation is not successful.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	27 = Write Access Control List code number
X1	TRAP	Trap location
X3	BUFLOC	Starting location of buffer in slave program which contains the five word blocks defining names and accesses to be written
X4	TREE_NAME	Location of the tree name of the file or event whose access is being modified
X5	TREE_SIZE	Number of words in the tree name
X6	BEHALF	Behalf Indicator - Bit 17 1 = Originator's behalf (normal setting) 0 = Owner's behalf
X7	BUFSIZE	Number of words in the buffer which contains the five word access control list blocks. The maximum size is 50 words and all intermediate sizes must be a multiple of 5.

STATUS RETURN WORDS

Word 1

0 - 17	Not specified
18 - 35	Logical status code (See Appendix A-2)

Format for the 5 Word Access Control List Block

Words 1-4	The name of the user as it appears in the root directory of the system catalog (i.e. the first level of the user's tree name).
Word 5	
Bits 0-17	Reserved for storing additional status by the executive. The executive stores 777777 ₈ in this field if the access specified in this block has not been successfully written to the access control list. This occurs when the name specified in Words 1-4 is not catalogued in the root directory.
Bit 29	Erase bit. If this bit is set ON the previously defined entry for the named user will be deleted from the access control list.
Bits 30-35	Access to be granted to the named user Bit 30 -- READ (file) Bit 31 -- WRITE (file) Bit 32 -- APPEND (file) or NOTIFY (event) Bit 33 -- EXECUTE (file) or CAUSE (event) Bit 34 -- LOCK (file,event) Bit 35 -- TRAP (not implemented yet)

REMARKS

If the owner's name appears on the list, the owner's access will be changed in the branch. If the delete bit is on, the owner's access will be set equal to the universal access.

RDACL TRAP, INDEX, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, NUMBER

The Read Access Control List Primitive reads entries from the access control list of a specified file, directory, or event, and writes them to a buffer in the user program. The name and access for each entry is written in a five word block, similar to that employed in the Write Access Control List Primitive.

Reading starts from the entry number in the control list which is specified by 'INDEX'. The number of entries read, up to a maximum of 10, is given by 'NUMBER'.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	28 = Read Access Control List Primitive code number
X1	TRAP	Trap location
X2	INDEX	Number of the access control list entry at which reading is to begin (=1 for first entry).
X3	BUFLOC	Location of buffer into which the five word block containing names and accesses are to be written by the executive
X4	TREE_NAME	Location of the tree name of the file, directory, or event whose access is being read
X5	TREE_SIZE	Number of words in the tree name
X6	BEHALF	1 = Originator's behalf (normal setting) 0 = Owner's behalf
X7	NUMBER	Number of ACL entries to be read, up to a maximum of 10. Reading begins from the entry number stored in -X2-.

STATUS RETURN WORDS

Word 1

0 - 17	Not specified
18 - 35	Logical status code (See Appendix A-2)

Read Access Control List (Continued)

Format for the 5 Word Access Control List Block

Words 1 - 4

The name of the user as it appears in the root directory of the system catalog (i.e. the first level of the user's tree name).

Word 5

Bits 30-35

Access which has been granted to user
Bit 30 -- READ (file)
Bit 31 -- WRITE (file)
Bit 32 -- APPEND (file) or NOTIFY (event)
Bit 33 -- EXECUTE (file) or CAUSE (event)
Bit 34 -- LOCK (file,event)
Bit 35 -- TRAP (not implemented at present)

RDDIR TRAP, INDEX, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, BUFSIZE

The Read Directory Primitive reads the contents of one or more branches from the directory specified by 'TREE_NAME' into a buffer area in the slave program which starts at 'BUFLOC'. The branches are read sequentially, the starting branch being specified by the number 'INDEX'. The number of branches read depends upon the buffer size, 'BUFSIZE', there being one branch read for each ten word block which is assigned. The number of branches read is returned in the upper half of Status Word 1.

Read Access is required for the directory specified.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	29 = Read Directory Primitive code number
X1	TRAP	Trap location
X2	INDEX	Index in directory of first branch to be read. (The index is =1 for the initial branch in the dir)
X3	BUFLOC	Starting location of slave buffer area where information from the specified branches will be returned
X4	TREE_NAME	Location of tree name of directory being read
X5	TREE_SIZE	Number of words in tree name
X6	BEHALF	1 = Originator's behalf (normal setting) 0 = Owner's behalf
X7	BUFSIZE	Size of slave buffer area where branch information will be returned. Ten words must be specified for each branch to be returned. The buffer starting location is stored in X3.

STATUS RETURN WORDS

Word 1

0 - 17

The number of branches which have been read into the buffer area

18 - 35

Logical status code (See Appendix A-2)

REMARKS

1) For system information, use the Read Branch Primitive.

Read Directory Primitive (Continued)

Format for Each 10 Word Block of Returned Branch Information

Word 1 Upper

Bits 0-1	Entry Type 00 - File 01 - Directory 10 - Event 11 - Link
Bit 3	Owner's Directory Privilege Switch (file)
Bits 12-17	Owner's Access (all except Link)
Bits 14-17	Number of names in tree name (link)

Word 1 Lower

Bit 18	Password Bit (=1 if item has password)
Bit 19	Active Bit (file or event) =1 if item is open
Bit 20	Write inhibit (file)
Bit 21	Access control list bit (not link) =1 if item has ACL
Bit 22	System information bit (file) =1 if file has system information in branch
Bits 31-35	Requestor's access (all except link)

Word 2 Upper

Bits 0-11	Day item last used (file or event)
Bits 12-16	Universal access (all except link)

Word 2 Lower

Bits 18-23	Not Used
Bits 24-35	Date Last Modified

Word 3 Upper

File Length (file) or Current Count (event) or No. of Blocks used (root directory entry)
--

Word 3 Lower

Maximum file length in units (file) or Maximum queue length (event) or Maximum number of blocks allowed (root dir. entry)

Read Directory Primitive (Continued)

Format for each 10 Word Block of Returned Branch Information (cont'd)

Word 4 Upper	Units allocated (file) or
Bit 6	Pass event indicator (event)
Bit 7	Unlock event indicator (event)
Bit 8	Mode (event)
Bit 9	Infinity bit (event)
Bit 10	Time event indicator (event)
Word 4 Lower	System ID. (file) =0 since no system information returned (This field may be non-zero for the Read Branch primitive) or Time Limit (event)
Words 5 and 6	Password (returned only for priv. ID)
Words 7-10	Branch name

NOTE: The first six words of returned information has the same format in the Read Branch, Read Directory, and Read Link Primitives. The last 4 words are different for each primitive.

OPENW TRAP,TREE_NAME,TREE_SIZE,BEHALF

The Open Working Directory Primitive resets the user's current working directory to the directory specified by 'TREE_NAME'. Execute access is required on the directory so specified.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	30 = Open Working Directory code number
X1	TRAP	Trap location
X4	TREE_NAME	Location of tree name of working directory
X5	TREE_SIZE	Number of words in tree name
X6	BEHALF	1 = Originator's behalf (normal setting) 0 = Owner's behalf

STATUS RETURN WORDS

Word 1

0 - 17

Not used

18 - 35

Logical status code (See Appendix A-2)

Word 2

Originator (For use by special SPAWN)

REMARKS

There are certain instances when it becomes necessary to reference, by tree name, several items which are catalogued in the same directory (e.g., when a user is initially opening several files at sign on). The open working directory primitive enables the user to expedite the tree search for such items by allowing the search to start at the level of the working directory specified instead of at the root level.

When a process is initially spawned by the LISTENER, its current working directory is set to the user's main directory (i.e. the directory at the level immediately below the root directory). The initial current working directory, so specified, must be referenced using the originator's behalf. The current working directory can be changed by re-issuing the open working directory primitive with the tree name for the new directory. If a father spawns a son, the son's current working directory is initialized to the fathers.

The special symbol '*' is used as the first component of a tree name

Open Working Directory Primitive (Continued)

REMARKS (Cont'd)

to indicate that referencing is to start from the directory specified by the current working directory. The '*' may be followed by a tree name of any level, up to the maximum allowable by the system.

Note that the '*' convention to be described can be used in specifying the tree name for any file or event primitive which utilizes the tree name parameter.

The following examples should clarify the use of the '*' convention and the current working directory.

Example 1

Assume the open working directory primitive is issued with the tree name as specified below:

ACI 6,GJF	Name of user's main directory
ACI 6,FORTRAN-PROGRAMS	Name of directory in main directory

The current working directory for this tree name is the directory GJF/FORTRAN-PROGRAMS.

File FORTA in the directory FORTRAN- PROGRAMS can be referenced by the following tree name using the '*' convention to denote use of the current working directory.

ACI 6,*	Indicator that initial components of tree name are specified by tree name of current working directory
ACI 6,FORTA	Name of file in current working directory.

In example 1 the tree name *,FORTA is equivalent to the tree name GJF FORTRAN-PROGRAMS/FORTA.

Example 2

Consider the working directory whose tree name is GJF/DIRECTORYA/DIRECTORYB.

Then the tree name * DIRECTORYC/DIRECTORYD/FILEA is equivalent to the tree name:

GJF/DIRECTORYA/DIRECTORYB/DIRECTORYC/DIRECTORYD/FILEA

Open Working Directory Primitive (Continued)

REMARKS (Cont'd)

This second example illustrates that the current working directory can both reference and be followed by multiple level tree names.

Access when using the '*' convention

The use of the '*' convention does not alter the access which is allowed the user. That is, the access associated with the current working directory is the same as the access that would be granted if the directory was referenced from the root level.

Read Branch Primitive

Code = 31

DBRN TRAP, SYSID, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF

The Read Branch Primitive reads the contents of the branch specified by the tree name, into a 10 word block in the slave program which starts at 'BUFLOC'. If a system identifier is specified, system information (previously entered by the write system information primitive) will also be supplied. The format for the returned branch information is specified below.

Read access is required for the directory specified.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	31 = Read Branch Primitive code number
X1	TRAP	Trap location
X2	SYSID	Specifies either the System Identifier Number or a Block Number. The Block Number denotes the block of system information to be read. System Identifier Number Code 0 - System information not requested 1 - TSS 2 - MARK2 3 - GECOS Block Number Code -J Specifies that the Jth block of system information is to be read (J=1, 2, 3 at present). Note the minus preceding J.
X3	BUFLOC	Starting location of 10 word buffer in a slave program where branch information is returned.
X4	TREE_NAME	Location of the tree name of the directory
X5	TREE_SIZE	Number of words in treename. If X5=0, X4 is assumed to contain the FRN of the item being accessed.
X6	BEHALF	1 = Originator's behalf (normal setting) 0 = Owner's behalf

STATUS RETURN WORDS

Word 1

0 - 17

Not specified

18 - 35

Logical status code (See Appendix A-2)

Read Branch Primitive (Continued)

Format for Returned Branch Information

Word 1 Upper

Bits 0-1	Entry type 00 - File 01 - Directory 10 - Event 11 - Link
Bit 3	Owner's Dir. Priv. Switch (file)
Bits 12-17	Owner's Access (all except link)
Bits 14-17	Number of names in tree name (link)

Word 1 Lower

Bit 18	Password Bit (=1 if item has password)
Bit 19	Active Bit (file or event) =1 if item is open
Bit 20	Write inhibit (file)
Bit 21	Access control list bit (not link) =1 if item has ACL
Bit 22	System information bit (file) =1 if file has system information in branch (See Remark 3)
Bits 31-35	Requestor's access (all except link)

Word 2 Upper

Bits 0-11	Day item last used (file or event)
Bits 12-16	Universal access (all except link)

Word 2 Lower

Not used

Word 3 Upper

File length (file)
or
Current count (event)
or
Number of blocks used (root directory entry)

Word 3 Lower

Maximum file length in units (file)
or
Maximum queue length (event)
or
Maximum number of blocks allowed (root dir. entry)

Read Branch Primitive (Continued)

Format for Returned Branch Information (cont'd)

Word 4 Upper	Units allocated (file) or Pass event indicator (event)
Bit 6	Pass event indicator (event)
Bit 7	Unlock event indicator (event)
Bit 8	Mode (event) 0=steady state 1=transient
Bit 9	Infinity bit (event)
Bit 10	Time event indicator (event)
Word 4 Lower	System ID (file) =0 if no system information returned = ID of system for returned information or Time limit (event)
Words 5 and 6	Password (returned only for priv. ID).
Words 7-10	System information

NOTE: The first 6 words of information have the same format as the information returned by the Read Directory and Read Link Primitives. The last 4 words are different for each primitive.

REMARKS

- 1) System information can only be obtained from the Read Branch Primitive. (It is not returned by the Read Directory Primitive).
- 2) The Read Branch Primitive can never return information about a link.
- 3) In general there may be several blocks of system information associated with a given branch, there being one block for each system which has written such information. The System Information Bit (Bit 22 in the lower half of Word 1) provides the following information about these blocks.

The System Information Bit is set ON if:

- a) The block of system information requested is not the last block of system information in the branch

Read Branch Primitive (Continued)

REMARKS (Cont'd)

- b) System information is present in the branch but none was returned (i.e., either 'SYSID' =0 was specified or the ID (or block) was invalid).

The system information Bit is set OFF if

- a) The block of system information requested is the last block in the branch or
- b) There is no system information at all in the branch

Note the above conditions hold whether 'SYSID' is an ID or a block number.

- 4) If system information is returned, the SYSID is given in Word 4 Lower of the information block.

RDLNK TRAP,BUFOC,TREE_NAME,TREE_SIZE,BEHALF,BUFSIZE

The Link name is returned in the user specified buffer area. If the buffer size specified is too small, then an error status is returned. A buffer size of 96₁₀ (6 X 16) is the maximum size possible and therefore, will guarantee a correct buffer size for any link name.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	32 = Read Link Primitive code number
X1	TRAP	Trap location
X3	BUFOC	Starting location of buffer area
X4	TREE_NAME	Location of tree name
X5	TREE_SIZE	Number of words in tree name
X6	BEHALF	1=originator's behalf (normal setting) 0=owner's behalf
X7	BUFSIZE	Size of buffer area must be a multiple of six words.

STATUS RETURN WORDS

Word 1

0 -17	Not specified
18 -35	Logical status code (See Appendix A-2)

FORMAT FOR RETURNED BUFFER INFORMATION

Words 1 to 6	Same as read directory
Words 7 to 12	First level link name
Words 13 to 18	Second level link name
Words 6N+1 to 6N+6	N'th level link name

WSINF TRAP, SYSID, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, DELETE

The Write System Information Primitive allows various subsystems (TSS, BASIC, GECOS, etc.) to append additional system information to the file branch specified by 'TREE_NAME'. The subsystem specifies the information to be added in a four word information buffer located at 'BUFLOC'. If this is the first time information is to be written for the particular subsystem, a new information block is added. Otherwise, the existing information block is modified. An existing information block is deleted if 'DELETE' is set ON.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	33 = Write System Information Primitive code number
X1	TRAP	Trap location
X2	SYSID	Sub-system identifier number 1 - TSS 2 - MARK2 3 - GECOS
X3	BUFLOC	Location of the four words of sub-system information to be written
X4	TREE_NAME	Location of the tree name of the file to whose branch the additional sub-system information is to be added.
X5	TREE_SIZE	Number of words in tree name (a multiple of six). If X5=0, X4 is assumed to contain the FRN of the item being accessed.
X6	BEHALF	1 = Originator's behalf (normal setting) 0 = Owner's behalf
X7	DELETE	Delete switch =0 Add or modify block =1 Delete block

STATUS RETURN WORDS

Word 1

0 - 17	Not specified
18 - 35	Logical status code (See Appendix A-2)

CATLK TRAP, LINK_NAME, LINK_SIZE, TREE_NAME, TREE_SIZE, BEHALF

The CATLK primitive catalogs a link with a name of TREE_NAME. The link points to the item specified in the tree name LINK_NAME. All passwords associated with the item must be included in the LINK_NAME. The TREE_NAME is the name of the link, and the LINK_NAME is the name of the actual file pointed at by the link.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	34 = Catalog Link Primitive code number
X1	TRAP	Trap location
X2	L-NAME	Location of link name
X3	L-SIZE	Number of words in link name, must be a multiple of six
X4	TREE_NAME	Location of tree name
X5	TREE_SIZE	Number of words in tree name, must be a multiple of six
X6	BEHALF	1=originator's behalf (normal setting) 0=owner's behalf

STATUS RETURN WORDS

Word 1

0 -17	Not specified
18-35	Logical status code (See Appendix A-2)

REMARK

Example - if user X attempts to link to user Y's file, then on a 2 level basis,

TREENAME = X/LINK

LINKNAME = Y/FILE

WTBRN TRAP,BUFLOC,TREE_NAME,TREE_SIZE,BEHALF

The WTBRN primitive is used to alter the information or name of a file, event or directory. It allows slave processes to change certain information in the branch (that is, directory, file, event and link blocks).

F=File Block; E=Event Block
D=Directory Block; L=Link Block

The information that may be changed is:

- 1) password and name for F,E,D and L (the password is changed only if the password bit is on)
- 2) owners and universal access in F,E, and D
- 3) maximum length in F and E
- 4) owners access directory privilege switch in F
- 5) time limit in E

In the case of events, the following checks are made:

- 1) Time limit must be less than 1800 sec. and greater than zero. Otherwise, an error is returned.
- 2) Maximum Q length must be less than 15 and greater than zero. Otherwise, an error is returned.

In the case of files, the OADPS is changed in the AFT entry if the file is open. Also, the maximum length of the file is not changed if the write inhibit bit is on.

In the case of root directories, the maximum number of blocks allowed is changed if the user is a privileged user.

The buffer should be set up as a 10 word buffer with the format of the first six words of the RBRN buffer. The last 4 words contain the new name of the entry. If no change in the name is desired, the original name must be inserted.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	35 = Write Branch Primitive code number
X1	TRAP	Trap location
X3	BUFLOC	Location of 10-word buffer
X4	TREE_NAME	Location of tree name
X5	TREE_SIZE	Number of words in tree name
X6	BEHALF	1=originator's ID (normal setting) 0=owner's ID

Write Branch Primitive (Continued)

STATUS RETURN WORDS

Word 1

0 -17

Undefined

18-35

Logical status code (See Appendix A-2)

LOCK TRAP, RN

The Lock Primitive enables a user to lock a file or event which he has opened. This prevents other users from operating on it until he has unlocked it. If the user has lock access, the executive inhibits interrupts while determining whether the item referenced by 'RN' (Reference Number) is currently locked. If the item is locked, interrupts are restored and a busy signal returned in the status word.

Otherwise, the item is locked on behalf of the user, interrupts are restored, and a successful return is made.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	36 = Lock Code Number
X1	TRAP	Trap address
X2	RN	Reference number of file or event to be locked

STATUS RETURN WORDS

Word 1

0 - 17

Undefined

18 - 35

Logical Status Code (See Appendix A-2)

Unlock Primitive

Code = 37

UNLCK TRAP, RN

The Unlock Primitive enables the user to unlock a file or event, specified by 'RN' (Reference Number", that he had previously locked. A successful return is also given if the item referenced was not locked to begin with.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	37 = Unlock code number
X1	TRAP	Trap address
X2	RN	Reference number of file or event to be locked

STATUS RETURN WORDS

Word 1

0 - 17	Undefined
18 - 35	Logical Status Code (See Appendix A-2)

NOTIF TRAP,ERN,CTRAP,STATE

The process which issues the notify primitive is trapped at location 'TRAPC' when the event specified by reference number 'RN' is caused with the given 'STATE'. If the process which causes event 'RN' has specified a message, this message is returned in Status Word 2 of TRAPC.

The location 'TRAP' is associated with the issuance of the notify. Control is returned to 'TRAP' after the notify has been completed with the acceptance (or rejection) indicated in the Status Word 1.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	38 = Notify primitive code number
X1	TRAP	Trap location associated with the issuance of the notify. This trap is similar to the trap associated with the issuance of any other primitive. When the operation of issuing the notify is completed, control returns to 'TRAP' with the success indicated in the corresponding status words.
X2	ERN	Event Reference Number of the event, whose causing will result in the process being notified.
X3	TRAPC	Trap location associated with the causing of event 'RN'. Control is returned to 'TRAPC' after some process issues a cause to event 'RN' with a state matching that specified by 'STATE'.
A	STATE	Value which must be specified by the CAUSE for the notify to occur. If the state is all ones (i.e. 7777777777 ₈), the process will be notified regardless of the state specified in the cause. Note, state can only be defined for transient mode events. For steady state events, state must be set to zero. If not, the primitive will be rejected.

STATUS RETURN WORDS

TRAP

Word 1

0 - 17

Not specified

18 - 35

Logical Status Code (See Appendix A-2)

Notify Primitive (Continued)

STATUS RETURN WORDS (Cont'd)

TRAPC

Word 1

0 - 17	FRN of passed item (pass event)
18 - 35	Logical Status Code (See Appendix A-2) Code is zero if trap has returned due to issuing of cause. Error code if trap returns because time limit is exceeded.

Word 2

Non-pass event

0 - 35	Message (optional) returned by process issuing cau
--------	--

Pass Event

0 - 17	Message (optional) returned by process issuing cau
18 - 35	Accounting ID for pass event

WARNING

If a slave process continues processing after issuing a NOTIFY (i.e. it does not issue the PAUSE primitive) then it must be prepared to handle the notify trap whenever it occurs. In particular this means that the trap routine for the NOTIFY and all other trap routines which may occur at the same time as the NOTIFY must be coded to exit with a RET to the trap routine exit location. This is necessary since the order in which the traps return cannot be pre-determined. (The programmer should review the discussion on trap handling in Section II if this is not clear).

Care must also be taken in using the PAUSE primitive when a NOTIFY trap is outstanding. The trouble occurs when the trap for the primitive returns "fast" so that the PAUSE is issued when it is not really required. For example, suppose a primitive is issued followed by a PAUSE, and the executive returns control to the trap routine (i.e. the fast return) rather than to the instruction following the primitive MME. The trap routine, using the standard RET to the trap exit location, causes the PAUSE to be issued, even though the primitive has already been completed. Normally the PAUSE would be ignored by the executive since there would be no additional traps outstanding. However, since there is a NOTIFY trap outstanding, the PAUSE will be accepted and the process will go into a blocked state. The process will remain blocked until the CAUSE for the NOTIFY is issued which is not generally what the programmer wants. To avoid this possibility when a NOTIFY trap is outstanding, DO NOT ISSUE THE PAUSE IF THE TRAP FOR THE PRIMITIVE OPERATION HAS ALREADY TURNED.

Notify Primitive (Continued)

The following code may be employed to determine whether a PAUSE should be issued when a NOTIFY trap is outstanding.

STZ	TRAP+2	Zero trap return location before issuing MM
MME	0	MME initiating the primitive operation
INHIB	SAVE,ON	Inhibit interrupts
SZN	TRAP+2	Test if trap has returned
TNZ	*+4	If so, bypass the PAUSE
LDXO	15,DU	Otherwise issue the PAUSE
MME	0	
INHIB	RESTORE	Restore interrupts
TRA	*-4	Insure that exit is when trap returns

CAUSE TRAP,ERN,STATE,MESSAGE,NUMBER,FRN,ACCESS

This primitive causes a subset of the processes awaiting event 'RN' whose state matches 'STATE' to be trapped. The number of processes to be trapped is specified by 'NUMBER'. (The address at which a process is trapped, and the state to be matched against, is specified by the Notify Primitive.)

'MESSAGE' specifies an optional one word message which the causing process can return to the process being notified.

Upon completion of the operation, the process issuing the cause is trapped at location 'TRAP' with the number of processes notified returned in the upper half of Status Word 1.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	39 = Cause Primitive code number
X1	TRAP	Trap location associated with issuance of the Cause
X2	ERN	Reference number of event being caused
X3	NUMBER	Number of processes (with state matching 'STATE') which are to be notified (i.e., trapped). If 'NUMBER'=0, all processes (with matching state) awaiting notification will be trapped. The current count is incremented by 'NUMBER' when the event is caused and decremented by one for each process which is trapped (for steady state events).
X6	RN	RN of item to pass (for pass mode)
X7	ACCESS (Bits 13-17)	Access on item passed (for pass mode)
A	STATE	The state parameter is defined only for transient mode events. It must be set to zero for steady state events. Only those processes on the event queue whose state matches that specified by 'STATE' will be trapped. If the state word is set to all ones (i.e. 777777777777) the number of processes specified by 'NUMBER' will be trapped regardless of the state in their queue entry.
Q	MESSAGE	An optional one word message that the causing process can send to the process being notified.

Cause Primitive (Continued)

STATUS RETURN WORDS

Word 1

0 - 17

The number of processes trapped as a result of issuing CAUSE. If this field is zero for a CAUSE to a transient mode event, then there is no NOTIFY outstanding for the CAUSE. The CAUSE should be reissued in this case.

18 - 35

Logical Status Code (See Appendix A-2)

Delete Event Primitive

Code = 40

DELET TRAP, ERN, STATE

An entry for this process which has the specified state is deleted from the event queue specified by 'RN' (Reference Number).

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	40 = Delete event code number
X1	TRAP	Trap address
X2	ERN	Reference number of event to be deleted
A	STATE	State (must be zero for steady state event)

STATUS RETURN WORDS

Word 1

0 - 17

=1 if an entry was successfully deleted

18 - 35

Logical status code (See Appendix A-2)

Uncause Event Primitive

Code = 41

UNCAU TRAP, RN, NUMBER

The Uncause Primitive decreases the current count of the event specified by 'RN' (Reference Number) by the amount specified by 'NUMBER'.

The Uncause Primitive is meaningful only for events in the steady state mode since the current count will not be allowed to go negative.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	41 = Uncause event primitive code number
X1	TRAP	Trap address
X2	RN	Event reference number
X3	NUMBER	Number by which current count is to be decreased

STATUS RETURN WORDS

Word 1

0 - 17

Undefined

18 - 35

Logical Status Code (See Appendix A-2)

REMARKS

- 1) See "Summary of Macro Calls for Event Primitives" for a description of CURRENT COUNT.

Open Scratch Event Primitive

Code = 42

OPSCE TRAP,TIMLIM,MODE,MAXLEN

The Open Scratch Event Primitive creates a scratch event, returning the reference number for this event in the upper half of Status Word 1. Most further references to this event will be in terms of the reference number.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	42 = Open Scratch Event Primitive code number
X1	TRAP	Trap address
X3	TIMLIM	Time limit for process awaiting event (secs.) If zero, the maximum limit of 1/2 hour is granted.
X4	MODE	Mode bit and pass event indicator Bit 17 - Mode Bit = 0 Steady State mode = 1 Transient mode Bit 16 - Pass Event Indicator = 0 not an event of type "pass" = 1 scratch event is type "pass" (Only for transient mode events)
X5	MAXLEN	Maximum queue length for event (1 to 16). If set to zero, maximum of 16 is granted.

STATUS RETURN WORDS

Word 1

0 - 17	Event reference number of opened scratch event
18 - 35	Logical status code (See Appendix A-2)

MSTA TRAP,PLOC,LENGTH,BUFLOC

The 'status' primitive transfers the contents of absolute core locations into a slave program buffer area. It also returns a pointer to the MMDDT symbol table in the upper half of the first status word. This primitive can be issued only by privileged processes.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	45 = System Status Measurement Primitive code number
X1	TRAP	Trap address
X2	PLOC	Address of argument list in slave program
X3	LENGTH	# of arguments in list
X4	BUFLOC	Address of buffer where the status is to be put

REMARK

A check is made to see that the address of all the arguments is within the bounds of the BAR.

STATUS RETURN WORDS

Word 1

- 0 -17 Pointer to the MMDDT symbol table
- 18-35 Logical status code (See Appendix A-1)

FORMAT OF PARAMETER LIST

Each argument is 1 word in length

- upper half of each word is the # of words to be read
- lower half of each word is the absolute core location to start reading from

RD ME TRAP,BUFLOC,BSIZE

This primitive is issuable only by certain privileged processes (the Listener, in particular). It is a request by a slave program to have a block of information transferred into the accounting stream. It is assumed the block specified contains one or more complete accounting blocks, each properly identified.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	46 = Measure Read Me Primitive code number
X1	TRAP	Trap Location
X2	BUFLOC	Beginning address of buffer
X3	BUFSZE	Length of buffer (in words)

REMARK

A maximum of 32 words will be transferrable by one primitive call.

STATUS RETURN WORDS

Word 1

0 -17

Undefined

18-35

Logical status code (See Appendix A-1)

Create Segment Primitive

Code = 47

CRSG TRAP,FRN,SEGNUM

The create segment primitive creates a segment from a specified file. It allows creation of a write inhibited segment, from a file, after the process has been spawned. The segment will be write inhibited only if the file was write inhibited.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	47 = Create Segment Primitive code number
X1	TRAP	Trap Location
X2	FRN	File Reference Number
X3	SEGNUM	Segment Number

STATUS RETURN WORDS

Word 1

0 -17	Undefined
18-35	Logical Status Code (See Spawn Primitive)

REMARKS

Access

Seg 0	Execute
Seg 1-3	Execute or Read

Write Me Primitive

Code = 48

WRITEM TRAP,TRCPR,PLOC,LENGTH

The Write Me Primitive allows writing into absolute core location and may start 'sample' tracking of the process.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	48 = Write Me Primitive code number
X1	TRAP	Trap Location
X2	TRCPR	Tracking Parameter 0=to be ignored 1=start tracking process
X4	PLOC	Location of array of arguments
X5	LENGTH	Length of array of arguments (must be less than or equal to 64)

STATUS RETURN WORDS

Word 1

0 -17	Not specified
18-35	Logical status code (See Appendix A-1)

FORMAT OF AN ARGUMENT PAIR:

First Word: DU = Absolute Address
Second Word: New Value of Location

Who Am I Primitive

Code = 49

WAMI TRAP,CORELOC

The Who Am I primitive returns the first level of the current working directory and the Acode in a five word buffer which the user specifies.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	49 = Who Am I primitive code number
X1	TRAP	Trap Location
X4	CORELOC	Location of 5-word buffer

STATUS RETURN WORDS

Word 1

0 -17	Not specified
18-35	Logical status code
	5 - I/O out of bounds

FORMAT OF RETURNED BUFFER INFORMATION

Words 1-4 First level of current working directory name

Word 5 Acode

Request Working Directory Primitive

Code = 51

RQWD TRAP,CORELOC

The Request Working Directory returns up to 6 levels of the current working directory in a 36 word buffer which the user specifies. The Acode is returned in the lower half of status return word 2.

<u>Register</u>	<u>Parameter</u>	<u>Description</u>
X0	(Implicit)	51 = Request Working Directory Primitive code number
X1	TRAP	Trap Location
X4	CORELOC	Location of 36-word buffer

STATUS RETURN WORDS

Word 1

0 -17 Number of levels passed back
18-35 Logical status code
 5 - I/O out of bounds

Word 2

0 -17 Not specified
18-35 Acode

ASCII treenames to be returned in buffer.

APPENDIX A

SUMMARY OF LOGICAL STATUS CODES

LOGICAL STATUS CODE FOR I/O PRIMITIVES

Value		Meaning
Dec	Oct	
0	0	Successful operation
1	1	Invalid FRN
2	2	Invalid access specified
3	3	Operation cannot be done at this time (software)
4	4	Invalid operation for device type
5	5	I/O would be out of bounds
6	6	Amount requested greater than file length
7	7	Element size not a multiple of unit size
8	10	Too many units specified for device
9	11	Invalid mode specified
10	12	Hardware error - operation not complete
11	13	Device unavailable (hardware)
12	14	Parity error in transfer - operation completed
13	15	No freelist available for operation entry
14	16	End-of file encountered
15	17	Subchannel not currently connected
17	21	Element count invalid
19	23	Referencing a locked item
20	24	Drum transfer must start at even core location
21	25	Reading into a protected segment
22	26	No file storage available
23	27	*Terminate-writing* this subchannel
24	30	End of tape
25	31	Subchannel connection not established
27	33	*Quit-Reading* this subchannel
29	35	Timer Run Out on operator's console or ring already present on CLM channel
30	36	A to D conversion stopped, No read
31	37	Line-break has occurred
32	40	TTY parity error (used only for listener)

Note: The primitives, classified as 'I/O', for which the above status code is returned are:

- 04--Sequential Read
- 05--Append to File
- 06--Read Random File
- 07--Write Random File
- 08--Scratch File
- 09--Set Pointer
- 10--Request Status
- 11--Request Date and Time
- 49--Who Am I

LOGICAL STATUS CODE FOR FILE AND EVENT PRIMITIVES

Value		Meaning
Dec	Oct	
0	0	Operation Was Successful
1	1	Illegal File Reference Number
2	2	Access Denied
3	3	Operation Cannot Be Done At This Time
4	4	Branch Improperly Formed
5	5	Name Invalid
6	6	Directory Full
7	7	Unrecoverable Error
8	10	Directory Not Empty
9	11	Time Limit Exceeded
10	12	Invalid Command
11	13	Item Locked
12	14	Illegal Element Size
13	15	Illegal Device ID
14	16	Illegal Password
15	17	Buffer too Small
16	20	Access denied to Open on Behalf of Owner
17	21	Too many File Operations Outstanding
18	22	#AIT free list entries under threshold
19	23	KIT Free list is empty
20	24	No execute access on Directory Above Item Being Accessed
21	25	Device not allocated (for deallocate event only)

Note: The Primitives, classified as 'File and Event', for which the above status code is returned are:

- 20--Open
- 21--Close
- 22--Catalog
- 23--Destroy
- 24--Open Scratch
- 25--Update
- 26--Catalog Directory
- 27--Write Access Control List
- 28--Read Access Control List
- 29--Read Directory
- 30--Open Working Directory
- 31--Read Branch
- 32--Read Link
- 33--Write System Information
- 34--Catalog Link
- 35--Write Branch
- 36--Lock
- 37--Unlock
- 38--Notify
- 39--Cause
- 40--Delete Entry
- 41--Uncause
- 42--Open Scratch Event

SUMMARY OF STATUS RETURNS FOR CONTROL PRIMITIVES

Set Up Fault Vector, Set Up Squeeze Mode, Enter Squeeze Mode

<u>Dec</u>	<u>Oct</u>	<u>Meaning</u>
0	0	Successful Return
1	1	Out of Bounds
2	2	Upper Portion of Slave Fault Vector out of Bounds
4	4	Squeeze Mode Not Set Up

Spawn and Create Segment

<u>Dec</u>	<u>Oct</u>	
4	4	Segment zero must be specified
5	5	Illegal segment specification
6	6	Invalid access on segment specification
7	7	Illegal File reference number
8	10	Only a file or event may be passed
9	11	Resources not available
12	14	Argument list must be at least 14 words long

Segment Primitives

Open, Close, Change Length, Exchange

<u>Dec</u>	<u>Oct</u>	<u>Meaning</u>
1	1	Segment number out of range
2	2	Segment not known
4	4	I/O activity in progress
5	5	Segment is write inhibit
6	6	Requested segment length equals zero
7	7	Segment length out of range
8	10	Segment already opened

APPENDIX B

SUMMARY OF THE MODE PARAMETERS FOR DIFFERENT DEVICE TYPES

SUMMARY OF THE MODE PARAMETERS FOR DIFFERENT DEVICE TYPES

Disc, Drum

Modes issued thru READ
5 - Rewind

Operator's Console

Modes issued through APEND
0 - Status
1 - Attention
2 - Apend
3 - Standby

Modes issued through READ
0 - Status
1 - Attention
2 - Read
3 - Standby

Card Reader

0 - Status
1 - Attention
2 - Read Card Binary
3 - Read Card Hollerith
4 - Read Card Mixed

Card Punch

0 - Status
1 - Attention
2 - Write Card Binary
3 - Write Card Hollerith
4 - Write Card Hollerith Edited

Magnetic Tape

Modes issued through READ
0 - Request Status
1 - Read Tape Decimal
2 - Read Tape Binary
3 - Set High Density
4 - Set Low Density
5 - Rewind
6 - Rewind/Unload
7 - Forward Space File
8 - Backspace File
9 - Forward Space Record
10 - Backspace Record

Magnetic Tape (Con't)

11 - Read Decimal Recovery
12 - Read Binary Recovery
13 - Read Tape 9
14 - Read Tape 9, Recovery
15 - Reread Decimal
16 - Reread Binary
17 - Set File Protect

Memory Interface

0 - Status
1 - Attention
2 - Read or Append

Magnetic Tape

Modes issued through APEND

0 - Status
1 - Write Decimal
2 - Write Binary
3 - Erase
4 - Write End-of-File
(normal use)
5 - Write EOF Decimal
6 - Write EOF Binary
7 - Append Decimal Recovery
8 - Append Binary Recovery
9 - Write Tape 9
10 - Write Tape 9, Recovery

Line Printer

0 - Status
1 - Attention
2 - Write edited continuous,
no slew
3 - Edited, no slew
4 - Edited, slew 1 line
5 - Edited, slew 2 lines
6 - Edited, slew top of form
7 - Non-edited, no slew
8 - Non-edited, slew 1 line
9 - Non-edited, slew 2 lines
10 - Non-edited, slew top of
form
11 - Slew 1 line
12 - Slew 2 lines
13 - Slew top of form

Summary of the Mode Parameters for Different Device Types (con't)

Communications Line Multiplexers

Generally the CLM10 Modes are identical to the teletype modes (See Below).

CLM10 Modes

Modes issued thru APPEND or READ

<u>Oct</u>	<u>Dec</u>	
00	00	Status Request
01	01	Initialize CLM10 system
02	02	Not used
03	03	Unmask one SBCH
04	04	Mask one SBCH
05	05	Accept ring
06	06	Initiate ACU

Modes issued only thru *APPEND*

07	07	Write to one SBCH
10	08	Terminate writing to one SBCH

Modes issued only thru *READ*

11	09	Quit reading from one SBCH
12	10	Accept attention read
13-17	11-15	Not presently in use

Read Modes transmitting all LF's

20	16	Issue LF and READ to CR or TRO
21	17	Read to CR, TRO, EOM, or EOT
22	18	Issue LF and READ to TRO with LF
23	19	READ to TRO or EOT
24-27	20-23	Not presently in use

Begin READ modes suppressing CRS not following NON-CRS

30	24	Issue LF and READ to CR
31	25	READ to CR
32	26	Issue LF and READ to TRO with LF
33	27	READ to TRO
34-37	28-31	Not presently in use

SUMMARY OF MODE PARAMETERS (Continued)

TTY

Modes which may be issued through APEND or READ

- 0 - Status request
- 1 - Disable all subchannels (SBCHS) (Init. TTY system)
- 2 - Enable all SBCHS
- 3 - Unmask one SBCH
- 4 - Mask one SBCH
- 5 - Accept ring
- 6 - Initiate ACU

Modes which may be issued only thru APEND

- 7 - Write to one SBCH
- 8 - Terminate writing to one SBCH

All of the modes which follow may be issued only through READ

- 9 - Quit reading from one SBCH
- 10-15- Not presently in use

Read modes transmitting all LF's

- 16 - Issue LF and READ to CR
- 17 - Just READ to CR
- 18 - Issue LF and READ to TRO with LF
- 19 - Just READ to TRO
- 20-23- Not presently in use

Read modes suppressing CR's not following Non-CR's

- 24 - Issue LF and READ to CR
- 25 - Just READ to CR
- 26 - Issue LF and READ to TRO with LF
- 27 - Just READ to TRO
- 28-31- Not presently in use

Note on READ-to-CR: Occurrence of TRO before CR terminates the READ.

APPENDIX C

Primitive Command Summary

SUMMARY OF PRIMITIVE COMMANDS AND THEIR CODE NUMBERS

Primitives Executed Directly by the Master Mode Executive

Primitive Code and Name	Primitive Classification
00--Privileged Command From Slave Exec	CONTROL
01--Set Up Fault Vector	CONTROL
02--Set Up Squeeze Mode	CONTROL
03--Enter Squeeze Mode	CONTROL
04--Read	I/O
05--Append	I/O
06--Random Read	I/O
07--Random Write	I/O
08--Scratch File	I/O
09--Set Pointer	I/O
10--Request Status	I/O
11--Request Date and Time	CONTROL
12--Request Elapsed Run Time	CONTROL
45--System Status Measurement	CONTROL
46--Measure Read Me	CONTROL
48--Write Me	CONTROL
49--Who Am I	CONTROL
51--Request Working Directory	CONTROL

Primitives Executed by the Slave Mode Executive

13--Spawn	CONTROL
14--Terminate	CONTROL
15--Pause	CONTROL
16--Open Segment	CONTROL
17--Close Segment	CONTROL
18--Change Segment Length	CONTROL
19--Exchange Segments	CONTROL
20--Open	FILE AND EVENT
21--Close	FILE AND EVENT
22--Catalog	FILE AND EVENT
23--Destroy	FILE AND EVENT
24--Open Scratch	FILE
25--Update	FILE
26--Catalog Directory	FILE
27--Write Access Control List	FILE
28--Read Access Control List	FILE
29--Read Directory	FILE
30--Open Working Directory	FILE
31--Read Branch	FILE
32--Read Link	FILE
33--Write System Information	FILE
34--Catalog Link	FILE
35--Write Branch	FILE
36--Lock	FILE AND EVENT
37--Unlock	FILE AND EVENT
38--Notify	EVENT
39--Cause	EVENT
40--Delete Entry	EVENT
41--Uncause	EVENT

Primitives Executed by the Slave Mode Executive (con't.)

42--Open Scratch Event
47--Create Segment

EVENT
CONTROL

Summary of Macro Calls for System Primitives

<u>Primitive Code</u>	<u>Macro Call</u>
01	SETFV TRAP, CORELOC
02	SETSQ TRAP, LOCTBL
03	SQUEZ TRAP, REGS, IC
04	READ TRAP, FRN, CORELOC, N, MODE
05	APEND TRAP, FRN, CORELOC, N, MODE
06	RRF TRAP, FRN, FILELOC, CORELOC, N
07	WRF TRAP, FRN, FILELOC, CORELOC, N
08	SCR TRAP, FRN, FILELOC
09	SPTR TRAP, FRN, N
10	RQST TRAP, RN
11	RQDT No Arguments
12	RQERT No Arguments
13	SPAWN TRAP, PLOC, LENGTH
14	TERM No Arguments
15	PAUSE No Arguments
16	OSEG TRAP, SEGNUM, LENGTH
17	CLSEG TRAP, SEGNUM
18	CHSEG TRAP, SEGNUM, LENGTH
19	EXSEG TRAP, SEG1NUM, SEG2NUM
20	OPEN TRAP, TREE_NAME, TREE_SIZE, BEHALF, ELSIZE, ACCESS
21	CLOSE TRAP, RN
22	CATALOG TRAP, RN, TREE_NAME, TREE_SIZE, SWITCH, UACCESS, OACCESS
23	DESTRO TRAP, TREE_NAME, TREE_SIZE, BEHALF
24	OPENS TRAP, DEV ID, MAXLEN, ELSIZE
25	UPDATE TRAP, FRN
26	CATDIR TRAP, TREE_NAME, TREE_SIZE, BEHALF, UACCESS, OACCESS
27	WRACL TRAP, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, BUFSIZE
28	RDACL TRAP, INDEX, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, NUMBER
29	RDDIR TRAP, INDEX, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, BUFSIZE
30	OPENW TRAP, TREE_NAME, TREE_SIZE, BEHALF
31	RDBRN TRAP, SYS ID, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF
32	RDLNK TRAP, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, BUFSIZE
33	WSINF TRAP, SYS ID, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, DELETE
34	CATLK TRAP, LINK_NAME, LINK_SIZE, TREE_NAME, TREE_SIZE, BEHALF
35	WTBRN TRAP, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF
36	LOCK TRAP, RN
37	UNLOCK TRAP, RN
38	NOTIF TRAP, ERN, CT RAP, STATE
39	CAUSE TRAP, ERN, STATE, MESSAGE, NUMBER
40	DELET TRAP, ERN, STATE
41	UNCAU TRAP, ERN, NUMBER
42	OPSCE TRAP, TIMLIM, MODE, MAXLEN
45	MSTA TRAP, PLOC, LENGTH, BUFLOC
46	RDME TRAP, BUFLOC, BUFSIZE
47	CRSG TRAP, FRN, SEGNUM
48	WRITEM TRAP, TRCPR, PLOC, LENGTH
49	WAMI TRAP, CORELOC
51	RQWD TRAP, CORELOC

Summary of Macro Calls for Control Primitives

<u>Code</u>	<u>Macro Call</u>	<u>Description</u>
01	SETFV TRAP,CORELOC	Setup Fault Vector
02	SETSQ TRAP,LOCTBL	Setup Squeeze Mode
03	SQUEZ TRAP,REGS,IC	Enter Squeeze Mode
11	RQDT No Arguments	Request Time and Date
12	RQERT No Arguments	Request Elapsed Run Time
13	SPAWN TRAP,PLOC,LENGTH	Spawn a process
14	TERM No Arguments	Terminate a process
15	PAUSE No Arguments	Block a process
16	OSEG TRAP,SEGNUM,LENGTH	Open a segment
17	CLSEG TRAP,SEGNUM	Close a segment
18	CHSEG TRAP,SEGNUM,LENGTH	Change segment length
19	EXSEG TRAP,SEG1NUM,SEG2NUM	Exchange segment numbers
45	MSTA TRAP,PLOC,LENGTH,BUFLOC	System status measurement
46	RDME TRAP,BUFLOC,BUFSIZE	Measure read me
48	WRITEM TRAP,TRCPR,PLOC,LENGTH	Write me
49	WAMI TRAP,CORELOC	Who am I
51	RQWD TRAP,CORELOC	Request working directory
47	CRSG TRAP,FRN,SEGNUM	Create Segment

Parameter Summary for Control Primitive Macro Calls

CORELOC	Location of slave fault vector
LENGTH	SPAWN - No. of words in spawn parameter list at 'PLOC' OSEG and CHSEG - No. of words in segment
LOCTBL	Location of squeeze mapping table
BLOC	Beginning address of block
PLOC	Location of SPAWN parameter list
REGS	Location of register settings to be used on entry to squeezed prog.
IC	IC and indicators on entry to squeezed program
SEGNUM SEG1NUM SEG2NUM	Segment numbers (0, 1, 2, 3)
TRCPR	Tracking parameters 0 (to be ignored) 1 (to start tracking this process)
TRAP	Location of trap routine

Summary of Macro Calls for I/O Primitives

<u>Code</u>	<u>Macro Call</u>	<u>Description</u>
04	READ TRAP FRN CORELOC,N,MODE	Sequential Read
05	APEND TRAP,FRN,CORELOC,N,MODE	Sequential Write
06	RRF TRAP,FRN,FILELOC,CORELOC,N	Random File Read
07	WRF TRAP,FRN,FILELOC,CORELOC,N	Random File Write
08	SCR TRAP,FRN,FILELOC	Scratch File
09	SPTR TRAP,FRN,N	Set File Pointer
10	RQST TRAP,RN	Request Status

Parameter Summary for I/O Primitive Macro Calls

TRAP	Location of trap routine
FRN	Reference number of a file
RN	Reference number of a file or an event
CORELOC	Starting core location in slave program for data transmission
FILELOC	Starting file location for data transmission (expressed as an element number). The first element is equal to zero.
N	Number of elements to be transmitted.
MODE	Device dependent indicator specifying mode of data transmission.

Summary of Macro Calls for File Primitives

<u>Code</u>	<u>Macro Call</u>
20	OPEN TRAP,TREE_NAME,TREE_SIZE,BEHALF,ELSIZE,ACCESS
21	CLOSE TRAP,RN
22	CATLOG TRAP,RN,TREE_NAME,TREE_SIZE,SWITCH,UACCESS,OACCESS
23	DESTRO TRAP,TREE_NAME,TREE_SIZE,BEHALF
24	OPENS TRAP,DEVID,MAXLEN,ELSIZE
25	UPDATE TRAP,RN
26	CATDIR TRAP,TREE_NAME,TREE_SIZE,BEHALF,UACCESS,OACCESS
27	WRACL TRAP,BUFLOC,TREE_NAME,TREE_SIZE,BEHALF,BUFSIZE
28	RDACL TRAP,INDEX,BUFLOC,TREE_NAME,TREE_SIZE,BEHALF,NUMBER
29	RDDIR TRAP,INDEX,BUFLOC,TREE_NAME,TREE_SIZE,BEHALF,BUFSIZE
30	OPENW TRAP,TREE_NAME,TREE_SIZE,BEHALF

<u>Code</u>	<u>Macro Call</u>
31	RDBRN TRAP, SYSID, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF
32	RDLNK TRAP, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, BUFSIZE
33	WSINF TRAP, SYSID, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF, DELETE
34	CATLK TRAP, LINK_NAME, LINK_SIZE, TREE_NAME, TREE_SIZE, BEHALF
35	WTBRN TRAP, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF
36	LOCK TRAP, RN
37	UNLOCK TRAP, RN

Parameter Summary for File Primitive Macro Calls

Parameter Meaning

ACCESS Requested access
UACCESS Access granted to universe
OACCESS Access granted to owner

0	31	32	33	34	3
Not Presently Used	R E A D	W R I T E	A P E N	E X E C	C

BEHALF Specifies whose ID is used in checking whether access is to be granted
 =1 Originator's ID (normal setting)
 =0 Owner's ID

BUFLOC Location of buffer in slave program for reading and writing branch information

BUFSIZ Number of words in buffer located at 'BUFLOC'

DELETE Specifies action on system information block
 =0 Add or modify block
 =1 Delete block

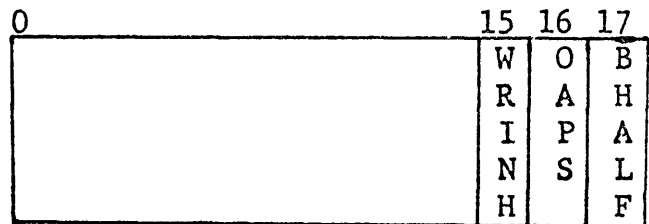
DEVID Specifies device on which scratch file is opened
 =1 Disc
 =2 Drum (only granted to privileged users)

ELSIZE Element size, expressed in bits, in which subsequent data transactions for file are to be expressed. Must be a multiple of unit size.
 =0 Element size set equal to unit size

<u>Device</u>	<u>Unit Size</u>
Disc, Drum	1152 bits (32 words)
Card Reader, Card Punch, Line Printer	6 bits
Teletypewriter	9 bits
Magnetic Tape	36 bits
CLM-10	36 bits
A to D converter	36 bits

Parameter Summary for File Primitive Macro Calls (Continued)

<u>Parameter</u>	<u>Meaning</u>
INDEX	The entry number at which reading begins (=1 for first entry).
LINK_NAME	The tree name which is stored in a branch of type "link".
LINK_SIZE	Number of words in LINK_NAME. Must be multiple of 6.
MAXLEN	Maximum length of scratch file, in elements
NUMBER	Number of ACL entries to be read. Maximum value is 10.
RN	Reference number of file or event
SWITCH	Indicator associated with the cataloging of a file Bit 17 - ON if originator's behalf Bit 16 - OFF if access not granted to owner's files Bit 15 - OFF if file is not write inhibit when spawned



SYSID	Identification code for system information stored in branch =0 System information not requested =1 TSS =2 MARK 2 =3 GECOS =-J (J=1,2,3) specifies Jth block of system information
TRAP	Location of trap routine
TREE_NAME	Location of tree name of entity
TREE_SIZE	Number of words in 'TREE_NAME'. Must be a multiple of 6.

Summary of Macro Calls for Event Primitives

The following calls are applicable for files and events

<u>Code</u>	<u>Macro Call</u>
20	OPEN TRAP, TREE_NAME, TREE_SIZE, BEHALF, ELSIZE, ACCESS
21	CLOSE TRAP, RN

Summary of Macro Calls for Event Primitives Continued

<u>Code</u>	<u>Macro Call</u>
22	CATALOG TRAP,RN,TREE_NAME,TREE_SIZE,SWITCH,UACCESS,OACCESS
23	DESTRO TRAP,TREE_NAME,TREE_SIZE,BEHALF
36	LOCK TRAP,RN
37	UNLCK TRAP,RN

The following calls apply only to events

<u>Code</u>	<u>Macro Call</u>
38	NOTIF TRAP,ERN,CTRAP,STATE
39	CAUSE TRAP,ERN,STATE,MESSAGE,NUMBER
40	DELET TRAP,ERN,STATE
41	UNCAU TRAP,ERN,NUMBER
42	OPSCE TRAP,TIMLIM,MODE,MAXLEN

Parameter Summary for Event Primitive Macro Calls

Parameter Meaning

ACCESS	Requested access	0	33	34	35
UACCESS	Access granted to universe		N	C	L
OACCESS	Access granted to owner		O	A	O
			T	U	C
			I	S	K
			F	E	
BEHALF	Specifies whose ID is used in checking whether access is to be granted =1 Originator's ID (normal setting) =0 Owner's ID				
ELSIZE	Not specified for events				
ERN	Reference number of an event				
MAXLEN	Maximum number of entries which can be placed on the queue for a particular event. May be from 1 to 16.				
MESSAGE	One word message to be transmitted to the notified process				
MODE	Specifies event mode (Bit 17) and event type (Bit 16) Bits 16-17 00 -- Steady state mode, regular event				

Parameter Summary for Event Primitive Macro Calls (Continued)

Parameter Meaning

	10 -- Transient mode, regular event
	11 -- Transient mode, event of type "pass"
	10 -- Error. Pass event must be transient
NUMBER	Number of processes which are to be notified
RN	Reference number of a file or an event
STATE	Only those processes on the event queue whose state matches that specified by 'STATE' will be trapped as a result of a CAUSE. Must be set to zero for steady state events.
SWITCH	Bit 17 Behalf indicator. ON if originator's behalf (normal setting) Bits 15-16 Not specified for events
TIMLIM	Maximum time a process allowed to wait for an event to be caused. Expressed in seconds up to a maximum of 1800.
TRAP	Trap location associated with the issuance of the primitive.
TRAPC	Trap location associated with the causing of event 'ERN'.

REGISTER ASSIGNMENT FOR SYSTEM PRIMITIVES

REGISTER ASSIGNMENTS

PRIMITIVE	A	Q	X0	X1	X2	X3	X4	X5	X6	X7
SET UP FAULT VECTOR			= 1	1	TRAP	CORELOC				
SET UP SQUEEZE MODE			= 2	2	TRAP	LOCAL				
ENTER SQUEEZE MODE	IC		= 3	3	TRAP	RESS				
READ			= 4	4	TRAP	FRN		CORELOC	N	MODE
APPEND			= 5	5	TRAP	FRN		CORELOC	N	MODE
RANDOM READ			= 6	6	TRAP	FRN	FILELOC	CORELOC	N	
RANDOM WRITE			= 7	7	TRAP	FRN	FILELOC	CORELOC	N	
SCRATCH FILE			= 8	10	TRAP	FRN	FILELOC			
SET POINTER			= 9	11	TRAP	FRN	N			
REQUEST STATUS			= 10	12	TRAP	RN				
REQUEST DATE AND TIME			= 11	13						
REQUEST ELAPSED RUN TIME			= 12	14						
SPAWN			= 13	15	TRAP			PLOC	LENGTH	
TERMINATE			= 14	16						
PAUSE			= 15	17						
OPEN SEGMENT			= 16	20	TRAP	SEGNUM	LENGTH			
CLOSE SEGMENT			= 17	21	TRAP	SEGNUM				
CHANGE SEGMENT LENGTH			= 18	22	TRAP	SEGNUM	LENGTH			
EXCHANGE SEGMENT			= 19	23	TRAP	SEGNUM	SEGNUM			
OPEN		ACCESS	= 20	24	TRAP			T-NAME	T-SIZE	BHALF
CLOSE			= 21	25	TRAP	RN				
CATALOG	UACCESS	OACCESS	= 22	26	TRAP	RN		T-NAME	T-SIZE	SWITCH
DESTROY			= 23	27	TRAP			T-NAME	T-SIZE	BHALF
OPEN SCRATCH			= 24	30	TRAP			DEVID	MAXLEN	
UPDATE			= 25	31	TRAP	FRN				
CATALOG DIRECTORY	UACCESS	OACCESS	= 26	32	TRAP			T-NAME	T-SIZE	BHALF
WRITE ACCESS CONTROL LIST			= 27	33	TRAP		BUFLOC	T-NAME	T-SIZE	BHALF
READ ACCESS CONTROL LIST			= 28	34	TRAP	INDEX	BUFLOC	T-NAME	T-SIZE	BHALF
READ DIRECTORY			= 29	35	TRAP	INDEX	BUFLOC	T-NAME	T-SIZE	BHALF
OPEN WORKING DIRECTORY			= 30	36	TRAP			T-NAME	T-SIZE	BHALF
READ BRANCH			= 31	37	TRAP	SYSID	BUFLOC	T-NAME / RN	T-SIZE / 0	BHALF
READ LINK			= 32	40	TRAP		BUFLOC	T-NAME	T-SIZE	BHALF
WRITE SYSTEM INFORMATION			= 33	41	TRAP	SYSID	BUFLOC	T-NAME / FRN	T-SIZE / 0	BHALF
CATALOG LINK			= 34	42	TRAP	L-NAME	L-SIZE	T-NAME	T-SIZE	BHALF
WRITE BRANCH			= 35	43	TRAP		BUFLOC	T-NAME	T-SIZE	BHALF
LOCK			= 36	44	TRAP	RN				
UNLOCK			= 37	45	TRAP	RN				
NOTIFY	STATE		= 38	46	TRAP	RN	TRAPC			
CAUSE	STATE	MESSAGE	= 39	47	TRAP	RN	NUMBER			FRN (PASS)
DELETE ENTRY	STATE		= 40	50	TRAP	RN				ACCESS (PASS)
UNCAUSE			= 41	51	TRAP	RN	NUMBER			
OPEN SCRATCH EVENT			= 42	52	TRAP		TIMELIM	MODE	MAXLEN	
			= 43	53						
			= 44	54						
SYSTEM STATUS MEASUREMENTS			= 45	55	TRAP	PLOC	LENGTH	BUFLOC		
MEASURE READ ME			= 46	56	TRAP	BUFLOC	BUFLOC			
CREATE SEGMENT			= 47	57	TRAP	FRN	SEGNUM			
WRITE ME			= 48	58	TRAP	TRAP		PLOC	LENGTH	
WHO AM I			= 49	59	TRAP			CORELOC		
OUTPUT MEASUREMENTS			= 50	60	TRAP	BUFLOC	LENGTH			
REQUEST WORKING DIR			= 51	60	TRAP			CORELOC		

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APPENDIX D

SUMMARY OF MACRO PROTOTYPES FOR SYSTEM PRIMITIVES

MACROS FOR CONTROL PRIMITIVES (CODE NUMBERS 1 THRU 3)

```

                                SETFV   TRAP,CORELOC

SETFV   MACRO
        REM
        LDX0   1,DU   SETUP FAULT VECTOR CODE NUMBER
        LDX1   #1     TRAP ADDRESS
        LDX2   #2     LOCATION OF SLAVE FAULT VECTOR
        MME    0
        REM
        ENDM
    
```

```

                                SETSQ   TRAP,LOCTBL

SETSQ   MACRO
        REM
        LDX0   2,DU   SETUP SQUEEZE MODE PRIMITIVE CODE NUMBER
        LDX1   #1     TRAP ADDRESS
        LDX2   #2     LOCATION OF SQUEEZE TABLE
        MME    0
        REM
        ENDM
    
```

```

                                SQUEZ   TRAP,REGS,IC

SQUEZ   MACRO
        REM
        LDX0   3,DU   SQUEEZE PRIMITIVE CODE NUMBER
        LDX1   #1     TRAP ADDRESS
        LDX2   #2     LOCATION OF REGISTER SETTINGS
        LDA    #3     INSTRUCTION COUNTER INDICATORS
        MME
        REM
    
```

MACROS FOR I/O PRIMITIVES (CODE NUMBERS 4 THRU 10)

```

                                READ    TRAP,FRN,CORELOC,N,MODE

READ    MACRO
        REM
    
```

MACROS FOR I/O PRIMITIVES (CODE NUMBERS 4 THRU 10) cont'd

		READ	TRAP,FRN,CORELOC,N,MODE
READ	LDX0	4,DU	READ PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	FILE REFERENCE NUMBER
	LDX4	#3	STARTING LOCATION IN SLAVE AREA FOR READ
	LDX5	#4	NUMBER OF ELEMENTS TO BE READ
	LDX6	#5	MODE INDICATOR - DEVICE DEPENDENT SPECIFYING
	MME	0	ISSUE THE READ
	REM		
	ENDM		
		APEND	TRAP,FRN,CORELOC,N,MODE
END	MACRO		
	LDX0	5,DU	APEND PRIMITIVE CODE # (I.E. SEQUENTIAL WRIT)
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	FILE REFERENCE NUMBER
	LDX4	#3	STARTING LOCATION IN SLAVE AREA FOR WRITE
	LDX5	#4	NUMBER OF ELEMENTS TO BE WRITTEN
	LDX6	#5	DEVICE DEPENDENT MODE INDICATOR FOR WRITE
	MME	0	ISSUE THE WRITE
	REM		
	ENDM		
		RRF	TRAP,FRN,FILELOC,CORELOC,N
RRF	MACRO		
	REM		
	LDX0	6,DU	RANDOM READ CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	FILE REFERENCE NUMBER
	LDX3	#3	STARTING LOC. IN FILE FOR READ (ELEMENT #)
	LDX4	#4	STARTING LOC. IN SLAVE AREA FOR READ (WORD)
	LDX5	#5	NUMBER OF ELEMENTS TO BE READ
	MME	0	ISSUE THE RANDOM READ
	REM		
	ENDM		

MACROS FOR I/O PRIMITIVES (CODE NUMBERS 4 THRU 10) cont'd

		WRF	TRAP,FRN,FILELOC,CORELOC,N
WRF	MACRO		
	REM		
	LDX0	7,DU	RANDOM WRITE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	FILE REFERENCE NUMBER
	LDX3	#3	STARTING LOCATION IN FILE FOR WRITE (ELEMENT)
	LDX4	#4	STARTING LOC. IN SLAVE AREA FOR WRITE (WORD)
	LDX5	#5	NUMBER OF ELEMENTS TO BE WRITTEN
	MME	0	ISSUE THE RANDOM WRITE
	REM		
	ENDM		
		SCR	TRAP,FRN,FILELOC
SCR	MACRO		
	REM		
	LDX0	8,DU	SCRATCH PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	FILE REFERENCE NUMBER
	LDX3	#3	ELEMENT IN FILE TO SCRATCH FROM
	MME	0	ISSUE THE SCRATCH REQUEST
	REM		
	ENDM		
		SPTR	TRAP,FRN,N
SPTR	MACRO		
	REM		
	LDX0	9,DU	SET POINTER CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	FILE REFERENCE NUMBER
	LDX3	#3	# OF ELEMENTS READ PTR IS SHIFTED
	MME	0	ISSUE THE SCRATCH COMMAND
	REM		
	ENDM		

MACROS FOR I/O PRIMITIVES (CODE NUMBERS 4 THRU 10) cont'd

		RQST	TRAP,RN
RQST	MACRO		
	REM		
	LDX0	10,DU	REQUEST STATUS CODE NUMBER.
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	FILE OR EVENT REFERENCE NUMBER
	MME	0	
	REM		
	ENDM		

MACROS FOR CONTROL PRIMITIVES (CODE NUMBERS 11 THRU 19)

		RQDT	NO ARGUMENTS
RQDT	MACRO		
	REM		
	LDX0	11,DU	REQUEST DATE AND TIME PRIMITIVE CODE NUMBER
	MME	0	
	REM		
	ENDM		

		RQERT	NO ARGUMENTS
RQERT	MACRO		
	REM		
	LDX0	12,DU	REQUEST ELAPSED RUN TIME CODE NUMBER
	MME	0	
	REM		
	ENDM		

		SPAWN	TRAP,PLOC,LENGTH
SPAWN	MACRO		
	REM		
	LDX0	13,DU	SPAWN PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX4	#2	STARTING ADDRESS OF PARAMETER LIST FOR SPAWN

MACROS FOR CONTROL PRIMITIVES (CODE NUMBERS 11 THRU 19) cont'd

		SPAWN	TRAP,PLOC,LENGTH
SPAWN	LDX5	#3	NUMBER OF WORDS IN ABOVE PARAMETER LIST
	LDQ	#4	ORIGINATOR (FOR SPECIAL SPAWN)
	MME	0	
	REM		
	ENDM		

		TERM	NO ARGUMENTS
TERM	MACRO		
	REM		
	LDX0	14,DU	TERMINATE PRIMITIVE CODE NUMBER
	MME	0	
	REM		
	ENDM		

		PAUSE	NO ARGUMENTS
PAUSE	MACRO		
	REM		
	LDX0	15,DU	PAUSE PRIMITIVE CODE NUMBER
	MME	0	
	REM		
	ENDM		

MACROS FOR CONTROL PRIMITIVES (CODE NUMBERS 11 THRU 19)

		OPSEG	TRAP,SEGNUM,LENGTH
OPSEG	MACRO		
	REM		
	LDX0	16,DU	OPEN SEGMENT PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	SEGMENT NUMBER (SPECIFIED BY USER PROCESS)
	LDX3	#3	LENGTH OF SEGMENT TO BE OPENED
	MME	0	
	REM		
	ENDM		

MACROS FOR CONTROL PRIMITIVES (CODE NUMBERS 11 THRU 19) cont'd

		CLSEG	TRAP,SEGNUM
CLSEG	MACRO		
	REM		
	LDX0	17,DU	CLOSE SEGMENT PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	SEGMENT NUMBER
	MME	0	
	REM		
	ENDM		

		CHSEG	TRAP,SEGNUM,LENGTH
CHSEG	MACRO		
	REM		
	LDX0	18,DU	CHANGE SEGMENT LENGTH PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	SEGMENT NUMBER
	LDX3	#3	DESIRED SEGMENT LENGTH
	MME	0	
	REM		
	ENDM		

		EXSEG	TRAP,SEG1NUM,SEG2NUM
EXSEG	MACRO		
	REM		
	LDX0	19,DU	EXCHANGE SEGMENT PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	SEGMENT 1 NUMBER
	LDX3	#3	SEGMENT 2 NUMBER
	MME	0	
	REM		
	ENDM		

MACROS FOR FILE PRIMITIVES (CODE NUMBERS 20 THRU 37)

		OPEN	TRAP, TNAME,TSIZ ,BHALF ,ELSIZ ,ACCESS
OPEN	MACRO		
	REM		

MACROS FOR FILE PRIMITIVES (CODE NUMBERS 20 THRU 37) cont'd

		OPEN	TRAP,STATUS,TNAME,TSIZ,BHALF,ELSIZ,ACCESS
OPEN	LDX0	20,DU	OPEN PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX4	#2	LOCATION OF TREE NAME
	LDX5	#3	NUMBER OF WORDS IN TREE NAME
	LDX6	#4	BEHALF INDICATOR
	LDX7	#5	ELEMENT SIZE
	LDQ	#6	REQUESTED ACCESS
	MME	0	
	REM		
	ENDM		
		CLOSE	TRAP,RN
CLOSE	MACRO		
	REM		
	LDX0	21,DU	CLOSE PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	FILE OR EVENT REFERENCE NUMBER
	MME	0	
	REM		
	ENDM		
		CATLOG	TRAP,RN,TNAME,TSIZE,SWITCH,UACCESS,OACCESS
CATLOG	MACRO		
	REM		
	LDX0	22,DU	CATLOG PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	FILE OR EVENT REFERENCE NUMBER
	LDX4	#3	LOCATION OF TREE NAME
	LDX5	#4	NUMBER OF WORDS IN TREE NAME
	LDX6	#5	SWITCH INDICATOR
	LDA	#6	UNIVERSAL ACCESS
	LDQ	#7	OWNER'S ACCESS
	MME	0	
	REM		
	ENDM		

MACROS FOR FILE PRIMITIVES (CODE NUMBERS 20 THRU 37)

		DESTRO	TRAP, TREE-NAME, TREE-SIZE, BEHALF
DESTRO	MACRO		
	REM		
	LDX0	23, DU	DESTROY PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX4	#2	TREE NAME LOCATION
	LDX5	#3	NUMBER OF WORDS IN TREE NAME
	LDX6	#4	BEHALF
	MME	0	
	REM		
	ENDM		
		OPENS	TRAP, DEVID, MAXLEN, ELSIZE
OPENS	MACRO		
	REM		
	LDX0	24, DU	# OF THE OPEN SCRATCH COMMAND
	LDX1	#1	TRAP ADDRESS
	LDX4	#2	DEVICE ID
	LDX5	#3	MAXIMUM FILE LENGTH IN ELEMENTS
	LDX7	#4	ELEMENT SIZE IN BITS
	MME	0	
	REM		
	ENDM		
		UPDATE	TRAP, FRN
UPDATE	MACRO		
	REM		
	LDX0	25, DU	# OF UPDATE COMMAND
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	FILE REFERENCE #
	MME	0	
	REM		
	ENDM		
		CATDIR	TRAP, TNAME, TSIZE, BHALF, UACCESS, OACCESS
CATDIR	MACRO		
	REM		
	LDX0	26, DU	CATALOG DIRECTORY PRIMITIVE CODE NUMBER

MACROS FOR FILE PRIMITIVES (CODE NUMBERS 20 THRU 37) cont'd

		CATDIR	TRAP, TNAME, TSIZE, BHALF, UACCESS, OACCESS
CATDIR	LDX1	#1	TRAP ADDRESS
	LDX4	#2	LOCATION OF TREE NAME
	LDX5	#3	NUMBER OF WORDS IN TREE NAME
	LDX6	#4	BEHALF INDICATOR
	LDA	#5	UNIVERSAL ACCESS
	LDQ	#6	OWNER'S ACCESS
	MME	0	
	REM		
	ENDM		
		WRACL	TRAP, BUFLOC, TNAME, TSIZE, BHALF, BUFSIZE
WRACL	MACRO		
	REM		
	LDX0	27, DU	WRITE ACCESS CONTROL LIST CODE NUMBER
	LDX1	#1	TRAP LOCATION
	LDX3	#2	BUFFER LOCATION
	LDX4	#3	LOCATION OF TREE NAME
	LDX5	#4	NUMBER OF WORDS IN TREE NAME
	LDX6	#5	BEHALF INDICATOR
	LDX7	#6	BUFFER SIZE
	MME	0	
	REM		
	ENDM		
		RDACL	TRAP, INDEX, BUFLOC, TNAME, TSIZE, BHALF, NUMBER
RDACL	MACRO		
	REM		
	LDX0	28, DU	READ ACCESS CONTROL LIST CODE NUMBER
	LDX1	#1	TRAP LOCATION
	LDX2	#2	INDEX INTO ACL FOR READ TO BEGIN
	LDX3	#3	BUFFER LOCATION
	LDX4	#4	LOCATION OF TREE NAME
	LDX5	#5	NUMBER OF WORDS IN TREE NAME
	LDX6	#6	BEHALF INDICATOR
	LDX7	#7	NUMBER OF ACL ENTRIES TO BE READ
	MME	0	
	REM		
	ENDM		

MACROS FOR FILE PRIMITIVES (CODE NUMBERS 20 THRU 37) cont'd

		RDDIR	TRAP, INDEX, BUFLOC, TNAME, TSIZE, BEHALF, BUFSIZE
RDDIR	MACRO		
	REM		
	LDX0	29,DU	READ DIRECTORY CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	INDEX IN DIRECTORY OF FIRST BRANCH READ
	LDX3	#3	SLAVE LOC. WHERE BRANCH INFORMATION IS STORED
	LDX4	#4	LOCATION OF TREE NAME
	LDX5	#5	NUMBER OF WORDS IN TREE NAME
	LDX6	#6	BEHALF INDICATOR
	LDX7	#7	SIZE OF BUFFER WHERE BRANCH IS RETURNED
	MME	0	
	REM		
	ENDM		
		OPENW	TRAP, TREE-NAME, TREE-SIZE, BEHALF
OPENW	MACRO		
	REM		
	LDX0	30,DU	OPEN WORKING DIRECTORY CODE NUMBER
	LDX1	#1	TRAP LOCATION
	LDX4	#2	LOCATION OF TREE NAME
	LDX5	#3	NUMBER OF WORDS IN TREE NAME
	LDX6	#4	BEHALF INDICATOR
	MME	0	
	REM		
	ENDM		
		RDBR	TRAP, SYSID, BUFLOC, TNAME, TSIZE, BEHALF
RDBRN	MACRO		
	REM		
	LDXC	31,DU	READ BRANCH PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	SYSTEM ID
	LDX3	#3	STARTING LOCATION OF BUFFER
	LDX4	#4	LOCATION OF TREE NAME
	LDX5	#5	NUMBER OF WORDS IN TREE NAME
	LDX6	#6	BEHALF INDICATOR
	MME	0	
	REM		
	ENDM		

MACROS FOR FILE PRIMITIVES (CODE NUMBERS 20 THRU 37) cont'd

		RDLNK	TRAP, BUFLOC, TREENAME, TREESIZE, BHALF, BUFSIZE
RDLNK	MACRO		
	REM		
	LDX0	32, DU	READ LINK PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX3	#2	STARTING LOCATION OF BUFFER
	LDX4	#3	LOCATION OF TREE NAME
	LDX5	#4	NUMBER OF WORDS IN TREE NAME
	LDX6	#5	BEHALF INDICATOR
	LDX7	#6	SIZE OF BUFFER
	MME	0	
	REM		
	ENDM		
		WSINF	TRAP, SYSID, BUFLOC, TNAME, TSIZ, BHALF, DELETE
WSINF	MACRO		
	REM		
	LDX0	33, DU	WRITE SYSTEM INFORMATION CODE NUMBER
	LDX1	#1	TRAP LOCATION
	LDX2	#2	SYSTEM ID
	LDX3	#3	LOCATION OF FOUR WORD INF BUFFER
	LDX4	#4	LOCATION OF TREE NAME
	LDX5	#5	NUMBER OF WORDS IN TREE NAME
	LDX6	#6	BEHALF INDICATOR
	LDX7	#7	DELETE SWITCH (=1 DELETE)
	MME	0	
	REM		
	ENDM		
		CATLK	TRAP, LNAME, LSIZ, TNAME, TSIZ, BHALF
CATLK	MACRO		
	REM		
	LDX0	34, DU	CATALOG LINK CODE NUMBER
	LDX1	#1	TRAP LOCATION
	LDX2	#2	LOCATION OF NAME TO BE CATALOGUED IN LINK
	LDX3	#3	NUMBER OF WORDS IN ABOVE NAME
	LDX4	#4	LOCATION OF TREE NAME OF DIRECTORY
	LDX5	#5	NUMBER OF WORDS IN TREE NAME FOR -X4-
	LDX6	#6	BEHALF INDICATOR
	MME	0	
	REM		
	ENDM		

MACROS FOR FILE PRIMITIVES (CODE NUMBERS 20 THRU 37) cont'd

```

                                WTBRN   TRAP, BUFLOC, TREE_NAME, TREE_SIZE, BEHALF

WTBRN   MACRO
        REM
        LDX0   35, DU   WRITE BRANCH CODE NUMBER
        LDX1   #1      TRAP LOCATION
        LDX3   #2      STARTING LOCATION OF BUFFER
        LDX4   #3      TREE NAME LOCATION
        LDX5   #4      # WORDS IN TREE NAME
        LDX6   #5      BEHALF INDICATOR
        MME    0
        REM
        ENDM

                                LOCK     TRAP, RN

LOCK    MACRO
        REM
        LDX0   36, DU   LOCK PRIMITIVE CODE NUMBER
        LDX1   #1      TRAP ADDRESS
        LDX2   #2      FILE OR EVENT REFERENCE NUMBER
        MME    0
        REM
        ENDM

                                UNLCK    TRAP, RN

UNLCK   MACRO
        REM
        LDX0   37, DU   UNLOCK PRIMITIVE CODE NUMBER
        LDX1   #1      TRAP ADDRESS
        LDX2   #2      FILE OR EVENT REFERENCE NUMBER
        MME    0
        REM
        ENDM

MACROS FOR EVENT PRIMITIVES (CODE NUMBERS 38 THRU 42)

                                NOTIF    TRAP, ERN, TRAPC, STATE

NOTIF   MACRO
        REM
        LDX0   38, DU   NOTIFY EVENT PRIMITIVE CODE NUMBER
        LDX1   #1      TRAP LOCATION FOR COMPLETION OF NOTIFY
        LDX2   #2      EVENT REFERENCE NUMBER
        LDX3   #3      TRAPC LOC (CNTRL RETURNED WHEN EVNT CAUSED)
        LDA    #4      STATE
        MME    0
        REM
        ENDM
    
```


MACROS FOR EVENT PRIMITIVES (CODE NUMBERS 38 THRU 42) cont'd

		CAUSE	TRAP,ERN,NUMBER,STATE,MESSAGE,RN,ACCESS
CAUSE	MACRO		
	REM		
	LDX0	39,DU	CAUSE PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	EVENT REFERENCE NUMBER
	LDX3	#3	NUMBER OF PROCESSES TO BE TRAPPED
	LDX6	#6	REFERENCE # FOR PASS EVENT
	LDX7	#7	ACCESS FOR PASS EVENT
	LDA	#4	STATE
	LDQ	#5	MESSAGE
	MME	0	
	REM		
	ENDM		
		DELET	TRAP,ERN,STATE
DELET	MACRO		
	REM		
	LDX0	40,DU	DELETE EVENT PRIMITIVE CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	EVENT REFERENCE NUMBER
	LDA	#3	STATE
	MME	0	
	REM		
	ENDM		
		UNCAU	TRAP,ERN,NUMBER
UNCAU	MACRO		
	REM		
	LDX0	41,DU	UNCAUSE EVENT CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	EVENT REFERENCE NUMBER
	LDX3	#3	NUMBER BY WHICH CURRENT COUNT DECREASED
	MME	0	
	REM		
	ENDM		

MACROS FOR CONTROL PRIMITIVES (CODE NUMBERS 45 THRU 51)

		OPSCE	TRAP, TIMLIM, MODE, MAXLEN
OPSCE	MACRO		
	REM		
	LDX0	42, DU	OPEN SCRATCH EVENT CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX3	#2	TIME LIMIT
	LDX4	#3	MODE (STEADY-S-00, TRANS-01, PASS EVNT-11)
	LDX5	#4	MAXIMUM QUEUE LENGTH
	MME	0	
	REM		
	ENDM		
		MSTA	TRAP, PLOC, LENGTH, BUFLOC
MSTA	MACRO		
	REM		
	LDX0	45, DU	SYSTEM STATUS MEASUREMENTS CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	ADDRESS OF ARGUMENT LIST
	LDX3	#3	# ARGUMENTS
	LDX4	#4	BUFFER ADDRESS
	MME	0	
	REM		
	ENDM		
		RDME	TRAP, BUFLOC, BUFSIZE
RDME	MACRO		
	REM		
	LDX0	46, DU	MEASURE READ ME CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	BUFFER LOCATION
	LDX3	#3	BUFFER SIZE
	MME	0	
	REM		
	ENDM		

MACROS FOR CONTROL PRIMITIVES (CODE NUMBERS 45 THRU 51) cont'd

		CRSG	TRAP,FRN,SEGNUM
CRSG	MACRO		
	REM		
	LDX0	47,DU	CREATE SEGMENT CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	FILE REFERENCE #
	LDX3	#3	SEGMENT #
	MME	0	
	REM		
	ENDM		
		WRITEM	TRAP,TRCPR,PLOC,LENGTH
WRITEM	MACRO		
	REM		
	LDX0	#48,DU	WRITE ME CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX2	#2	TRACKING PARAMETER
	LDX4	#3	ADDRESS OF ARGUMENT LIST
	LDX5	#4	# ARGUMENTS
	MME	0	
	REM		
	ENDM		
		WAMI	TRAP,CORELOC
WAMI	MACRO		
	REM		
	LDX0	49,DU	WHO AM I CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX4	#2	CORE LOCATION
	MME	0	
	REM		
	ENDM		
		RQWD	TRAP,CORELOC
RQWD	MACRO		
	REM		
	LDX0	51,DU	REQUEST WORKING DIRECTORY CODE NUMBER
	LDX1	#1	TRAP ADDRESS
	LDX4	#2	CORE LOCATION
	MME	0	
	REM		
	ENDM		

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	7	*		HOUSEKEEPING CARDS		150
	8	*				160
	9	*				170
	10		PCC	ON	PRINT CONTROL CARDS	180
	11		PMC	ON	EXPAND MACROS	190
	12		DETAIL	ON	EXPAND OCT,DEC,BCI,ASCII,DUP,ETC.	200
	13		DETAIL	OFF		210
	14		PCC	OFF		220
000000	20		ORG	0	SET ORIGIN	280
	21		HEAD		AND UNHEADED	290
	22		LBL	IOS,IOS	I-NPUT/O-UTPUT S-CHEDEULER	300
	23	*				310
	24	*				320
	25	*				330
	26	*		SQUASH ASSEMBLER BUG		340
	27	*				350
001000 400013	28	MME	OPD	012/0010,6/,02/2,6/,6/,02/2,02/3		360
	29	*				370
	30	*				380
	31	*				390
	32	*		DEBUGGING AIDS		400
	33	*				410
	34	*		IF THE DEBUG SWITCH IS SET ON, THEN THE DEBUG MACROS		420
	35	*		WILL BE EXPANDED, OTHERWISE NOT.		430
	36	*				440
000000	37	OFF	EQU	0	OFF SWITCH	450
000001	38	ON	EQU	1	ON SWITCH	460
	39	*				470
000001	40	DBG	SET	ON	INITIALLY ON	480
	41	*				490
	42	*				500
	44	*				520
	45	TTL	OPSYN	TTL	FOR SYSPROG PPRNT	530

ASSEMBLY DECK SETUP

47	HEAD	550
48 *		560
49 *		570
50 *	ASSEMBLY DECK SETUP	580
51 *		590
52 *		600
53 *	GMAP ASSEMBLY DECK SETUP -- THE DECK SETUP FOR ASSEMBLING THE	610
54 *	THIS PROGRAM (HEREAFTER IOS) IS AS FOLLOWS. ALL CONTROL	620
55 *	CARDS BEGIN WITH A 'S'. THE FOLLOWING DECK WILL PRODUCE	630
56 *	A NEW K* TAPE, A P* TAPE FOR THE LISTING AND IS ALSO USED	640
57 *	TO PRODUCE A SYMBOL TABLE FOR DDT. THE SECOND ACTIVITY,	650
58 *	SYSPROG PPRNT, CREATES THE SYMBOL TABLE AND STORES THE	660
59 *	RESULTS IN THE FILE 'RUBENS/IOS-SYM-TAB. TO DO AN ASSEMBLY	670
60 *	SIMPLY SUBMIT THE FOLLOWING PROGRAM TO TECOS:	680
61 *	SOURCE RUBENS/<NAME-OF-DECK>;RUN;EXIT	690
62 *	ALTER CARDS SHOULD BE SAVED PREVIOUSLY IN FILE 'RUBENS/ALTERS'	700
63 *		710
64 *		720
65 *\$	SNUMB MBR	730
66 *\$	IDENT RUBENS,MICHAEL	740
67 *\$	GMAP COMDK,DECK	750
68 *\$	TAPE G*,X1D,,TAPE#A,,IOSA	760
69 *\$	TAPE *1,X2R	770
70 *\$	TAPE K*,X3D,,,,IOSB	780
71 *\$	TAPE P*,X4S,,,,IOSPRINT	790
72 *\$	ASCII A*,BCDT,RUBENS/ALTERS	800
73 *\$	SYSPROG PPRNT	810
74 *\$	TAPE IN,X4D,,,,IOSPRINT	820
75 *\$	PRMFL A1,S,R,RUBENS/IOS-SYM-TAB-A	830
76 *\$	COMMENT PLEASE PRINT P* 'IOSPRINT' FOR MBR. THANX.	840
77 *\$	ENDJOB	850
78 *\$EOD		860

BINDER DECK SETUP

80	HEAD	880
81 *		890
82 *		900
83 *	BINDER DECK SETUP	910
84 *		920
85 *		930
86 *	THE BINDER DECK SETUP -- THE DECK SETUP FOR LOADING THE	940
87 *	ASSEMBLED PROGRAM INTO THE R & DC SYSTEM IS AS FOLLOWS. NOTE	950
88 *	THAT THE DECK IS LOADED VIA 'MULTIBINDER' AND NOT THE USUAL	960
89 *	'BINDER'. THIS IS NECESSARY IF BOTH MULTI-SEGMENTS AND USE	970
90 *	COUNTERS ARE TO BE USED.	980
91 *		990
92	DCARD 3.\$	1000
93 \$BUILD	SPAWN\$SYS,RUBENS,MULTIBINDER	1010
94 \$	FNAME */XIOSA	1020
95 \$	OBJECT	1030

LOCATION COUNTERS

	97		HEAD		1050
	98	*			1060
	99	*			1070
	100	*		LOCATION COUNTERS	1080
	101	*			1090
	102	*			1100
	103	*	THE FOLLOWING PREDEFINES THE ORDER IN WHICH THE LOCATION		1110
	104	*	COUNTERS WILL OCCUR, INDEPENDENT OF THE ORDER IN WHICH		1120
	105	*	THEY ARE USED WITHIN THE PROGRAM.		1130
	106	*			1140
000000	107		USE	CODE	MAIN PROGRAM SEGMENT
000000	108	ZCODE	BSS	0	1150
	109	*			1160
	110	*			1170
004620	111		USE	CONST	STORAGE FOR CONSTANTS, TABLES
004620	112	ZCONS	BSS	0	1180
	113	*			1190
	114	*			1200
005140	115		USE	QSTOR	FOR ALL QUEUES
005140	116	ZQSTR	BSS	0	1210
	117	*			1220
	118	*			1230
005300	119		USE	STORE	FOR ALL DYNAMIC STORAGE
005300	120	ZSTOR	BSS	0	1240
	121	*			1250
	122	*			1260
000000	123		USE	CODE	CODE LOCATION COUNTER INITIALLY
	124	*S*	DISK	MMEFFS	1270
					1280
					1290
					1300
					1310
					1320

DEFINITIONS OF EXECUTIVE SYSTEM CONSTANTS

	126		HEAD		110
	127	*			120
	128	*			130
	129	*		EXECUTIVE CONSTANTS	140
	130	*			150
	131	*			160
	132	*	THE DEFINITIONS OF SYSTEM PARAMETERS SUCH AS MME NUMBERS		170
	133	*	ARE INDICATED BY SYMBOLS WHICH START WITH A DECIMAL POINT.		180
	134	*	THEY ARE NOT HEADED SO A TYPICAL REFERENCE IS LDX X0,S.OPEN,DU.		190
	135	*			200
	136	*	DEFINITION OF MME NUMBERS		210
	137	*			220
000000	138	.PRIV	EQU 0	MICRO-CODED PRIVILEGED PRIMITIVE	230
000001	139	.SETFV	EQU 1	SET UP FAULT VECTOR	240
000002	140	.SETSQ	EQU 2	SET UP SQUEEZE MODE	250
000003	141	.SQUEEZ	EQU 3	ENTER SQUEEZE MODE	260
000004	142	.READ	EQU 4	READ	270
000005	143	.APEND	EQU 5	APPEND	280
000006	144	.RRF	EQU 6	READ RANDOM FILE	290
000007	145	.WRF	EQU 7	WRITE RANDOM FILE	300
000010	146	.SCR	EQU 8	SCRATCH	310
000011	147	.SPTR	EQU 9	SET POINTER	320
000012	148	.RQST	EQU 10	REQUEST STATUS	330
000013	149	.RQDT	EQU 11	REQUEST DATE AND TIME	340
000014	150	.RQRT	EQU 12	REQUEST ELAPSED RUN TIME	350
000015	151	.SPAWN	EQU 13	SPAWN	360
000016	152	.TERM	EQU 14	TERMINATE	370
000017	153	.PAUSE	EQU 15	PAUSE	380
000020	154	.OPSEG	EQU 16	OPEN SEGMENT	390
000021	155	.CLSEG	EQU 17	CLOSE SEGMENT	400
000022	156	.CHSEG	EQU 18	CHANGE SEGMENT LENGTH	410
000023	157	.EXSEG	EQU 19	EXCHANGE TWO SEGMENTS	420
000024	158	.OPEN	EQU 20	OPEN	430
000025	159	.CLOSE	EQU 21	CLOSE	440
000026	160	.CATLG	EQU 22	CATALOGUE	450
000027	161	.DESTR	EQU 23	DESTROY	460
000030	162	.OPENS	EQU 24	OPEN SCRATCH	470
000031	163	.UPDAT	EQU 25	UPDATE	480
000032	164	.CATDR	EQU 26	CATALOGUE DIRECTORY	490
000033	165	.WRACL	EQU 27	WRITE ACCESS CONTROL LIST	500
000034	166	.RDAACL	EQU 28	READ ACCESS CONTROL LIST	510
000035	167	.RDDIR	EQU 29	READ DIRECTORY	520
000036	168	.OPENW	EQU 30	OPEN WORKING DIRECTORY	530
000037	169	.RDBRN	EQU 31	READ BRANCH	540
000040	170	.RDLK	EQU 32	READ LINK	550
000041	171	.WSINF	EQU 33	WRITE SYSTEM INFORMATION	560
000042	172	.CATLK	EQU 34	CATALOGUE LINK	570
000043	173	.WBRAN	EQU 35	WRITE BRANCH	580
000044	174	.LOCK	EQU 36	LOCK	590
000045	175	.UNLCK	EQU 37	UNLOCK	600

DEFINITIONS OF EXECUTIVE SYSTEM CONSTANTS

000046	176	.NOTIF EQU	38	NOTIFY	610
000047	177	.CAUSE EQU	39	CAUSE	620
000050	178	.DELET EQU	40	DELETE EVENT	630
000051	179	.UNCAU EQU	41	UNCAUSE EVENT	640
000052	180	.OPSCE EQU	42	OPEN SCRATCH EVENT	650
000055	181	.MSTA EQU	45	SYSTEM STATUS MEASUREMENTS	660
000056	182	.RDME EQU	46	MEASURE READ ME	670
000057	183	.CRSG EQU	47	CREATE SEGMENT	680
000060	184	.WTME EQU	48	WRITE ME	690
000061	185	.WAMI EQU	49	WHO AM I	700
000063	186	.RQWD EQU	51	REQUEST WORKING DIRECTORY	710
	187	*S* DISK	XBITS		720

BIT AND STATUS DEFINITIONS

	189		HEAD	B		110
	190 *					120
	191 *					130
	192 *				BIT DEFINITIONS	140
	193 *					150
	194 *					160
	195 *					170
	196 *				DEFINITIONS FOR REPEAT INSTRUCTIONS	180
	197 *					190
002000	198 RPT	BOOL		002000	COUNT FIELD FOR RPT INSTRUCTIONS	200
001000	199 ABIT	BOOL		001000	INCREMENT FIRST INDEX REGISTER	210
000400	200 BBIT	BOOL		000400	INCREMENT SECOND INDEX REGISTER	220
000200	201 CBIT	BOOL		000200	LOAD C(X) FROM REPEAT INSTRUCTION	230
000100	202 TZE	BOOL		000100	ZERO	240
000040	203 TNZ	BOOL		000040	NON ZERO	250
000020	204 TMI	BOOL		000020	NEGATIVE	260
000010	205 TPL	BOOL		000010	POSITIVE	270
000004	206 TRC	BOOL		000004	CARRY	280
000002	207 TNC	BOOL		000002	NO CARRY	290
000001	208 TOV	BOOL		000001	OVERFLOW	300
	209 *					310
	210 *					320
	211 *				DEFINITIONS FOR INDICATOR REGISTER	330
	212 *					340
400000	213 ZER	BOOL		400000	ZERO INDICATOR BIT	350
200000	214 NEG	BOOL		200000	NEGATIVE	360
100000	215 CAR	BOOL		100000	CARRY	370
040000	216 OVF	BOOL		040000	OVERFLOW	380
020000	217 EOY	BOOL		020000	EXPONENT OVERFLOW	390
010000	218 EUN	BOOL		010000	EXPONENT UNDERFLOW	400
004000	219 OVM	BOOL		004000	OVERFLOW MASK -- ON PREVENTS OVERFLOW FAULTS	420
002000	220 TAL	BOOL		002000	TALLY RUNOUT	430
001000	221 PAR	BOOL		001000	PARITY ERROR	440
000400	222 PAM	BOOL		000400	PARITY ERROR MASK	450
000200	223 MOD	BOOL		000200	MASTER MODE	460
	224 *					470
	225 *					480
	226 *				DEFINITION FOR ACCESS BITS	490
	227 *					500
000020	228 RD	BOOL		20	READ ACCESS	510
000010	229 WT	EQU		RD/2	WRITE	520
000004	230 AP	EQU		WT/2	APPEND	530
000002	231 EX	EQU		AP/2	EXECUTE	540
000001	232 LK	EQU		EX/2	LOCK	550
000037	233 ALL	EQU		RD+WT+AP+EX+LK	ALL	560
000004	234 NO	BOOL		4	NOTIFY	570
000002	235 CA	EQU		NO/2	CAUSE	580
	236 *					

B

BIT AND STATUS DEFINITIONS

	238 *				600
	239 *				610
	240 *			DEFINITION FOR SPAWN BITS	620
	241 *				630
000010	242 CEND	BOOL	10	CORE END SEGMENT	640
000004	243 BSTRD	BOOL	4	PROCESS TO KNOW OF ITSELF--BASTARD BIT	650
400000	244 WP	BOOL	400000	WRITE PROTECTED	660
000000	245 VOID	BOOL	0	VOID SEGMENT	670
000001	246 PS	BOOL	1	SPAWN FROM PARENT'S SEGMENT	680
000002	247 PF	BOOL	2	SPAWN FROM PARENT'S FILE	690
	248 *				700
	249 *				710
	250 *			DEFINITIONS FOR MODE BITS AND PASS INDICATORS	720
	251 *				730
000000	252 SS	BOOL	0	STEADY STATE	740
000001	253 TRANS	BOOL	1	TRANSIENT	750
000000	254 NPASS	BOOL	0	NOT OF TYPE PASS	760
000002	255 PASS	BOOL	2	PASS EVENT	770

B

STATUS DEFINITIONS

	257 *				790
	258 *				800
	259 *			DEFINITIONS FOR EXEC STATUS RETURNS	810
	260 *				820
	261 *			LOGICAL STATUS CODE FOR I/O PRIMITIVES	830
	262 *				840
000000	263 OK	BOOL	00	OK	850
000001	264 IFRN	BOOL	01	INVALID FRN	860
000002	265 IACC	BOOL	02	INVALID ACCESS SPECIFIED	870
000003	266 BZ	BOOL	03	EXECUTIVE TOO BUSY	880
000004	267 IOP	BOOL	04	INVALID OPERATION FOR THIS DEVICE	890
000005	268 IPTR	BOOL	5	COPY POINTER IS OUT OF BOUNDS	900
000006	269 IREQ	BOOL	6	AMOUNT REQUESTED GREATER THAN FILE LENGTH	910
000007	270 IELT	BOOL	07	ELEMENT SIZE IS NOT A MULTIPLE OF UNIT SIZE	920
000011	271 IMOD	BOOL	11	INVALID MODE	930
000012	272 HDWE	BOOL	12	HARDWARE ERROR -- OPERATION NOT COMPLETE	940
000013	273 DBZ	BOOL	13	DEVICE UNAVAILABLE (HARDWARE)	950
000016	274 EOF	BOOL	16	END-OF-FILE ENCOUNTERED	960
000026	275 NSTR	BOOL	26	NO FILE STORAGE AVAILABLE	970
000035	276 TRO	BOOL	35	TIMER RUN OUT ON OPERATOR'S CONSOLE	980
	277 *				990
	278 *				1000
	279 *			LOGICAL STATUS CODE FOR FILE AND EVENT PRIMITIVES	1010
	280 *				1020
000005	281 ITN	BOOL	5	INVALID NAME	1030
000007	282 UERR	BOOL	7	UNRECOVERABLE ERROR	1040
000011	283 TLE	BOOL	11	TIME LIMIT EXCEEDED	1050
000013	284 LOCK	BOOL	13	ITEMED LOCKED	1060
000016	285 IPWD	BOOL	16	INVALID PASSWORD	1070
	286 *				1080
	287 *				1090
	288 *			LOGICAL STATUS CODES FOR CONTROL PRIMITIVES	1100
	289 *				1110
	290 *				1120
000004	291 IO	BOOL	4	I/O ACTIVITY IN PROGRESS	1130
	292 *				1140
	293 *				1150
	294 *			LOGICAL STATUS CODED FOR SPAWN AND CREATE SEGMENT	1160
	295 *				1170
000011	296 RNA	BOOL	11	RESOURCES NOT AVAILABLE (BOO=HISS)	1180
	297 *				1190
	298 *				1200
	299 *			MISCELLANEOUS BITS	1210
	300 *				1220
000077	301 STMK	BOOL	77	STATUS BIT MASK	1230
400000	302 SIGN	BOOL	400000	SIGN BIT	1240
400000	303 TERM	BOOL	400000	TERMINATOR BIT	1250
200000	304 DELIM	EQU	TERM/2	DELIMINATOR	1260
100000	305 DIGIT	EQU	DELIM/2	DIGIT	1270
040000	306 OPR	EQU	DIGIT/2	OPERATOR	1280

B

STATUS DEFINITIONS

	308 *				1300
	309 *				1310
	310 *		DEFINITIONS FOR JSFLAG		1320
	311 *				1330
	312 *		JSFLAGS (UPPER)		1340
	313 *				1350
001700	314 MODMK	BOOL	1700	MEDIA MASK FOR RCW (BITS 26 - 29)	1360
740000	315 JOBMK	BOOL	740000	JOB NUMBER MASK (TOP 4 BITS)	1370
000017	316 BJBMK	BOOL	17	4 BIT MASK	1380
020000	317 HDRMK	BOOL	020000	HEADER MASK (ON = OUTPUT BANNER)	1390
000001	318 BHDR	BOOL	1	HEADER BIT MASK	1400
010000	319 OUTMK	BOOL	010000	OUTPUT TYPE MASK (OFF = 512; ON = 320)	1410
000001	320 BOTMK	BOOL	1	OUTPUT BIT MASK	1420
007777	321 SRTMK	BOOL	007777	START ADDRESS IN ELEMENTS OF JSBIJFSZ	1430
007777	322 BSTMK	BOOL	7777	BIT MASK	1440
	323 *				1450
	324 *				1460
	325 *		COMMANDS RETURNED TO MONITOR		1470
	326 *				1480
002000	327 GET	BOOL	002000	PERIPHERAL GOTTEN (NOT SENT)	1490
004000	328 KILL	BOOL	004000	PERIPHERAL KILLED	1500
006000	329 REL	BOOL	006000	PERIPHERAL RELEASED	1510
010000	330 XXXX	BOOL	010000	NOT USED	1520
012000	331 RSTRT	BOOL	012000	PERIPHERAL RESTARTED	1530
014000	332 DONE	BOOL	014000	JOB SUCCESSFULLY FINISHED	1540
016000	333 RDY	BOOL	016000	READY PERIPHERAL	1550
	334 *				1560
	335 *				1570
	336 *		COMMANDS SENT TO SUB-MODULES		1580
	337 *				1590
000000	338 .ABRT	BOOL	000000	ABORT PERIPHERAL XXXX	1600
002000	339 .GET	BOOL	002000	GET PERIPHERAL XXXX	1610
004000	340 .KILL	BOOL	004000	KILL PERIPHERAL XXXX	1620
006000	341 .REL	BOOL	006000	RELEASE PERIPHERAL XXXX	1630
010000	342 .XXXX	BOOL	010000	UNDEFINED	1640
012000	343 .STRT	BOOL	012000	RESTART PERIPHERAL XXXX	1650
	344 *\$*	DISK	ASCII		1660

B

ASCII CHARACTERS

	346		HEAD	A		110
	347	*				120
	348	*				130
	349	*			ASCII CHARACTERS	140
	350	*				150
000000	351	NUL	BOOL	0	NULL	160
600004	352	EOT	BOOL	4+B\$DELIM+B\$TERM	EOT	170
000007	353	BELL	BOOL	7	BELL	180
000012	354	LF	BOOL	12	LINE FEED	190
000015	355	CR	BOOL	15	CARRIAGE RETURN	200
200040	356	SP	BOOL	40+B\$DELIM	SPACE	210
000040	357	BLANK	BOOL	40	BLANK	220
600043	358	TERM	BOOL	43+B\$DELIM+B\$TERM	ASCII #	230
200044	359	DOL	BOOL	44+B\$DELIM	DOLLAR SIGN	240
600046	360	AMPER	BOOL	46+B\$DELIM+B\$TERM	ASCII AMPERSAND	250
000052	361	AST	BOOL	52	ASTERISK	260
200054	362	COMMA	BOOL	54+B\$DELIM	COMMA	270
000056	363	DP	BOOL	56	DECIMAL POINT	280
240057	364	SLASH	BOOL	57+B\$DELIM+B\$OPR	SLASH	290
100060	365	DO	BOOL	60+B\$DIGIT	DIGIT ZERO	300
000072	366	COL	BOOL	72	COLON	310
200073	367	SCOL	BOOL	73+B\$DELIM	SEMI-COLON	320
000077	368	QM	BOOL	77	QUESTION MARK	330
000101	369	LA	BOOL	101	LETTER A	340
000177	370	DEL	BOOL	177	DELETE	350
	371	*				360
	372	*				370
000177	373	MA\$K	BOOL	177	ASCII CHARACTER MA\$K	380

A		SPECIAL ASCII TABLE				
		375	HEAD	A		400
		376 *				410
		377 *				420
		378 *			SPECIAL ASCII TABLE	430
		379 *				440
		380 *				450
	004620	381	USE	CONST		460
	004620	382 ASCTB	BSS	0		470
004620	000000200040	383	VFD	036/A\$SP		480
004621	000000000041	384	VFD	036/41	1	490
004622	000000000042	385	VFD	036/42	"	500
004623	000000600043	386	VFD	036/A\$TERM	#	510
004624	000000200044	387	VFD	036/A\$DOL	\$	520
004625	000000000045	388	VFD	036/45	%	530
004626	000000600046	389	VFD	036/A\$AMPER	&	540
004627	000000000047	390	VFD	036/47	'	550
004630	000000000050	391	VFD	036/50	(560
004631	000000000051	392	VFD	036/51)	570
004632	000000000052	393	VFD	036/52	*	580
004633	000000400053	394	VFD	036/53+B\$OPR	+	590
004634	000000200054	395	VFD	036/A\$COMMA	,	600
004635	000000400055	396	VFD	036/55+B\$OPR	-	610
004636	000000000056	397	VFD	036/56	.	620
004637	000000240057	398	VFD	036/A\$SLASH	/	630
004640	000000100060	399	VFD	036/60+B\$DIGIT	0	640
004641	000000100061	400	VFD	036/61+B\$DIGIT	1	650
004642	000000100062	401	VFD	036/62+B\$DIGIT	2	660
004643	000000100063	402	VFD	036/63+B\$DIGIT	3	670
004644	000000100064	403	VFD	036/64+B\$DIGIT	4	680
004645	000000100065	404	VFD	036/65+B\$DIGIT	5	690
END OF BINARY CARD IOS00003						
004646	000000100066	405	VFD	036/66+B\$DIGIT	6	700
004647	000000100067	406	VFD	036/67+B\$DIGIT	7	710
004650	000000100070	407	VFD	036/70+B\$DIGIT	8	720
004651	000000100071	408	VFD	036/71+B\$DIGIT	9	730
004652	000000000072	409	VFD	036/72	/	740
004653	000000200073	410	VFD	036/A\$SCOL	:	750
004654	000000400074	411	VFD	036/74+B\$OPR	<	760
004655	000000000075	412	VFD	036/75	=	770
004656	000000400076	413	VFD	036/76+B\$OPR	>	780
004657	000000000077	414	VFD	036/77	?	790
	000040	415 ASCLN	EQU	*-ASCTB	TABLE LENGTH	800
	000000	416	USE	PREVIOUS		810
		417 **	DISK	ACCOUNT		820

A

ACCOUNTING BLOCK DESCRIPTION

	419	HEAD	A		110	
	420	*			120	
	421	*			130	
	422	*	ACB		140	
	423	*			150	
	424	*	THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS		160	
	425	*	IN THE ACCOUNTING CONTROL BLOCK (ACB).		170	
	426	*			180	
000000	427	FAC	EQU	0	FACILITY USED FOR ACCESSED	190
000001	428	TIMES	EQU	FAC+1	TIME OF START	200
000002	429	DATES	EQU	TIMES+1	DATE OF START	210
000003	430	ID	EQU	DATES+1	USER ID	220
000007	431	SHOP	EQU	ID+4	SHOP ORDER	230
000012	432	UNUSED	SET	SHOP+3	NOT USED	240
000013	433	DISK	EQU	UNUSED+1	DISK UNITS	250
000014	434	DRUM	EQU	DISK+1	DRUM UNITS	260
000015	435	OPCON	EQU	DRUM+1	OPERATOR'S CONSOLE	270
000016	436	CP	EQU	OPCON+1	CARD PUNCH	280
000017	437	LP	EQU	CP+1	LINE PRINTER	290
000020	438	CRD	EQU	LP+1	CARD READER	300
000021	439	UNUSED	SET	CRD+1	NOT USED	310
000022	440	CLM	EQU	UNUSED+1	CLM 10	320
000023	441	MT	EQU	CLM+1	MAGNETIC TAPE	330
000024	442	AD	EQU	MT+1	A/D CONVERTER	340
000025	443	UNUSED	SET	AD+1	NOT USED	350
000026	444	UNUSED	SET	UNUSED+1	NOT USED	360
000027	445	MIP	EQU	UNUSED+1	MEMORY INTERFACE PROCESSOR	370
000030	446	UNUSED	SET	MIP+1	NOT USED	380
000031	447	UNUSED	SET	UNUSED+1	NOT USED	390
000032	448	CPU	EQU	UNUSED+1	CPU TIME	400
000033	449	CORE	EQU	CPU+1	CORE PRODUCT	410
000035	450	RL	EQU	CORE+2	RL NUMBER (TTY)	420
000037	451	TIMEO	EQU	RL+2	TIME OFF	430
	452	**	DISK	TCB		440

A

TRAP BLOCK DESCRIPTION

	454		HEAD	T		110
	455	*				120
	456	*				130
	457	*			TCB	140
	458	*				150
	459	*				160
	460	*	THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS			170
	461	*	IN THE TRAP BLOCK (TBLOCK).			180
	462	*				190
000000	463	SRW1	EQU	0	FIRST STATUS RETURN WORD FROM EXEC	200
000001	464	SRW2	EQU	1	SECOND STATUS RETURN WORD	210
000002	465	RET	EQU	2	SAVED IC/IR WHEN EXEC SPRINGS TRAP	220
000003	466	XED	EQU	3	CONTROL IS TRANSFERRED HERE WHEN EXEC	230
	467	*			SPRINGS THE TRAP. IT CONTAINS AN XED	240
	468	*			OF A CHAIN WHICH LINKS THE TRAP TO THE	250
	469	*			MASTER TASK QUEUE.	260
000004	470	TRA	EQU	4	(UPPER) RESTART ADDRESS FOR TASKS ON	270
	471	*			ON A QUEUE (SUCH AS THE Q\$TASK)	280
	472	*			(LOWER) MAY BE USED TO SAVE RETURN	290
	473	*			FROM A REENRANT ROUTINE	300
000005	474	LINK	EQU	5	(UPPER) LINK TO PREVIOUS TCB	310
000006	475	NCB	EQU	6	(UPPER) POINTER TO NCB	320
000006	476	JCB	EQU	NCB	(LOWER) POINTER TO JCB	330
000007	477	SPARE	EQU	7	SPARE	340
000030	478	LEN	EQU	24	LENGTH OF TCB (NICE IF MULTIPLE OF 8)	350
	479	*				360
	480	*				370
000027	481	TEMP1	EQU	LEN-1	TEMPORARY STORAGE AT END OF BLOCK	380
000026	482	TEMP2	EQU	TEMP1-1	MORE TEMPORARY STORAGE	390
000025	483	TEMP3	EQU	TEMP2-1		400
000024	484	TEMP4	EQU	TEMP3-1		410
000023	485	TEMP5	EQU	TEMP4-1		420
000022	486	TEMP6	EQU	TEMP5-1		430
000021	487	TEMP7	EQU	TEMP6-1		440
000020	488	TEMP8	EQU	TEMP7-1		450
	489	*				460
	490	*	NO ONE EXCEPT R\$GETC SHOULD USE TEMP9 - TEM16			470
	491	*				480
000017	492	TEM9	EQU	TEMP8-1		490
000016	493	TEM10	EQU	TEMP9-1		500
000015	494	TEM11	EQU	TEMP10-1		510
000014	495	TEM12	EQU	TEMP11-1		520
000013	496	TEM13	EQU	TEMP12-1		530
000012	497	TEM14	EQU	TEMP13-1		540
000011	498	TEM15	EQU	TEMP14-1		550
000010	499	TEM16	EQU	TEMP15-1		560
	500	*\$*	DISK	JCB		570

T

JOB CONTROL BLOCK DESCRIPTION

	502		HEAD	J		110
	503	*				120
	504	*				130
	505	*		JCB		140
	506	*				150
	507	*				160
	508	*				170
	509	*	THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS			180
	510	*	IN THE JOB CONTROL BLOCK (HEREAFTER CALL JCB).			190
	511	*				200
000000	512	QFRN	EQU	0	(UPPER) FRN OF ASSOCIATED INPUT FILE	210
000001	513	QFLOC	EQU	QFRN+1	R/W PTR POSITION OF INPUT FILE IN *QFRN*	220
000002	514	FRN	EQU	QFLOC+1	FRN OF FILE TO BE PROCESSED	230
000003	515	TYPE	EQU	FRN+1	(UPPER) TYPE	240
000003	516	DISP	EQU	TYPE	(LOWER) DISPOSITION	250
000004	517	ACODE	EQU	DISP+1	ACODE FOR ACCOUNTING	260
000005	518	NCB	EQU	ACODE+1	(UPPER) PTR TO NCB	270
000005	519	TCB	EQU	NCB	(LOWER) PTR TO TCB	280
000006	520	STATI	EQU	NCB+1	INITIATE STATE FOR COMMUNICATIONS	290
000007	521	JOB	EQU	STATI+1	JOB NUMBER	300
000007	522	STATT	EQU	JOB	TERMINATE STATE FOR COMMUNICATIONS	310
000010	523	MESS	EQU	STATT+1	MESSAGE FOR COMMUNICATIONS	320
000011	524	BUF	EQU	MESS+1	WORKING BUFFER	330
000012	525	SIZE	EQU	BUF+1	AMOUNT OF DATA TO BE PROCESSED	340
000013	526	RES	EQU	SIZE+1	START OF RESOURCE REQUIREMENT LIST	350
000026	527	TT	SET	RES+3+1-QFRN+7	ROUND TO MULTIPLE OF 8	360
000020	528	TT	SET	TT/8*8	ROUND	370
000020	529	LEN	EQU	TT	LENGTH OF JCB (MINIMUM LENGTH = 16.)	380
	530	*S*	DISK	NCB		390

J

NOTIFY BLOCK DESCRIPTION

	532	HEAD	C		110
	533	*			120
	534	*			130
	535	*		NCB	140
	536	*			150
	537	*		THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS	160
	538	*		IN THE NOTIFY BLOCK (ALIAS NCB).	170
	539	*			180
	540	*		A NCB IS A TCB WITH EXTRAS.	190
	541	*			200
000000	542	SRW1	EQU	TSSRW1	210
000001	543	SRW2	EQU	TSSRW2	220
000002	544	RET	EQU	T\$RET	230
000003	545	XED	EQU	T\$XED	240
000004	546	TRA	EQU	T\$TRA	250
000005	547	LINK	EQU	T\$LINK	260
000005	548	RLINK	EQU	LINK	(LOWER) RESTART AFTER NOTIFY
000006	549	NCB	EQU	T\$NCB	270
000006	550	JCB	EQU	T\$JCB	280
000007	551	ABBR	EQU	T\$SPARE	ASCII ABBREVIATION OF NCB
000027	552	TEMP1	EQU	T\$TEMP1	290
000026	553	TEMP2	EQU	T\$TEMP2	300
000025	554	TEMP3	EQU	T\$TEMP3	310
000024	555	TEMP4	EQU	T\$TEMP4	320
000023	556	TEMP5	EQU	T\$TEMP5	330
000022	557	TEMP6	EQU	T\$TEMP6	340
000021	558	TEMP7	EQU	T\$TEMP7	350
000020	559	TEMP8	EQU	T\$TEMP8	360
000017	560	TEM9	EQU	T\$TEM9	370
000016	561	TEM10	EQU	T\$TEM10	380
000015	562	TEM11	EQU	T\$TEM11	390
000014	563	TEM12	EQU	T\$TEM12	400
000013	564	TEM13	EQU	T\$TEM13	410
000012	565	TEM14	EQU	T\$TEM14	420
000011	566	TEM15	EQU	T\$TEM15	430
000010	567	TEM16	EQU	T\$TEM16	440
000024	568	ERN	EQU	TEMP4	ERN FOR NOTIFY ***BENE NOTA***
000025	569	STATE	EQU	TEMP3	STATE FOR NOTIFY ***BENE NOTA***
000030	570	QFRN	EQU	T\$LEN	(UPPER) INPUT Q FILE
000031	571	QFLOC	EQU	QFRN+1	R/W PTR FOR "QFRN"
000032	572	BUSY	EQU	QFLOC+1	NO. OF FILES CURRENTLY ACTIVE FROM THIS
	573				INPUT QUEUE FILE
000033	574	RUN	EQU	BUSY+1	PTR TO R\$OMAX FOR THE TYPE REOURCES
	575				NEEDED BY THIS JOB. IF R\$OMAX = 0, THEN WE
	576				SHOULD IGNORE HIM FOR NOW (SAVVY?)
000034	577	QUEUE	EQU	RUN+1	PTR TO WAIT Q-LIST TO PLACE JOB ON
000035	578	RES	EQU	QUEUE+1	RESOURCE LIST (MUST BE LAST)
	579				THIS WAY WE TEST FIRST TO SEE IF WE
	580				HAVE ANY PERIPHERAL AT ALL

C

NOTIFY BLOCK DESCRIPTION

582 *					610
583 *					620
584 *				THIS MACRO IS USED TO GENERATE THE NECESSARY NOTIFY CONTROL	630
585 *				BLOCKS FOR THE COMMUNICATIONS NETWORK.	640
586 *					650
587 *					660
588 *					670
589 NCB	MACRO	LABEL, ABBR, RESTART=ADD, ERN, STATE, R\$OMAX, QLIST, RES, NO, RES, NO, ...			690
590	USE	STORE			700
591	EIGHT				710
592 #1	BSS	0	LABEL		720
593	ZERO	0, B\$TRO	SRW1: SIMULATE A TIMER RUNOUT		730
594	ZERO	0, 0	SRW2		740
595	ZERO	0, 0	RET		750
596	ZERO	0, 0	XED		760
597	ZERO	**0	TRA		770
598	ZERO	0, #3	LINK/ RESTART		780
599	ZERO	**C\$NCB, **C\$JCB	*NCB/ JCB POINT TO THEMSELVES		790
600	UASCI	1, #2	ASCII ABBREVIATION		800
601	DUP	1, 16-4	TEM16 THRU TEMP5		810
602	DEC	0			820
603 FR#1	ARG	#4	ERN		830
604	ZERO	#5, 0	STATE		840
605	DUP	1, 2	TEMP2 & TEMP1		850
606	DEC	0			860
607	ARG	-1	QFRN		870
608	DEC	0	QFLOC		880
609	DEC	0	BUSY		890
610	ARG	#6	RUN PTR TO R\$OMAX OF TYPE NEEDED		900
611	ARG	#7	Q-LIST PTR		910
612	INE	'#8, ...			920
613	ZERO	#8, #9	RESOURCE TYPE/NUMBER NEEDED		930
614	INE	'#10, ...			940
615	ZERO	#10, #11	DITTO		950
616	INE	'#12, ...			960
617	ZERO	#12, #13			970
618	DEC	-1	MARK END OF RESOURCE REQUIREMENT LIST		980
619	USE	PREVIOUS			990
620	ENDM	NCB			990
621 **	DISK	INPUT			1000

C

INPUT DESCRIPTION FORMATS

	623		HEAD			110
	624 *					120
	625 *					130
	626 *		THE INPUT FILES TO BE PROCESSED ARE NAMED IN A FILE			140
	627 *		'PRINT-FILE-QUEUE', OR 'PUNCH-FILE-QUEUE'. BOTH			150
	628 *		ARE CATALOGED IN THE DIRECTORY 'SYSOUT'. EACH FILE			160
	629 *		WHICH REPRESENTS A JOB IS NAMED IN A 64. WORD RECORD.			170
	630 *					180
	631 *		THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS			190
	632 *		IN THE RECORD.			200
	633 *					210
000000	634 CKSM	EQU	0	CHECKSUM OF OTHER 63. WORDS		220
000001	635 TNSZ	EQU	1	(UPPER) NUMBER OF WORDS IN TREE-NAME		230
000001	636 BANR	EQU	1	(LOWER) NON-ZERO MEANS NO BANNER		240
000002	637 TYPE	EQU	2	(UPPER) TYPE OF FILE		250
	638			0 = 512 WORD EDITED BLOCK		260
	639			1 = 320 GECOS FORMAT		270
000002	640 DISP	EQU	TYPE	(LOWER) DISPOSITION OF FILE		280
	641			0 = DESTROY, SCRATCH, & CLOSE		290
	642			1 = SCRATCH, & CLOSE		300
	643			2 = CLOSE		310
000003	644 ACODE	EQU	3	ACODE FOR BILLING		320
000004	645 TN	EQU	4	START OF TREE-NAME		330
	646 *					340
	647 *					350
000001	648 TYPK	EQU	1	TYPE MASK		360
000003	649 DISMK	EQU	3	DISPOSITION MASK		370
	650 *					380
	651 *					390
000100	652 QBFSZ	EQU	64	INPUT BUFFER SIZE		400
004400	653 QELSZ	EQU	36*QBFSZ	ELEMENT SIZE = ONE RECORD		410
	654 **	DISK	OCB			420

QUEUE MANAGEMENT DEFINITIONS

	656		HEAD	Q		110
	657	*				120
	658	*				130
	659	*			QCB	140
	660	*				150
	661	*				160
	662	*	THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS			170
	663	*	IN A QBLOCK GENERATED BY THE QUEUE MACRO.			180
	664	*				190
000000	665	FIRST	EQU	0	POINTER TO FIRST BLOCK OF QUEUE	200
000001	666	LAST	EQU	FIRST+1	POINTER TO LAST BLOCK OF QUEUE	210
000002	667	XADD	EQU	LAST+1	INSTRUCTION PAIR FOR ADDING A BLOCK	220
000004	668	XENQ	EQU	XADD+2	INSTRUCTION PAIR FOR ENQUEUEING	230
000006	669	XDEQ	EQU	XENQ+2	INSTRUCTION PAIR FOR DEQUEUEING	240
000010	670	XINV	EQU	XDEQ+2	INSTRUCTION PAIR FOR INVERTING	250
000012	671	BUSY	EQU	XINV+2	RESPONSIBLE BLOCK IF QUEUE IS BUSY	260
	672				ZERO OTHERWISE	270
000013	673	MAX	EQU	BUSY+1	MAXIMUM NUMBER OF ITEMS ASSOCIATED WITH Q	280
000014	674	AVAIL	EQU	MAX+1	NUMBER OF ITEMS CURRENTLY AVAILABLE	290
000015	675	SPAR1	EQU	AVAIL+1	SPAR1	300
000016	676	SPAR2	EQU	SPAR1+1	SPAR2	310
000017	677	ABBR	EQU	SPAR2+1	ASCII ABBREVIATION OF QUEUE	320
000020	678	LEN	EQU	ABBR+1	LENGTH OF QUEUE (WISE TO KEEP EVEN)	330
	679	*				340
	680	*				350
000004	681	OFFST	EQU	4	OFFSET FOR QUEUE POINTER	360
000003	682	LINK	EQU	OFFST-1	FORWARD LINK POINTER	370
	683	*\$*	DISK	CCB		380

Q

CORE MANAGEMENT DEFINITIONS

	685		HEAD	R		110
	686	*				120
	687	*				130
	688	*			CCB	140
	689	*				150
	690	*				160
	691	*	THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS IN			170
	692	*	A BLOCK ON THE FREE MEMORY LIST.			180
	693	*				190
000000	694	LINKF	EQU	0	POINTER TO SUCCESSOR (UPPER)	200
000000	695	LEN	EQU	LINKF	TOTAL LENGTH (IN WORDS) OF BLOCK (LOWER)	210
000001	696	LINKB	EQU	LINKF+1	POINTER TO PREDECESSOR	220
	697	*S*	DISK	HEAD		230

R

GLOBAL DEFINITIONS

699	HEAD			110
700 *				120
701 *				130
702 *			GLOBALS	140
703 *				150
704 *			HEAD SYMBOL USAGE	160
705 *				170
706	HEAD		GLOBAL AND EXEC CONSTANTS	180
707	HEAD	B	GENERAL PURPOSE BITS	190
708	HEAD	C	COMMUNICATIONS ROUTINES	200
709	HEAD	J	JOB SYMBOLS AND ROUTINES	210
710	HEAD	O	OPERATOR INTERFACE AND LOGGING	220
711	HEAD	Q	QUEUE SYMBOLS AND ROUTINES	230
712	HEAD	R	RESOURCE ALLOCATION	240
713	HEAD	S	STATISTICS COUNTERS	250
714	HEAD	T	TRAP SYMBOLS AND ROUTINES	260
715	HEAD	X	DIAGNOSTIC ROUTINES	270
716	HEAD			280
717 *				290
718 *				300
719 *			INDEX REGISTER DEFINITIONS	310
720 *				320
721 *			THE SYMBOLIC INDEX REGISTERS USED IN THIS PROGRAM ARE	330
722 *			ONE CHARACTER SYMBOLS, DEFINED UNDER EACH HEAD SYMBOL	340
723 *			IN USE IN THE PROGRAM. INDEX REGISTER 0 IS SPECIAL,	350
724 *			SINCE IT IS USED FOR REPEAT INSTRUCTIONS, SO IT IS NOT	360
725 *			SYMBOLIC.	370
726 *				380
727	HEAD		O,C,J,O,Q,R,T,X	390
728 *				400
000001	729 T	EQU	1 TRAP BLOCK POINTER	410
000002	730 X	EQU	2 TEMP	420
000003	731 Y	EQU	3 TEMP	430
000004	732 Z	EQU	4 TEMP	440
000005	733 Q	EQU	5 QUEUES AND GENERAL USE	450
000006	734 J	EQU	6 JOB NUMBER	460
000007	735 L	EQU	7 LINK REGISTER FOR SUBROUTINE CALLS	470
	736 *			480
	737 *			490
	738 *		OTHER GLOBAL SYMBOLS	500
	739 *			510
	740	HEAD		520
777777	741 ERROR	EQU	=1 USED TO GENERATE MEMORY FAULTS	530
525200	742 BUG	BOOL	525200 BUGGING QUANTITY	540
525200	743 BUGBUG	SET	BUG MR. G. M. I. A. BUGGER	550
000777	744 CKMK	BOOL	777 STATUS MASK (9 BITS)	560
000040	745 TALLYB	BOOL	40 TALLYB BIT	570
777700	746 TALMK	BOOL	777700 MASK FOR TALLY COUNT FIELD	580
000100	747 TAL	BOOL	100 TALLY DISPLACEMENT	590
000005	748 RTMAX	EQU	5 RETRY ERROR ONLY 5 TIMES	600

GLOBAL CONSTANTS

		750	HEAD			620
		751 *				630
		752 *				640
		753 *		GLOBALS		650
		754 *				660
		755 *				670
		756 *	DUMPFIL	PARAMETERS		680
		757 *				690
	004660	758	USE	CONST		700
	122125102105	759 DTN	UASCI	6,RUBENS		710
	004666	760	UASCI	6,IOSDUMP		720
END OF BINARY CARD IOS00004						
	000014	761 DTSZ	EQU	*-DTN	TREE-SIZE	730
	044000	762 DESZ	EQU	36*512	ELEMENT SIZE (ONE PAGE OF CORE)	740
	000037	763 DACC	BOOL	B\$ALL		750
	000000	764	USE	PREVIOUS		760
		765 *				770
		766 *				780
		767 *		MEMORY MANAGEMENT PARAMETERS		790
		768 *				800
	000000	769 BUFSEG	EQU	0	SEGMENT WHERE DYNAMIC BUFFER IS LOCATED	810
	001000	770 MQUAN	EQU	512	QUANTUM FOR MEMROY REQUEST	820
	002000	771 1K	EQU	1024	ONE K DECIMAL	830
	004001	772 RQMAX	EQU	2*1K+1	MAX MEMORY REQUEST/SHOT	840
		773 *				850
	005300	774	USE	STORE		860
005300	000400	775 AVAIL	ZERO	ZZ1,0	MEMORY AVAILABLE	870
005301	001000	776 MEMRQ	ZERO	MQUAN,0	MEMORY REQUIREMENT/ NEED ONE UNIT ALWAYS	880
005302	006350	777 MTOPO	ARG	ZTOPO	END OF PROGRAM	890
005303	007000	778 MTOP	ARG	ZTOP	TOP OF MEMORY	900
	000000	779	USE	PREVIOUS		910
		780 *\$*	DISK	FAULT		920

LOW CORE ALLOCATION -- FAULT VECTOR

Address	Usage	Code	Description	Address
000000	782	USE		110
	783	HEAD	X	120
	784 *			130
	785 *			140
	786 *		FAULT VECTOR	150
	787 *			160
	788 *		CONSIDER ALL FAULTS FATAL	170
	789 *			180
000000	790	ORG	0	190
000000	791 FV	BSS	0	200
000000	792	TRA	\$UP	210
000001	793	XED	FAULT	220
000002	794 MEM	ZERO		230
000003	795	XED	FAULT	240
000004	796 MME	ZERO		250
000005	797	XED	FAULT	260
000006	798 FT	ZERO		270
000007	799	XED	FAULT	280
000010	800 TIMER	ZERO		290
000011	801	XED	FAULT	300
000012	802 COMND	ZERO		310
000013	803	XED	FAULT	320
000014	804 DRL	ZERO		330
000015	805	XED	FAULT	340
000016	806 LOCK	ZERO		350
END OF BINARY CARD 10S00005				
000017	807	XED	FAULT	360
000020	808 CONCT	ZERO		370
000021	809	XED	FAULT	380
000022	810 PARTY	ZERO		390
000023	811	XED	FAULT	400
000024	812 OP	ZERO		410
000025	813	XED	FAULT	420
000026	814 ONC	ZERO		430
000027	815	XED	FAULT	440
000030	816 STRT	ZERO		450
000031	817	XED	FAULT	460
000032	818 OFLOW	ZERO		470
000033	819	XED	FAULT	480
000034	820 DIV	ZERO		490
000035	821	XED	FAULT	500
000036	822 XEC	ZERO		510
000037	823	XED	FAULT	520

X

LOW CORE ALLOCATION -- DEBUG STORAGE

		825 *			540
		826 *			550
		827 *		DEBUG STORAGE	560
		828 *			570
		829 *		STORAGE FOR REGISTERS AND IC ON CRASH	580
		830 *			590
	000040	831	EIGHT		600
	000040	832 REGS	BSS 8	STORAGE FOR CRASH REGISTERS	610
000050	000000 000000	833 IC1	ZERO	IC BEFORE FAULT	620
000051	000000 000000	834 IC	ZERO	IC AT FAULT	630
		835 *			640
		836 *		DATE AND TIME OF CRASH	650
		837 *			660
	000052	838 DATE	BSS 1	DATE OF CRASH	670
	000053	839 TIME	BSS 1	TIME OF CRASH	680
000054	001101070701	840 DATEA	DATE	ASSEMBLY DATE	690
END OF BINARY CARD IOS00006					
000055	000000 0000 00	841 SBAR	ARG 0	BAR SETTING WHEN CRASHED	700
		842 *			710
		843 *		PATCH AREA	720
		844 *			730
	000056	845	EVEN		740
	000056	846 PATCH	BSS 64	LEAVE LOTS OF ROOM	750
		847 *			760
		848 *		STORAGE FOR DEBUGGING QUEUE	770
		849 *			780
	000160	851	EIGHT	QUASH STUPID ASSEMBLER BUG.	800
000160	000170 0000 00	852 REG	ARG DBGQ	POINTER TO NEXT ENTRY	810
	000020	853 DBGQN	EQU 16	NUMBER OF ENTRIES	820
	000170	854	EIGHT		830
	000170	855 DBGQ	BSS 8*DBGQN	RESERVE SPACE	840
		856 **	DISK DIAG		850

X

DIAGNOSTICS

Address	Code	Label	Description	Page
000370	858	USE	CODE	110
	859	HEAD	X	120
	860	*		130
	861	*		140
	862	*	DIAGNOSTICS	150
	863	*		160
	864	*	THIS SECTION IS ENTERED FROM THE FAULT VECTOR IN THE	170
	865	*	EVENT OF A PROGRAMMING ERROR. THEN REGISTERS ARE	180
	866	*	PRESERVED. THE ENTIRE PROGRAM IS WRITTEN OUT INTO	190
	867	*	THE FILE RUBENS/LPCPDUMP.	200
	868	*		210
	869	*	CONSIDER ALL FAULTS FATAL.	220
	870	*		230
	871	*	ENTER FROM THE FAULT VECTOR BY XED X\$FAULT	240
	872	*		250
	000370	873	EVEN	260
	000370	874	FAULT	270
000370	000051	5540	00	280
000371	000372	7100	00	290
000372	000040	7530	00	300
000373	000051	2200	00	310
000374	777776	2350	10	320
000375	000050	7550	00	330
000376	000013	2200	03	340
000377	000000	0010	00	350
000400	000052	7570	00	360
	884	*		370
	885	*	OPEN DUMP FILE	380
	886	*		390
	000401	887	FT1	400
000401	000024	2200	03	410
000402	000414	2210	03	420
000403	004660	2240	03	430
000404	000014	2250	03	440
000405	000001	2260	03	450
000406	044000	2270	03	460
END OF BINARY CARD	IOS00007			
000407	000037	2360	07	470
000410	000000	0010	00	480
	894	LDQ	\$DACC,DL	490
	895	MME		500
	896	*		510
	897	*	PAUSE TILL OPENED	520
	898	*		530
000411	000017	2200	03	540
000412	000000	0010	00	550
000413	000411	7100	00	560
	000414			
000417	000414	2220	00	570
000420	000425	6010	00	580
000421	000414	7200	00	590
000422	000003	1000	03	
	900	MME		
	901	TRA	*-2	
	902	TRAP1	BSS 3	
	903	LDX	2,TRAP1	
	904	TNZ	FT2	
	905	LXL	0,TRAP1	
	906	CMPX	0,\$\$BZ,DU	

X			DIAGNOSTICS				
000423	000401	6000 00	907	TZE	FT1	YES, SO RETRY	600
000424	000470	7100 00	908	TRA	TERM	NOPE, WELL THAT'S IT	610
			909 *				620
			910 *	SCRATCH DUMP FILE			630
			911 *				640
		000425	912 FT2	BSS	0		650
000425	000010	2200 03	913	LDX	0,%SCR,DU	MME NUMBER	660
000426	000435	2210 03	914	LDX	1,TRAP2,DU	TRAP ADDRESS	670
000427	000414	2220 00	915	LDX	2,TRAP1	FRN OF FILE	680
000430	000000	2230 03	916	LDX	3,0,DU	SCRATCH IT	690
000431	000000	0010 00	917	MME			700
			918 *				710
			919 *	PAUSE TILL SCRATCHED			720
			920 *				730
000432	000017	2200 03	921	LDX	0,%PAUSE,DU	ALWAYS PAUSE	740
000433	000000	0010 00	922	MME		WAIT	750
000434	000432	7100 00	923	TRA	*-2	FOREVER	760
		000435	924 TRAP2	BSS	3		770
000440	000435	7200 00	925	LXL	0,TRAP2	CHECK EXEC STATUS RETURN	780
END OF BINARY CARD	IOS00008						
000441	000445	6000 00	926	TZE	FT3	OK, CONTINUE	790
000442	000003	1000 03	927	CMPX	0,%SBZ,DU	NO, WELL WAS THE EXEC TOO BUSY?	800
000443	000425	6000 00	928	TZE	FT2	YES, JUST RETRY	810
000444	000470	7100 00	929	TRA	TERM	NOPE, JUST BLEWIT	820
			930 *				830
			931 *	START CORE TO FILE DUMP			840
			932 *				850
		000445	933 FT3	BSS	0		860
000445	000007	2200 03	934	LDX	0,%WRF,DU	WRITE RANDON FILE	870
000446	000461	2210 03	935	LDX	1,TRAP3,DU	TRAP ADDRESS	880
000447	000414	2220 00	936	LDX	2,TRAP1	FRN OF DUMPFIL	890
000450	000000	2230 03	937	LDX	3,0,DU	TO: FILE LOCATION	900
000451	000000	2240 03	938	LDX	4,0,DU	FROM: CORE LOCATION	910
000452	000055	5500 00	939	SBAR	SBAR	GET CORE-SIZE	920
000453	000055	2250 00	940	LDX	5,SBAR	LOAD NUMBER OF ELEMENTS	930
000454	000377	3650 03	941	ANX5	=0377,DU	MASK TO NUMBER OF ELEMENTS	940
000455	000000	0010 00	942	MME		INITIATE THE COPY	950
			943 *				960
			944 *	PAUSE TILL DUMP IS DONE			970
			945 *				980
000456	000017	2200 03	946	LDX	0,%PAUSE,DU	ALWAYS PAUSE	990
000457	000000	0010 00	947	MME		WAIT	1000
000460	000456	7100 00	948	TRA	*-2	FOREVER	1010
		000461	949 TRAP3	BSS	3	COPY TO DUMP FILE	1020
000464	000461	7200 00	950	LXL	0,TRAP3	GET STATUS	1030
000465	000470	6000 00	951	TZE	TERM	NOW IT IS TIME TO TERMINATE	1040
000466	000003	1000 03	952	CMPX	0,%SBZ,DU	WAS THE EXEC TOO BUSY?	1050
000467	000445	6000 00	953	TZE	FT3	YES, SO JUST RETRY	1060
		000470	954 TERM	BSS	0	TIME TO SAY SO LONG	1070
000470	000016	2200 03	955	LDX	0,%TERM,DU	TERMINATE	1080

X

DIAGNOSTICS

END OF BINARY CARD IOS00009
000471 000000 0010 00
000472 000470 7100 00

956 MME
957 TRA *-2
958 ** DISK EXITM

BYE-BYE
TAKE NO CHANCES

1090
1100
1110

X

EXIT MACRO

000473

960	USE	CODE	110
961	HEAD		120
962 *			130
963 *			140
964 *		EXIT	150
965 *			160
966 *	EXIT TERMINATES A THREAD OF CONTROL BY RETURNING TO THE		170
967 *	TASK DISTRIBUTOR.		180
968 *			190
969 EXIT	MACRO	NO ARGUMENTS	200
970	TRA	\$EXIT	210
971	ENDM	EXIT	220
972 **	DISK	BUGM	222

BUGGING MACROS

000473	974	USE	CODE	110
	975	HEAD		120
	976	*		130
	977	*		140
	978	*	BUGGING MACROS PLANT ADDRESSES IN INVALID DATA AREAS SO THAT	150
	979	*	ANY UNAUTHORIZED USE OF SUCH DATA WILL RESULT IN A MEMORY FAULT	160
	980	*	OR EXECUTIVE CALL REJECT.	170
	981	*		180
	982	*		190
	983	*	BUG	200
	984	*		210
	985	*	BUG FILLS BOTH UPPER AND LOWER HALVES OF A STORAGE WORD WITH	220
	986	*	THE BUG PATTERN \$BUGBUG.	230
	987	*		240
	988	BUG	MACRO STORAGE-ADDRESS	250
	989	IFE	\$DBG,\$ON,4	260
	990	BUGBUG SET	BUGBUG+1	270
	991	LDX	0,BUGBUG,DU	280
	992	STX	0,#1	290
	993	SXL	0,#1	300
	994	ENDM	BUG	310
	995	*		320
	996	*		330
	997	*	BUGU	340
	998	*		350
	999	*	BUGU FILLS THE UPPER HALF OF A STORAGE WORD WITH THE BUG	360
	1000	*	PATTERN \$BUGBUG	370
	1001	*		380
	1002	BUGU	MACRO STORAGE-ADDRESS	390
	1003	IFE	\$DBG,\$ON,3	400
	1004	BUGBUG SET	BUGBUG+1	410
	1005	LDX	0,BUGBUG,DU	420
	1006	STX	0,#1	430
	1007	ENDM	BUGU	440
	1008	*		450
	1009	*		460
	1010	*	BUGL	470
	1011	*		480
	1012	*	BUGL FILL THE LOWER HALF OF A STORAGE WORD WITH THE BUG	490
	1013	*	PATTERN \$BUGBUG.	500
	1014	*		510
	1015	BUGL	MACRO STORAGE-ADDRESS	520
	1016	IFE	\$DBG,\$ON,3	530
	1017	BUGBUG SET	BUGBUG+1	540
	1018	LDX	0,BUGBUG,DU	550
	1019	SXL	0,#1	560
	1020	ENDM	BUGL	570

BUGGING MACROS

1022	*			590
1023	*			600
1024	*		BUGXR	610
1025	*			620
1026	*	BUGXR LOADS THE SPECIFIED INDEX REGISTER(S) WITH THE		630
1027	*			640
1028	BUGXR	MACRO INDEX-REGISTER(S)		650
1029		IFE \$DBG,\$ON,4		660
1030	BUGBUG	SET BUGBUG+1		670
1031		IDRP #1		680
1032		LDX #1,BUGBUG,DU		690
1033		IDRP		700
1034		ENDM BUGXR		710
1035	*			720
1036	*			730
1037	*		BUGA	740
1038	*			750
1039	*	BUGA LOADS THE CONTENTS OF THE THE A REGISTER WITH THE BUG		760
1040	*	PATTERN \$BUGBUG.		770
1041	*			780
1042	BUGA	MACRO <NO ARGUMENTS>		790
1043		IFE \$DBG,\$ON,3		800
1044	BUGBUG	SET BUGBUG+1		810
1045		LDA BUGBUG,DU		820
1046		ORA BUGBUG,DL		830
1047		ENDM BUGA		840
1048	*			850
1049	*			860
1050	*		BUGQ	870
1051	*			880
1052	*	BUGQ LOADS THE CONTENTS OF THE Q REGISTER WITH THE BUG		890
1053	*	PATTERN \$BUGBUG.		900
1054	*			910
1055	BUGQ	MACRO <NO ARGUMENTS>		920
1056		IFE \$DBG,\$ON,3		930
1057	BUGBUG	SET BUGBUG+1		940
1058		LDQ BUGBUG,DU		950
1059		ORQ BUGBUG,DL		960
1060		ENDM BUGQ		970
1061	*			980
1062	*			990
1063	*		DECRM MACRO	1000
1064	*			1010
1065	*	DECREMENT A COUNTER		1020
1066	*			1030
1067	DECRM	MACRO COUNTER-ADDRESS		1040
1068		LCQ 1,DL		1050
1069		ASQ #1		1060
1070		ENDM DECRM		1070
1071	**	DISK CKPTM		1080

CHECKPOINT MACRO

			000473	1073	USE	CODE		110
				1074	HEAD	X		120
				1075	*			130
				1076	*			140
				1077	*			150
				1078	*			160
				1079	*			170
				1080	*			180
				1081	*			190
				1082	*			200
				1083	*			210
				1084	*	C(0) =	C(X0) (UPPER)	220
				1085	*		C(X1) (LOWER)	230
				1086	*	C(1) =	C(X2) (UPPER)	240
				1087	*		C(X3) (LOWER)	250
				1088	*	C(2) =	C(X4) (UPPER)	260
				1089	*		C(X5) (LOWER)	270
				1090	*	C(3) =	C(X6) (UPPER)	280
				1091	*		C(X7) (LOWER)	290
				1092	*	C(4) =	C(A)	300
				1093	*	C(5) =	C(Q)	310
				1094	*	C(6) =	C(E) (0-7 BITS)	320
				1095	*	C(7) =	C(TR) (0-23 BITS)	330
				1096	*			340
				1097	*			350
				1098	*			360
				1099	*			370
				1100	CKPT	MACRO	<NO ARGUMENTS>	380
				1101		IFE	\$DBG,\$ON,1	390
				1102		XED	X\$CKPT	400
				1103		ENDM	CKPT	410
				1104	*			420
				1105	*			430
				1106	*			440
				1107	*		CKPT -- SUBROUTINE	450
				1108	*			460
			000474	1109	CKPT	EVEN		470
			000474			BSS	0	ENTRY POINT
				1110		STC1	CKIC	SAVE IC
			000474	1111		TRA	*+1	BREAK XED
			000475	1112		SREG	CKREG	SAVE REGISTERS FOR A RELOAD
			000476	1113		SREG	REG,I	SAVE IN 8-WORD BLOCK
			000477	1114	*			500
				1115	*			510
				1116	*			520
				1117	*			530
			000500	1117		LDX	0,REG	GET CURRENT ADDRESS
			000501	1118		ADLX	0,8,DU	BUMP TO NEXT ENTRY
			000502	1119		CMPL	0,DBGQ+8+DBGQN,DU	OVER THE END?
			000503	1120		TNC	**2	NO, THIS IS VALID
			000504	1121		LDX	0,DBGQ,DU	YES, RESET TO BEGINNING
			000505	1122		STX	0,REG	SAVE FOR NEXT TIME

X

CHECKPOINT MACRO

000506	005310	0730	00	1123	LREG	CKREG	RESTORE REGISTERS	610
000507	005304	6300	00	1124	RET	CKIC	RESTORE IC	620
				1125	*			630
				1126	*			640
	005304			1127	USE	STORE		650
	005304			1128	CKIC	BSS	TEMP STORAGE FOR IC/IR	660
	005310			1129		EIGHT		670
	005310			1130	CKREG	BSS	TEMP STORAGE FOR REGISTERS	680
	000510			1131		USE		690
				1132	***	DISK	SETUPM	700

X

TRAP SETUP MACRO

		000510	1134	USE	CODE		110
			1135	HEAD			120
			1136	*			130
			1137	*			140
			1138	*	SETUP MACRO		150
			1139	*			160
			1140	SETUP	MACRO	NO ARGUMENTS	170
			1141	XED	\$SETUP		180
			1142	ENDM	SETUP		190
			1143	*			200
			1144	*			210
			1145	*	SETUP -- SUBROUTINE TO SET UP A TRAP		220
			1146	*			230
			1147	*	CALL WITH		240
			1148	*	C(XT) = TBLOCK-ADDRESS		250
			1149	*	C(XJ) = JBLOCK ADDRESS		260
			1150	*	C(XO) = TRANSFER ADDRESS FOR J\$TRA		270
			1151	*	ENTER BY		280
			1152	*	XED T\$SETUP		290
			1153	*	DESTROYS C(A), C(Q), C(XO)		300
			1154	*	USES NO TEMPORARIES		310
			1155	*			320
			1156	*			330
		000510	1157	EVEN			340
		000510	1158	SETUP	BSS	0	350
000510	000004	7400	1159	STX	0,T\$TRA,T	SET T\$TRA = RESTART ADDRESS	360
000511	000512	7000	1160	TSX	0,**1	BREAK XED	370
000512	000000	4310	1161	FLD	0,DU	ZERO OUT A AND Q	380
END OF BINARY CARD 10\$00010							
000513	000000	7570	1162	STAG	T\$SRW1,T	ZERO STATUS WORDS	390
000514	000520	2370	1163	LDAQ	TRAP-1	GET ZERO, XED WORDS	400
000515	000002	7570	1164	STAG	T\$XED-1,T	SAVE ZERO, XED	410
000516	000000	7100	1165	TRA	0,0	RETURN	420
			1166	*			430
			1167	*			440
			1168	*	TRAP -- XED SEQUENCE TO PUT BLOCK ON Q\$TASK		450
			1169	*			460
		000520	1170	EVEN			470
000520	000000	000000	1171	ZERO		CAN BE USED FOR CLEARING RET WORDS	480
000521	000522	7170	1172	TRAP	XED	**1 THIS IS EXECUTED FROM THE TBLOCK	490
000522	005161	5540	1173	STC1	Q\$LAST+Q\$TASK*DI	*UPDATE PREVIOUS LAST POINTER	500
000523	000524	7170	1174	XED	**1	CONTINUE WITHOUT AFFECTING IC	510
000524	005161	5540	1175	STC1	Q\$LAST+Q\$TASK	UPDATE POINTER TO LAST	520
000525	777777	6300	1176	RET	-1,IC	RETURN TO POINT OF INTERRUPTION	530
			1177	*\$*	DISK	SYSCALLS	540

SYSTEM CALL MACRO DESCRIPTIONS

000526	1179	USE	CODE	110
	1180	HEAD		120
	1181 *			130
	1182 *			140
	1183 *			150
	1184 *	ALL SYSTEM CALLS ARE DONE THRU PRE-DEFINED MACROS. THOSE		160
	1185 *	MACROS ARE LISTED IN THE FOLLOWING PAGES. EACH OF THESE		170
	1186 *	MACROS IS CODED TO SPECIFIC CONVENTIONS:		180
	1187 *	ENTER BY		190
	1188 *	TSX 0,\$<MACRO-NAME>		200
	1189 *	ENTERED WITH		210
	1190 *	C(XT) = TBLOCK-ADDRESS		220
	1191 *	C(XJ) = JBLOCK-ADDRESS		230
	1192 *	CALLS		240
	1193 *	SETUP		250
	1194 *	ISSUES MME AND THEN 'EXITS'		260
	1195 *	RETURNS TO FIRST LOCATION AFTER MACRO		270
	1196 *	RETURNS WITH		280
	1197 *	C(XT) = TBLOCK-ADDRESS		290
	1198 *	C(XJ) = JBLOCK-ADDRESS		300
	1199 *	C(XL) = RESTART-ADDRESS		310
	1200 *			320
	1201 *	IN ACTUALITY, THE FOLLOWING HAPPENS: THE MACRO DOES		330
	1202 *	DO THE 'TSX0'. HOWEVER, FOLLOWING THAT INSTRUCTION IS		340
	1203 *	THE SET OF ARGUMENTS FOR THE MME. THE SUBROUTINE ENTERED		350
	1204 *	WHICH ALWAYS HAS THE SAME NAME AS THE MACRO KNOWS THE		360
	1205 *	NUMBER OF ARGUMENTS IT IS PASSED. THEREFORE IT SIMPLY CAL-		370
	1206 *	CULATES THE RESTART ADDRESS. IT THEN CALLS 'SETUP' WHICH		380
	1207 *	RESETS THE TRAP BLOCK (C(XT) = X1) WITH THE FIRST THREE WORDS		390
	1208 *	BEING ZEROED AND THE LINK WORD FOR THE EXEC SET TO THE		400
	1209 *	XED INSTRUCTION TO PLACE THE BLOCK ON THE QSTASK QUEUE. HENCE		410
	1210 *	WHEN TRAPPED, THE BLOCK WILL BE LINKED ON THE QSTASK QUEUE		420
	1211 *	AND THE INTERRUPT WILL BE TRANSPARENT TO THE CURRENTLY RUN-		430
	1212 *	NING TASK UNLESS THE CURRENT TASK IS 'PAUSE'. THE PARAMETERS		440
	1213 *	ARE THEN FETCHED (SEE PROGRAMMER'S REFERENCE MANUAL IF YOU		450
	1214 *	DON'T FULLY UNDERSTAND 'IDC' MODIFICATIONS; CAUSE IF YOU DON'T		460
	1215 *	YOU WILL BE LOST) AND THE MME EXECUTED. 'EXIT' IS CALLED		470
	1216 *	THUS PLACING THE CURRENTLY RUNNING TASK IN A BLOCKED STATE		480
	1217 *	WHILE STARTING THE NEXT READY-TO-RUN TASK. THIS IS HOW WE		490
	1218 *	MULTI-PROGRAM. WHEN THE EXEC TRAPS THIS OPERATION, AS		500
	1219 *	WE HAVE SAID, THE XED INSTRUCTION PLACES THE TASK BACK		510
	1220 *	ON THE QSTASK QUEUE.		520
	1221 **	DISK SETFVM		530

SETUP FAULT VECTOR MACRO

	000526	1223	USE	CODE		110
		1224	HEAD			120
		1225 *				130
		1226 *				140
		1227 *		SETFV		150
		1228 *				160
		1229	SETFV	MACRO	CORELOC	170
		1230	TSX	0,%SETFV		180
		1231	ARG	#1	ADDRESS OF SLAVE FAULT VECTOR	190
		1232	ENDM	SETFV		200
		1233 *				210
		1234 *				220
		1235 *		SETFV -- SUBROUTINE		230
		1236 *				240
		1237 *		THIS SUBROUTINE IS CALLED BY THE SETFV MACRO. IT ISSUES THE		250
		1238 *		COMMAND TO LOCATE THE SLAVE FAULT VECTOR.		260
		1239 *				270
		1240 *		CALL WITH		280
		1241 *		C(XT) = TBLOCK-ADDRESS		290
		1242 *		C(XJ) = JBLOCK ADDRESS		300
		1243 *		ENTER BY		310
		1244 *		TSX 0,%SETFV		320
		1245 *		ARG CORE LOCATION TO FAULT VECTOR		330
		1246 *		RETURNS TO FIRST LOC AFTER MACRO EXPANSION		340
		1247 *		RETURNS WITH		350
		1248 *		C(XT) = TBLOCK-ADDRESS		360
		1249 *		C(XJ) = JBLOCK ADDRESS		370
		1250 *		C(XL) = RESTART ADDRESS		380
		1251 *		USES LOCAL TEMPORARY ONLY		390
		1252 *				400
		1253 *				410
000526	005320 7400 00	1254	SETFV	STX	0,%SETFT	420
000527	000001 0200 03	1255		ADLX	0,%DU	430
	000530	1256		SETUP		440
000530	000510 7170 00			XED	%SETUP	
000531	005320 2220 57	1257		LDX	2,%SETFT,%IDC	450
000532	000001 2200 03	1258		LDX	0,%SETFV,%DU	460
	000533	1259		CKPT		470
000533	000474 7170 00			XED	X\$CKPT	
000534	000000 0010 00	1260		MME		480
	000535	1261		EXIT		490
000535	003074 7100 00			TRA	%EXIT	
		1262 *				500
	005320	1263		USE	STORE	510
005320	000000 0000 20	1264	SETFT	ARG	0,%*	520
	000536	1265		USE	PREVIOUS	530
		1266	*\$*	DISK	READM	540

READ MACRO

000536	1268	USE	CODE	110
	1269	HEAD		120
	1270 *			130
	1271 *			140
	1272 *		READ	150
	1273 *			160
	1274 READ	MACRO	FRN,CORELOC,N,MODE	170
	1275	TSX	0,\$READ	180
	1276	ARG	#1 FRN ADDRESS	190
	1277	ARG	#2 ADDRESS OF CORE LOC	200
	1278	ARG	#3 NUMBER OF ELEMENTS	210
	1279	ARG	#4 MODE	220
	1280	ENDM	READ	230
	1281 *			240
	1282 *			250
	1283 *		READ -- SUBROUTINE	260
	1284 *			270
	1285 *		THIS SUBROUTINE IS CALLED BY THE READ MACRO. IT ISSUES THE	280
	1286 *		COMMAND TO READ THE NEXT N ELEMENTS OF FRN IN A PARTICULAR MODE.	290
	1287 *			300
	1288 *	CALL WITH		310
	1289 *		C(XT) = TBLOCK-ADDRESS	320
	1290 *		C(XJ) = JBLOCK-ADDRESS	330
	1291 *	ENTER BY		340
	1292 *		TSX 0,\$READ	350
	1293 *		ARG ADDRESS OF FRN	360
	1294 *		ARG ADDRESS OF CORELOC	370
	1295 *		ARG N	380
	1296 *		ARG MODE	390
	1297 *		RETURNS TO FIRST LOC AFTER MACRO EXPANSION	400
	1298 *		RETURNS WITH	410
	1299 *		C(XT) = TBLOCK-ADDRESS	420
	1300 *		C(XJ) = JBLOCK-ADDRESS	430
	1301 *		C(XL) = RESTART-ADDRESS	440
	1302 *		USES LOCAL TEMPORARY ONLY	450
	1303 *			460
	1304 *			470
END OF BINARY CARD	IOS00011			
000536	005321	7400	00	1305 READ
000537	000004	0200	03	1306
	000540			1307
000540	000510	7170	00	
000541	005321	2220	57	1308
000542	005321	2240	57	1309
000543	005321	2250	57	1310
000544	005321	2260	57	1311
000545	000004	2200	03	1312
	000546			1313
000546	000474	7170	00	
000547	000000	0010	00	1314
				STX
				0,READT
				POINTER TO ARGUMENT LIST
				ADLX
				0,4,DU
				RESTART ADDRESS
				SETUP
				XED
				\$SETUP
				LDX
				2,READT,IDC
				LOAD FRN
				LDX
				4,READT,IDC
				LOAD CORE LOC
				LDX
				5,READT,IDC
				LOAD N
				LDX
				6,READT,IDC
				LOAD MODE
				LDX
				0,,READ,DU
				LOAD MME NUMBER
				CKPT
				CHECKPOINT
				XED
				X\$CKPT
				MME
				READ
				480
				490
				500
				510
				520
				530
				540
				550
				560
				570

READ MACRO

000550	003074	7100 00	000550	1315	EXIT			580
					TRA	\$EXIT		
				1316 *				590
005321	000000	0000 20	005321	1317	USE	STORE		600
				1318	READT	ARG	0,*	610
			000551	1319	USE	PREVIOUS	POINTER TO ARGUMENT LIST	620
				1320 ***	DISK	APPENDM		630

APEND MACRO

000551	1322	USE	CODE		110	
	1323	HEAD			120	
	1324 *				130	
	1325 *				140	
	1326 *			APEND	150	
	1327 *				160	
	1328	APEND MACRO	FRN,CORELOC,N,MODE		170	
	1329	TSX	0,\$APEND		180	
	1330	ARG	#1	FRN ADDRESS	190	
	1331	ARG	#2	ADDRESS OF CORE LOC	200	
	1332	ARG	#3	NUMBER OF ELEMENTS	210	
	1333	ARG	#4	MODE	220	
	1334	ENDM	APEND		230	
	1335 *				240	
	1336 *				250	
	1337 *			APEND -- SUBROUTINE	260	
	1338 *				270	
	1339 *			THIS SUBROUTINE IS CALLED BY THE APEND MACRO. IT ISSUES	280	
	1340 *			THE COMMAND TO APEND N ELEMENTS TO THE FRN SPECIFIED	290	
	1341 *			VIA THE SPECIFIED MODE.	300	
	1342 *				310	
	1343 *			CALL WITH	320	
	1344 *			C(XT) = TBLOCK-ADDRESS	330	
	1345 *			C(XJ) = JBLOCK-ADDRESS	340	
	1346 *			ENTER BY	350	
	1347 *			TSX 0,\$APEND	360	
	1348 *			ARG FRN ADDRSS	370	
	1349 *			ARG ADDRESS OF CORE LOC	380	
	1350 *			ARG NUMBER OF ELEMENTS	390	
	1351 *			ARG MODE	400	
	1352 *			RETURNS TO FIRST LOC AFTER MACRO EXPANSION	410	
	1353 *			RETURNS WITH	420	
	1354 *			C(XT) = TBLOCK-ADDRESS	430	
	1355 *			C(XJ) = JBLOCK ADDRESS	440	
	1356 *			C(XL) = RESTART ADDRESS	450	
	1357 *			USES LOCAL TEMPORARY ONLY	460	
	1358 *				470	
	1359 *				480	
000551	005322	7400	00	1360	APEND STX 0,APNDT POINTER TO ARGUMENT LIST	490
000552	000004	0200	03	1361	ADLX 0,4,DU RESTART ADDRESS	500
		000553		1362	SETUP	510
000553	000510	7170	00		XED \$SETUP	
000554	005322	2220	57	1363	LDX 2,APNDT,IDC LOAD FRN	520
000555	005322	2240	57	1364	LDX 4,APNDT,IDC LOAD CORE LOC	530
000556	005322	2250	57	1365	LDX 5,APNDT,IDC LOAD NUMBER OF ELEMENTS	540
000557	005322	2260	57	1366	LDX 6,APNDT,IDC LOAD MODE	550
000560	000005	2200	03	1367	LDX 0,\$APEND,DU LOAD MME NUMBER	560
		000561		1368	CKPT CHECKPOINT	570
END OF BINARY CARD	IOS00012					
000561	000474	7170	00		XED X\$CKPT	

APEND MACRO

000562	000000	0010 00	1369	MME		APEND	580
		000563	1370	EXIT			590
000563	003074	7100 00		TRA	\$EXIT		
		005322	1371 *				600
			1372	USE	STORE		610
005322	000000	0000 20	1373	APNDT	ARG	0,*	620
		000564	1374	USE	PREVIOUS	POINTER TO ARGUMENT LIST	630
			1375	**	DISK	RRFM	640

READ RANDOM MACRO

000564	1377	USE	CODE		110		
	1378	HEAD			120		
	1379 *				130		
	1380 *				140		
	1381 *			READR	150		
	1382 *				160		
	1383 RRF	MACRO	FRN,FILELOC,CORELOC,N		170		
	1384	TSX	0,\$RRF		180		
	1385	ARG	#1	FILE REFERENCE ADDRESS	190		
	1386	ARG	#2	SOURCE ELEMENT ADDRESS	200		
	1387	ARG	#3	DESTINATION ADDRESS	210		
	1388	ARG	#4	NUMBER OF ELEMENTS TO TRANSMIT	220		
	1389	ENDM	RRF		230		
	1390 *				240		
	1391 *				250		
	1392 *		RRF -- SUBROUTINE		260		
	1393 *				270		
	1394 *			THIS SUBROUTINE IS CALLED BY THE RRF MACRO. IT ISSUES THE	280		
	1395 *			COMMAND TO READ RANDOMLY FROM THE FILE SPECIFIED BY THE	290		
	1396 *			FRN STARTING AT ELEMENT NUMBER FILELOC TO CORE STARTING AT	300		
	1397 *			CORELOC FOR N ELEMENTS.	310		
	1398 *				320		
	1399 *		CALL WITH		330		
	1400 *		C(XT) = TBLOCK-ADDRESS		340		
	1401 *		C(XJ) = JBLOCK ADDRESS		350		
	1402 *		ENTER BY		360		
	1403 *		TSX 0,\$RRF		370		
	1404 *		ARG FILE-REFERENCE-NUMBER		380		
	1405 *		ARG SOURCE-ELEMENT-NUMBER		390		
	1406 *		ARG DESTINATION-ADDRESS		400		
	1407 *		ARG NUMBER-OF-ELEMENTS		410		
	1408 *		RETURNS TO FIRST LOC AFTER MACRO EXPANSION		420		
	1409 *		RETURNS WITH		430		
	1410 *		C(XT) = TBLOCK-ADDRESS		440		
	1411 *		C(XJ) = JBLOCK ADDRESS		450		
	1412 *		C(XL) = RESTART ADDRESS		460		
	1413 *		USES ONLY LOCAL TEMPORARY		470		
	1414 *				480		
000564	005323	7400	00	1415 RRF	STX 0,RRFT	POINTER TO ARGUMENT LIST	490
000565	000004	0600	03	1416	ADX 0,4,DU	RESTART ADDRESS	500
		000566		1417	SETUP		510
000566	000510	7170	00		XED \$SETUP		
000567	005323	2220	57	1418	LDX 2,RRFT,IDC	LOAD FRN	520
000570	005323	2230	57	1419	LDX 3,RRFT,IDC	LOAD SOURCE ELEMENT NUMBER	530
000571	005323	2240	57	1420	LDX 4,RRFT,IDC	DESTINATION ADDRESS	540
000572	005323	2250	57	1421	LDX 5,RRFT,IDC	NUMBER OF ELEMENTS TO TRANSFER	550
000573	000006	2200	03	1422	LDX 0,\$RRF,DU	LOAD MME NUMBER	560
		000574		1423	CKPT	CHECKPOINT	570
000574	000474	7170	00		XED X\$CKPT		
000575	000000	0010	00	1424	MME	READ RANDOM FILE	580

READ RANDOM MACRO

000576	003074	000576 7100 00	1425	EXIT		590
				TRA	\$EXIT	
			1426 *			600
		005323	1427	USE	STORE	610
005323	000000	0000 20	1428 RRFT	ARG	C,*	620
		000577	1429	USE	PREVIOUS	630
			1430 *\$*	DISK	WRFM	640
					POINTER TO ARGUMENT LIST	

WRITE RANDOM MACRO

000577	1432	USE	CODE		110		
	1433	HEAD			120		
	1434 *				130		
	1435 *				140		
	1436 *			WRF	150		
	1437 *				160		
	1438 WRF	MACRO	FRN,FILELOC,CORELOC,N		170		
	1439	TSX	0,\$WRF		180		
	1440	ARG	#1	FILE REFERENCE ADDRESS	190		
	1441	ARG	#2	DESTINATION ELEMENT ADDRESS	200		
	1442	ARG	#3	SOURCE ADDRESS (CORE)	210		
	1443	ARG	#4	NUMBER OF ELEMENTS TO TRANSMIT	220		
	1444	ENDM	WRF		230		
	1445 *				240		
	1446 *				250		
	1447 *			WRF -- SUBROUTINE	260		
	1448 *				270		
	1449 *			THIS COMMAND IS CALLED BY THE WRF MACRO. IT ISSUES THE	280		
	1450 *			COMMAND TO WRITE RANDOMLY TO THE FILE SPECIFIED BY THE	290		
	1451 *			FRN STARTING AT ELEMENT NUMBER FILELOC FROM CORE STARTING AT	300		
	1452 *			CORELOC FOR N ELEMENTS.	310		
	1453 *				320		
	1454 *			CALL WITH	330		
	1455 *			C(XT) = TBLOCK ADDRESS	340		
	1456 *			C(XJ) = JBLOCK ADDRESS	350		
	1457 *			ENTER BY	360		
	1458 *			TSX 0,\$WRF	370		
	1459 *			ARG FILE-REFERENCE-NUMBER	380		
	1460 *			ARG DESTINATION-ELEMENT-NUMBER	390		
	1461 *			ARG SOURCE-ADDRESS	400		
	1462 *			ARG NUMBER-OF-ELEMENTS	410		
	1463 *			RETURNS TO FIRST LOC AFTER MACRO EXPANSION	420		
	1464 *			RETURNS WITH	430		
	1465 *			C(XT) = TBLOCK ADDRESS	440		
	1466 *			C(XJ) = JBLOCK ADDRESS	450		
	1467 *			C(XL) = RESTART ADDRESS	460		
	1468 *			USES ONLY LOCAL TEMPORARY	470		
	1469 *				480		
000577	005324	7400	00	1470 WRF	STX 0,\$WRFT	POINTER TO ARGUMENT LIST	490
000600	000004	0600	03	1471	ADX 0,4,DU	RESTART ADDRESS	500
	000601			1472	SETUP		510
END OF BINARY CARD IOS00013							
000601	000510	7170	00		XED	\$SETUP	
000602	005324	2220	57	1473	LDX 2,\$WRFT, IDC	LOAD FRN	520
000603	005324	2230	57	1474	LDX 3,\$WRFT, IDC	LOAD DESTINATION ELEMENT NUMBER	530
000604	005324	2240	57	1475	LDX 4,\$WRFT, IDC	LOAD SOURCE ADDRESS	540
000605	005324	2250	57	1476	LDX 5,\$WRFT, IDC	NUMBER OF ELEMENTS TO TRANSFER	550
000606	000007	2200	03	1477	LDX 0,\$WRF, DU	LOAD MME NUMBER	560
000607	000000	0010	00	1478	MME	WRITE RANDOM FILE	570
	000610			1479	CKPT	CHECKPOINT	580

WRITE RANDOM MACRO

000610	000474	7170 00		XED	X\$CKPT		
		000611	1480	EXIT			590
000611	003074	7100 00		TRA	\$EXIT		
		005324	1481 *				600
			1482	USE	STORE		610
005324	000000	0000 20	1483 WRFT	ARG	0.*	POINTER TO ARGUMENT LIST	620
		000612	1484	USE	PREVIOUS		630
			1485 **	DISK	SCRM		640

SCRATCH MACRO

	000612	1487	USE	CODE		110	
		1488	HEAD			120	
		1489 *				130	
		1490 *				140	
		1491 *			SCRATCH	150	
		1492 *				160	
		1493 SCR	MACRO	FRN,FLOC		170	
		1494	TSX	0,\$SCR		180	
		1495	ARG	#1	FILE REFERENCE ADDRESS	190	
		1496	ARG	#2	FILE LOCATION (ELEMENTS)	200	
		1497	ENDM	SCR		210	
		1498 *				220	
		1499 *				230	
		1500 *		SCRATCH -- SUBROUTINE		240	
		1501 *				250	
		1502 *		THIS SUBROUTINE IS CALLED BY THE SCR MACRO. IT ISSUES THE		260	
		1503 *		COMMAND TO SCRATCH A FILE. NOTE THAT THE FILE IS SCRATCHED TO		270	
		1504 *		THE BEGINNING.		280	
		1505 *				290	
		1506 *		CALL WITH		300	
		1507 *		C(XT) = TBLOCK ADDRESS		310	
		1508 *		C(XJ) = JBLOCK ADDRESS		320	
		1509 *		ENTER BY		330	
		1510 *		TSX 0,\$SCR		340	
		1511 *		ARG FILE=REFERENCE-ADDRESS		350	
		1512 *		RETURNS TO FIRST LOC AFTER MACRO EXPANSION		360	
		1513 *		RETURNS WITH		370	
		1514 *		C(XT) = TBLOCK ADDRESS		380	
		1515 *		C(XJ) = JBLOCK ADDRESS		390	
		1516 *		C(XL) = RESTART ADDRESS		400	
		1517 *		USES ONLY LOCAL TEMPORARY		410	
		1518 *				420	
000612	005325	7400	00	1519 SCR	STX 0,\$CRT	POINTER TO ARGUMENT LIST	430
000613	000002	0600	03	1520	ADX 0,2,DU	RESTART ADDRESS	440
		000614		1521	SETUP		450
000614	000510	7170	00		XED \$SETUP		
000615	005325	2220	57	1522	LDX 2,\$CRT, IDC	LOAD FILE REFERENCE NUMBER	460
000616	005325	2230	57	1523	LDX 3,\$CRT, IDC	LOAD STARTING SCRATCH ADDRESS	470
000617	000010	2200	03	1524	LDX 0,\$SCR, DU	LOAD MME NUMBER	480
		000620		1525	CKPT	CHECKPOINT	490
000620	000474	7170	00		XED X\$CKPT		
000621	000000	0010	00	1526	MME	SCRATCH FILE	500
		000622		1527	EXIT		510
000622	003074	7100	00		TRA \$EXIT		
		005325		1528 *			520
		005325		1529	USE	STORE	530
END OF BINARY CARD	IOS00014						
005325	000000	0000	20	1530 SCRT	ARG 0,*	POINTER TO ARGUMENT LIST	540
		000623		1531	USE	PREVIOUS	550
				1532 **	DISK	SPTRM	560

SET POINTER MACRO

		000623	1534	USE	CODE		110
			1535	HEAD			120
			1536	*			130
			1537	*			140
			1538	*		SET POINTER	150
			1539	*			160
			1540	SPTR	MACRO	FRN,N	170
			1541	TSX	0,%SPTR		180
			1542	ARG	#1	FRN ADDRESS	190
			1543	ARG	#2	NUMBER OF ELEMENTS TO MOVE POINTER	200
			1544	ENDM	SPTR		210
			1545	*			220
			1546	*			230
			1547	*		SET POINTER -- SUBROUTINE	240
			1548	*			250
			1549	*		THIS SUBROUTINE IS CALLED BY THE SPTR MACRO. IT ISSUES	260
			1550	*		THE COMMAND TO ADD (OR SUBTRACT) N ELEMENTS TO THE CUR-	270
			1551	*		RENT SETTING OF THE READ POINTER.	280
			1552	*			290
			1553	*		CALL WITH	300
			1554	*		C(XT) = TBLOCK-ADDRESS	310
			1555	*		C(XJ) = JBLOCK-ADDRESS	320
			1556	*		ENTER BY	330
			1557	*		TSX 0,%SPTR	340
			1558	*		ARG FRN	350
			1559	*		ARG NUMBER OF ELEMENTS	360
			1560	*		RETURNS TO FIRST LOC AFTER MACRO EXPANSION	370
			1561	*		RETURNS WITH	380
			1562	*		C(XJ) = JCB	390
			1563	*		C(XT) = TCB	400
			1564	*		C(XL) = RESTART ADDRESS	410
			1565	*		USES LOCAL TEMPORARY ONLY	420
			1566	*			430
000623	005326	7400 00	1567	SPTR	STX	0,%SPTR	440
000624	000002	0200 03	1568		ADLX	0,2,DU	450
		000625	1569		SETUP		460
000625	000510	7170 00			XED	%SETUP	
000626	005326	2220 57	1570		LDX	2,%SPTR, IDC	470
000627	005326	2230 57	1571		LDX	3,%SPTR, IDC	480
000630	000011	2200 03	1572		LDX	0,%SPTR, DU	490
		000631	1573		CKPT		500
000631	000474	7170 00			XED	X%CKPT	
000632	000000	0010 00	1574		MME		510
		000633	1575		EXIT		520
000633	003074	7100 00			TRA	%EXIT	
		005326	1576	*			530
			1577	USE	STORE		540
005326	000000	0000 20	1578	SPTRT	ARG	0,*	550
		000634	1579	USE	PREVIOUS		560
			1580	*\$*	DISK	RQSTM	570

REQUEST STATUS MACRO

000634	1582	USE	CODE		110			
	1583	HEAD			120			
	1584 *				130			
	1585 *				140			
	1586 *			REQUEST STATUS	150			
	1587 *				160			
	1588 RQST	MACRO	FRN		170			
	1589	TSX	0,\$RQST		180			
	1590	ARG	#1	FRN ADDRESS	190			
	1591	ENDM	RQST		200			
	1592 *				210			
	1593 *				220			
	1594 *			REQUEST STATUS -- SUBROUTINE	230			
	1595 *				240			
	1596 *			THIS SUBROUTINE IS CALLED BY THE RQST MACRO. IT ISSUES	250			
	1597 *			THE COMMAND TO REQUEST STATUS ON THE FRN SPECIFIED.	260			
	1598 *				270			
	1599 *			CALL WITH	280			
	1600 *		C(XT) = TCB		290			
	1601 *		C(XJ) = JCB		300			
	1602 *			ENTER BY	310			
	1603 *		TSX 0,\$RQST		320			
	1604 *		ARG FRN		330			
	1605 *			RETURNS TO FIRST LOC AFTER MACRO EXPANSION	340			
	1606 *			RETURNS WITH	350			
	1607 *		C(XT) = TCB		360			
	1608 *		C(XJ) = JCB		370			
	1609 *		C(XL) = RESTART ADDRESS		380			
	1610 *			USES LOCAL TEMPORARY ONLY	390			
	1611 *				400			
	1612 *				410			
000634	005327	7400	00	1613 RQST	STX 0,RQSTT	POINTER TO ARGUMENT LIST	420	
000635	000001	0200	03	1614	ADLX 0,1,DU	RESTART ADDRESS	430	
		000636		1615	SETUP		440	
000636	000510	7170	00		XED \$SETUP			
000637	005327	2220	57	1616	LDX 2,RQSTT, IDC	LOAD FRN	450	
000640	000012	2200	03	1617	LDX 0,\$RQST,DU	LOAD MME NUMBER	460	
		000641		1618	CKPT	CHECKPOINT	470	
000641	000474	7170	00		XED X\$CKPT			
000642	000000	0010	00	1619	MME	REQUEST STATUS	480	
		000643		1620	EXIT		490	
000643	003074	7100	00		TRA \$EXIT			
		005327		1621 *			500	
		005327		1622	USE	STORE	510	
END OF BINARY CARD	005327	000000	0000	20	1623 RQSTT	ARG 0,*	POINTER TO ARGUMENT LIST	520
		000644		1624	USE	PREVIOUS	530	
				1625 **	DISK	SPAWN	540	

SPAWN MACRO

			000644	1627	USE	CODE		110
				1628	HEAD			120
				1629	*			130
				1630	*			140
				1631	*			150
				1632	*			160
				1633	SPAWN	MACRO	PLOC,LENGTH,ORIGINATOR	170
				1634	TSX		0,%SPAWN	180
				1635	ARG	#1	PARAMETER LIST ADDRESS	190
				1636	ARG	#2	LENGTH ADDRESS	200
				1637	ARG	#3	ORIGINATOR ADDRESS	210
				1638	ENDM	SPAWN		220
				1639	*			230
				1640	*			240
				1641	*		SPAWN -- SUBROUTINE	250
				1642	*			260
				1643	*		THIS SUBROUTINE IS CALLED BY THE SPAWN MACRO. IT ISSUES THE	270
				1644	*		COMMAND TO SPAWN A PROGRAM.	280
				1645	*			290
				1646	*		CALL WITH	300
				1647	*		C(XT) = TBLOCK ADDRESS	310
				1648	*		C(XJ) = JBLOCK ADDRESS	320
				1649	*		ENTER BY	330
				1650	*		TSX 0,%SPAWN	340
				1651	*		ARG PARAMETER LIST ADDRESS	350
				1652	*		ARG LENGTH ADDRESS	360
				1653	*		ARG ORIGINATOR ADDRESS	370
				1654	*		RETURNS TO FIRST LOC AFTER MACRO EXPANSION	380
				1655	*		RETURNS WITH	390
				1656	*		C(XT) = TBLOCK ADDRESS	400
				1657	*		C(XJ) = JBLOCK ADDRESS	410
				1658	*		C(XL) = RESTART ADDRESS	420
				1659	*		USES ONLY LOCAL TEMPORARY	430
				1660	*			440
000644	005330	7400	00	1661	SPAWN	STX	0,%SPWNT	450
000645	000003	0600	03	1662		ADX	0,%DU	460
			000646	1663		SETUP		470
000646	000510	7170	00			XED	%SETUP	
000647	005330	2240	57	1664		LDX	4,%SPWNT,%IDC	480
000650	005330	2250	57	1665		LDX	5,%SPWNT,%IDC	490
000651	005330	2360	57	1666		LDQ	SPWNT,%IDC	500
000652	000015	2200	03	1667		LDX	0,%SPAWN,%DU	510
			000653	1668		CKPT		520
000653	000474	7170	00			XED	%CKPT	
000654	000000	0010	00	1669		MME		530
			000655	1670		EXIT		540
000655	003074	7100	00			TRA	%EXIT	
			005330	1671	*			550
				1672	USE	STORE		560
005330	000000	0000	20	1673	SPWNT	ARG	0,%*	570

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SPAWN MACRO

000656

1674 USE PREVIOUS
1675 *** DISK CHSEGM

580
590

CHANGE SEGMENT MACRO

	000656	1677	USE	CODE		110
		1678	HEAD			120
		1679 *				130
		1680 *				140
		1681 *		CHSEG		150
		1682 *				160
		1683	CHSEG	MACRO	SEGMENT=NUMBER,LENGTH	170
		1684	TSX	0,%CHSEG		180
		1685	ARG	#1	NUMBER-OF-SEGMENT	190
		1686	ARG	#2	NEW-LENGTH	200
		1687	ENDM	CHSEG		210
		1688 *				220
		1689 *				230
		1690 *		CHSEG -- SUBROUTINE		240
		1691 *				250
		1692 *		THIS SUBROUTINE IS CALLED BY THE CHSEG MACRO. IT ISSUES		260
		1693 *		THE COMMAND TO CHANGE THE LENGTH OF THE NAMED SEGMENT TO		270
		1694 *		THE NEW LENGTH SPECIFIED.		280
		1695 *				290
		1696 *		CALL WITH		300
		1697 *		C(XT) = TBLOCK-ADDRESS		310
		1698 *		C(XJ) = JBLOCK ADDRESS		320
		1699 *		ENTER BY		330
		1700 *		TSX 0,%CHSEG		340
		1701 *		ARG SEGMENT=NUMBER		350
		1702 *		ARG LENGTH		360
		1703 *		RETURNS TO FIRST LOC AFTER MACRO EXPANSION		370
		1704 *		RETURNS WITH		380
		1705 *		C(XT) = TBLOCK-ADDRESS		390
		1706 *		C(XJ) = JBLOCK ADDRESS		400
		1707 *		C(XL) = RESTART ADDRESS		410
		1708 *		USES LOCAL TEMPORARY ONLY		420
		1709 *				430
	000656	005331	7400	00		
	000657	000002	0200	03	1710 CHSEG STX 0,%CHSGT POINTER TO ARGUMENT LIST	440
				000660	1711 ADLX 0,2,DU RESTART ADDRESS	450
					1712 SETUP	460
	000660	000510	7170	00	XED \$SETUP	
	000661	005331	2220	57	1713 LDX 2,%CHSGT, IDC LOAD SEGMENT NUMBER	470
	000662	005331	2230	57	1714 LDX 3,%CHSGT, IDC LOAD SEGMENT LENGTH	480
	000663	000022	2200	03	1715 LDX 0,%CHSEG, DU LOAD MME NUMBER	490
				000664	1716 CKPT CHECKPOINT	500
	000664	000474	7170	00	XED X\$CKPT	
END	OF BINARY CARD	IOS00016				
	000665	000000	0010	00	1717 MME CHANGE SEGMENT	510
				000666	1718 EXIT	520
	000666	003074	7100	00	TRA \$EXIT	
					1719 *	530
				005331	1720 USE STORE	540
	005331	000000	0000	20	1721 CHSGT ARG 0,* POINTER TO ARGUMENT LIST	550
				000667	1722 USE PREVIOUS	560

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CHANGE SEGMENT MACRO

1723 ** DISK OPENM

570

OPEN MACRO

000667		1725	USE	CODE		110
		1726	HEAD			120
		1727 *				130
		1728 *				140
		1729 *		OPEN		150
		1730 *				160
		1731 OPEN	MACRO	TREENAME, TREESIZE, BEHALF, ELFSIZE, ACCESSSES		170
		1732	TSX	0, \$OPEN		180
		1733	ARG	#1 TREE-NAME-ADDRESS		190
		1734	ARG	#2 TREE-SIZE-ADDRESS		200
		1735	ARG	#3 BEHALF		210
		1736	ARG	#4 ELEMENT SIZE ADDRESS		220
		1737	ARG	#5 ACCESSSES		230
		1738	ENDM	OPEN		240
		1739 *				250
		1740 *				260
		1741 *		OPEN -- SUBROUTINE		270
		1742 *				280
		1743 *		THIS SUBROUTINE IS CALLED BY THE OPEN MACRO. IT ISSUES THE		290
		1744 *		COMMAND TO OPEN A FILE.		300
		1745 *				310
		1746 *		CALL WITH		320
		1747 *		C(XT) = TBLOCK ADDRESS		330
		1748 *		C(XJ) = JBLOCK ADDRESS		340
		1749 *		ENTER BY		350
		1750 *		TSX 0, \$OPEN		360
		1751 *		ARG TREE-NAME-ADDRESS		370
		1752 *		ARG TREE-SIZE		380
		1753 *		ARG BEHALF		390
		1754 *		ARG ELEMENT-SIZE		400
		1755 *		ARG ACCESSSES		410
		1756 *		RETURNS TO FIRST LOC AFTER MACRO EXPANSION		420
		1757 *		RETURNS WITH		430
		1758 *		C(XT) = TBLOCK ADDRESS		440
		1759 *		C(XJ) = JBLOCK ADDRESS		450
		1760 *		C(XJ) = RESTART ADDRESS		460
		1761 *		USES ONLY LOCAL TEMPORARY		470
		1762 *				480
000667	005332	7400	00	1763 OPEN	STX 0, OPENT	490
000670	000005	0600	03	1764	ADX 0, 5, DU	500
		000671		1765	SETUP	510
000671	000510	7170	00		XED \$SETUP	
000672	005332	2240	57	1766	LDX 4, OPENT, IDC	520
000673	005332	2250	57	1767	LDX 5, OPENT, IDC	530
000674	005332	2200	57	1768	LDX 0, OPENT, IDC	540
000675	005332	2270	57	1769	LDX 7, OPENT, IDC	550
000676	005332	2360	57	1770	LDQ OPENT, IDC	560
000677	000000	6260	10	1771	EAX 6, 0, 0	570
000700	000024	2200	03	1772	LDX 0, OPENT, DU	580
		000701		1773	CKPT	590

OPEN MACRO

000701	000474	7170	00		XED	X\$CKPT		
000702	000000	0010	00	1774	MME		OPEN	600
		000703		1775	EXIT			610
000703	003074	7100	00		TRA	\$EXIT		
		005332		1776	*			620
				1777	USE	STORE		630
005332	000000	0000	20	1778	OPENT	ARG	0.*	640
		000704		1779	USE	PREVIOUS	POINTER TO ARGUMENT LIST	650
				1780	*\$*	DISK	CLOSEM	660

CLOSE MACRO

			000704	1782	USE	CODE		110
				1783	HEAD			120
				1784	*			130
				1785	*			140
				1786	*		CLOSE	150
				1787	*			160
				1788	CLOSE	MACRO	FRN	170
				1789	TSX	0,%CLOSE		180
				1790	ARG	#1	FILE REFERENCE ADDRESS	190
				1791	ENDM	CLOS		200
				1792	*			210
				1793	*			220
				1794	*		CLOSE -- SUBROUTINE	230
				1795	*			240
				1796	*	THIS SUBROUTINE IS CALLED BY THE CLOS MACRO. IT ISSUES THE		250
				1797	*	MME TO CLOSE A FILE.		260
				1798	*			270
				1799	*	CALL WITH		280
				1800	*	C(XT) = TBLOCK-ADDRESS		290
				1801	*	C(XJ) = JBLOCK ADDRESS		300
				1802	*	ENTER BY		310
				1803	*	TSX 0,%CLOS		320
				1804	*	ARG FILE-REFERENCE-ADDRESS		330
				1805	*	RETURNS TO FIRST LOC AFTER MACRO EXPANSION		340
				1806	*	RETURNS WITH		350
				1807	*	C(XT) = TBLOCK-ADDRESS		360
				1808	*	C(XJ) = JBLOCK ADDRESS		370
				1809	*	C(XL) = RESTART ADDRESS		380
				1810	*	USES LOCAL TEMPORARY ONLY		390
				1811	*			400
				1812	CLOSE	STX	0,%CLOS	410
							POINTER TO ARGUMENT LIST	
				1813	ADLX	0,1,DU	RESTART ADDRESS	420
				1814	SETUP			430
					XED	\$SETUP		
				1815	LDX	2,%CLOS,%IDC	LOAD FILE REFERENCE	440
				1816	LDX	0,%CLOSE,%DU	LOAD MME NUMBER	450
				1817	CKPT		CHECKPOINT	460
					XED	X\$CKPT		
				1818	MME		CLOS	470
				1819	EXIT			480
					TRA	\$EXIT		
				1820	*			490
				1821	USE	STORE		500
				1822	CLOS	ARG	0,%*	510
							POINTER TO ARGUMENT LIST	
				1823	USE	PREVIOUS		520
				1824	*\$*	DISK	DESTROM	530

000704	005333	7400	00					
END	OF BINARY CARD	IOS00017						
000705	000001	0200	03					
		000706						
000706	000510	7170	00					
000707	005333	2220	57					
000710	000025	2200	03					
		000711						
000711	000474	7170	00					
000712	000000	0010	00					
		000713						
000713	003074	7100	00					
		005333						
005333	000000	0000	20					
		000714						

DESTROY MACRO

	000714	1826	USE	CODE		110
		1827	HEAD			120
		1828 *				130
		1829 *				140
		1830 *		DESTROY (UNCATALOG BY TREE-NAME)		150
		1831 *				160
		1832 DESTRO	MACRO	TREE-NAME, TREE-SIZE, BEHALF		170
		1833	TSX	0, \$DESTRO		180
		1834	ARG	#1 TREE-NAME ADDRESS		190
		1835	ARG	#2 TREE-SIZE ADDRESS		200
		1836	ARG	#3 BEHALF		210
		1837	ENDM	DEST		220
		1838 *				230
		1839 *				240
		1840 *		DESTROY -- SUBROUTINE		250
		1841 *				260
		1842 *		THIS SUBROUTINE IS CALLED BY THE DEST MACRO. IT ISSUES THE		270
		1843 *		COMMAND TO DESTROY (I.E. UNCATALOG) A FILE.		280
		1844 *				290
		1845 *		CALL WITH		300
		1846 *		C(XT) = TBLOCK ADDRESS		310
		1847 *		C(XJ) = JBLOCK ADDRESS		320
		1848 *		ENTER BY		330
		1849 *		TSX 0, \$DEST		340
		1850 *		ARG TREE-NAME-ADDRESS		350
		1851 *		ARG TREE-SIZE		360
		1852 *		ARG BEHALF		370
		1853 *		RETURNS TO FIRST LOC AFTER MACRO EXPANSION		380
		1854 *		RETURNS WITH		390
		1855 *		C(XT) = TBLOCK ADDRESS		400
		1856 *		C(XJ) = JBLOCK ADDRESS		410
		1857 *		C(XL) = RESTART ADDRESS		420
		1858 *		USES ONLY LOCAL TEMPORARY		430
		1859 *				440
000714	005334 7400 00	1860 DESTRO	STX	0, \$DESTT	POINTER TO ARGUMENT LIST	450
000715	000003 0600 03	1861	ADX	0, 3, DU	RESTART ADDRESS	460
	000716	1862	SETUP			470
000716	000510 7170 00		XED	\$SETUP		
000717	005334 2240 57	1863	LDX	4, \$DESTT, IDC	LOAD TREE-NAME	480
000720	005334 2250 57	1864	LDX	5, \$DESTT, IDC	TREE-SIZE	490
000721	005334 2260 57	1865	LDX	6, \$DESTT, IDC	BEHALF	500
000722	000027 2200 03	1866	LDX	0, \$DESTRO, DU	MME NUMBER	510
	000723	1867	CKPT		CHECKPOINT	520
000723	000474 7170 00		XED	X\$CKPT		
000724	000000 0010 00	1868	MME		DESTROY	530
	000725	1869	EXIT			540
000725	003074 7100 00		TRA	\$EXIT		
	005334	1870 *				550
		1871	USE	STORE		560
005334	000000 0000 20	1872 DESTT	ARG	0, *	POINTER TO ARGUMENT LIST	570

DESTROY MACRO

000726

1873 USE PREVIOUS
1874 *\$* DISK UPDATEM

580
590

UPDATE MACRO

000726	1876	USE	CODE		110				
	1877	HEAD			120				
	1878 *				130				
	1879 *				140				
	1880 *		UPDATE		150				
	1881 *				160				
	1882	UPDATE MACRO	FRN		170				
	1883	TSX	0,%UPDATE		180				
	1884	ARG	#1	FILE REFERNECE ADDRESS	190				
	1885	ENDM	UPDAT		200				
	1886 *				210				
	1887 *				220				
	1888 *		UPDATE -- SUBROUTINE		230				
	1889 *				240				
	1890 *		THIS SUBROUTINE IS CALLED BY THE UPDAT MACRO. IT ISSUES THE		250				
	1891 *		UPDATE MME ON THE FRN SPECIFIED		260				
	1892 *				270				
	1893 *		CALL WITH		280				
	1894 *		C(XT) = TBLOCK ADDRESS		290				
	1895 *		C(XJ) = JBLOCK ADDRESS		300				
	1896 *		ENTER BY		310				
	1897 *		TSX 0,%UPDATE		320				
	1898 *		ARG FILE=REFERENCE=ADDRESS		330				
	1899 *		RETURNS TO FIRST LOC AFTER MACRO EXPANSION		340				
	1900 *		RETURNS WITH		350				
	1901 *		C(XT) = TBLOCK ADDRESS		360				
	1902 *		C(XJ) = JBLOCK ADDRESS		370				
	1903 *		C(XL) = RETART ADDRESS		380				
	1904 *		USES ONLY LOCAL TEMPORARY		390				
	1905 *				400				
END OF BINARY CARD	10S00018								
000726	005335	7400	00	1906 UPDATE STX	0,%UPDTT	POINTER TO ARGUMENT LIST	410		
000727	000001	0600	03	1907	ADX	0,%DU	RESTART ADDRESS	420	
			000730	1908	SETUP			430	
000730	000510	7170	00		XED	\$\$SETUP			
000731	005335	2220	57	1909	LDX	2,%UPDTT,%IDC	LOAD FILE REFERENCE	440	
000732	000031	2200	03	1910	LDX	0,%UPDAT,%DU	MME NUMBER	450	
			000733	1911	CKPT		CHECKPOINT	460	
000733	000474	7170	00		XED	X\$CKPT			
000734	000000	0010	00	1912	MME		UPDATE	470	
			000735	1913	EXIT			480	
000735	003074	7100	00		TRA	\$\$EXIT			
			005335	1914 *				490	
005335	000000	0000	20	1915	USE	STORE		500	
			000736	1916	UPDTT	ARG	0,%*	POINTER TO ARGUMENT LIST	510
				1917	USE	PREVIOUS		520	
				1918	*\$*	DISK	LOCKM	530	

LOCK MACRO

			000736	1920	USE	CODE		110
				1921	HEAD			120
				1922	*			130
				1923	*			140
				1924	*	LOCK		150
				1925	*			160
				1926	LOCK	MACRO	FRN	170
				1927	TSX	0,%LOCK		180
				1928	ARG	#1	FILE-REFERENCE ADDRESS	190
				1929	ENDM	LOCK		200
				1930	*			210
				1931	*			220
				1932	*	LOCK -- SUBROUTINE		230
				1933	*			240
				1934	*	THIS SUBROUTINE IS CALLED BY THE LOCK MACRO. IT ISSUES THE		250
				1935	*	MME TO LOCK A FILE.		260
				1936	*			270
				1937	*	CALL WITH		280
				1938	*	C(XT) =	TBLOCK-ADDRESS	290
				1939	*	C(XJ) =	JBLOCK ADDRESS	300
				1940	*	ENTER BY		310
				1941	*	TSX	0,%LOCK	320
				1942	*	ARG	FILE-REFERENCE-ADDRESS	330
				1943	*	RETURNS	TO FIRST LOC AFTER MACRO EXPANSION	340
				1944	*	RETURNS	WITH	350
				1945	*	C(XT) =	TBLOCK-ADDRESS	360
				1946	*	C(XJ) =	JBLOCK ADDRESS	370
				1947	*	C(XL) =	RESTART ADDRESS	380
				1948	*	USES	LOCAL TEMPORARY ONLY	390
				1949	*			400
000736	005336	7400	00	1950	LOCK	STX	0,%LOCKT	410
000737	000001	0200	03	1951		ADLX	0,%DU	420
		000740		1952		SETUP		430
000740	000510	7170	00	1953		XED	\$SETUP	
000741	005336	2220	57	1953		LDX	2,%LOCKT,%IDC	LOAD FILE REFERENCE
000742	000044	2200	03	1954		LDX	0,%LOCK,%DU	MME NUMBER
		000743		1955		CKPT		CHECKPOINT
000743	000474	7170	00	1956		XED	X\$CKPT	
000744	000000	0010	00	1956		MME		LOCK
		000745		1957		EXIT		
000745	003074	7100	00	1957		TRA	\$EXIT	480
				1958	*			490
		005336		1959		USE	STORE	500
005336	000000	0000	20	1960	LOCKT	ARG	0,%*	510
		000746		1961		USE	PREVIOUS	520
				1962	***	DISK	UNLOCKM	530

UNLOCK MACRO

	000746	1964	USE	CODE		110
		1965	HEAD			120
		1966 *				130
		1967 *				140
		1968 *		UNLOCK		150
		1969 *				160
		1970 UNLCK	MACRO	FRN		170
		1971	TSX	0,\$UNLCK		180
		1972	ARG	#1	FILE REFERENCE ADDRESS	190
		1973	ENDM	UNLK		200
		1974 *				210
		1975 *				220
		1976 *		UNLOCK -- SUBROUTINE		230
		1977 *				240
		1978 *		THIS SUBROUTINE IS CALLED BY THE UNLK MACRO. IT ISSUES THE		250
		1979 *		MME TO UNLOCK A FILE.		260
		1980 *				270
		1981 *		CALL WITH		280
		1982 *		C(XT) = TBLOCK-ADDRESS		290
		1983 *		C(XJ) = JBLOCK ADDRESS		300
		1984 *		ENTER BY		310
		1985 *		TSX 0,\$UNLK		320
		1986 *		ARG FILE-REFERENCE-ADDRESS		330
		1987 *		RETURNS TO FIRST LOC AFTER THE MACRO EXPANSION		340
		1988 *		RETURNS WITH		350
		1989 *		C(XT) = TBLOCK-ADDRESS		360
		1990 *		C(XJ) = JBLOCK ADDRESS		370
		1991 *		C(XL) = RESTART ADDRESS		380
		1992 *		USES LOCAL TEMPORARY ONLY		390
		1993 *				400
END OF BINARY CARD	IOS00019					
000746	005337 7400 00	1994 UNLCK	STX	0,\$UNLKT	POINTER TO ARGUMENT LIST	410
000747	000001 0200 03	1995	ADLX	0,1,DU	RESTART ADDRESS	420
	000750	1996	SETUP			430
000750	000510 7170 00		XED	\$SETUP		
000751	005337 2220 57	1997	LDX	2,\$UNLKT, IDC	LOAD FILE REFERENCE	440
000752	000045 2200 03	1998	LDX	0,\$UNLCK, DU	MME NUMBER	450
	000753	1999	CKPT		CHECKPOINT	460
000753	000474 7170 00		XED	X\$CKPT		
000754	000000 0010 00	2000	MME		UNLOCK	470
	000755	2001	EXIT			480
000755	003074 7100 00		TRA	\$EXIT		
		2002 *				490
	005337	2003	USE	STORE		500
005337	000000 0000 20	2004 UNLKT	ARG	0,*	POINTER TO ARGUMENT LIST	510
	000756	2005	USE	PREVIOUS		520
		2006 *\$*	DISK	NOTIFM		530

NOTIFY MACRO

	000756	2008	USE	CODE		110			
		2009	HEAD			120			
		2010	*			130			
		2011	*		NOTIFY	140			
		2012	*			150			
		2013	NOTIF	MACRO	ERN,STATE	160			
		2014	TSX	0,\$NOTIF		170			
		2015	ARG	#1	FRN ADDRESS	180			
		2016	ARG	#2	STATE ADDRESS	190			
		2017	ENDM	NOTIF		200			
		2018	*			210			
		2019	*			220			
		2020	*		NOTIFY -- SUBROUTINE	230			
		2021	*			240			
		2022	*	THIS SUBROUTINE IS CALLED BY THE NOTIF MACRO. IT ISSUES		250			
		2023	*	THE NOTIFY ON THE SPECIFIED EVENT. NOTE THAT C(X3) POINT TO		260			
		2024	*	CTRAP.		270			
		2025	*			280			
		2026	*	CALL WITH		290			
		2027	*	C(X1) =	TBLOCK-ADDRESS	300			
		2028	*	C(X2) =	JBLOCK ADDRESS	310			
		2029	*	C(X3) =	CTRAP ADDRESS	320			
		2030	*	ENTER BY		330			
		2031	*	TSX	0,\$NOTIF	340			
		2032	*	ARG	EVENT-FRN	350			
		2033	*	ARG	STATE ADDRESS	360			
		2034	*	RETURNS TO FIRST LOC AFTER MACRO EXPANSION		370			
		2035	*	RETURNS	WITH	380			
		2036	*	C(X1) =	TBLOCK-ADDRESS	390			
		2037	*	C(X2) =	JBLOCK ADDRESS	400			
		2038	*	C(X4) =	RESTART ADDRESS	410			
		2039	*	USES LOCAL TEMPORARY ONLY		420			
		2040	*			430			
000756	005340	7400	00	2041	NOTIF	STX	0,\$NOTFT	POINTER OF ARGUMENT LIST	440
000757	000002	0200	03	2042		ADLX	0,\$DU	RESTART ADDRESS	450
		000760		2043		SETUP			460
000760	000510	7170	00			XED	\$SETUP		
000761	005340	2220	57	2044		LDX	2,\$NOTFT,\$IDC	LOAD EVENT FRN	470
000762	005340	2350	57	2045		LDA	NOTFT,\$IDC	LOAD STATE	480
000763	000046	2200	03	2046		LDX	0,\$NOTIF,\$DU	LOAD MME NUMBER	490
		000764		2047		CKPT		CHECKPOINT	500
000764	000474	7170	00			XED	X\$CKPT		
000765	000000	0010	00	2048		MME		NOTIFY	510
		000766		2049		EXIT			520
000766	003074	7100	00			TRA	\$EXIT		
		005340		2050	*				530
				2051		USE	STORE		540
005340	000000	0000	20	2052	NOTFT	ARG	0,\$*	POINTER TO ARGUMENT LIST	550
		000767		2053		USE	PREVIOUS		560
				2054	**\$*	DISK	CAUSEM		570

CAUSE MACRO

000767	2056	USE	CODE		110
	2057	HEAD			120
	2058	*			130
	2059	*			140
	2060	*		CAUSE	150
	2061	*			160
	2062	CAUSE	MACRO	ERN,NUMBER,STATE,MESSAGE,ACCESSES,FRN (N.B. ORDERING)	170
	2063	TSX		0,\$CAUSE	180
	2064	ARG	#1	FILE REFERENCE ADDRESS	190
	2065	ARG	#2	NUMBER WHICH ARE TO NOTIFIED	200
	2066	ARG	#3	STATE	210
	2067	ARG	#4	MESSAGE	220
	2068	ARG	#5	FRN OF ITME TO PASS	230
	2069	ARG	#6	ACCESSES ON PASSED ITEM	240
	2070	ENDM	CAUS		250
	2071	*			260
	2072	*			270
	2073	*		CAUSE -- SUBROUTINE	280
	2074	*			290
	2075	*		THIS SUBROUTINE IS CALLED BY THE CAUS MACRO. IT ISSUES THE	300
	2076	*		MME TO CAUSE THE SPECIFIED FILE.	310
	2077	*			320
	2078	*		CALL WITH	330
	2079	*		C(XJ) = JBLOCK ADDRESS	340
	2080	*		C(XT) = TBLOCK-ADDRESS	350
	2081	*		ENTER BY	360
	2082	*		TSX 0,\$CAUS	370
	2083	*		ARG FILE-REFERENCE-NUMBER OF EVENT	380
	2084	*		ARG NUMBER	390
	2085	*		ARG PASSED-FILE-REFERENCE	400
	2086	*		ARG ACCESSES-ON-PASSED-ITEMS	410
	2087	*		ARG STATE	420
	2088	*		ARG MESSAGE	430
	2089	*		RETURNS TO FIRST LOC AFTER THE MACRO EXPANSION	440
	2090	*		RETURNS WITH	450
	2091	*		C(XJ) = JBLOCK ADDRESS	460
	2092	*		C(XT) = TBLOCK-ADDRESS	470
	2093	*		C(XL) = RESTART ADDRESS	480
	2094	*		USES LOCAL TEMPORARY ONLY	490
	2095	*			500
END OF BINARY CARD IOS00020					
000767 005341 7400 00	2096	CAUSE	STX	0,CAUST	510
000770 000006 0200 03	2097		ADLX	0,6,DU	520
000771 000510 7170 00	2098		SETUP		530
000772 005341 2220 57	2099		XED	\$SETUP	
000773 005341 2230 57	2100		LDX	2,CAUST,IDC	LOAD FILE REFERENCE
000774 005341 2350 57	2101		LDX	3,CAUST,IDC	NUMBER
000775 005341 2360 57	2102		LDA	CAUST,IDC	STATE
000776 005341 2200 57	2103		LDQ	CAUST,IDC	MESSAGE
			LDX	0,CAUST,IDC	LOAD FRN
					580

CAUSE MACRO

000777	005341	2270	57	2104	LDX	7,CAUST, IDC	LOAD ACCESSES	590
001000	000000	6260	10	2105	EAX	6,0,0	MOVE FRN TO X6	600
001001	000047	2200	03	2106	LDX	0, CAUSE, DU	MME NUMBER	610
		001002		2107	CKPT		CHECKPOINT	620
001002	000474	7170	00		XED	X\$CKPT		
001003	000000	0010	00	2108	MME		CAUSE	630
		001004		2109	EXIT			640
001004	003074	7100	00		TRA	\$EXIT		
		005341		2110	*			650
				2111	USE	STORE		660
005341	000000	0000	20	2112	CAUST ARG	0,*	POINTER TO ARGUMENT LIST	670
		001005		2113	USE	PREVIOUS		680
				2114	*\$*	DISK	OPSCEN	690

OPEN SCRATCH EVENT MACRO

	001005	2116	USE	CODE		110		
		2117	HEAD			120		
		2118	*			130		
		2119	*			140		
		2120	*		OPSCE	150		
		2121	*			160		
		2122	OPSCE	MACRO	TIMLIM,MODE,MAXLEN	170		
		2123	TSX	0,\$OPSCE		180		
		2124	ARG	#1	TIME LIMIT	190		
		2125	ARG	#2	MODE	200		
		2126	ARG	#3	MAXIMUM QUEUE LENGTH	210		
		2127	ENDM	OPSCE		220		
		2128	*			230		
		2129	*			240		
		2130	*		OPEN SCRATCH EVENT --SUBROUTINE	250		
		2131	*			260		
		2132	*		THIS SUBROUTINE IS CALLED BY THE OPSCE MACRO. IT OPENS A	270		
		2133	*		SCRATCH EVENT.	280		
		2134	*			290		
		2135	*		CALL WITH	300		
		2136	*		C(XT) = TBLOCK ADDRESS	310		
		2137	*		C(XJ) = JBLOCK ADDRESS	320		
		2138	*		ENTER BY	330		
		2139	*		TSX 0,\$OPSCE	340		
		2140	*		ARG TIME-LIMIT	350		
		2141	*		ARG MODE	360		
		2142	*		ARG MAX-QUEUE-LENGTH	370		
		2143	*		RETURNS TO FIRST LOC AFTER MACRO EXPANSION	380		
		2144	*		RETURNS WITH	390		
		2145	*		C(XT) = TBLOCK ADDRESS	400		
		2146	*		C(XJ) = JBLOCK ADDRESS	410		
		2147	*		C(XL) = RESTART ADDRESS	420		
		2148	*		USES ONLY LOCAL TEMPORARY	430		
		2149	*			440		
	001005	005342	7400	00	2150 OPSCE	STX 0,OPSET	POINTER TO ARGUEMNT LIST	450
	001006	000003	0600	03	2151	ADX 0,3,DU	RESTART ADDRESS	460
				001007	2152	SETUP		470
	001007	000510	7170	00		XED \$SETUP		
	001010	005342	2230	57	2153	LDX 3,OPSET,IDC	LOAD TIME LIMIT	480
	001011	005342	2240	57	2154	LDX 4,OPSET,IDC	LOAD MODE	490
END	OF BINARY CARD	IOS00021						
	001012	005342	2250	57	2155	LDX 5,OPSET,IDC	LOAD MAX QUEUE LENGTH	500
	001013	000052	2200	03	2156	LDX 0,\$OPSCE,DU	LOAD MME NUMBER	510
				001014	2157	CKPT	CHECKPOINT	520
	001014	000474	7170	00		XED X\$CKPT		
	001015	000000	0010	00	2158	MME	OPEN SCRATCH EVENT	530
				001016	2159	EXIT		540
	001016	003074	7100	00		TRA \$EXIT		
					2160	*		550
		005342			2161	USE	STORE	560

OPEN SCRATCH EVENT MACRO

005342 000000 0000 20
001017

2162 OPSET ARG 0,*
2163 USE PREVIOUS
2164 *S* DISK ACCTM

POINTER TO ARGUMENT LIST

570
580
590

ACCOUNTING MACRO

001017	2166	USE	CODE	110				
	2167	HEAD		120				
	2168 *			130				
	2169 *			140				
	2170 *		ACCOUNTING	150				
	2171 *			160				
	2172 ACCT	MACRO	MODE,ACODE,DATA	170				
	2173	TSX	0,%ACCT	180				
	2174	ARG	#1 MODE	190				
	2175	ARG	#2 ACODE (ACODE/WORD NUMBER)	200				
	2176	ARG	#3 DATA	210				
	2177	ENDM	ACCT	220				
	2178 *			230				
	2179 *			240				
	2180 *		ACCOUNTING -- SUBROUTINE	250				
	2181 *			260				
	2182 *		THIS SUBROUTINE IS CALLED BY THE ACCT MACRO. IT ISSUES	270				
	2183 *		THE COMMAND TO 1: REQUEST ACODE, OR 2: ALTER ACCOUNTING	280				
	2184 *		INFORMATION DEPENDING ON THE MODE (0 AND 1 RESPECTIVELY).	290				
	2185 *			300				
	2186 *	CALL WITH		310				
	2187 *		C(XT) = TBLOCK-ADDRESS	320				
	2188 *		C(XJ) = JBLOCK-ADDRESS	330				
	2189 *	ENTER BY		340				
	2190 *		TSX 0,%ACCT	350				
	2191 *		ARG MODE	360				
	2192 *		ARG ACODE	370				
	2193 *		ARG DATA	380				
	2194 *	RETURNS TO FIRST LOC AFTER THE MACRO EXPANSION		390				
	2195 *	RETURNS WITH		400				
	2196 *		C(XT) = TBLOCK-ADDRESS	410				
	2197 *		C(XJ) = JBLOCK-ADDRESS	420				
	2198 *		C(XL) = RESTART-ADDRESS	430				
	2199 *	USES LOCAL TEMPORARY ONLY		440				
	2200 *			450				
001017	005343	7400	00	2201 ACCT	STX	0,%ACCT	POINTER TO ARGUMENT LIST	460
001020	000003	0200	03	2202	ADLX	0,%DU	RESTART ADDRESS	470
		001021		2203	SETUP			480
001021	000510	7170	00		XED	\$SETUP		
001022	001017	2220	57	2204	LDX	2,%ACCT,%IDC	LOAD MODE	490
001023	005343	2350	57	2205	LDA	ACCT,%IDC	LOAD ACODE	500
001024	005343	2360	57	2206	LDQ	ACCT,%IDC	LOAD DATA	510
001025	000056	2200	03	2207	LDX	0,%RDME,%DU	LOAD MME NUMBER	520
		001026		2208	CKPT		CHECKPOINT	530
001026	000474	7170	00		XED	X\$CKPT		
001027	000000	0010	00	2209	MME		ACCOUNTING CALL	540
		001030		2210	EXIT			550
001030	003074	7100	00		TRA	\$EXIT		
				2211 *				560
	005343			2212	USE	STORE		570

ACCOUNTING MACRO

005343 000000 0000 20
001031

2213 ACCT ARG 0.*
2214 USE PREVIOUS
2215 ** DISK CHECK

POINTER TO ARGUMENT LIST

580
590
600

CHECK MACRO

001031	2217	USE	CODE	110
	2218	HEAD		120
	2219	*		130
	2220	*		140
	2221	*	CHECK	150
	2222	*		160
	2223	*	THIS MACRO IS USED TO CHECK THE RESULT OF A TRAPPING MME.	170
	2224	*	THE FIRST ARGUMENT IS THE ADDRESS TO WHICH TO TRANSFER IF THE	180
	2225	*	STATUS RETURN IS ZERO. THIS ARGUMENT MAY BE OMITTED, AND NO	190
	2226	*	TEST WILL BE ASSEMBLED. THE REMAINING ARGUMENTS COME IN PAIRS.	200
	2227	*	THE FIRST OF THE PAIR IS A BOOLEAN PATTERN AGAINST WHICH A	210
	2228	*	COMPARISON WILL BE MADE. THE SECOND IS THE TRANSFER ADDRESS IN	220
	2229	*	CASE OF A MATCH. CURRENTLY THERE MAY BE ONLY 8 SUCH PAIRS.	230
	2230	*		240
	2231	*		250
	2232	CHECK	MACRO ZEROSTATADD,BOOLPAT,XFERADD,BOOLPAT,XFERADD,ETC.	260
	2233	LXL	0, TSSRW1, T PICK UP LOGICAL STATUS	270
	2234	ANX	0, B\$STMK, DU ISOLATE STATUS	280
	2235	INE	'#1',,,	290
	2236	TZE	#1 ZERO STATUS TEST	300
	2237	INE	'#2',,,23	310
	2238	CMPX	0, #2, DU	320
	2239	TZE	#3 FIRST PAIR OF TESTS	330
	2240	INE	'#4',,,20	340
	2241	CMPX	0, #4, DU	350
	2242	TZE	#5 SECOND PAIR OF TESTS	360
	2243	INE	'#6',,,17	370
	2244	CMPX	0, #6, DU	380
	2245	TZE	#7 THIRD PAIR OF TESTS	390
	2246	INE	'#8',,,14	400
	2247	CMPX	0, #8, DU	410
	2248	TZE	#9 FOURTH PAIR OF TESTS	420
	2249	INE	'#10',,,11	430
	2250	CMPX	0, #10, DU	440
	2251	TZE	#11 FIFTH PAIR OF TESTS	450
	2252	INE	'#12',,,8	460
	2253	CMPX	0, #12, DU	470
	2254	TZE	#13 SIXTH PAIR OF TESTS	480
	2255	INE	'#14',,,5	490
	2256	CMPX	0, #14, DU	500
	2257	TZE	#15 SEVENTH PAIR OF TESTS	510
	2258	INE	'#16',,,2	520
	2259	CMPX	0, #16, DU	530
	2260	TZE	#17 EIGHTH PAIR OF TESTS	540
	2261	TRA	\$ERROR DIE ON UNEXPECTED RETURN	550
	2262	ENDM	CHECK	560
	2263	*\$*	DISK GO	570

QUEUE MANAGEMENT -- GENERAL INTRODUCTION

001031	2265	USE	CODE	110
	2266	HEAD	Q	120
	2267	*		130
	2268	*	QUEUE MANAGEMENT -- GENERAL INTRODUCTION	140
	2269	*		150
	2270	*	EACH QUEUE IN THE PROGRAM HAS A SIMILAR STRUCTURE. A QUEUE	160
	2271	*	CONSISTS OF A (POSSIBLY EMPTY) LINKED LIST OF BLOCK. THE	170
	2272	*	POINTERS POINT TO WORD 4 (Q\$OFFST) OF A BLOCK. THE LINK	180
	2273	*	POINTERS ARE STORED IN WORD 3 (Q\$LINK) OF A BLOCK. THE WORD AT	190
	2274	*	LOCATION Q\$FIRST POINTS TO Q\$OFFST OF THE FIRST BLOCK OF THE	200
	2275	*	QUEUE. THE LOCATION Q\$LAST POINTS TO Q\$OFFST OF THE LAST BLOCK	210
	2276	*	OF THE QUEUE. THE EMPTY QUEUE IS DENOTED BY THE WORD AT Q\$LAST	220
	2277	*	POINTING TO Q\$FIRST+1.	230
	2278	*		240
	2279	*		250
	2280	*	QUEUE	260
	2281	*		270
	2282	*	THIS GENERATES A QBLOCK. THIS STRUCTURE MUST AGREE	280
	2283	*	WITH THE STRUCTURE DEFINED FOR QUEUE MANAGEMENT.	290
	2284	*		300
	2285	QUEUE	MACRO QBLOCK-LOCATION-SYMBOL,ASCII-NAME	310
	2286	USE	Q\$TOR PUT ALL QUEUES CONTIGUOUS	320
	2287	EVEN	FOR XED	330
	2288	#1 BSS	0 NAME OF QUEUE	340
	2289	ARG	\$ERROR FIRST	350
	2290	ARG	Q\$#1+Q\$FIRST+1 LAST	360
	2291	STX	0,Q\$#1+Q\$LAST,DI *XADD	370
	2292	STX	0,Q\$#1+Q\$LAST	380
	2293	EAX	Q,Q\$#1 ENQ	390
	2294	TSX	L,Q\$ENQ	400
	2295	EAX	Q,Q\$#1 DEQ	410
	2296	TSX	L,Q\$DEQ	420
	2297	EAX	Q,Q\$#1 INV	430
	2298	TSX	L,Q\$INV	440
	2299	ARG	0 BUSY	450
	2300	DEC	0 MAX	460
	2301	DEC	0 AVAIL	470
	2302	DEC	0 SPARE1	480
	2303	DEC	0 SPARE2	490
	2304	UASCI	1,#1 ABBREVIATION	500
	2305	ENDM	QUEUE	510
	2306	*\$*	DISK ENQ	520

0 QUEUE MANAGEMENT -- END

		001031	2308	USE	CODE		110
			2309	HEAD	Q		120
			2310	*			130
			2311	*			140
			2312	*		END	150
			2313	*			160
			2314	*	END	SUSPENDS A TASK UNTIL THE SPECIFIED QUEUE CAN BE MADE	170
			2315	*		AVAILABLE.	180
			2316	*			190
			2317	END	MACRO	QADDRESS	200
			2318		XED	Q\$#1+Q\$XEND	210
			2319		ENDM	END	220
			2320	*			230
			2321	*	END	-- SUBROUTINE TO SERIALIZE RESOURCE USE	240
			2322	*			250
			2323	*	THIS	SUBPROGRAM RETURNS IMMEDIATELY IF THERE IS NO NEED	260
			2324	*	TO	QUEUE. IF NECESSARY TO DO SO, IT SUSPENDS EXECUTION OF	270
			2325	*	THE	CURRENT TASK UNTIL A WAKE-UP IS GENERATED BY A DEG FOR	280
			2326	*	THIS	QUEUE AND THE RESOURCE CAN BE ALLOCATED TO THIS TASK.	290
			2327	*			300
			2328	*	CALL	WITH	310
			2329	*		C(XT) = TBLOCK-ADDRESS	320
			2330	*		C(XJ) = JBLOCK-ADDRESS	330
			2331	*		C(XQ) = QBLOCK-ADDRESS	340
			2332	*	ENTER	BY	350
			2333	*		TSX L,Q\$END	360
			2334	*	CLOBBERS	ALL BUT C(XT), C(XJ), AND C(XL)	370
			2335	*	USES	NO LOCAL TEMPORARIES	380
			2336	*			390
			2337		INHIB	SAVE,ON	400
		001031	2338	END	BSS	0	410
			2339		AOS	AVAIL,Q	420
						ONE MORE BLOCK CURRENTLY ON QUEUE	
			2340		LDA	AVAIL,Q	430
			2341		CMPA	MAX,Q	440
			2342		TNC	**2	450
			2343		STA	MAX,Q	460
			2344		SZN	RUSY,Q	470
			2345		TNZ	END1	480
			2346		STX	T,BUSY,Q	490
			2347		INHIB	RESTORE	500
			2348		BUGA		510
					BUGBUG	SET	BUGBUG+1
		001041			LDA	BUGBUG,DU	
		525201			ORA	BUGBUG,DL	
			2349		BUGQ		520
					BUGBUG	SET	BUGBUG+1
		001043			LDQ	BUGBUG,DU	
		525202			ORQ	BUGBUG,DL	
			2350		BUGXR	(0,X,Y,Z,Q)	530

END OF BINARY CARD IOS00022
 001031 000014 0542 15
 001032 000014 2352 15
 001033 000013 1152 15
 001034 001036 6022 00
 001035 000013 7552 15
 001036 000012 2342 15
 001037 001053 6012 00
 001040 000012 7412 15

001041
 525201
 001041 525201 2350 03
 001042 525201 2750 07
 001043
 525202
 001043 525202 2360 03
 001044 525202 2760 07
 001045

Q

QUEUE MANAGEMENT -- END

		525203		BUGBUG SET	BUGBUG+1			
001045	525203	2200	03	LDX	0,BUGBUG,DU			
001046	525203	2220	03	LDX	X,BUGBUG,DU			
001047	525203	2230	03	LDX	Y,BUGBUG,DU			
001050	525203	2240	03	LDX	Z,BUGBUG,DU			
001051	525203	2250	03	LDX	Q,BUGBUG,DU			
001052	000000	7100	17	2351	TRA	0,L	RETURN TO CALLER	
001053	000004	7470	11	2352	ENQ1	L,T\$TRA,T	SAVE RESTART ADDRESS	
001054	000004	6200	11	2353	EAX	0,OFFST,T	STORE POINTER WITH OFFSET	
001055	777777	6000	00	2354	TZE	\$ERROR	***DBG	
001056	777777	6040	00	2355	TMI	\$ERROR	***DBG	
001057	000002	7170	15	2356	XED	XADD,Q	IN QUEUE LINKED LIST OF BLOCKS	
		001060		2357	EXIT		600	
END OF BINARY CARD	IOS00023							
001060	003074	7100	00		TRA	\$EXIT		
				2358	**	DISK	ENQF	610

Q			QUEUE MANAGEMENT -- ENQF		
	001061		2360	USE CODE	110
			2361	HEAD 0	120
			2362 *		130
			2363 *		140
			2364 *	ENQF	150
			2365 *		160
			2366 *	THIS SUBROUTINE PUTS A TRAP BLOCK ON THE FRONT OF A QUEUE.	170
			2367 *		180
			2368 *	CALL WITH	190
			2369 *	C(IXT) = TRAP-BLOCK-ADDRESS	200
			2370 *	C(IXQ) = QUEUE-ADDRESS	210
			2371 *	ENTER BY	220
			2372 *	TSX L,Q\$ENQF	230
			2373 *	CLOBBERS C(IXQ), C(IXX)	240
			2374 *		250
			2375	INHIB SAVE,ON	260
		001061	2376 ENQF	BSS 0	270
001061	000012	2342 15	2377	SZN BUSY,Q IS QUEUE BUSY?	280
001062	001067	6012 00	2378	INZ ENQF0 YES, GET ON FRONT OF IT	290
001063	000012	7412 15	2379	STX T,BUSY,Q NO, SEIZE THE RESOURCE	300
		001064	2380	BUGXR (0,X)	310
		525204	BUGBUG	SET BUGBUG+1	
001064	525204	2202 03		LDX 0,BUGBUG,DU	
001065	525204	2222 03		LDX X,BUGBUG,DU	
001066	000000	7102 17	2381	TRA 0,L AND RETURN IMMEDIATELY	320
			2382 *		330
			2383 *	CHECK IF QUEUE IS EMPTY	340
			2384 *		350
		001067	2385 ENQF0	BSS 0	360
001067	000004	7472 11	2386	STX L,T\$TRA,T SAVE RESTART ADDRESS	370
001070	000004	6202 11	2387	EAX 0,Q\$OFFST,T GET ADDRESS OF FOURTH WORD OF BLOCK	380
001071	000001	6222 15	2388	EAX X,Q\$FIRST+1,Q IS THE QUEUE	390
001072	000001	1022 15	2389	CMPX X,Q\$LAST,Q EMPTY?	400
001073	001100	6002 00	2390	TZE ENQF2 YES, MUST HANDLE AS SPECIAL CASE	410
			2391 *		420
			2392 *	GET ON FRONT OF NON-EMPTY QUEUE	430
			2393 *		440
		001074	2394 ENQF1	BSS 0	450
001074	000000	2222 15	2395	LDX X,Q\$FIRST,Q POINT TO PRESENT FIRST BLOCK ON QUEUE	460
001075	000000	7402 15	2396	STX 0,Q\$FIRST,Q PLACE THIS BLOCK FIRST	470
001076	000003	7422 11	2397	STX X,Q\$LINK,T AND MOVE OLD ONE DOWN	480
		001077	2398	EXIT	490
001077	003074	7102 00		TRA \$EXIT	
			2399 *		500
			2400 *	GET ON FRONT OF EMPTY QUEUE BY MAKING Q\$FIRST AND Q\$LAST	510
			2401 *	POINT TO THIS BLOCK.	520
			2402 *		530
		001100	2403 ENQF2	BSS 0	540
001100	000000	7402 15	2404	STX 0,Q\$FIRST,Q MAKE IT THE FIRST BLOCK	550
001101	000001	7402 15	2405	STX 0,Q\$LAST,Q AND ALSO THE LAST	560

G

QUEUE MANAGEMENT -- ENQF

001102 003074 7102 00

2406	EXIT	EXIT	570
	TRA	\$EXIT	
2407	INHIB	RESTORE	580
2408 ***	DISK	DEQ	590

Q

QUEUE MANAGEMENT -- DEQ

		001103	2410	USE	CODE		110
			2411	HEAD	Q		120
			2412	*			130
			2413	*			140
			2414	*	DEQ		150
			2415	*	DEQ RELEASES A SERIALY REUSABLE RESOURCE SO THAT OTHER		160
			2416	*	PROCESSES MAY USE IT. OTHER TASKS ARE AUTOMATICALLY INITIATED		170
			2417	*	BY PUTTING THEM ON THE Q\$TASK QUEUE.		180
			2418	*			190
			2419	DEQ	MACRO QADDRESS		200
			2420	XED	Q\$#1+Q\$XDEQ		210
			2421	ENDM	DEQ		220
			2422	*			230
			2423	*	DEQ -- SUBROUTINE TO WAKE-UP WAITING TASKS		240
			2424	*			250
			2425	*	THIS SUBROUTINE CHECKS TO SEE IF ANY OTHER TASKS ARE ASLEEP		260
			2426	*	IN THE QUEUE. IF THE QUEUE IS NOT EMPTY, A TBLOCK IS TAKEN		270
			2427	*	FROM IT AND PUT ON THE Q\$TASK QUEUE TO WAKE IT UP. THIS SUB-		280
			2428	*	PROGRAM ALWAYS RETURNS TO THE CALLER IMMEDIATELY.		290
			2429	*			300
			2430	*	CALL WITH		310
			2431	*	C(X0) = QBLOCK-ADDRESS		320
			2432	*	C(XT) = TBLOCK-ADDRESS		330
			2433	*	C(XJ) = JBLOCK-ADDRESS		340
			2434	*	ENTER BY		350
			2435	*	TSX L,Q\$DEQ		360
			2436	*	DESTROYS C(X0), C(XX)		370
			2437	*	USES NO LOCAL TEMPORARIES		380
			2438	*			390
			2439		INHIB SAVE,ON		400
		001103	2440	DEQ	BSS 0		410
		001103	2441	DECRM	(AVAIL,Q) ONE LESS BLOCK ON QUEUE		420
				LCQ	1,DL		
				ASQ	AVAIL,Q		
			2442	SZN	BUSY,Q	IF QUEUE IS NOT BUSY	430
			2443	TZE	\$ERROR	WE ARE IN BAD TROUBLE	440
			2444	STZ	BUSY,Q	MARK QUEUE NOT BUSY	450
			2445	EAX	0,FIRST+1,Q	ADDRESS OF FIRST ELEMENT	460
			2446	CMPX	0,LAST,Q	DOES LAST POINT TO IT?	470
			2447	TZE	0,L	YES, QUEUE EMPTY -- RETURN	480
			2448	LDX	0,FIRST,Q	GET OFFSET POINTER TO BLOCK	490
			2449	TZE	\$ERROR	***BLEWIT	500
			2450	TMI	\$ERROR	***DBG	510
			2451	EAX	X,-OFFST,Q	RELATE TO THE BEGINNING OF BLOCK	520
			2452	STX	0,BUSY,Q	REMEMBER WHO IS RESPONSIBLE	530
			2453	LDX	X,LINK,X	GET NEXT ELEMENT ON QUEUE	540
			2454	STX	X,FIRST,Q	NOW MAKE IT FIRST	550
			2455	XED	XADD,TASK	ADD TO TASK QUEUE	560
			2456	CMPX	0,LAST,Q	LAST BLOCK ON QUEUE?	570

001103 000001 3362 07
 END OF BINARY CARD 10S00024
 001104 000014 0562 15
 001105 000012 2342 15
 001106 777777 6002 00
 001107 000012 4502 15
 001110 000001 6202 15
 001111 000001 1002 15
 001112 000000 6002 17
 001113 000000 2202 15
 001114 777777 60C2 00
 001115 777777 6042 00
 001116 777774 6222 10
 001117 000012 7402 15
 001120 000003 2222 12
 001121 000000 7422 15
 001122 005162 7172 00
 001123 000001 1002 15

	Q		QUEUE MANAGEMENT -- DEQ					
001124	000000	6012	17	2457	TNZ	0,L	NO, RETURN	580
001125	000001	6202	15	2458	EAX	0,FIRST+1,Q	YES, SET UP QUEUE TO APPEAR EMPTY	590
001126	000001	7402	15	2459	STX	0,LAST,Q	BY MAKING LAST POINT TO FIRST+1	600
				2460	INHIB	RESTORE		610
001127	000000	7100	17	2461	TRA	0,L	RETURN	620
				2462	***	DISK	INVERT	630

Q QUEUE MANAGEMENT -- INV

		001130	2464	USE	CODE		110
			2465	HEAD	Q		120
			2466 *				130
			2467 *				140
			2468 *		INV		150
			2469 *				160
			2470 *	THIS ROUTINE INVERTS THE ORDER OF THE TOP TWO ITEMS ON			170
			2471 *	THE SPECIFIED QUEUE.			180
			2472 *				190
			2473 INV	MACRO	QADDRESS		200
			2474	XED	Q\$#1+Q\$XINV		210
			2475	ENDM	INV		220
			2476 *				230
			2477 *	INV -- SUBROUTINE TO SWAP THE TOP TWO ITEMS ON A LIST.			240
			2478 *				250
			2479 *	THIS SUBROUTINE TRIES TO SWITCH THE ORDER OF THE TOP TWO ITEMS			260
			2480 *	ON THE NAMED QUEUE. IF THE NAMED QUEUE IS EMPTY OF HAS ONLY			270
			2481 *	A SINGLE ITEM, NO ACTION IS TAKEN; OTHERWISE THE TWO ARE			280
			2482 *	ARE INVERTED.			290
			2483 *				300
			2484 *	CALL WITH			310
			2485 *	C(XT) = TCB			320
			2486 *	C(XJ) = JCB			330
			2487 *	C(XQ) = QUEUE-ADDRESS			340
			2488 *	ENTER BY			350
			2489 *	TSX L,Q\$INV			360
			2490 *	CLOBBERS C(XQ), C(XX), C(XY)			370
			2491 *				380
			2492	INHIB	SAVE,ON	PREPARE TO FOOL WITH QUEUES	390
		001130	2493 INV	BSS	Q		400
			2494 *				410
			2495 *	CHECK IF QUEUE IS EMPTY			420
			2496 *				430
			2497	EAX	X,FIRST+1,Q	DOES FIRST POINT TO LAST?	440
001130	000001	6222 15					
END	OF BINARY CARD	IOS00025					
001131	000001	1022 15	2498	CMPX	X,LAST,Q	EMPTY?	450
001132	001146	6002 00	2499	TZE	INV1	IF SO, EXIT	460
			2500 *				470
			2501 *	CHECK TO SEE IF AT LEAST TWO ITEMS			480
			2502 *				490
001133	000000	2222 15	2503	LDX	X,FIRST,Q	DOES FIRST	500
001134	000001	1022 15	2504	CMPX	X,LAST,Q	POINT TO LAST?	510
001135	001146	6002 00	2505	TZE	INV1	IF SO, EXIT	520
			2506 *				530
			2507 *	SWAP THE TOP TWO			540
			2508 *				550
001136	777777	2232 12	2509	LDX	Y,-OFFST+LINK,X	*C(X) = TOP	560
			2510			C(Y) = SECOND	570
001137	777777	2202 13	2511	LDX	Q,-OFFST+LINK,Y	*SAVE PTR TO THIRD	580
001140	000000	7432 15	2512	STX	Y,FIRST,Q	MAKE SECOND FIRST	590

G		QUEUE MANAGEMENT -- INV					
001141	777777	7422	13	2513	STX	X,-OFFST+LINK,Y *MAKE FIRST SECOND	600
001142	777777	7402	12	2514	STX	0,-OFFST+LINK,X *MAKE SECOND POINT TO THIRD	610
001143	000001	1032	15	2515	CMPX	Y, LAST,Q WAS SECOND REALLY LAST?	620
001144	001146	6012	00	2516	TNZ	*+2 NO	630
001145	000001	7422	15	2517	STX	X, LAST,Q YES, SWITCH IT	640
				2518	*		650
				2519	*	DONE, BUG REGISTERS AND RETURN	660
				2520	*		670
		001146		2521	INV1	BSS 0	680
		001146		2522		BUGXR (0,X,Y)	690
		525205			BUGBUG	SET BUGBUG+1	
001146	525205	2202	03		LDX	0,BUGBUG,DU	
001147	525205	2222	03		LDX	X,BUGBUG,DU	
001150	525205	2232	03		LDX	Y,BUGBUG,DU	
001151	000000	7102	17	2523	TRA	0,L RETURN TO CALLER	700
				2524	INHIB	RESTORE	710
				2525	*S*	DISK BRANCH	720

Q

QUEUE MANAGEMENT -- BRANCH MACRO

001152	2527	USE	CODE	110
	2528	HEAD	Q	120
	2529	*		130
	2530	*		140
	2531	*	BRANCH MACRO	150
	2532	*		160
	2533	*	THIS MACRO CREATES AN ASYNCHRONOUS TASK TO BE PERFORMED AT A	170
	2534	*	LATER TIME. IT CAN GIVE THE CREATED TASK THE CURRENT TBLOCK	180
	2535	*	OR GIVE IT A NEW TBLOCK. EITHER WAY FOUR PARAMETERS MAY BE	190
	2536	*	BETWEEN THE TWO TBLOCKS.	200
	2537	*		210
	2538	*	PAST INFORMATION IS PLACED IN TEMP1 THRU TEMP4 OF	220
	2539	*	THE TASK PLACED ON THE Q\$TASK QUEUE.	230
	2540	*		240
	2541	*	CALLS	250
	2542	*	T\$GETT	260
	2543	*	CLOBBERS C(XX), C(X0)	270
	2544	*		280
	2545	*		290
	2546	BRANCH MACRO	PASS,XFER ADD,C(TEMP1),C(TEMP2),C(TEMP3),C(TEMP4)	300
	2547	TSX	0,T\$GETT GET A NEW TBLOCK	310
	2548	EAX	X,0,T C(XX) POINTS TO NEW TBLOCK	320
	2549	LDX	T,T\$LINK,X C(XT) POINTS TO OLD TBLOCK	330
	2550	INE	'#3',',',2	340
	2551	LDQ	#3 SAVE FIRST PARAMETER	350
	2552	STQ	T\$TEMP1,X	360
	2553	INE	'#4',',',2	370
	2554	LDQ	#4 SAVE SECOND PARAMETER	380
	2555	STQ	T\$TEMP2,X	390
	2556	INE	'#5',',',2	400
	2557	LDQ	#5 SAVE THIRD PARAMETER	410
	2558	STQ	T\$TEMP3,X	420
	2559	INE	'#6',',',2	430
	2560	LDQ	#6 SAVE FOURTH PARAMETER	440
	2561	STQ	T\$TEMP4,X	450
	2562	INE	'#1',',PASS',1 T IS THE BLOCK THAT IS PASSED	460
	2563	EAX	T,0,X PASS NEW BLOCK	470
	2564	EAX	0,#2 POINT TO TRANSFER ADDRESS	480
	2565	STX	0,T\$TRA,T INTO QUEUE BLOCK	490
	2566	EAX	0,Q\$OFFST,T PREPARE TO QUEUE	500
	2567	XED	Q\$XADD+Q\$TASK GET ON THE TASK QUEUE	510
	2568	IFE	'#1',',PASS',1 WHICH BLOCK TO WE GIVE BACK AS CURRENT	520
	2569	EAX	T,0,X GIVE BACK NEW BLOCK	530
	2570	INE	'#1',',PASS',1	540
	2571	LDX	T,T\$LINK,X NO, GIVE BACK OLD BLOCK	550
	2572	BUGXR	(0,X)	560
	2573	ENDM	BRANCH	570
	2574	*\$*	DISK OP	580

Q

QUEUES -- Q\$OP00 QUEUE

2576	HEAD	Q	110
2577 *			120
2578 *			130
2579 *		Q\$OP00 QUEUE	140
2580 *			150
2581 *	SINCE THE LOGGING DEVICE IS A SINGLE RESOURCE, TASKS MUST		160
2582 *	QUEUE FOR IT. HENCE THIS QUEUE HOLDS THOSE TASKS WAITING		170
2583 *	TO LOG A MESSAGE TO THE OPERATOR'S CONSOLE TTY.		180
2584 *			190
2585	QUEUE	OP00	200

001152
005140
005140
005140

005140 777777 0000 00
005141 005141 0000 00
005142 005141 7400 54
005143 005141 7400 00
END OF BINARY CARD IOS00026
005144 005140 6250 00
005145 001031 7070 00
005146 005140 6250 00
005147 001103 7070 00
005150 005140 6250 00
005151 001130 7070 00
005152 000000 0000 00
005153 000000000000
005154 000000000000
005155 000000000000
005156 000000000000
005157 117120060060

USE	Q\$TOR
EVEN	
BSS	0
ARG	\$ERROR
ARG	Q\$OP00+Q\$FIRST+1
STX	0,Q\$OP00+Q\$LAST,DI
STX	0,Q\$OP00+Q\$LAST
EAX	0,Q\$OP00
TSX	L,Q\$ENG
EAX	0,Q\$OP00
TSX	L,Q\$DE0
EAX	0,Q\$OP00
TSX	L,Q\$INV
ARG	0
DEC	0
DEC	0
DEC	0
DEC	0
UASCI	1,OP00
EQU	OP00
DISK	Q1

ABBREVIATION
***ASSUME ONLY ONE OP CONSOLE EVER; (REASONABLE)
220

Q

QUEUES -- Q\$TASK QUEUE

2589 HEAD Q 110
 2590 * 120
 2591 * 130
 2592 * Q\$TASK QUEUE 140
 2593 * 150
 2594 * THIS QUEUE IS USED TO SCHEDULE THE ACTIVITY OF THE PROCESSOR 160
 2595 * ENTRIES ARE MADE BY THE USE OF THE NORMAL XED Q\$ADD 170
 2596 * FEATURE AND ALSO BY AN XED CHAIN INSTIGATED BY THE ACTION OF 180
 2597 * A TRAP BEING SPRUNG BY THE EXECUTIVE. ENTRIES ARE REMOVED BY 190
 2598 * A SPECIAL PROGRAM MODULE WHICH IS CALLED BY THE EXIT MACRO 200
 2599 * WHENEVER THE PROCESSOR IS FREE TO WORK ON A NEW TASK. 210
 2600 * 220
 2601 230

005160
 005160
 005160
 005160
 005160 777777 0000 00
 005161 005161 0000 00
 005162 005161 7400 54
 005163 005161 7400 00
 005164 005160 6250 00
 005165 001031 7070 00
 005166 005160 6250 00
 005167 001103 7070 00
 005170 005160 6250 00
 END OF BINARY CARD IOS00027
 005171 001130 7070 00
 005172 000000 0000 00
 005173 000000000000
 005174 000000000000
 005175 000000000000
 005176 000000000000
 005177 124101123113

QUEUE TASK
 USE Q\$TOR
 EVEN
 TASK BSS 0
 ARG \$ERROR
 ARG Q\$TASK+Q\$FIRST+1
 STX 0,Q\$TASK+Q\$LAST,DI
 STX 0,Q\$TASK+Q\$LAST
 EAX 0,Q\$TASK
 TSX L,Q\$END
 EAX 0,Q\$TASK
 TSX L,Q\$DEQ
 EAX 0,Q\$TASK
 TSX L,Q\$INV
 ARG 0
 DEC 0
 DEC 0
 DEC 0
 DEC 0
 UASCI 1,TASK ABBREVIATION

0		QUEUES -- Q\$CORE QUEUE		
		2603	HEAD Q	250
		2604 *		260
		2605 *		270
		2606 *	Q\$CORE QUEUE	280
		2607 *		290
		2608 *	THIS QUEUE HOLDS THOSE TASKS WHICH REQUIRE MEMORY ALLOCATIONS	300
		2609 *	WHEN A PRIOR MEMORY REQUEST IS IN OPERATION. THIS IS NOT	310
		2610 *	TO BE CONFUSED WITH THE THE FREE MEMORY LIST.	320
		2611 *		330
	005200	2612	QUEUE CORE	340
	005200		USE Q\$TOR	
	005200		EVEN	
	005200	CORE	BSS 0	
005200	777777 0000 00		ARG \$ERROR	
005201	005201 0000 00		ARG Q\$CORE+Q\$FIRST+1	
005202	005201 7400 54		STX 0,Q\$CORE+Q\$LAST,DI	
005203	005201 7400 00		STX 0,Q\$CORE+Q\$LAST	
005204	005200 6250 00		EAX Q,Q\$CORE	
005205	001031 7070 00		TSX L,Q\$ENQ	
005206	005200 6250 00		EAX Q,Q\$CORE	
005207	001103 7070 00		TSX L,Q\$DEQ	
005210	005200 6250 00		EAX Q,Q\$CORE	
005211	001130 7070 00		TSX L,Q\$INV	
005212	000000 0000 00		ARG 0	
005213	000000000000		DEC 0	
005214	000000000000		DEC 0	
005215	000000000000		DEC 0	
END OF BINARY CARD	IOS00028			
005216	000000000000		DEC 0	
005217	103117122105	UASCI	1,CORE ABBREVIATION	

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Q
QUEUES -- Q$INP1 QUEUE

2614 HEAD Q 360
2615 * 370
2616 * 380
2617 * Q$INP1 QUEUE 390
2618 * 400
2619 * THIS IS THE GENERAL JOB INPUT QUEUE. JOBS REQUESTING ONE 410
2620 * OR MORE RESOURCES ARE PLACED ON THIS LIST. NOTE THAT THIS IS A 420
2621 * LIST AS OPPOSED TO A TRUE QUEUE. 430
2622 * 440
005220 2623 QUEUE INP1 450
005220 USE Q$TOR
005220 EVEN
005220 INP1 BSS 0
005220 777777 0000 00 ARG $ERROR
005221 005221 0000 00 ARG Q$INP1+Q$FIRST+1
005222 005221 7400 54 STX 0,Q$INP1+Q$LAST,DI
005223 005221 7400 00 STX 0,Q$INP1+Q$LAST
005224 005220 6250 00 EAX Q,Q$INP1
005225 001031 7070 00 TSX L,Q$ENQ
005226 005220 6250 00 EAX Q,Q$INP1
005227 001103 7070 00 TSX L,Q$DEQ
005230 005220 6250 00 EAX Q,Q$INP1
005231 001130 7070 00 TSX L,Q$INV
005232 000000 0000 00 ARG 0
005233 000000000000 DEC 0
005234 000000000000 DEC 0
005235 000000000000 DEC 0
005236 000000000000 DEC 0
005237 111116120061 UASCI 1,INP1 ABBREVIATION

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Q

QUEUES -- Q\$INP2 QUEUE

2625	HEAD	Q	470
2626	*		480
2627	*		490
2628	*	Q\$INP2 QUEUE	500
2629	*		510
2630	*	THIS IS A SPECIAL LIST (REALLY A LIST, NOT A QUEUE). HERE GOES	520
2631	*	ALL SHORT PRINTER JOBS. A SHORT PRINTER JOB IS ONE THAT REQUIRES	530
2632	*	AN ARBITRARY NUMBER OF PAGES TO BE PRINTED OR LESS.	540
2633	*		550
2634			560

005240
005240
005240
005240

INP2

005240 777777 0000 00
005241 005241 0000 00
END OF BINARY CARD ICS00029
005242 005241 7400 54
005243 005241 7400 00
005244 005240 6250 00
005245 001031 7070 00
005246 005240 6250 00
005247 001103 7070 00
005250 005240 6250 00
005251 001130 7070 00
005252 000000 0000 00
005253 000000000000
005254 000000000000
005255 000000000000
005256 000000000000
005257 111116120062

QUEUE INP2
USE Q\$TOR
EVEN
BSS 0
ARG \$ERROR
ARG Q\$INP2+Q\$FIRST+1
STX 0,Q\$INP2+Q\$LAST,DI
STX 0,Q\$INP2+Q\$LAST
EAX Q,Q\$INP2
TSX L,Q\$ENQ
EAX Q,Q\$INP2
TSX L,Q\$DEQ
EAX Q,Q\$INP2
TSX L,Q\$INV
ARG 0
DEC 0
DEC 0
DEC 0
UASCI 1,INP2

ABBREVIATION

Q

QUEUES -- Q\$INP3 QUEUE

2636	HEAD	Q	580
2637 *			590
2638 *			600
2639 *		Q\$INP3 QUEUE	610
2640 *			620
2641 *	THIS IS A SPECIAL LIST (REALLY, A LIST; NOT A QUEUE). HERE		630
2642 *	GOES ALL MEDIUM SIZE PRINTER JOBS. A MEDIUM PRINTER JOB IS ONE		640
2643 *	THAT REQUIRES AN ARBITRARY NUMBER OF PAGES TO BE PRINTED WHICH		650
2644 *	IS BETWEEN TWO LIMITS (SMALL AND LARGE OF COURSE).		660
2645 *			670
2646	QUEUE	INP3	680

005260
005260
005260
005260

INP3

005260 777777 0000 00
005261 005261 0000 00
005262 005261 7400 54
005263 005261 7400 00
005264 005260 6250 00
005265 001031 7070 00
005266 005260 6250 00
END OF BINARY CARD 10S00030
005267 001103 7070 00
005270 005260 6250 00
005271 001130 7070 00
005272 000000 0000 00
005273 000000000000
005274 000000000000
005275 000000000000
005276 000000000000
005277 111116120063

USE Q\$TOR
EVEN
BSS 0
ARG \$ERROR
ARG Q\$INP3+Q\$FIRST+1
STX 0,Q\$INP3+Q\$LAST,DI
STX 0,Q\$INP3+Q\$LAST
EAX 0,Q\$INP3
TSX L,Q\$ENG
EAX 0,Q\$INP3
TSX L,Q\$DEQ
EAX Q,Q\$INP3
TSX L,Q\$INV
ARG 0
DEC 0
DEC 0
DEC 0
DEC 0
UASCI 1,INP3
DISK GETC

ABBREVIATION

G

CORE MANAGEMENT -- GENERAL INTRODUCTION

		001152	2649	USE	CODE		110	
			2650	HEAD	R		120	
			2651	*			130	
			2652	*		GENERAL INTRODUCTION	140	
			2653	*			150	
			2654	*		BELOW IS THE FREE MEMORY LIST. THE LIST CONSISTS OF A	160	
			2655	*		POSSIBLY EMPTY LINKED LIST OF BLOCKS. THE FORWARD/BACKWARD	170	
			2656	*		POINTERS OF A BLOCK POINT TO THE FIRST WORD OF THE SUCCEEDING/	180	
			2657	*		PRECEEDING BLOCKS, RESPECTIVELY. THE LINK POINTERS ARE	190	
			2658	*		STORED IN WORDS 0 AND 1, RESPECTIVELY, OF THE BLOCK.	200	
			2659	*		HENCE THE MINIMAL SIZE OF A BLOCK IS TWO (2) WORDS. THE	210	
			2660	*		TOTAL LENGTH OF THE BLOCK IS ALSO KEPT IN WORD 0. BY	220	
			2661	*		DESIGN CONVENTIONS, THE POINTERS ARE UPPER HALF QUANTITIES	230	
			2662	*		AND THE LENGTH IS A LOWER HALF QUANTITY. THE EMPTY LIST	240	
			2663	*		IS DENOTED BY THE FORWARD LINK OF THE 'FIRST' POINTING TO	250	
			2664	*		'LAST' AND BY THE BACKWARD LINK OF 'LAST' POINTING TO 'FIRST'.	260	
			2665	*			270	
			2666	*			280	
			2667	*		NOTE THAT ALL ALLOCATIONS ARE DONE IN MULTIPLES OF EIGHT.	290	
			2668	*		A BLOCK MUST BE DE-ALLOCATED AS ONE ENTITY. NO PARTIAL RELEASES	300	
			2669	*		ARE ALLOWED.	310	
			2670	*			320	
			2671	*		FREE MEMORY LIST	330	
			2672	*			340	
		005344	2673	USE	STORE		350	
005344	006400	000000	2674	FIRST	ZERO	\$NEXTF,0	FORWARD LINK/ LENGTH OF BLOCK	360
005345	000000	000000	2675		ZERO	0,	BACKWARD LINK/ <NOT USED>	370
			2676	*			380	
			2677	*			390	
005346	000000	000000	2678	LAST	ZERO	0,0	FORWARD LINK/ LENGTH OF BLOCK	400
005347	006400	000000	2679		ZERO	\$NEXTB,	BACKWARD LINK/ <NOT USED>	410
		001152	2680	USE		PREVIOUS	420	

R

CORE MANAGEMENT -- MACROS

001152	2682	USE	CODE	440
	2683	HEAD	R	450
	2684 *			460
	2685 *			470
	2686 *		MACROS	480
	2687 *			500
	2688 *			510
	2689 *	THE FOLLOWING MACROS ARE USED TO ALLOCATE/DEALLOCATE BLOCKS		520
	2690 *	OF CORE. ONLY INDEX REGISTER J IS GUARANTEED ACROSS THESE		530
	2691 *	CALLS.		540
	2692 *			550
	2693 *		GETC MACRO	560
	2694 *			570
	2695 *	CALL WITH		580
	2696 *		C(AL) = NUMBER-OF-WORDS-REQUESTED	590
	2697 *	ENTER BY		600
	2698 *		TSX L,R\$GETC	610
	2699 *	RETURNS TO	D,L	620
	2700 *	RETURNS WITH		630
	2701 *		C(AU) = BUFFER-ADDRESS	640
	2702 *		C(AL) = BUFFER-LENGTH	650
	2703 *			660
	2704	GETC	MACRO WORD-COUNT-ADDRESS/ 'A'	670
	2705		INE '#1','A'	680
	2706		LDA #1	690
	2707		TSX L,R\$GETC	700
	2708		ENDM GETC	710
	2709 *			720
	2710 *			730
	2711 *		RELC MACRO	740
	2712 *			750
	2713 *	CALL WITH		760
	2714 *		C(AU) = BUFFER-ADDRESS	770
	2715 *		C(AL) = BUFFER-ADDRESS	780
	2716 *	ENTER BY		790
	2717 *		TSX L,R\$RELC	800
	2718 *	RETURNS TO	D,L	810
	2719 *	RETURNS WITH		820
	2720 *		DESTROYS C(A)	830
	2721 *			840
	2722	RELC	MACRO RELEASE-ADDRESS&COUNT/ 'A'	850
	2723		INE '#1','A'	860
	2724		LDA #1	870
	2725		TSX L,R\$RELC	880
	2726		ENDM RELC	890
	2727	**	DISK GETC1	920

R CORE MANAGEMENT -- ALLOCATION

		001152	2729	USE	CODE		110	
			2730	HEAD	R		120	
			2731 *				130	
			2732 *				140	
			2733 *			ALLOCATION	150	
			2734 *				160	
			2735 *			THIS SUBROUTINE REMOVES A BLOCK OF N WORDS FROM THE FREE	170	
			2736 *			MEMORY LIST. IF THERE IS NO BLOCK OF N WORDS OR GREATER	180	
			2737 *			ON THE FREE LIST, THERE A REQUEST FOR MORE MEMORY IS MADE.	190	
			2738 *			AND THE PROCESS IS REPEATED. SINCE MEMORY REQUESTS ARE	200	
			2739 *			OF THE TRAPPING MME BRAND, IT IS NECESSARY TO FIRST	210	
			2740 *			CHECK TO SEE IF A MEMORY REQUEST IS CURRENTLY IN OPERATION.	220	
			2741 *			IF SO, THEN THIS REQUEST IS PUT TO SLEEP BY QUEUEING IT	230	
			2742 *			ON THE QSCORE QUEUE. WHEN THE MEMORY REQUEST COMPLETES	240	
			2743 *			THE NEXT ITEM (IF ANY) ON QSCORE IS AWAKENED. CORE ALLO-	250	
			2744 *			CATION IS ON A FIRST FIT BASIS. THIS IS TO ALLOW HOLES	260	
			2745 *			TO FLOW TOWARD HIGHER MEMORY.	270	
			2746 *				280	
			2747 *	CALL BY			290	
			2748 *		TSX L,R\$GETC		300	
			2749 *	CALL WITH			310	
			2750 *		C(AL) = NUMBER-OF-WORDS-REQUESTED		320	
			2751 *		C(T) = TRAP BLOCK		330	
			2752 *	CALLS			340	
			2753 *		ENQ CORE		350	
			2754 *		R\$MORE		360	
			2755 *		R\$RELC		370	
			2756 *	DEQ	CORE		380	
			2757 *	RETURNS WITH			390	
			2758 *		C(AU) = ADDRESS		400	
			2759 *		C(AL) = NUMBER OF WORDS		410	
			2760 *	EXIT TO 0,L			420	
			2761 *	PRESERVES ALL REGISTERS EXCEPT C(A)			430	
			2762 *	USES TEMPORARIES T\$TEM9 THRU T\$TEM16			440	
			2763 *				450	
		001152	2764	USE	CODE		460	
		001152	2765	GETC	BSS	0	470	
001152	000010	7530	11	2766	SREG	T\$TEM16,T	SAVE ALL REGISTERS	480
		001153		2767	ENQ	CORE	AND GET ON THE CORE QUEUE	490
001153	005204	7170	00		XED	QSCORE+Q\$XENQ		
		001154		2768	GETC1	BSS	0	500
001154	000014	2350	11	2769	LDA	T\$TEM12,T	RESTORE A AND	510
001155	000007	0350	07	2770	ADLA	7,DL	ROUND UP LENGTH	520
001156	777770	3750	07	2771	ANA	=0777770,DL	TO A MULTIPLE OF 8	530
001157	777777	6000	00	2772	TZE	\$ERROR	***BLEWIT	540
001160	004001	1150	07	2773	CMPA	\$RQMAX,DL	HOW MUCH IS REQUESTED	550
END OF BINARY CARD IOS00031								
001161	777777	6030	00	2774	TRC	\$ERROR	TOO MUCH	560
001162	000014	7550	11	2775	STA	T\$TEM12,T	SAVE NEW LENGTH	570
001163	000000	6350	05	2776	EAA	0,AL	MOVE LENGTH TO AU	580

R CORE MANAGEMENT -- ALLOCATION

001164	000016	7550	11	2777	STA	T\$TEM10,T	SAVE FOR INDEX REGISTER OPERATIONS	590
			001165	2778	GETC3	BSS	0	600
001165	000014	2350	11	2779	LDA	T\$TEM12,T	GET BACK REQUEST	610
001166	005344	2240	00	2780	LIX	Z,FIRST	GET PTR TO FIRST FREE BLOCK	620
			001167	2781	GETC6	BSS	0	630
001167	000302	5002	00	2782	RPL	,TNC,T7E	RUN DOWN LINKED LIST	640
001170	000000	1150	14	2783	CMPA	0,Z	FOR A BLOCK BIG ENOUGH	650
001171	001176	6000	00	2784	TZE	GETC7	FOUND A BLOCK THAT IS 'JUST RIGHT'	660
001172	001176	6020	00	2785	TNC	GETC7	MORE THAT BIG ENOUGH	670
001173	000000	2240	14	2786	LIX	Z,0,Z	GET BACK PTR OF WHERE WE LEFT OFF	680
001174	001167	6010	00	2787	TNZ	GETC6	IF NOT AT END OF LIST, KEEP SEARCHING	690
001175	001241	7100	00	2788	TRA	MORE	WON'T FIT ANYWHERE, GET MORE CORE	700
001176	000014	7440	11	2789	GETC7	STX	Z,T\$TEM12,T	SAVE PTR TO BLOCK TO RETURN TO CALLER
				2790	*			720
				2791	*	Z	POINTS TO BLOCK TO DELINK	730
				2792	*			740
001177	000001	2230	14	2793	LIX	Y,LINKR,Z	ASSIGN Y TO PREDECESSOR	750
001200	000000	2220	14	2794	LIX	X,LINKF,Z	AND X TO SUCCESSORE	760
001201	000000	7200	14	2795	LxL	0,LEN,Z	GET THE LENGTH OF THIS BLOCK	770
001202	000016	1200	11	2796	SBLX	0,T\$TEM10,T	MINUS THE AMOUNT REQUIRED	780
001203	001212	6000	00	2797	T7E	GETC4	AH HA, BLOCK JUST FITS	790
001204	777777	6040	00	2798	TMI	SERROR	***BLEWIT	800
001205	000016	0240	11	2799	ADLX	Z,T\$TEM10,T	SET Z TO START OF EXCESS OF BLOCK	810
001206	000000	4400	14	2800	SXL	0,LEN,Z	SET THE LENGTH OF THE REMAINING BLOCK	820
END OF BINARY CARD	INS00032							
001207	000000	7420	14	2801	STX	X,LINKF,Z	SET FORWARD LINK OF REMAINING BLOCK	830
001210	000001	7440	12	2802	STX	Z,LINKR,X	" BACKWARD " " " " "	840
001211	000000	6220	14	2803	EAX	X,0,Z	COPY Z INTO X FOR FUDGE	850
001212	000000	7420	13	2804	GETC4	STX	X,LINKF,Y	SET FORWARD LINK OF PREDECESSOR
001213	000001	4500	12	2805	STZ	LINKR,X	***DBG	870
001214	000001	7430	12	2806	STX	Y,LINKR,X	SET BACKWARD LINK OF SELF	880
			001215	2807	DEQ	CORE	SEE IF ANYONE ELSE WANTS CORE	890
001215	005206	7170	00		XED	0\$CORE+0\$XDEQ		
				2808	*			900
				2809	*	ZERO	BLOCK FOR USER	910
				2810	*			920
001216	000014	2350	11	2811	LDA	T\$TEM12,T	GET LENGTH	930
001217	777777	3750	07	2812	ANA	-1,DL	MASK TO COUNT	940
001220	777777	6240	05	2813	EAX	Z,-1,AL	SAVE COUNT MINUS ONE IN Z	950
001221	000010	7350	00	2814	ALS	10-2	PUT COUNT IN REPEAT FIELD	960
001222	001400	6200	05	2815	EAX	0,\$\$ABIT+\$\$\$BIT,AL	TERMINATE CONDITIONS AND COUNT	970
001223	000000	4310	07	2816	FLD	0,DL	CLEAR AQ	980
001224	000014	2220	11	2817	LIX	X,T\$TEM12,T	GET STARTING ADDRESS	990
001225	000002	6230	12	2818	EAX	Y,2,Y	SET SET INDEX REGISTER	1000
001227	000000	5602	04	2819	GETC8	RPOX	,4	ZERO MEMORY LIKE MAD
001230	000000	7570	12	2820	STAQ	0,X	CHONK	1020
001231	000000	7570	13	2821	STAQ	0,Y	CHONK	1030
001232	002000	1240	03	2822	SELX	Z,\$IK,DU	REMOVE A K AS DONE	1040
001233	001227	6030	00	2823	TRC	GETC8	TEST FOR MORE	1050
				2824	*			1060

R

CORE MANAGEMENT -- ALLOCATION

			2825 *	FINISH UP		1070
			2826 *			1080
		001234	2827	GETC5 BSS 0		1090
001234	000016	3220 11	2828	LCX X,STEM10,T	GET COMPLEMENT OF LENGTH TO RETURN	1100
END OF BINARY CARD	IOS00033					
001235	005300	0420 00	2829	ASX X,\$AVAIL	REDUCE AVAIL BY THAT AMOUNT	1110
001236	777777	6040 00	2830	TMI \$ERROR	***BLEWIT	1120
001237	000010	0730 11	2831	LREG TSTEM16,T	RESTORE REGISTERS	1130
001240	000000	7100 17	2832	TRA 0,L	RETURN	1140

R CORE MANAGEMENT -- REQUEST MORE

		001241	2834	USE	CODE		1160
			2835	HEAD	R		1170
			2836	*			1180
			2837	*			1190
			2838	*		REQUEST MORE	1200
			2839	*			1210
			2840	*		THIS SUBROUTINE DOES A REQUEST FOR MORE MEMORY. IT ASKS	1220
			2841	*		FOR ONE CORE UNIT. UPON THE SUCCESSFUL COMPLETION OF THE	1230
			2842	*		REQUEST, THE NEW MEMORY IS LINKED ON THE FREE LIST BY	1240
			2843	*		CALLING RELC.	1250
			2844	*			1260
			2845	*		CALL BY	1270
			2846	*		TRA MORE (ONLY *GETC* SHOULD CALL THIS ROUTINE)	1280
			2847	*		CALL WITH	1290
			2848	*		C(I) = TRAP-BLOCK	1300
			2849	*		CALLS	1310
			2850	*		\$CHSEG	1320
			2851	*		R\$RELC	1330
			2852	*		EXIT TO R\$GETC3	1340
			2853	*		DESTROYS <ALL REGISTERS>	1350
			2854	*		USES NO LOCAL TEMPORARIES	1360
			2855	*			1370
		001241	2856	MORE	BSS	0	1380
001241	005303	2220 00	2857		LDX	X,\$MTOPI GET CURRENT TOP OF MEMORY	1390
001242	001000	0220 03	2858		ADLX	X,\$MQUAN,DU ADD ONE CORE UNIT	1400
001243	005303	7420 00	2859		STX	X,\$MTOPI SAVE NEW EXPECTED TOP	1410
001244	0C1443	7070 00	2860		TSX	L,MREQ REQUEST MEMORY	1420
		001245	2861	MORE1	BSS	0	1430
001245	005303	2350 00	2862		LDA	\$MTOPI GET BACK OLD TOP OF MEMORY	1440
001246	001000	1350 03	2863		SBLA	\$MQUAN,DU IN AU	1450
001247	001000	2750 07	2864		ORA	\$MQUAN,DL AND LENGTH IN AL	1460
		001250	2865		RELC	A NOW DO A RELEASE TO LINK ON FREE LIST	1470
001250	001252	7070 00			TSX	L,R\$RELC	
001251	001165	7100 00	2866		TRA	GETC3 TRY AGAIN	1480
			2867	*\$*	DISK	RELC	1490

R CORE MANAGEMENT -- DE-ALLOCATION

Address	Code	Description	Address
001252	2869	USE	110
	2870	HEAD R	120
	2871 *		130
	2872 *		140
	2873 *	DE-ALLOCATION	150
	2874 *		160
	2875 *		170
	2876 *	THIS ROUTINE LINKS THE 'RELEASED' BLOCK OF MEMORY INTO	180
	2877 *	THE FREE MEMORY LIST ACCORDING TO THE BLOCK'S ADDRESS.	190
	2878 *	SINCE THE FREE LIST IS ORDERED BY BLOCK ADDRESSES FROM	200
	2879 *	LOWEST TO HIGHEST, IN ORDER TO MAKE INSERTIONS AND DE-	210
	2880 *	LETIONS EASIEST THERE IS ASSOCIATED WITH EACH BLOCK A	220
	2881 *	FORWARD AND BACKWARD POINTER AS WELL AS A COUNT OF THE	230
	2882 *	TOTAL NUMBER OF WORDS IN THE BLOCK.	240
	2883 *	CALL BY	250
	2884 *	TSX L,R\$RELC	260
	2885 *	CALL WITH	270
	2886 *	C(AU) = BLOCK-ADDRESS	280
	2887 *	C(AL) = LENGTH	290
	2888 *	CALLS	300
	2889 *	R\$MEMCK (CONDITIONALLY)	310
	2890 *	EXIT TO 0,L	320
	2891 *	PRESERVES ALL REGISTERS EXCEPT C(A)	330
	2892 *	USES LOCAL TEMPORARIES MREG THRU MREG+7	340
	2893 *		350
	2894	USE	360
	001252	0	370
	001252	RELEASE A BLOCK OF MEMORY	380
001252	005370	7530 00	390
001253	777777	3750 03	400
001254	005376	7550 00	410
001255	005302	2350 00	420
001256	005303	2360 00	430
001257	005376	1110 00	440
001260	777777	6010 00	450
END OF BINARY CARD	IOS00034		460
001261	005376	2220 00	470
001262	005374	7200 00	480
001263	777777	6000 00	490
001264	005376	0400 00	500
001265	000000	4400 12	510
001266	005376	1110 00	520
001267	777777	6010 00	530
001270	005344	2220 00	540
001271	001273	7100 00	550
	2903	LDX X,TEMP	560
	2904	LXL 0,MREG+4	570
	2905	TZE \$ERROR	580
	2906	ASX 0,TEMP	590
	2907	SXL 0,LEN,X	600
	2908	CWL TEMP	610
	2909	TNZ \$ERROR	620
	2910	LDX X,FIRST	630
	2911	TRA **2	640
	2912 *		650
	2913 *	LOCATE WHERE TO INSERT BLOCK IN FREE LIST ACCORDING TO ADDRESS	660
	2914 *		670
001272	000000	2220 12	680
001273	001306	6000 00	690
001274	005374	1020 00	700
	2915	LDX X,LINKF,X	710
	2916	TZE RELC2	720
	2917	CMPIX X,INSRT	730

R CORE MANAGEMENT -- DE-ALLOCATION

001275	001272	6020	00	2918	TNC	**3	NO, LOOK AGAIN	600
				2919	*			610
				2920	*			620
				2921	*			630
				2922	*		LOCATED POSITION OF BLOCK WITH XR=X POINTING TO SUCCESSOR	640
				2923	*			650
		001276		2924	RELC1	BSS	0	660
001276	005374	2240	00	2925	LDX	Z,INSRT	NOW Z POINTS TO INSERT	670
001277	000001	2230	12	2926	LDX	Y,LINKR,X	*** Y POINTS TO PREDECESSOR	680
				2927			*** X POINTS TO SUCCESSOR	690
001300	000001	4500	14	2928	STZ	LINKR,Z	***DRG	750
001301	000001	7430	14	2929	STX	Y,LINKR,Z	SET BACKWARD LINK OF INSERT	760
001302	000000	7420	14	2930	STX	X,LINKR,Z	" FORWARD " " "	770
001303	000001	7440	12	2931	STX	Z,LINKR,X	RESET SUCCESSOR'S BACKWARD LINK	780
001304	000000	7440	13	2932	STX	Z,LINKR,Y	AND FINISH LINKING BY RESETTING PREDECESSOR'S	
				2933			***FORWARD LINK.	800
001305	001310	7100	00	2934	TRA	RELC3	NOW DO SOME RE-COMBINING	810
				2935	*			820
				2936	*		RELEASED BLOCK FITS BETWEEN THE LAST FREE BLOCK AND 'LAST'	830
				2937	*			840
		001306		2938	RELC2	BSS	0	850
001306	005346	6220	00	2939	EAX	X,LAST	SO MAKE X POINT TO SUCCESSOR	860
END OF BINARY CARD	IOS00035							
001307	001276	7100	00	2940	TRA	RELC1	AND TREAT NORMALLY	870
				2941	*			880
				2942	*			890
				2943	*		NOW THAT THE BLOCK HAS BEEN CORRECTLY LINKED ON THE FREE	900
				2944	*		LIST TRY TO RECOMBINE WITH ITS BUDDIES.	910
				2945	*		THERE ARE FOUR (4) CASES TO CONSIDER:	920
				2946	*			925
				2947	*		CASE I: THERE EXISTS A BUDDY ABOVE IN MEMORY	930
				2948	*		CASE II: THERE EXISTS A BUDDY BELOW IN MEMORY	940
				2949	*		CASE III: ALL OF THE ABOVE	950
				2950	*		CASE IV: NONE OF THE ABOVE	960
				2951	*			970
		001310		2952	RELC3	BSS	0	980
001310	001350	7070	00	2953	TSX	L,RELC4	CASE I: TRY REJOINING WITH BUDDY ABOVE	990
001311	000000	6220	14	2954	EAX	X,0,Z	LET X POINT TO INSERT	1000
001312	000000	6240	13	2955	EAX	Z,0,Y	LET Z POINT TO ITS PREDECESSOR	1010
001313	005374	7440	00	2956	STX	Z,INSRT	NOW CALL THE PREDECESSOR INSERT	1020
001314	001350	7070	00	2957	TSX	L,RELC4	FUDGE CASE II TO CASE I	1030

R CORE MANAGEMENT -- DE-ALLOCATION

				2959 *		1050
				2960 *	WE'RE DONE. BLOCK IS CORRECTLY LINKED IN LIST.	1060
				2961 *	GRAB CALLER'S REGISTERS AND RETURN.	1070
				2962 *		1080
001315	005374	7220	00	2963	LXL X,MREG+4 GET BACK AMOUNT RELEASED	1090
001316	005300	0420	00	2964	ASX X,FAVAIL STATISTIC: FREE CORE	1100
001317	005303	2220	00	2965	LDX X,SMTOP GET TOP OF MEMORY	1110
001320	005302	1220	00	2966	SBLX X,SMTOP0 COMPUTE BUFFER SIZE	1120
001321	001000	1220	03	2967	SBLX X,SMQUAN,DU MINUS A CORE QUANTUM	1130
001322	001344	6020	00	2968	TNC RELCX OK, FORGET ABOUT IT	1140
001323	005301	1020	00	2969	CMPX X,SMEMRQ COMPARE IT TO MEMORY REQUIREMENTS	1150
001324	001344	6020	00	2970	TNC RELCX CAN WE AFFORD TO RELEASE SOME MEMORY?	1160
001325	005212	2340	00	2971	SZN Q\$BUSY+Q\$CORE BUT FIRST IS MEMORY BUSY?	1170
001326	001344	6010	00	2972	TNZ RELCX DON'T UNDER ANY CIRCUMSTANCES TRY TO CALL MEMCK.	
001327	005350	2210	03	2973	LDX T,TRAP,DU GET DUMMY TCR	1190
001330	005350	7260	07	2974	LXL J,TRAP,DL AND DUMMY JCR	1200
		001331		2975	BRANCH NOPASS, MEMCK OK TO SET UP TASK TO RELEASE MEMORY	1210
001331	001467	7000	00		TSX O,T\$GETT	
001332	000000	6220	11		EAX X,O,T	
001333	000005	2210	12		LDX T,T\$LINK,X	
001334	000000	6210	12		EAX T,O,X	
END OF BINARY CARD	10500036					
001335	001365	6200	00		EAX O, MEMCK	
001336	000004	7400	11		STX O,T\$TRA,T	
001337	000004	6200	11		EAX O,Q\$OFFST,T	
001340	005162	7170	00		XED Q\$XADD+Q\$TASK	
001341	000005	2210	12		LDX T,T\$LINK,X	
		001342			BUGXR (O,X)	
		525206			BUGRUG SET BUGRUG+1	
001342	525206	2200	03		LDX O,BUGRUG,DU	
001343	525206	2220	03		LDX X,BUGRUG,DU	
		001344		2976	RELCX BSS 0 EXIT	1220
001344	005370	0730	00	2977	LREG MREG RESTORE REGISTERS	1230
		001345		2978	BUGA REMIND HIM THAT A IS INVALID	1240
		525207			BUGRUG SET BUGRUG+1	
001345	525207	2350	03		LDA BUGRUG,DU	
001346	525207	2750	07		ORA BUGRUG,DL	
001347	000000	7100	17	2979	TRA O,L RETURN TO CALLER	1250

E CORE MANAGEMENT -- DE-ALLOCATION

			2981 *			1270
			2982 *	THIS SUBROUTINE TRIES TO RECOMBINE A BLOCK OF MEMORY		1280
			2983 *	AND ITS BUDDY ABOVE (IF IT IS ALSO IN THE FREE LIST)		1290
			2984 *	INTO A SINGLE BLOCK.		1300
			2985 *			1310
			2986 *	CALL BY		1320
			2987 *	TSX L,RELCA		1330
			2988 *	CALL WITH		1340
			2989 *	C(Z) = INSERT-BLOCK-ADDRESS		1350
			2990 *	C(X) = INSERT'S SUCCESSOR		1360
			2991 *	EXIT TO O,L		1370
			2992 *	DESTROYS O,X,Z		1380
			2993 *			1390
		001350	2994 RELCA	BSS 0		1400
001350	000000	7200 14	2995	LXL O,LEN,Z	COMPUTE ADDRESS OF BUDDY IN	1410
001351	005374	0200 00	2996	ADLX O,INSRT	...UPPER MEMORY	1420
001352	000000	1000 14	2997	CMPX O,LINKF,Z	IS IT PART OF THE FREE LIST?	1430
001353	000000	6010 17	2998	TNZ O,L	NO, SO RETURN TO CALLER	1440
			2999 *			1450
			3000 *	FOUND BUDDY		1460
			3001 *			1470
001354	000000	2200 12	3002	LDX O,LINKF,X	RESET INSERT'S FORWARD LINK TO	1480
001355	000000	7400 14	3003	STX O,LINKF,Z	THAT OF BUDDY'S.	1490
001356	000001	7440 10	3004	STX Z,LINKB,0	RESET PREDECESSOR'S BACKWARD POINTER	1500
001357	000000	7200 12	3005	LXL O,LEN,X	GET THE LENGTH OF BUDDY	1510
001360	005376	7400 00	3006	STX O,TEMP	SAVE IT	1520
001361	000000	7200 14	3007	LXL O,LEN,Z	GET LENGTH OF INSERT	1530
001362	005376	0200 00	3008	ADLX O,TEMP	TO GET TOTAL LENGTH	1540
END	OF BINARY CARD	10900037				
001363	000000	4400 14	3009	SXL O,LEN,Z	AND RESET LENGTH OF BLOCK	1550
001364	000000	7100 17	3010	TRA O,L	RETURN TO CALLER	1560
			3011 *			1570
			3012 *			1580
	005350		3013	USE STORE		1590
	005350		3014	EIGHT		1600
	005350		3015 TRAP	BSS T\$LEN	DUMMY TCB WHEN CREATING MEMCK	1610
	005370		3016 MREG	EQU TRAP+T\$LEN-8	TEMP REGISTER STORAGE	1620
	005374		3017 INSRT	EQU MREG+4	POINTER TO INSERT BLOCK	1630
	005376		3018 TEMP	EQU MREG+6	FOR SCRATCH WORK	1640
	001365		3019	USE PREVIOUS		1650
			3020 **	DISK MEMCK		1660

P CORE MANAGEMENT -- MEMORY RELEASE

		001365	3022	USE	CODE		110
			3023	HEAD	R		120
			3024 *				130
			3025 *				140
			3026 *		MEMORY RELEASE		150
			3027 *				160
			3028 *		THIS TASK CHECKS TO SEE IF IT CAN GIVE BACK MEMORY TO THE		170
			3029 *		SYSTEM. IF SO, IT RETURNS BLOCKS IN MULTIPLES OF \$MQUAN.		180
			3030 *		THE ALGORITHM IS AS FOLLOWS:		190
			3031 *		GIVEN:		200
			3032 *		MEMRQ -- AMOUNT OF MEMORY NEEDED THOUGH NOT NECESSARILY IN USE		210
			3033 *		TOTAL -- TOTAL BUFFER AREA SIZE		220
			3034 *		AVAIL -- AMOUNT OF FREE CORE THOUGH NOT NECESSARILY CONTIGUOUS		230
			3035 *		USED -- AMOUNT OF CORE BUSY (TOTAL-AVAIL)		240
			3036 *		REQUIREMENT:		250
			3037 *		USED <= MEMRQ		260
			3038 *		ENTERED WHEN:		270
			3039 *		TOTAL-MEMRQ => MQUAN		280
			3040 *		RESTRICTIONS:		290
			3041 *		(1) CAN RELEASE ONLY THE LAST PHYSICAL BLOCK		300
			3042 *		(2) A RELEASE CAN BE EFFECTED ONLY IF AFTER THE RELEASE		310
			3043 *		THERE IS AT LEAST ONE BLOCK OF MEMORY THAT IS OF		320
			3044 *		MEMRQ-USED SIZE.		330
			3045 *		MEMCK IS AN ASYNCHRONOUS TASK, HENCE IT MAY ALL TEMP'S		340
			3046 *		USES NO LOCAL TEMPORARIES		350
			3047 *				360
		001365	3048	MEMCK	BSS	0	370
		001365	3049		END	CORE	380
						GET ON CORE QUEUE	
001365	005204	7170	00		XED	Q\$CORE+Q\$XEND	
001366	005303	2220	00	3050	LDX	X,\$MTOP	GET TOP OF BUFFER AREA
001367	005302	1220	00	3051	SBLX	X,\$MTOPO	MINUS BOTTOM = TOTAL
001370	000027	7420	11	3052	STX	X,\$TEMP1,T	C(\$TEMP1,T) = TOTAL
001371	005300	1220	00	3053	SBLX	X,\$AVAIL	MINUS AVAIL = USED
001372	777777	6040	00	3054	TMI	\$ERROR	***BLEWIT
001373	000026	7420	11	3055	STX	X,\$TEMP2,T	C(\$TEMP2,T) = USED
001374	005301	2350	00	3056	LDA	\$MEMRQ	GET AMOUNT OF MEMORY REQUIRED
001375	000026	1350	11	3057	SBLA	T\$TEMP2,T	MINUS USED = AMOUNT NEEDED STILL
001376	000025	7550	11	3058	STA	T\$TEMP3,T	SAVE IT
001377	001440	6040	00	3059	TMI	MEMX	EXIT
001400	001440	6000	00	3060	TZE	MEMX	EXIT IF CONDITIONS CHANGED ON US
001401	000022	7710	00	3061	ARL	36-18	MOVE TO AL FOR RPL
				3062 *			510
				3063 *		SEARCH FOR BLOCK OF 'NEEDED' SIZE	520
				3064 *			530
001402	005344	2240	00	3065	LDX	Z,FIRST	GET POINTER TO FIRST FREE BLOCK
001403	000302	5002	00	3066	MEM1	RPL	*TNC,TZE
							RUN DOWN LINKED LIST
001404	000000	1150	14	3067	MEM1	0,Z	FOR A BLOCK BIG ENOUGH
001405	001412	6000	00	3068	TZE	MEM2	FOUND A BLOCK THAT IS 'JUST RIGHT'
001406	001412	6020	00	3069	TNC	MEM2	MORE THAN BIG ENOUGH
001407	000000	2240	14	3070	LDX	Z,0,Z	GET BACK PTR OF WHERE WE LEFT OFF

R CORE MANAGEMENT -- MEMORY RELEASE

END OF BINARY CARD IOS00038

001410	001403	6010	00	3071	TNZ	MEM1	IF NOT AT END OF LIST, KEEP SEARCHING	600	
001411	001440	7100	00	3072	TRA	MEMX	OTHERWISE EXIT	610	
				3073 *				620	
				3074 *		Z POINTS TO BLOCK NEEDED EVENTUALLY		630	
				3075 *				640	
001412	000000	7230	14	3076	MEM2	LXL	Y,LEN,7	GET THE LENGTH OF THIS BLOCK	650
001413	005346	6220	00	3077		EAX	X,LAST	GET PTR TO LAST BLOCK	660
001414	000001	1040	12	3078		CMPX	Z,LINKB,X	DOES Z POINT TO LAST BLOCK?	670
001415	001420	6010	00	3079		TNZ	**3	NO, NOT LAST	680
001416	000025	1230	11	3080		SBLX	Y,T\$TEMP3,T	YES, SO SUBTRACT OFF NEEDED	690
001417	001422	7100	00	3081		TRA	MEM3	CONTINUE	700
001420	000001	2240	12	3082		LDX	Z,LINKB,X	GET PTR TO LAST FREE BLOCK	710
001421	000000	7230	14	3083		LXL	Y,LEN,7	GET ITS LENGTH	720
001422	001000	1030	03	3084	MEM3	CMPX	Y,\$MQUAN,DU	COMPARE TO CORE QUANTUM	730
001423	001440	6020	00	3085		TNC	MEMX	IF SMALLER, EXIT	740
001424	777000	3630	03	3086		ANX	Y,\$MQUAN,DU	OTHERWISE ROUND DOWN TO MULTIPLE OF MQUAN	750
001425	000024	7430	11	3087		STX	Y,T\$TEMP4,T	SAVE AMOUNT FOR RELEASE	760
001426	000000	7200	14	3088		LXL	0,LEN,7	GET BACK ITS LENGTH	770
001427	000024	1200	11	3089		SBLX	0,T\$TEMP4,T	MINUS AMOUNT TO RELEASE	780
001430	000000	4400	14	3090		SXL	0,LEN,7	RESTORE NEW LENGTH	790
001431	001434	6010	00	3091		TNZ	**3	IS THE BLOCK NULL	800
001432	000001	2200	14	3092		LDX	0,LINKB,Z	YES, RESET LAST	810
001433	000001	7400	12	3093		STX	0,LINKB,X	TO POINT TO NEW LAST BLOCK	820
001434	000024	3220	11	3094		LCX	X,T\$TEMP4,T	GET BACK AMOUNT TO BE RELEASED	830
001435	005303	0420	00	3095		ASX	X,\$MTP	SUBTRACT FROM TOP OF MEMORY PTR	840
END OF BINARY CARD IOS00039									
001436	005300	0420	00	3096		ASX	X,\$AVAIL	SUBTRACT FROM AVAILABLE	850
001437	001443	7070	00	3097		TSX	L,MREQ	RELEASE MEMORY TO SYSYTEM	860
		001440		3098	MEMX	BSS	0	DONE	870
		001440		3099		DEQ	CORE	RELEASE CORE QUEUE, AWAKEN NEXT TASK	880
001440	005206	7170	00			XED	Q\$CORE+Q\$XDEQ		
001441	001477	7000	00	3100		TSX	0,T\$RELT	AND RELEASE TCB	890
		001442		3101		EXIT		EVAPORATE	900
001442	003074	7100	00			TRA	\$EXIT		
				3102	**	DISK	MREQ		910

P

CORE MANAGEMENT -- MEMORY REQUESTS

				001443	3104	USE	CODE		110
					3105	HEAD	R		120
					3106	*			130
					3107	*		MEMORY REQUESTS	140
					3108	*			150
					3109	*	THIS SUBROUTINE RESETS THE TOP OF MEMORY TO THE ADDRESS		160
					3110	*	SPECIFIED BY C(MTOP).		170
					3111	*			180
					3112	*	CALL BY		190
					3113	*	TSX L,MREQ		200
					3114	*	CALL WITH		210
					3115	*	C(XT) = TBLOCK-ADDRESS		220
					3116	*	C(XJ) = JBLOCK-ADDRESS		230
					3117	*	C(MTOP) = NEW SETTING		240
					3118	*	CALLS		250
					3119	*	\$CHSEG		260
					3120	*	EXITS TO 0,L		270
					3121	*	RETURNS WITH		280
					3122	*	C(XT) = TBLOCK-ADDRESS		290
					3123	*	C(XJ) = JBLOCK-ADDRESS		300
					3124	*	C(XL) = RESTART ADDRESS		310
					3125	*	C(MTOP) = NEW SETTING		320
					3126	*	USES NO LOCAL TEMPORARIES, ONLY TSTEM9		330
					3127	*			340
001443	000017	4470	11		3128	MREQ	SXL L,TSTEM9,T	SAVE RETURN ADDRESS	350
001444	005303	2200	00		3129		LDX 0,\$MTOP	GET PASSED SETTING	360
001445	000777	0200	03		3130		ADLX 0,\$MQUAN-1,DU	ROUND UP TO	370
001446	777000	3600	03		3131		ANX 0,\$MQUAN,DU	NEXT CORE MULTIPLE	380
001447	005303	7400	00		3132		STX 0,\$MTOP	AND SAVE IT	390
			001450		3133	MREQ1	CHSEG \$BUFSEG,\$MTOP	MEMORY REQUEST	400
001450	000656	7000	00				TSX 0,\$CHSEG		
001451	000000	0000	00				ARG \$BUFSEG		
001452	005303	0000	00				ARG \$MTOP		
			001453		3134		CHECK MREQ2,\$RZR,MREQ1,\$SIO,MREQ1		410
001453	000000	7200	11				LXL 0,\$SRW1,T		
001454	000077	3600	03				ANX 0,\$STMK,DU		
001455	001463	6000	00				TZE MREQ2		
001456	000003	1000	03				CMPX 0,\$RZR,DU		
001457	001450	6000	00				TZE MREQ1		
001460	000004	1000	03				CMPX 0,\$SIO,DU		
001461	001450	6000	00				TZE MREQ1		
001462	777777	7100	00				TRA \$ERROR		
			001463		3135	MREQ2	BSS 0	SUCCESSFUL REQUEST	420
END OF BINARY CARD	IOS00040								
001463	000017	7270	11		3136		LXL L,TSTEM9,T	RETRIEVE RETURN ADDRESS	430
			001464		3137		BUGL (TSTEM9,T)	BUG IT	440
			525210			BUGRUG	SET BUGBUG+1		
001464	525210	2200	03				LDX 0,BUGBUG,DU		
001465	000017	4400	11				SXL 0,TSTEM9,T		
001466	000000	7100	17		3138		TRA 0,L	RETURN TO CALLER	450

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INPUT/OUTPUT SCHEDULER -- IOS MONITOR
CORE MANAGEMENT -- MEMORY REQUESTS
3139 *** DISK TBLOCK

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R

TRAP MANAGEMENT -- DESCRIPTION

001467	3141	USE	CODE		110
	3142	HEAD	T		120
	3143	*			130
	3144	*			140
	3145	*	THESE MACROS GET AND RELEASE TRAP BLOCKS.		150
	3146	*	CLOBBERS C(D), C(X), C(T).		160
	3147	*			170
	3148	*			180
	3149	*		GETT	190
	3150	*			200
	3151	GETT	MACRO	<NO-ARGUMENTS>	210
	3152	TSX	0,T\$GETT	CALL SUBROUTINE	220
	3153	ENDM	GETT		230
	3154	*			240
	3155	*			250
	3156	*		RELT MACRO	260
	3157	*			270
	3158	RELT	MACRO	<NO-ARGUMENTS>	280
	3159	TSX	0,T\$RELT	CALL SUBROUTINE	290
	3160	ENDM	RELT		300

T TRAP MANAGEMENT -- GETT

			001467	3162	USE	CODE		320
				3163	HEAD	T		330
				3164 *				340
				3165 *				350
				3166 *		GETT		360
				3167 *				370
				3168 *	THIS SUBROUTINE GETS A TRAP BLOCK. C(XT) POINTS TO THE			380
				3169 *	CURRENT TBLOCK WHILE C(T\$LINK,T) POINTS TO THE OLD ONE			390
				3170 *				400
				3171 *	ENTERED	WITH		410
				3172 *		C(XT) = TCR		420
				3173 *		C(XJ) = JCB		430
				3174 *	ENTERED	BY		440
				3175 *		TSX 0,R\$GETT		450
				3176 *	CALLS			460
				3177 *		R\$GETC		470
				3178 *	RETURNS	0,0		480
				3179 *	RETURNS	WITH		490
				3180 *		C(XT) = NEW TCR		500
				3181 *		C(XJ) = JCB		510
				3182 *				520
				3183 *				530
			001467	3184	GETT	BSS	0	540
			001467	3185	GETC	(LEN,DL)	GET A BLOCK ABOUT THE SIZE OF A TBLOCK	550
001467	000030	2350	07		LDA	LEN,DL		
001470	001152	7070	00		TSX	L,R\$GETC		
001471	000005	7410	01	3186	STX	T,LINK,AU	POINT TO PREVIOUS TBLOCK	560
001472	000000	6210	01	3187	EAX	T,0,AU	MAKE C(XT) POINT TO NEW BLOCK	570
001473	777777	6000	00	3188	TZE	\$ERROR	***DBG	580
001474	777777	6040	00	3189	TMI	\$ERROR	***DRG	590
001475	000006	4460	11	3190	SXL	J,T\$JCB,T	SET JCB POINTER	600
001476	000000	7100	10	3191	TRA	0,0	RETURN TO CALLER	610

T			TRAP MANAGEMENT -- RELT				
		001477	3193	USE	CODE		630
			3194	HEAD	T		640
			3195 *				650
			3196 *				660
			3197 *		RELT		670
			3198 *				680
			3199 *	THIS SUBROUTINE RELEASES THE CURRENT TRAP BLOCK.			690
			3200 *				700
			3201 *	ENTERED WITH			710
			3202 *	C(XT) = TCB			720
			3203 *	C(XJ) = JCB			730
			3204 *	ENTERED BY			740
			3205 *	TSX 0,R\$RELT			750
			3206 *	CALLS			760
			3207 *	R\$RELC			770
			3208 *	RETURNS TO 0,0			780
			3209 *	RETURNS WITH			790
			3210 *	C(XJ) = JCB			800
			3211 *				810
			3212 *				820
		001477	3213	RELT	BSS	0	830
001477	000000	6350 11	3214	EAA	0,T	TRAP ADDRESS TO AU	840
001500	777777	6000 00	3215	TZE	\$ERROR	CHECK FOR LEGAL RELEASE	850
001501	000030	2750 07	3216	ORA	LEN,DL	TRAP LEN TO AL	860
		001502	3217	RELC	A	RELEASE IT	870
001502	001252	7070 00		TSX	L,R\$RELC		
		001503	3218	BUGXR	T	SPPML	880
		525211		BUGRUG	SET	BUGRUG+1	
001503	525211	2210 03		LDX	T,BUGBUG,DU		
001504	000000	7100 10	3219	TRA	0,0	RETURN TO CALLER	890
			3220	**	DISK	JBLOCKS	900

T

JOB CONTROL BLOCK MANAGEMENT -- DESCRIPTION

001505	3222	USE	CODE		110
	3223	HEAD	J		120
	3224 *				130
	3225 *				140
	3226 *	THESE MACROS GET AND RELEASE JOB CONTROL BLOCKS.			150
	3227 *	GLOBBERS C(X0), C(XX), AND C(XJ).			160
	3228 *				170
	3229 *				180
	3230 *			GETJ	190
	3231 *				200
	3232 GETJ	MACRO	<NO-ARGUMENTS>		210
	3233	TSX	0,J\$GETJ	CALL SUBROUTINE	220
	3234	ENDM	GETJ		230
	3235 *				240
	3236 *				250
	3237 *			RELJ	260
	3238 *				270
	3239 RELJ	MACRO	<NO-ARGUMENTS>		280
	3240	TSX	0,J\$RELJ	CALL SUBROUTINE	290
	3241	ENDM	RELJ		300

J

JOB CONTROL BLOCK MANAGEMENT -- GETJ

		001505	3243	USE	CODE		320
			3244	HEAD	J		330
			3245	*			340
			3246	*			350
			3247	*		GETJ	360
			3248	*			370
			3249	*	THIS SUBROUTINE GETS A JOB CONTROL BLOCK. C(XJ) POINTS TO		380
			3250	*	THE NEW JCB.		390
			3251	*			400
			3252	*	ENTERED WITH		410
			3253	*	C(XT) = TCB		420
			3254	*	ENTERED BY		430
			3255	*	TSX 0,J\$GETJ		440
			3256	*	CALLS		450
			3257	*	R\$GETC		460
			3258	*	RETURNS 0,0		470
			3259	*	RETURNS WITH		480
			3260	*	C(XT) = TCB		490
			3261	*	C(XJ) = NEW JCB		500
			3262	*	CLOBBERS C(A), C(XL)		510
			3263	*	USES NO TEMPS ITSELF		520
			3264	*			530
		001505	3265	GETJ	BSS	0	540
		001505	3266	GETC	(LEN,DL)	GET A BLOCK ABOUT THE SIZE OF A JBLOCK	550
				LDA	LEN,DL		
				TSX	L,R\$GETC		
			3267	EAX	J,0,0AU	MAKE C(XJ) POINT TO NEW BLOCK	560
			3268	TRA	0,0	RETURN TO CALLER	570
001505	000020	2350	07				
END	OF BINARY CARD	10800041					
001506	001152	7070	00				
001507	000000	6260	01				
001510	000000	7100	10				

J

JOB CONTROL BLOCK MANAGEMENT -- RELJ

		001511	3270	USE	CODE		590
			3271	HEAD	J		600
			3272	*			610
			3273	*			620
			3274	*	RELJ		630
			3275	*			640
			3276	*	THIS SUBROUTINE RELEASES THE CURRENT JOB CONTROL BLOCK.		650
			3277	*			660
			3278	*	ENTERED WITH		670
			3279	*	C(XT) = TCB		680
			3280	*	C(XJ) = JCB TO BE RELEASED		690
			3281	*	ENTERED BY		700
			3282	*	TSX 0,J\$RELJ		710
			3283	*	CALLS		720
			3284	*	RSRELC		730
			3285	*	RETURNS TO 0,0		740
			3286	*	RETURNS WITH		750
			3287	*	C(XT) = TCB		760
			3288	*	CLOBBERS C(A), C(XL)		770
			3289	*	USES NO TEMPS ITSELF		780
			3290	*			790
		001511	3291	RELJ	BSS	0	800
001511	000000	6350 16	3292	EAA	0,J	JCB ADDRESS TO AU	810
001512	777777	6000 00	3293	TZE	\$ERROR	CHECK FOR LEGAL RELEASE	820
001513	000020	2750 07	3294	ORA	LEN,DL	JCB LEN TO AL	830
		001514	3295	RELC	A	RELEASE IT	840
001514	001252	7070 00		TSX	L,RSRELC		
		001515	3296	BUGXR	J	SPPML	850
		525212		BUGBUG	SET	BUGBUG+1	
001515	525212	2260 03		LDX	J,BUGBUG,DU		
001516	000000	7100 10	3297	TRA	0,0	RETURN TO CALLER	860
			3298	*\$*	DISK	LOG1	870

J

LOGGING -- DISCRPTION

001517	3300	USE	CODE	110
	3301	HEAD	C	120
	3302 *			130
	3303 *			140
	3304 *			150
	3305 *			160
	3306 *			170
	3307 *			180
	3308 *			190
	3309 *			200
	3310 *			210
	3311 *			220
	3312 *			230
	3313 *			240
	3314 *			250
	3315 *			260
	3316 *			270
	3317 *			280
	3318 *			290
	3319 *	LOGS	START OF LOG MESSAGE	300
	3320 *	LOGC	LOG TEXT CHARACTERS (TALLY WORD IS ARG 1)	310
	3321 *	LOG	FORCE THE FLUSHING OF OUTPUT BUFFER	320
	3322 *	LOGX	END OF MESSAGE -- EXIT AND SEND MESSAGE	330
	3323 *			340
	3324 *			350
	3325 *			360
	3326 *			370

0

LOGGING -- MACROS

001517	3328	USE	CODE	390
	3329	HEAD	0	400
	3330	*		410
	3331	*		420
	3332	*		430
	3333	*	LOGS MACRO	440
	3334	*	SET UP FOR LOGGING	450
	3335	*		460
	3336	LOGS	MACRO <NO-ARGUMENTS>	470
	3337	TSX	L,0\$LOGS	480
	3338	ENDM	LOGS	490
	3339	*		500
	3340	*		510
	3341	*	LOGC MACRO	520
	3342	*		530
	3343	*	LOG ASCII STRING POINTER TO BE FIRST ARGUMENT	540
	3344	*		550
	3345	LOGC	MACRO SC POINTER ADDRESS/'A'	560
	3346	INE	'#1','A'	570
	3347	LDA	#1 GET SC POINTER IN A	580
	3348	TSX	L,0\$LOGC	590
	3349	ENDM	LOGC	600
	3350	*		610
	3351	*		620
	3352	*	LOG MACRO	630
	3353	*		640
	3354	*	FLUSH THE OUTPUT BUFFER	650
	3355	*		660
	3356	LOG	MACRO <NO-ARGUMENTS>	670
	3357	TSX	L,0\$LOG	680
	3358	ENDM	LOG	690
	3359	*		700
	3360	*		710
	3361	*	LOGX MACRO	720
	3362	*		730
	3363	*	CLEANUP AND EXIT	740
	3364	*		750
	3365	LOGX	MACRO <NO-ARGUMENTS>	760
	3366	TSX	L,0\$LOGX	770
	3367	ENDM	LOGX	780
	3368	*		790
	3369	*		800
	3370	*	LCRLF MACRO	810
	3371	*		820
	3372	*	LOG A CARRIAGE RETURN/ LINE FEED	830
	3373	*		840
	3374	LCRLF	MACRO <NO-ARGUMENTS>	850
	3375	TSX	L,0\$LCRLF CALL SUBROUTINE	860
	3376	ENDM	LCRLF	870
	3377	*		880

C

LOGGING -- MACROS

3378 *				890
3379 *			LSP MACRO	900
3380 *				910
3381 *		LOG A SPACE		920
3382 *				930
3383 LSP	MACRO	<NO-ARGUMENTS>		940
3384	TSX	L,%LSP	CALL SUBROUTINE	950
3385	ENDM	LSP		960
3386 *				970
3387 *				980
3388 *			LCHR MACRO	990
3389 *				1000
3390 *		LOG A CHARACTER		1010
3391 *				1020
3392 LCHR	MACRO	<NO-ARGUMENTS>		1030
3393	TSX	L,%LCHR	CALL SUBROUTINE	1040
3394	ENDM	LCHR		1050

0

LOGGING SUBROUTINES -- LOGS

	001517	3396	USE	CODE		1070
		3397	HEAD	0		1080
		3398 *				1090
		3399 *				1100
		3400 *	THE FOLLOWING SUBROUTINES ARE CALLED BY THE LOGGING MACROS.			1110
		3401 *				1120
		3402 *				1130
		3403 *			LOGS	1140
		3404 *				1150
		3405 *				1160
		3406 *	LOGS ENQUEUES FOR THE CONTROL TTY. AFTER SEIZING IT, IT			1170
		3407 *	LOCKS THE DEVICE, LOGS A HELLO MESSAGE, AND RETURNS TO THE			1180
		3408 *	CALLER.			1190
		3409 *				1200
		3410 *				1210
		3411 *	ENTERED WITH			1220
		3412 *		C(XT) = TCB		1230
		3413 *		C(XJ) = JCB		1240
		3414 *	ENTERED BY			1250
		3415 *		TSX L,0\$LOGS		1260
		3416 *	CALLS			1270
		3417 *		Q\$ENG		1280
		3418 *		\$LOCK		1290
		3419 *	RETURN TO 0,L			1300
		3420 *	RETURNS WITH			1310
		3421 *		C(XT) = TCB		1320
		3422 *		C(XJ) = JCB		1330
		3423 *		C(XL) = RESTART ADDRESS		1340
		3424 *	USES			1350
		3425 *		NO LOCALS		1360
		3426 *		T\$TEMP7,T (UPPER)		1370
		3427 *				1380
		3428 *				1390
		3429 LOGS	BSS	0	ENTRY POINT	1400
001517	000021 7470 11	3430	STX	L,T\$TEMP7,T	SAVE RETURN ADDRESS	1410
	001520	3431	ENQ	OP	WAIT FOR OP CONSOLE TO BE FREE	1420
001520	005144 7170 00		XED	Q\$OP+Q\$XENG		
	001521	3432 LOGS1	LOCK	R\$OPFRN	NOW LOCK IT	1430
001521	000736 7000 00		TSX	0,\$LOCK		
001522	005665 0000 00		ARG	R\$OPFRN		
	001523	3433	CHECK	LOGS2,R\$BZ,LOGS1,B\$LOCK,LOGS1		1440
001523	000000 7200 11		LXL	0,T\$SRW1,T		
001524	000077 3600 03		ANX	0,B\$STMK,DU		
001525	001533 6000 00		TZE	LOGS2		
001526	000003 1000 03		CMPX	0,B\$BZ,DU		
001527	001521 6000 00		TZE	LOGS1		
001530	000013 1000 03		CMPX	0,B\$LOCK,DU		
001531	001521 6000 00		TZE	LOGS1		
END OF BINARY CARD	I0S00042		TRA	\$ERROR		
001532	777777 7100 00					

0

LOGGING SUBROUTINES -- LOGS

001533	004675	2350	00	001533	3434	LOGS2	BSS	0	CONSOLE SUCCESSFULLY SEIZED	1450
					3435		LDA	LGTL1	INITIALIZE TALLY WORD	1460
001534	005400	7550	00		3436		STA	LGTL		1470
001535	004676	2370	00		3437		LDAQ	LMI	PUT *IOS* MESSAGE IN BUFFER	1480
001536	005420	7570	00		3438		STAQ	LGRUF		1490
001537	000021	2270	11		3439		LDX	L,T\$TEMP7,T	RETRIEVE RETURN ADDRESS	1500
				001540	3440		BUGU	(T\$TEMP7,T)	BUG IT	1510
				525213		BUGBUG	SET	BUGRUG+1		
001540	525213	2200	03				LDX	0,BUGBUG,DU		
001541	000021	7400	11				STX	0,T\$TEMP7,T		
001542	000000	7100	17		3441		TRA	0,L	RETURN TO CALLER	1520

0

LOGGING SUBROUTINES -- LOGC

			001543	3443	USE	CODE		1540
				3444	HEAD	0		1550
				3445	*			1560
				3446	*			1570
				3447	*			1580
				3448	*		LOGC	1590
				3449	*		LOG CHARACTER STRING GIVEN BY SC POINTER IN A-REG	1600
				3450	*			1610
				3451	*	ENTERED WITH		1620
				3452	*	C(XT) = TCB		1630
				3453	*	C(XJ) = JCB		1640
				3454	*	C(A) = TALLY WORD		1650
				3455	*	ENTERED BY		1660
				3456	*	TSX L,OSLOGC		1670
				3457	*	CALLS		1680
				3458	*	OSLCHR		1690
				3459	*	OSLOG		1700
				3460	*	RETURNS TO O,L		1710
				3461	*	RETURNS WITH		1720
				3462	*	C(XT) = TCB		1730
				3463	*	C(XJ) = JCB		1740
				3464	*	USES		1750
				3465	*	OSLGCTL		1760
				3466	*	T\$TEMP7,T (UPPER)		1770
				3467	*	CLOBBERS C(A), C(X0)		1780
				3468	*			1790
				3469	LOGC	STX	L,T\$TEMP7,T	SAVE RETURN ADDRESS
				3470		STA	LGCTL	STORE TALLY WORD
			001545	3471	LOGC1	BSS	0	LOOP POINT
				3472		LDA	LGCTL,SC	GET NEXT CHARACTER
				3473		TTF	**2	CONTINUE IF WE GOT ONE
				3474		TRA	LOGC2	EXIT IF DONE
			001550	3475		LCHR		LOG THE CHARACTER
						TSX	L,OSLCHR	
			001551			TRA	LOGC1	GET NEXT CHARACTER
				3477	*			1800
				3478	*	END OF TEXT STRING		1890
				3479	*			1900
			001552	3480	LOGC2	BSS	0	1910
				3481		LDX	L,T\$TEMP7,T	RETRIEVE RETURN ADDRESS
			001553	3482		BUGU	(T\$TEMP7,T)	BUG IT
			525214		BUGBUG	SET	BUGBUG+1	1930
						LDX	0,BUGBUG,DU	
			001554			STX	0,T\$TEMP7,T	
			001555	3483		BUGA		AND A-REG
			525215		BUGBUG	SET	BUGBUG+1	1940
						LDA	BUGBUG,DU	
			001555			ORA	BUGBUG,DL	
			001556					
			001557	3484		TRA	0,L	RETURN TO CALLER
								1950

END OF BINARY CARD IOS00043

0 LOGGING SUBROUTINES -- LOGX

Address	Code	Label	Operation	Comment	Line No.	
001560	3486	USE	CODE		1970	
	3487	HEAD	0		1980	
	3488	*			1990	
	3489	*		LOGX	2000	
	3490	*			2010	
	3491	*		CLEAN UP, SEND MESSAGE, AND EXIT	2020	
	3492	*			2030	
	3493	*		ENTERED WITH	2040	
	3494	*		C(XT) = TCB	2050	
	3495	*		C(XJ) = JCB	2060	
	3496	*		ENTER BY	2070	
	3497	*		TSX L,0\$LOGX	2080	
	3498	*		CALLS	2090	
	3499	*		0\$LCRLF	2100	
	3500	*		0\$LOG	2110	
	3501	*		\$UNLCK	2120	
	3502	*		0\$DE0	2130	
	3503	*		RETURNS TO 0,L	2140	
	3504	*		RETURNS WITH	2150	
	3505	*		C(XT) = TCB	2160	
	3506	*		C(XJ) = JCB	2170	
	3507	*		USES	2180	
	3508	*		T\$TEMP7,T (UPPER)	2190	
	3509	*			2200	
	001560	3510	LOGX	BSS 0	ENTRY POINT	
001560	000021	7470	11	3511	STX L,T\$TEMP7,T	SAVE RETURN ADDRESS
	001561	3512		LCRLF	PUT A CR/LF ON MESSAGE	
001561	001600	7070	00	3513	TSX L,0\$LCRLF	
	001562	3513		LOG	LOG BUFFER TO TTY	
001562	001645	7070	00	3514	LOGX1	
	001563	3514	LOGX1	UNLCK	R\$OPFRN	
001563	000746	7000	00	3515	TSX 0,\$UNLCK	
001564	005665	0000	00	3515	ARG R\$OPFRN	
	001565	3515		CHECK	LOGX2,\$BZ,LOGX1	
001565	000000	7200	11	LXL	0,T\$SRW1,T	
001566	000077	3600	03	ANX	0,\$S1MK,DU	
001567	001573	6000	00	TZE	LOGX2	
001570	000003	1000	03	CMPX	0,\$BZ,DU	
001571	001563	6000	00	TZE	LOGX1	
001572	777777	7100	00	TRA	\$ERROR	
	001573	3516	LOGX2	BSS 0		
	001573	3517		DEQ	OP	
001573	005146	7170	00	XED	0\$OP+Q\$XDEQ	RELEASE OP CONSOLE
001574	000021	2270	11	3518	LDX L,T\$TEMP7,T	RETRIEVE RETURN ADDRESS
	001575	3519		BUGU	(T\$TEMP7,T)	BUG IT
	525216	BUGRUG	SET	RUGRUG+1		
001575	525216	2200	03	LDX	0,BUGBUG,DU	
001576	000021	7400	11	STX	0,T\$TEMP7,T	
001577	000000	7100	17	3520	TRA 0,L	RETURN TO CALLER

0 LOGGING -- SPECIAL SUBROUTINES

		001600	3522	USE	CODE		2330
			3523	HEAD	0		2340
			3524 *				2350
			3525 *				2360
			3526 *			LCRLF	2370
			3527 *				2380
			3528 *	PUT A CR/ LF IN OUTPUT BUFFER			2390
			3529 *				2400
		001600	3530	LCRLF	BSS	0	2410
			3531	STX	L,T\$TEMP6,T	ENTRY POINT	2420
001600	000022	7470 11	3532	LDA	A\$CR,DL	SAVE RETURN ADDRESS	2430
001601	000015	2350 07	3533	TSX	L,LCHR	GET A CR	2440
001602	001620	7070 00				LOG IT	2450
END	OF BINARY CARD	10S00044					2460
001603	000012	2350 03	3534	LDA	A\$LF,DU	GET A LF	2470
001604	001620	7070 00	3535	TSX	L,LCHR	LOG IT, ALSO	2480
001605	000022	2270 11	3536	LDX	L,T\$TEMP6,T	RETRIEVE RETURN ADDRESS	2490
		001606	3537	BUGU	(T\$TEMP6,T)	BUG IT	2500
		525217		BUGBUG	SET	BUGBUG+1	2510
001606	525217	2200 03		LDX	0,BUGBUG,DU		2520
001607	000022	7400 11		STX	0,T\$TEMP6,T		2530
001610	000000	7100 17	3538	TRA	0,L	RETURN TO CALLER	2540
			3539 *				2550
			3540 *				2560
			3541 *			LSP	2570
			3542 *				2580
			3543 *	PUT A SPACE IN OUTPUT BUFFER			2590
			3544 *				2600
		001611	3545	LSP	BSS	0	2610
001611	000022	7470 11	3546	STX	L,T\$TEMP6,T	LOG A SPACE	2620
001612	200040	2350 07	3547	LDA	A\$SP,DL	SAVE RETURN ADDRESS	2630
001613	001620	7070 00	3548	TSX	L,LCHR	GET A SP	2640
001614	000022	2270 11	3549	LDX	L,T\$TEMP6,T	LOG IT	2650
		001615	3550	BUGU	(T\$TEMP6,T)	RETRIEVE RETURN ADDRESS	2660
		525220		BUGBUG	SET	BUGBUG+1	2670
001615	525220	2200 03		LDX	0,BUGBUG,DU	BUG IT	2680
001616	000022	7400 11		STX	0,T\$TEMP6,T		2690
001617	000000	7100 17	3551	TRA	0,L	RETURN TO CALLER	2700
			3552 *				2710
			3553 *				2720
			3554 *			LCHR	2730
			3555 *				2740
			3556 *	PUT CHARACTER IN AL IN OUTPUT BUFFER			2750
			3557 *				2760
			3558 *				2770
		001620	3559	LCHR	BSS	0	2780
001620	000177	3750 07	3560	ANA	A\$MASK,DL	LOG CHARACTER IN A	2790
001621	005400	7550 52	3561	STA	LGTAL,SC	ISCLATE ASCII CHARACTER	2800
001622	000000	6070 17	3562	TTF	0,L	DROP CHARACTER INTO BUFFER	2810
001623	001645	7100 00	3563	TRA	LOG	IF BUFFER NOT FULL, RETURN TO CALLER	2820
						OTHERWISE FORCE BUFFER	2830

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LOGGING -- SPECIAL SUBROUTINES

			3565 *				2760
			3566 *			LOCTF	2770
			3567 *				2780
			3568 *			LOG C(Q) AS 12 OCTAL DIGITS	2790
			3569 *				2800
		001624	3570	LOCF	BSS	0	2810
001624	000014	3200 03	3571		LCX	0,12,DU	2820
001625	001632	7100 00	3572		TRA	LOCT	2830
						LOG 12 DIGITS	
						DO IT	
			3573 *				2840
			3574 *				2850
			3575 *			LOCTU	2860
			3576 *				2870
			3577 *			LOG C(QU) AS 6 OCTAL DIGITS	2880
			3578 *				2890
		001626	3579	LOCTU	BSS	0	2900
001626	000006	3200 03	3580		LCX	0,6,DU	2910
001627	001632	7100 00	3581		TRA	LOCT	2920
						LOG 6 DIGITS	
						DO IT	
			3582 *				2930
			3583 *				2940
			3584 *			LOCTL	2950
			3585 *				2960
			3586 *			LOG C(QL) AS 6 OCTAL DIGITS	2970
			3587 *				2980
		001630	3588	LOCTL	BSS	0	2990
001630	000000	6360 06	3589		EAQ	0,QL	3000
END OF BINARY CARD	IOS00045					MOVE NUMBER TO QL	
001631	001626	7100 00	3590		TRA	LOCTU	3010
						PRINT AS QU	
			3591 *				3020
			3592 *				3030
			3593 *			LOCT	3040
			3594 *				3050
			3595 *			LOGS OCTAL DIGITS	3060
			3596 *			ENTER WITH	3070
			3597 *			C(Q) = DIGITS TO LOG (LEFT JUSTIFIED)	3080
			3598 *			C(O) = NUMBER OF DIGITS	3090
			3599 *				3100
		001632	3600	LOCT	BSS	0	3110
001632	000022	7470 11	3601		STX	L,T\$TEMP6,T	3120
001633	000000	2350 07	3602	LOCT1	LDA	0,DL	3130
001634	000003	7370 00	3603		LLS	3	3140
001635	100060	0750 07	3604		ADA	A\$D0,DL	3150
001636	001620	7070 00	3605		TSX	L,LCHR	3160
001637	000001	6600 03	3606		ADX	0,1,DU	3170
001640	001633	6040 00	3607		TMI	LOCT1	3180
001641	000022	2270 11	3608		LDX	L,T\$TEMP6,T	3190
		001642	3609		BUGU	(T\$TEMP6,T)	3200
		525221			BUGBUG	SET	
001642	525221	2200 03			LDX	0,BUGBUG,DU	
001643	000022	7400 11			STX	0,T\$TEMP6,T	
001644	000000	7100 17	3610		TRA	0,L	3210
						RETURN TO CALLER	

0			LOGGING SUBROUTINES -- LOG				
		001645	3612	USE	CODE		3230
			3613	HEAD	0		3240
			3614 *				3250
			3615 *				3260
			3616 *			LOG	3270
			3617 *				3280
			3618 *	FLUSHES THE BUFFER TO OP CONSOLE AND RESETS TALLY WORD			3290
			3619 *				3300
		001645	3620	LOG	BSS	0	3310
			3621	SREG	LGREG	ENTRY POINT	3320
			3622	LDA	LGTLO	SAVE REGISTERS	3330
			3623	SBLA	LGTAL	GET OUTPUT TALLY PROTOTYPE	3340
			3624	ADLA	#040,DL	COMPUTE NUMBER OF CHARACTERS TO SEND	3350
			3625	ANA	\$TALMK,DL	TAKE OF BORROW, IF ANY	3360
			3626	TZE	LOG2	MASK TO CHARACTER COUNT	3370
			3627	ALS	18-6	IF ZERO, NOTHING TO DO	3380
			3628	STA	LNCHR	RIGHT JUSTIFY COUNT IN AU	3390
			3629	LOG1	APEND	AND SAVE IT	3400
		001655			R\$OPFRN,(LGBUF,DU),LNCHR,(2,DU)		
					0,\$APEND		
END	OF BINARY CARD	10S00046					
		001656		ARG	R\$OPFRN		
		001657		ARG	LGBUF,DU		
		001660		ARG	LNCHR		
		001661		ARG	2,DU		
		001662	3630	CHECK	LOG2,B\$BZ,LOG1		3410
				LXL	0,T\$SRW1,T		
				ANX	0,B\$STMK,DU		
				TZE	LOG2		
				CMPX	0,B\$BZ,DU		
				TZE	LOG1		
				TRA	\$ERROR		
			3631 *				3420
			3632 *	MESSAGE OUT OK ON OP CONSOLE			3430
			3633 *				3440
		001670	3634	LOG2	BSS	0	3450
			3635	LDA	LGTLO	RE-INITIALIZE OUTPUT TALLY	3460
			3636	STA	LGTAL		3470
			3637	LREG	LGREG	RESTORE THE REGISTERS	3480
			3638	TRA	0,L	RETURN TO CALLER	3490
			3639 *				3500
			3640 *				3510
			3641 *	CONSTANT AND STORAGE AREA			3520
			3642 *				3530
		004674	3643	USE	CONST		3540
			3644	LGTLO	TALLYH	LGBUF,120	TALLY FOR FILLING BUFFER
			3645	LGTLO	TALLYH	LGBUF+2,120-8	TALLY INCLUDING INITIAL HEADER
			3646	EVEN			3570
			3647	LM1	OCT	177007015012	GR7CH BELL LF CR
			3648	LM2	UASCI	1,10S	HEADFR MESSAGE
			3649	USE	PREVIOUS		3590
		001674					3600

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LOGGING SUBROUTINES -- LOG

	005400	3650	USE	STORE		3610
	005400	3651	LG TAL	BSS	1	3620
	005401	3652	LG CTL	BSS	1	3630
005402	000000	3653	LN CHR	ZERO	***	3640
	005410	3654		EIGHT		3650
	005410	3655	LG REG	BSS	8	3660
	005420	3656		EVEN		3670
	005420	3657	LG BUF	BSS	128/4	3680
	001674	3658		USE	PREVIOUS	3690
		3659	***	DISK	OPCON1	3700

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OPERATOR INTERFACE --DESCRIPTION

001674	3661	USE	CODE	110
	3662	HEAD	0	120
	3663 *			130
	3664 *			140
	3665 *		OPERATOR INTERFACE	150
	3666 *			160
	3667 *		THE SKEDS INPUT DEVICE (OPERATOR'S CONSOLE) IS A SINGLE	170
	3668 *		RESOURCE, AND HENCE MUST BE QUEUED FOR. (SEE LOGGING.)	180
	3669 *		THE INPUT ROUTINES ARE CALLED BY A SET OF MACROS WHICH	190
	3670 *		REMOVE SPECIFIED INFORMATION FROM THE INPUT MESSAGE BUFFER.	200
	3671 *			210
	3672 *		THE FOLLOWING MACROS CAN BE USED TO CALL THE INPUT ROUTINES:	220
	3673 *			230
	3674 *	ICHR	INPUT NEXT CHAR FROM INPUT STREAM	240
	3675 *	INBLK	INPUT NEXT NON-BLANK CHAR	250
	3676 *	ICMD	INPUT THREE LETTER COMMAND	260
	3677 *	IDELM	INPUT THE HIGHEST PRIORITY DELIMITER	270

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OPERATOR INTERFACE -- MACROS

001674	3679	USE	CODE		290
	3680	HEAD	0		300
	3681	*			310
	3682	*			320
	3683	*		ICHR	330
	3684	*			340
	3685	*	INPUT NEXT CHARACTER FROM INPUT STREAM		350
	3686	*			360
	3687	ICHR	MACRO	<NO-ARGUMENTS>	370
	3688	TSX	L,%\$ICHR	CALL SUBROUTINE	380
	3689	ENDM	ICHR		390
	3690	*			400
	3691	*			410
	3692	*		INBLK	420
	3693	*			430
	3694	*	INPUT NEXT NON-BLANK CHARACTER FROM INPUT STREAM		440
	3695	*			450
	3696	INBLK	MACRO	<NO-ARGUMENTS>	460
	3697	TSX	L,%\$INBLK	CALL SUBROUTINE	470
	3698	ENDM	INBLK		480
	3699	*			490
	3700	*			500
	3701	*		ICMD	510
	3702	*			520
	3703	*	INPUT THREE LETTER COMMAND FROM INPUT STREAM		530
	3704	*			540
	3705	ICMD	MACRO	<NO-ARGUMENTS>	550
	3706	TSX	L,%\$ICMD	CALL SUBROUTINE	560
	3707	ENDM	ICMD		570
	3708	*			580
	3709	*			590
	3710	*		IDELM	600
	3711	*			610
	3712	*	INPUT THE HIGHEST PRIORITY DELIMITER		620
	3713	*			630
	3714	IDELM	MACRO	<NO-ARGUMENTS>	640
	3715	TSX	L,%\$IDELM	CALL SUBROUTINE	650
	3716	ENDM	IDELM		660

0 OPERATOR INTERFACE -- IDELM

			001730	3798	USE	CODE		1480
				3799	HEAD	0		1490
				3800	*			1500
				3801	*			1510
				3802	*		IDELM	1520
				3803	*			1530
				3804	*		THIS SUBROUTINE LOOKS FOR THE HIGHEST PRIORITY DELIMITER	1540
				3805	*			1550
				3806	*	ENTER WITH		1560
				3807	*		C(XT) = TCB	1570
				3808	*		C(XJ) = JCB	1580
				3809	*	ENTER BY		1590
				3810	*		TSX L,0\$IDELM	1600
				3811	*	CALLS		1610
				3812	*		0\$INBLK	1620
				3813	*	RETURNS TO 0,L		1630
				3814	*	RETURNS WITH		1640
				3815	*		C(XT) = TCB	1650
				3816	*		C(XJ) = JCB	1660
				3817	*		C(AL) = DELIMITING CHARACTER	1670
				3818	*	USES		1680
				3819	*		NO LOCALS	1690
				3820	*		T\$TEMP5,T (LOWER)	1700
				3821	*	CLOBBERS C(X0)		1710
				3822	*			1720
			001730	3823	IDELM	BSS	0	1730
001730	000023	4470	11	3824	SXL	L,T\$TEMP5,T	SAVE RETURN ADDRESS	1740
001731	005461	2350	00	3825	LDA	LTCHR	GET BACK LAST CHARACTER	1750
001732	200000	3150	07	3826	CANA	B\$DELIM,DL	MUST HAVE BEEN A DELIMITER	1760
001733	002105	6000	00	3827	TZE	FORER	IF NOT, FORMAT ERROR	1770
001734	005460	0540	00	3828	AOS	RFLG	BACK OVER IT	1780
			001735	3829	INBLK		GET NEXT NON-BLANK	1790
001735	001720	7070	00		TSX	L,0\$INBLK		
001736	400000	3150	07	3830	CANA	B\$TERM,DL	SEE IF TERMINATOR	1800
001737	001742	6010	00	3831	TNZ	**3	IF SO, BACK OVER IT	1810
001740	200000	3150	07	3832	CANA	B\$DELIM,DL	SEE IF DELIMITER	1820
001741	001743	6010	00	3833	TNZ	**2	IF SO RETURN	1830
001742	005460	0540	00	3834	AOS	RFLG	MUST HAVE BEEN A SPACE	1840
001743	000023	7270	11	3835	LXL	L,T\$TEMP5,T	RETRIEVE RETURN ADDRESS	1850
			001744	3836	BUGL	(T\$TEMP5,T)	BUG IT	1860
			525223		BUGBUG	SET	BUGBUG+1	
001744	525223	2200	03		LDX	0,BUGBUG,DU		
001745	000023	4400	11		SXL	0,T\$TEMP5,T		
END OF BINARY CARD	IOS00049							
001746	000000	7100	17	3837	TRA	0,L	RETURN TO CALLER	1870

0 OPERATOR INTERFACE -- GET COMMAND

Address	Code	Label	Operation	Comment	Address
001747	3839	USE	CODE		1890
	3840	HEAD	0		1900
	3841	*			1910
	3842	*			1920
	3843	*		ICMD	1930
	3844	*			1940
	3845	*		PICK UP A 4 CHARACTER OR LESS SYMBOL FOR COMMAND NAME.	1950
	3846	*		THROW AWAY ALL ALPHARETIC CHARACTERS AFTER THE FIRST 4	1960
	3847	*		UNTIL REACHING A DIGIT, DELIMITER, OR TERMINATOR.	1970
	3848	*			1980
001747	3849	ICMD	BSS	0	ENTRY POINT
001747	3850	SXL	L,T\$TEMP6,T		SAVE RETURN ADDRESS
001750	3851	LDQ	BLNKS		PAD Q WITH BLANKS
001751	3852	LDX	Y,36,DU		GET UP TO FOUR 9-BIT CHARACTERS
	3853	INBLK			GET FIRST NON-BLANK CHARACTER
001752	3854	TSX	L,0,\$INRLK		
001753	3855	ICMD1	ICHR		ENTER LOOP
					GET NEXT CHARACTER
001754	3856	CANA	B\$TERM+B\$DELIM+B\$DIGIT+B\$OPR,DL		SEARCH FOR END OF COMMAND
001755	3857	TNZ	ICMD2		EXIT, IF FOUND
001756	3858	ALS	36-9		MOVE TO TOP OF A-REG
001760	3859	LLR	9		NOW INTO BOTTOM OF Q-REG
001761	3860	SBLX	Y,9,DU		DECREMENT 9-BIT CHARACTER COUNTER
001762	3861	TNZ	ICMD1		TEST FOR DONE
	3862	ICHR			DONE, FIND END OF COMMAND
001763	3863	CANA	B\$TERM+B\$DELIM+B\$DIGIT+B\$OPR,DL		
001764	3864	TZE	*-2		KEEP SEARCHING FOR END
001766	3865	ICMD2	AOS	RFLG	SET RE-READ FLAG
001767	3866	QLR	0,Y		POSITION LEFT JUSTIFIED, BLANKED FILL
001770	3867	LLS	36		MOVE INTO A-REG
001771	3868	LXL	L,T\$TEMP6,T		RETRIEVE RETURN ADDRESS
	3869	BUGL	(T\$TEMP6,T)		BUG IT
001772		BUGBUG	SET	BUGBUG+1	
			LDX	0,BUGBUG,DU	
END OF BINARY CARD			SXL	0,T\$TEMP6,T	
001773			TRA	0,L	RETURN TO CALLER
001774					
	3870				2200
	3871	*			2210
	3872	*			2220
	3873	USE	CONST		2230
004700	3874	BLNKS	JASCI	1,	2240
	3875	USE	PREVIOUS		2250
	3876	**	DISK	OPCON	2260

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OPERATOR INTERFACE -- ENTRY

			3878	USE	CODE		110
			3879	HEAD	0		120
			3880	*			130
			3881	*			140
			3882	*			150
			3883	*	ENTER		160
			3884	*	THE OPERATOR INTERFACE IS ENTERED FROM THE NOTIFY SERVICE		170
			3885	*	ROUTINE ON RECEIPT OF A CAUSE FROM THE JOB STREAM SCHEDULER		180
			3886	*	TO READ THE CONTROL TTY. IT SETS UP THE NECESSARY TASK TO		190
			3887	*	TALK WITH THE OPERATOR, AND EXITS TO CONTINUE NOTIFY SERVICE.		200
			3888	*			210
			3889	*			220
		001775	3890	ENTER	BSS	0	230
		001775	3891	BRANCH	NOPASS,INPUT	CREATE TASK TO CONVERSE WITH OP	240
001775	001467	7000	00	TSX	0,T\$GETT		
001776	000000	6220	11	EAX	X,0,T		
001777	000005	2210	12	LDX	T,T\$LINK,X		
002000	000000	6210	12	EAX	T,0,X		
002001	002011	6200	00	EAX	0,INPUT		
002002	000004	7400	11	STX	0,T\$TRA,T		
002003	000004	6200	11	EAX	0,Q\$OFFST,T		
002004	005162	7170	00	XED	Q\$XADD+Q\$TASK		
002005	000005	2210	12	LDX	T,T\$LINK,X		
		002006		BUGXR	(0,X)		
		525225		BUGBUG	SET	BUGBUG+1	
002006	525225	2200	03	LDX	0,BUGBUG,DU		
002007	525225	2220	03	LDX	X,BUGBUG,DU		
002010	003171	7100	00	3892	TRA	C\$NSRVX	RE-ISSUE NOTIFY

0 OPERATOR INTERFACE -- INITIALIZE

		002011	3894	USE	CODE		270
			3895	HEAD	0		280
			3896 *				290
			3897 *				300
			3898 *			INITIALIZATION	310
			3899 *				320
			3900 *	INITIALIZE	IN ORDER TO TALK WITH OPERATOR		330
			3901 *				340
		002011	3902	INPUT	BSS	0	ENTER HERE TO SET CONVERSATIONAL MODE
		002011	3903		LOGS		SEIZE CTY, LOCK IT, SEND IDENTIFIER
002011	001517	7070 00		TSX	L,0\$LOGS		
		002012	3904	INP1	LOG		370
002012	001645	7070 00		TSX	L,0\$LOG		
002013	000200	2350 03	3905	LDA	IBFSz*4,DU	GET NUMBER OF CHARACTERS TO READ	380
002014	005463	7550 00	3906	STA	INCHR	SETUP FOR READ	390
			3907 *				400
			3908 *	READ	INPUT FROM CTY		410
			3909 *				420
		002015	3910	INP2	READ	R\$OPFRN,(IBUF,DU),INCHR,(2,DU)	430
END OF BINARY CARD	IOS00051						
002015	000536	7000 00		TSX	0,\$READ		
002016	005665	0000 00		ARG	R\$OPFRN		
002017	005464	0000 03		ARG	IBUF,DU		
002020	005463	0000 00		ARG	INCHR		
002021	000002	0000 03		ARG	2,DU		
		002022	3911	CHECK	INP3,B\$BZ,INP2,B\$TRO,INP2		440
002022	000000	7200 11		LXL	0,T\$SRW1,T		
002023	000077	3600 03		ANX	0,B\$STMK,DU		
002024	002032	6000 00		TZE	INP3		
002025	000003	1000 03		CMPX	0,B\$BZ,DU		
002026	002015	6000 00		TZE	INP2		
002027	000035	1000 03		CMPX	0,B\$TRO,DU		
002030	002015	6000 00		TZE	INP2		
002031	777777	7100 00		TRA	\$ERROR		
			3912 *				450
			3913 *	INFORMATION	READ		460
			3914 *				470
		002032	3915	INP3	BSS	0	
002032	000000	2350 11	3916	LDA	T\$SRW1,T	GET NUMBER OF CHARACTER RECEIVED	480
002033	777777	3750 03	3917	ANA	-1,DU	ISOLATE NUMBER	490
002034	002015	6000 00	3918	TZE	INP2	NOTHING, READ AGAIN	500
002035	000014	7710 00	3919	ARL	18-6	MOVE LENGTH TO CHARACTER TALLY POSITION	510
002036	000140	0750 07	3920	ADA	STAL+\$TALYB,DL	SET TALLY BYTE BIT AND BUMP TALLY ONE	520
002037	005464	2750 03	3921	ORA	IBUF,DU	SET IN ADDRESS	530
002040	005462	7550 00	3922	STA	ITAL	SAVE AS WORKING TALLY	540
		002041	3923	INP4	BSS	0	550
002041	005526	4500 00	3924	STZ	ERCMD	RESET COMMAND ERROR COUNT	560
002042	005461	4500 00	3925	STZ	LTCHR	RESET LAST CHARACTER RECEIVED	570
END OF BINARY CARD	IOS00052						
002043	005460	4500 00	3926	STZ	RFLG	RESET RE-READ FLAG	580

0		OPERATOR INTERFACE -- INITIALIZE				
002044	002045 7100 00	3927	TRA	LSCAN	GO TO LINE SCAN	600
		3928	*			610
	005462	3929	USE	STORE		620
	000040	3930	IBFSZ	EQU	32	LENGTH OF INPUT BUFFER
005462	005464 0024 40	3931	ITAL	TALLYR	IBUF,4*ILEN,0	INPUT TALLY WORD
005463	000000 000000	3932	INCHR	ZERO	**	NUMBER OF CHARACTERS TO READ
		3933	*			660
		3934	*			670
	005464	3935	IBUF	BSS	0	INPUT AREA
005464	107105124040	3936	UASCI	4,GET LP01# EXIT		680
005470	004004004004	3937	OCT	004004004004	FAKE END OF READ	690
	000005	3938	ILEN	EQU	*-IBUF	700
	005471	3939	BSS	IBFSZ-ILEN	RESERVE REST OF BUFFER	710
005524	004004004004	3940	OCT	004004004004	PAD THE END	720
	002045	3941	USE	PREVIOUS		730
		3942	**	DISK	LSCAN	740
						750

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OPERATOR INTERFACE -- LINE SCAN

Address	Code	Label	Description	Line
002045	3944	USE		110
	3945	HEAD	0	120
	3946	*		130
	3947	*		140
	3948	*	LINE SCAN	150
	3949	*		160
	3950	*	THIS ROUTINE RECOGNIZES COMMANDS. A TERMINATOR	170
	3951	*	ON THE LAST COMMAND OF AMPERSAND IS TAKEN TO	180
	3952	*	INDICATE A REPETITION OF THAT COMMAND, ELSE	190
	3953	*	A NEW COMMAND IS PICKED UP AND EXECUTED.	200
	3954	*		210
	002045	3955	LSCAN BSS 0	220
002045	005460	4500	00 3956 STZ RFLG	230
002046	005461	2350	00 3957 LDA LTCHR	240
002047	600046	1150	07 3958 CMPA A\$AMPER,DL	250
002050	005525	6000	51 3959 TZE LCOMD,1	260
		002051	3960 LSCAN1 BSS 0	270
002051	600004	1150	07 3961 CMPA A\$EOT,DL	280
002052	002070	6000	00 3962 TZE LSCANX	290
		002053	3963 INBLK	300
002053	001720	7070	00 TSX L,0\$INBLK	
002054	400000	3150	07 3964 CANA B\$TERM,DL	310
002055	002051	6010	00 3965 TNZ LSCAN1	320
END OF BINARY CARD	10S00053			
002056	005460	0540	00 3966 AOS RFLG	330
		002057	3967 LSCAN3 BSS 0	340
		002057	3968 ICMU	350
002057	001747	7070	00 TSX L,0\$ICMD	
002060	005526	0540	00 3969 AOS ER CMD	360
002061	000000	2220	03 3970 LDX X,0,DU	370
002062	012300	5202	02 3971 RPT CMDLN,2,TZE	380
002063	004717	1150	12 3972 CMPA CMDT9,X	390
002064	002075	6010	00 3973 TNZ CMDBR	400
002065	777777	2220	12 3974 LDX X,-1,X	410
002066	005525	7420	00 3975 STX X,LCOMD	420
002067	000000	7100	12 3976 TRA 0,X	430
			3977 *	440
			3978 *	450
			3979 *	460
			3980 *	470
		005525	3981 USE STORE	480
		525226	3982 BUGBUG SET BUGBUG+1	490
005525	525226	0000	00 3983 LCOMD ARG BUGBUG	500
005526	000000000000		3984 ER CMD DEC 0	510
		002070	3985 USE PREVIOUS	520

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OPERATOR INTERFACE -- MORE

		002070	3987	USE	CODE		540
			3988	HEAD	0		550
			3989 *				560
			3990 *				570
			3991 *			MORE?	580
			3992 *				590
			3993 *	THIS ROUTINE ASKS THE OP FOR MORE COMMANDS.			600
			3994 *	NOTE THAT IT RELEASES AND THEN SEIZES THE CONTROL TTY			610
			3995 *	TO ALLOW ANY OTHER WAITING TASKS TO LOG THEIR MESSAGES.			620
			3996 *				630
		002070	3997	LSCNX	BSS	0	640
		002070	3998	LOGX		RELEASE CONTROL -- ALLOW OTHERS TO SPEAK	650
002070	001560	7070 00		TSX	L,0\$LOGX		
		002071	3999	LOGS		SEIZE CONTROL AGAIN	660
002071	001517	7070 00		TSX	L,0\$LOGS		
		002072	4000	LOGC	MORMS	*MORE? *	670
002072	004701	2350 00		LDA	MORMS		
002073	001543	7070 00		TSX	L,0\$LOGC		
002074	002012	7100 00	4001	TRA	INP1	READ REPLY	680
			4002 *				690
		004701	4003	USE	CONST		700
004701	004702	0013 40	4004	MORMS	TALLYR	**MORE? *	710
004702	177177015012		4005	OCT	177177015012	WARM UP TTY	720
END OF BINARY CARD IOS00054							
004703	115117122105		4006	UASCI	2, MORE?		730
	002075		4007	USE	PREVIOUS		740

0 OPERATOR INTERFACE -- COMMAND TABLE

	002110	4052	USE	CODE		110
		4053	HEAD	0		120
		4054 *				130
		4055 *				140
		4056 *			COMMAND TABLE	150
		4057 *				160
		4058 *	THESE COMMANDS ARE IN ALPHABETICAL ORDER			170
		4059 *				180
	004717	4060	USE	CONST		190
	004717	4061	CMDTB	BSS	0	200
					BACKWARD COMMAND TABLE	
004717	105130111124	4062	UASCI	1,EXIT	EXIT -- RELEASE TTY	210
004720	002372 0000 00	4063	ARG	OPX		220
004721	107105124040	4064	UASCI	1,GET	REOPEN CLOSED PERIPHERAL	230
004722	002110 0000 00	4065	ARG	GET		240
004723	113111114114	4066	UASCI	1,KILL	KILL A PERIPHERAL DEVICE	250
004724	002220 0000 00	4067	ARG	KILL		260
004725	122105114105	4068	UASCI	1,RELEASE	CLOSE A PERIPHERAL	270
004726	002253 0000 00	4069	ARG	REL		280
004727	122105123124	4070	UASCI	1,RESTART	RESTART A PERIPHERAL	290
004730	002277 0000 00	4071	ARG	STRT		300
	000012	4072	CMDLX	EQU	*-CMDTB	TABLE LENGTH
	000005	4073	CMDLN	EQU	CMDLX/?	NUMBER OF TABLE ENTRIES
	002110	4074	USE	PREVIOUS		330
		4075	***	DISK	OGET	340

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OPERATOR INTERFACE -- GET COMMAND

		002110	4077	USE	CODE		110
			4078	HEAD	0		120
			4079	*			130
			4080	*			140
			4081	*		GET COMMAND	150
			4082	*			160
			4083	*	RE-OPENS THE PERIPHERAL IDENTIFIED BY THE FOUR LETTER		170
			4084	*	ABBREVIATION. THE FORMAT IS:		180
			4085	*			190
			4086	*	GET <PERIPHERAL-NAME>:...;<PERIPHERAL-NAME>		200
			4087	*			210
			4088	*			220
		002110	4089	GET	BSS	0	230
	002110	002332	4090	TSX	L,PERI	GET PERIPHERAL INFORMATION	240
	002111	000023	4091	STX	X,T\$TEMP5,T	SAVE TYPE AND	250
	002112	000023	4092	SXL	Z,T\$TEMP5,T	DEVICE POINTERS	260
			4093	*			270
			4094	*	CHECK IF CURRENTLY CLOSED		280
			4095	*			290
	002113	000002	4096	LDA	R\$FLAG,Z	GET PERIPHERAL FLAGS	300
	002114	200000	4097	CANA	R\$CLOSE,DU	CHECK IF CLOSED	310
	002115	002206	4098	TZE	GET5	IGNORE COMMAND IF NOT	320
END	OF BINARY CARD	IOS00056					
	002116	000000	4099	LDA	R\$ABBR,Z	GET ITS NAME	330
	002117	005157	4100	CMPA	Q\$OP00+Q\$ABBR	DON'T ALLOW OP TO	340
	002120	002206	4101	TZE	GET5	FOOL WITH HIS CONSOLE	350
	002121	005535	4102	STA	PTN+6	CONSTRUCT TREE-NAME	360
	002122	000002	4103	LDX	0,R\$ELT,X	GET ELEMENT SIZE	370
	002123	000027	4104	STX	0,T\$TEMP1,T	SAVE FOR OPEN	380
		002124	4105	GET1	OPEN	(PTN,DU),(PTS,DU),(1,DU),(T\$TEMP1,T),(0,DU)	390
	002124	000667		TSX	0,\$OPEN		
	002125	005527		ARG	PTN,DU		
	002126	000014		ARG	PTS,DU		
	002127	000001		ARG	1,DU		
	002130	000027		ARG	T\$TEMP1,T		
	002131	000000		ARG	0,DU		
		002132	4106	CHECK	GET2,R\$BZ,GET1,\$LOCK,GET4		400
	002132	000000		LXL	0,T\$SRW1,T		
	002133	000077		ANX	0,\$\$STK,DU		
	002134	002142		TZE	GET2		
	002135	000003		CMPX	0,\$\$BZ,DU		
	002136	002124		TZE	GET1		
	002137	000013		CMPX	0,\$\$LOCK,DU		
	002140	002204		TZE	GET4		
	002141	777777		TRA	\$ERROR		
			4107	*			410
			4108	*	PERIPHERAL OPENED		420
			4109	*			430
		002142	4110	GET2	BSS	0	440
002142	000023	2220	4111	LDX	X,T\$TEMP5,T	RESTORE PERIPHERAL POINTERS	450

0			OPERATOR INTERFACE -- GET COMMAND			
END	002143	000023 7240 11	4112	LXL	Z,T\$TEMP5,T	460
		OF BINARY CARD 10S00057				
	002144	000000 2200 11	4113	LDX	0,T\$SRW1,T	470
	002145	000001 7400 14	4114	STX	0,R\$FRN,Z	480
	002146	000025 7400 11	4115	STX	0,T\$TEMP3,T	490
	002147	000004 0540 12	4116	AOS	R\$DMAX,X	500
	002150	000005 0540 12	4117	AOS	R\$AVAIL,X	510
		002151	4118	DECRM	(R\$OPER,X)	520
	002151	000001 3360 07		LCQ	1,DL	
	002152	000006 0560 12		ASQ	R\$OPER,X	
	002153	777777 6040 00	4119	TMI	\$ERROR	530
	002154	077777 2200 03	4120	LDX	0,-1-R\$BUSY-R\$CLOSE-R\$RSVE,DU	540
	002155	000002 3400 14	4121	ANSX	0,R\$FLAG,Z	550
			4122 *			560
			4123 *	SEND PERIPHERAL TO SUBMODULE		570
			4124 *			580
	002156	000001 2350 12	4125	LDA	R\$STATE,X	590
	002157	000027 7550 11	4126	STA	T\$TEMP1,T	600
	002160	000000 2350 14	4127	LDA	R\$ABR,7	610
	002161	000007 3750 07	4128	ANA	7,DL	620
	002162	000022 7350 00	4129	ALS	18	630
	002163	002000 2750 03	4130	ORA	B\$,GET,DU	640
	002164	000026 7550 11	4131	STA	T\$TEMP2,T	650
	002165	000002 7200 12	4132	LXL	0,R\$ACC,X	660
	002166	000024 7400 11	4133	STX	0,T\$TEMP4,T	670
		002167	4134	BSS	0	680
		002167	4135	CAUSE	C\$FRN2,(1,DU),(T\$TEMP1,T),(T\$TEMP2,T)	690
		002167	4136	ETC	(T\$TEMP3,T),(T\$TEMP4,T)	700
	002167	000767 7000 00		TSX	0,\$CAUSE	
	002170	006221 0000 00		ARG	C\$FRN2	
	002171	000001 0000 03		ARG	1,DU	
END		OF BINARY CARD 10S00058				
	002172	000027 0000 11		ARG	T\$TEMP1,T	
	002173	000026 0000 11		ARG	T\$TEMP2,T	
	002174	000025 0000 11		ARG	T\$TEMP3,T	
	002175	000024 0000 11		ARG	T\$TEMP4,T	
		002176	4137	CHECK	GET4,B\$BZ,GET3 CHECK STATUS	710
	002176	000000 7200 11		LXL	0,T\$SRW1,T	
	002177	000077 3600 03		ANX	0,B\$STMK,DU	
	002200	002204 6000 00		TZE	GET4	
	002201	000003 1000 03		CMPX	0,B\$BZ,DU	
	002202	002167 6000 00		TZE	GET3	
	002203	777777 7100 00		TRA	\$ERROR	
		002204	4138	GET4	BSS	0
	002204	000000 2220 11	4139	LDX	X,T\$SRW1,T	720
	002205	002167 6000 00	4140	TZE	GET3	730
		002206	4141	GET5	BSS	0
			4142 *			740
			4143 *	PERIPHERAL PASSED, TELL OPERATOR		750
			4144 *			760
						770
						780

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OPERATOR INTERFACE -- GET COMMAND

		002206	4145	LOGC	OPMS	TELL OPERATOR PERIPHERAL OPENED:	790
002206	004731	2350 00		LDA	OPMS		
002207	001543	7070 00		TSX	L,0\$LOGC		
		004731	4146	USE	CONST		800
004731	004732	0030 40	4147	OPMS	**1,23+1,0	*PERIPHERAL OPENED: *	810
004732	177177	015012	4148	OCT	177177015012		820
004733	120105	122111	4149	UASCI	5,PERIPHERAL OPENED:		830
		002210	4150	USE	PREVIOUS		840
END OF BINARY CARD I/O00059							
002210	000023	7240 11	4151	LXL	Z,T\$TEMP5,T	RESTORE DEVICE POINTER	850
002211	000000	6350 14	4152	EAA	RSABR,Z	POINT TO ABBREVIATION	860
002212	000540	2750 07	4153	ORA	4*\$TAL+\$TAL+\$TALYB,DL	ABBREVIATIONS ARE FOUR CHARACTERS	870
		002213	4154	LOGC	A		880
002213	001543	7070 00		TSX	L,0\$LOGC		
			4155 *				890
			4156 *		OPERATOR INFORMED, SEE IF MORE		900
			4157 *				910
		002214	4158	IDELM		GET DELIMITER	920
002214	001730	7070 00		TSX	L,0\$IDELM		
002215	200073	1150 07	4159	CMPA	A\$SCOL,DL	SEMI-COLON?	930
002216	002110	6000 00	4160	TZE	GET	GET NEXT	940
002217	002045	7100 00	4161	TRA	LSCAN	OTHERWISE GET NEXT COMMAND	950
			4162 *				960
			4163 *				970
		005527	4164	USE	STORE		980
		005527	4165	PTN	0	PERIPHERAL TREE-NAME	990
005527	104105	126111	4166	UASCI	6,DEVICE		1000
005535	117120	060060	4167	UASCI	1,OP00	DEVICE TO BE OPENED GOES HERE	1010
005536	040040	040040	4168	UASCI	5,		1020
		000014	4169	PTS	EQU	**PTN	TREE-SIZE
		002220	4170	USE	PREVIOUS		1040
			4171 **	DISK	OKILL		1050

		0		OPERATOR INTERFACE -- KILL COMMAND			
		002220		4173	USE	CODE	110
				4174	HEAD	0	120
				4175 *			130
				4176 *			140
				4177 *		KILL COMMAND	150
				4178 *			160
				4179 *	FORCES THE IMMEDIATE TERMINATION OF THE PERIPHERAL IDENTIFIED		170
				4180 *	BY THE FOUR LETTER ABBREVIATION. THE FORMAT IS:		180
				4181 *			190
				4182 *	KILL <PERIPHERAL-NAME>\$. . . ; <PERIPHERAL-NAME>		200
				4183 *			210
		002220		4184	KILL	BSS 0	220
END OF BINARY CARD	IOS00060						
002220	002332	7070	00	4185	TSX	L,PEPI GET PERIPHERAL INFORMATION	230
002221	000023	7420	11	4186	STX	X,T\$TEMP5,T SAVE TYPE AND	240
002222	000023	4440	11	4187	SXL	Z,T\$TEMP5,T DEVICE POINTERS	250
				4188 *			260
				4189 *	CHECK IF PERIPHERAL IS IN USE		270
				4190 *			280
002223	000002	2350	14	4191	LDA	R\$FLAG,Z GET PERIPHERAL FLAG	290
002224	400000	3150	03	4192	CANA	R\$BUSY,DU CHECK IF BUSY	300
002225	002250	6000	00	4193	TZE	KILL1 EXIT IF NOT	310
				4194 *			320
				4195 *	SEND MESSAGE TO KILL THIS JOB		330
				4196 *			340
				4197		SEND MESSAGE TO ABORT JOB IN PROGRESS	350
002226	000001	2350	12	4198	LDA	R\$STATE,X GET STATE FOR CAUSE	360
002227	000027	7550	11	4199	STA	T\$TEMP1,T SAVE FOR COMMUNICATIONS ROUTINE	370
002230	000000	2350	14	4200	LDA	R\$ABBR,Z GET NAME OF PERIPHERAL TO CLOSE	380
002231	000007	3750	07	4201	ANA	7,DL MASK TO UNIT NUMBER	390
002232	000022	7350	00	4202	ALS	18 POSITION IN MESSAGE FIELD	400
002233	004000	2750	03	4203	ORA	B\$,KILL,DU COMMAND: KILL	410
002234	000026	7550	11	4204	STA	T\$TEMP2,T SAVE FOR COMMUNICATIONS	420
		002235		4205	BRANCH	PASS,C\$MESSX CREATE TASK TO SEND MESSAGE	430
002235	001467	7000	00		TSX	0,T\$GETT	
002236	000000	6220	11		EAX	X,0,T	
002237	000005	2210	12		LDX	T,T\$LINK,X	
002240	003140	6200	00		EAX	0,C\$MESSX	
002241	000004	7400	11		STX	0,T\$TRA,T	
002242	000004	6200	11		EAX	0,Q\$OFFST,T	
002243	005162	7170	00		XED	Q\$XADD+Q\$TASK	
002244	000000	6210	12		EAX	T,0,X	
		002245			BUGXR	(0,X)	
		525227			BUGRUG	SET BUGRUG+1	
002245	525227	2200	03		LDX	0,BUGRUG,DU	
END OF BINARY CARD	IOS00061						
002246	525227	2220	03		LDX	X,BUGRUG,DU	
				4206 *			440
				4207 *	MESSAGE SENT, SEE IF MORE		450
				4208 *			460

0			OPERATOR INTERFACE -- KILL COMMAND				
002247	001730	7070 00	4209	IDELM TSX	L,0%IDELM	GET DELIMITER	470
		002247					
		002250	4210	KILL	BSS	0	480
002250	200073	1150 07	4211	CMPA	A\$SCOL,DL	SEMI-COLON?	490
002251	002220	6000 00	4212	TZE	KILL	YES, GET NEXT PERIPHERAL NAME	500
002252	002045	7100 00	4213	TRA	LSCAN	NO, GET NEXT COMMAND	510
			4214	**	DISK	OREL	520

0 OPERATOR INTERFACE -- CLOSE COMMAND

			002253	4216	USE	CODE		110
				4217	HEAD	0		120
				4218	*			130
				4219	*			140
				4220	*		CLOSE COMMAND	150
				4221	*			160
				4222	*		CLOSES THE PERIPHERAL IDENTIFIED BY THE FOUR LETTER	170
				4223	*		ABBREVIATION. THE FORMAT IS:	180
				4224	*			190
				4225	*		RELE <PERIPHERAL-NAME>; ...;<PERIPHERAL-NAME>	200
				4226	*			210
			002253	4227	REL	BSS	0	220
002253	002332	7070	00	4228	TSX	L,PERI	GET PERIPHERAL INFORMATION	230
002254	000023	7420	11	4229	STX	X,T\$TEMP5,T	SAVE TYPE AND	240
002255	000023	4440	11	4230	SXL	Z,T\$TEMP5,T	DEVICE POINTERS	250
				4231	*			260
				4232	*		CHECK FOR ALREADY CLOSED	270
				4233	*			280
002256	000002	2350	14	4234	LDA	R\$FLAG,Z	GET PERIPHERAL FLAGS	290
002257	200000	3150	03	4235	CANA	R\$CLOSE,DU	CHECK IF CLOSED	300
002260	002273	6010	00	4236	TNZ	REL2	EXIT IF SO	310
002261	100000	2750	03	4237	ORA	R\$RSVE,DU	ASK TO HAVE IT RESERVED	320
002262	000002	7550	14	4238	STA	R\$FLAG,Z	RESTORE FLAGS	330
002263	400000	3150	03	4239	CANA	R\$BUSY,DU	CHECK IF CURRENTLY BUSY	340
002264	002273	6010	00	4240	TNZ	REL2	BUSY--IT WILL CLOSE EVENTUALLY	350
				4241	*			360
				4242	*		DEVICE IDLE -- SEIZE IT	370
				4243	*			380
			002265	4244	REL1	BSS	0	390
002265	500000	2350	03	4245	LDA	R\$BUSY+R\$RSVE,DU	*MARK IT BUSY AND RESERVED	400
002266	000002	2550	14	4246	ORSA	R\$FLAG,Z		410
002267	000002	4460	14	4247	SXL	J,R\$ALLO,Z	AND ALLOCATED TO US	420
002270	000006	4460	11	4248	SXL	J,T\$JCB,T	FUDGE	430
002271	000023	2350	11	4249	LDA	T\$TEMP5,T	GET RELEASE PARAMTERS	440
002272	002447	7070	00	4250	TSX	L,R\$RELP	CALL RELEASE	450
				4251	*			460
				4252	*		PERIPHERAL RELEASED, CHECK IF MORE	470
				4253	*			480
			002273	4254	REL2	BSS	0	490
			002273	4255	REL2	IDELM	GET DELIMITER	500
END OF BINARY CARD	IOS00062							
002273	001730	7070	00		TSX	L,OSIDELM		
002274	200073	1150	07	4256	CMPA	A\$SCOL,DL	SEMI-COLON?	510
002275	002253	6000	00	4257	TZE	REL	YES, GET NEXT PERIPHERAL NAME	520
002276	002045	7100	00	4258	TRA	LSCAN	NO, GET NEXT COMMAND	530
				4259	**	DISK	O\$TRT	540

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OPERATOR INTERFACE -- START COMMAND

		002277	4261	USE	CODE		110	
			4262	HEAD	0		120	
			4263	*			130	
			4264	*			140	
			4265	*		START COMMAND	150	
			4266	*			160	
			4267	*		STARTS (RESTARTS) THE PERIPHERAL IDENTIFIED BY THE FOUR LETTER	170	
			4268	*		ABBREVIATION. THE FORMAT IS:	180	
			4269	*			190	
			4270	*		START <PERIPHERAL-NAME> ; . . . ; <PERIPHERAL-NAME>	200	
			4271	*			210	
		002277	4272	STRT	BSS	0	220	
002277	002332	7070	00	4273	TSX	L,PERI	GET PERIPHERAL INFORMATION	230
002300	000023	7420	11	4274	STX	X,T\$TEMP5,T	SAVE TYPE AND	240
002301	000023	4440	11	4275	SXL	Z,T\$TEMP5,T	DEVICE PTRS	250
				4276	*		260	
				4277	*	CHECK IF PERIPHERAL IS IN USE	270	
				4278	*		280	
002302	000002	2350	14	4279	LDA	R\$FLAG,Z	GET PERIPHERAL FLAG	290
002303	400000	3150	03	4280	CANA	R\$BUSY,DU	CHECK IF BUSY	300
002304	002326	6000	00	4281	TZE	STR2	EXIT IF NOT	310
				4282	*		320	
				4283	*	GET RESTART ADDRESS (CURRENTLY NOT IMPLEMENTED)	330	
				4284	*		340	
				4285	*		350	
				4286	*	SEND MESSAGE TO SUBMODULE TO RESTART	360	
				4287	*		370	
002305	000001	2350	12	4288	LDA	R\$STATE,X	GET STATE FOR CAUSE	380
002306	000027	7550	11	4289	STA	T\$TEMP1,T	SAVE FOR COMMUNICATIONS ROUTINE	390
002307	000000	2350	14	4290	LDA	R\$RBR,Z	GET NAME OF PERIPHERAL TO RESTART	400
002310	000007	3750	07	4291	ANA	7,DL	MASK TO UNIT NUMBER	410
002311	000022	7350	00	4292	ALS	18	POSITION IN MESSAGE FIELD	420
002312	012000	2750	03	4293	ORA	B\$,STR2,DU	COMMAND: RESTART	430
002313	000026	7550	11	4294	STA	T\$TEMP2,T	SAVE MESSAGE	440
		002314		4295	BRANCH	PASS,C\$MESSX	CREATE TASK TO SEND MESSAGE	450
002314	001467	7000	00		TSX	0,T\$GETT		
002315	000000	6220	11		EAX	X,0,T		
002316	000005	2210	12		LDX	T,T\$LINK,X		
002317	003140	6200	00		EAX	0,C\$MESSX		
END OF BINARY CARD	IOS00063							
002320	000004	7400	11		STX	0,T\$TRA,T		
002321	000004	6200	11		EAX	0,Q\$OFFST,T		
002322	005162	7170	00		XED	Q\$XADD+Q\$TASK		
002323	000000	6210	12		EAX	T,0,X		
		002324			BUGXR	(0,X)		
		525230			BUGBUG	SET	BUGBUG+1	
002324	525230	2200	03		LDX	0,BUGBUG,DU		
002325	525230	2220	03		LDX	X,BUGBUG,DU		
		002326		4296	STRT2	BSS	0	460
				4297	*		470	

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OPERATOR INTERFACE -- START COMMAND

			4298 *	MESSAGE SENT, SEE IF MORE		480
			4299 *			490
		002326	4300	IDELM	GET DELIMITER	500
002326	001730	7070 00		TSX	L,OSIDELM	
002327	200073	1150 07	4301	CMPA	A\$SCOL,DL	SEMI-COLON?
002330	002277	6000 00	4302	TZE	STRY	YES, GET NEXT PERIPHERAL NAME
002331	002045	7100 00	4303	TRA	LSCAN	NO, GET NEXT COMMAND
			4304 **	DISK	OPCON2	540

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OPERATOR INTERFACE -- PERI SUBROUTINE

			4306	HEAD	0		110	
	002332		4307	USE	CODE		120	
			4308	*			130	
			4309	*			140	
			4310	*		PERI SUBROUTINE	150	
			4311	*			160	
			4312	*		GET PERIPHERAL ABBREVIATION FROM INPUT STREAM. IT SETS	170	
			4313	*		C(XX) TO THE DEVICE HEADER AND C(XZ) TO THE DEVICE TABLE	180	
			4314	*		ENTRY. IF IT FINDS NO SUCH DEVICE, IT LOGS A MESSAGE	190	
			4315	*		SAYING SO.	200	
			4316	*			210	
			4317	*		PERIPHERAL ABBREVIATION IS OF THE FORM:	220	
			4318	*		<PER. ABBR.>:='2 ALPHABETICS'+2 DIGITS' (E.G. LP01)	230	
			4319	*			240	
			4320	*		RETURNS WITH	250	
			4321	*		C(XX) = PTR TO DEVICE HEADER	260	
			4322	*		C(XZ) = PTR TO DEVICE UNIT	270	
			4323	*			280	
		002332	4324	PERI	BSS	0	290	
002332	000022	4470 11	4325	SXL	L,T\$TEMP6,T	SAVE RETURN ADDRESS	300	
002333	000044	3230 03	4326	LCX	Y,36,DU	LOCK FOR FOUR 9-BIT CHARACTERS	310	
		002334	4327	INBLK		GET FIRST NON-BLANK CHARACTER	320	
002334	001720	7070 00		TSX	L,0\$INBLK			
002335	002337	7100 00	4328	TRA	**2	ENTER LOOP	330	
		002336	4329	PERI1	ICHR		GET NEXT CHARACTER	340
002336	001674	7070 00		TSX	L,0\$ICHR			
002337	640000	3150 07	4330	CANA	B\$TERM+B\$DELIM+B\$OPR,DL	*TEST FOR DELIMITER	350	
002340	002346	6010 00	4331	TNZ	PERI2	END OF ABBREVIATION	360	
002341	000033	7350 00	4332	ALS	36-9	MOVE TO AU (LEFT JUSTIFIED)	370	
002342	000011	7770 00	4333	LLR	9	NOW TO BOTTOM OF Q	380	
002343	000011	0230 03	4334	ADLX	Y,9,DU	BUMP 9-BIT COUNTER	390	
002344	002336	6040 00	4335	TMI	PERI1	LOCK IF MORE	400	
END OF BINARY CARD	10S00064							
002345	002347	7100 00	4336	TRA	**2	DON'T BACK UP	410	
002346	005460	0540 00	4337	PERI2	AOS	RFLG	BACK OVER LAST CHARACTER	420
		002347	4338	INBLK		GET NEXT NON-BLANK	430	
002347	001720	7070 00		TSX	L,0\$INBLK			
002350	600000	3150 07	4339	CANA	B\$TERM+B\$DELIM,DL	*LOOK FOR TERMINATOR OR DELIMITER	440	
002351	002105	6000 00	4340	TZE	FORER	FORMAT ERROR	450	
			4341	*			460	
			4342	*		FIND TABLE ENTRY	470	
			4343	*			480	
002352	005540	2220 03	4344	LDX	X,R\$DEVHR-R\$DHLEN,DU	POINT TO DEVICE HEADERS	490	
		002353	4345	PERI3	BSS	0	LOOP HERE	500
002353	000010	0220 03	4346	ADLX	X,R\$DHLEN,DU	BUMP TO NEXT HEADER	510	
002354	005620	1020 03	4347	CMPX	X,R\$XDVHR,DU	TEST FOR DONE	520	
002355	002105	6030 00	4348	TRC	FORER	FORMAT ERROR	530	
002356	000000	2240 12	4349	LDX	Z,R\$PTR,X	GET PTR TO UNIT TABLE	540	
002357	000003	2350 12	4350	LDA	R\$MAX,X	GET NUMBER TO SEARCH	550	
002360	000012	7350 00	4351	ALS	18-8	SET UP REPEAT	560	

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002361	001100	6200	05	4352	EAX	0,B\$ABIT+B\$TZE,AL	*TERMINATE CONDITIONS	570
002362	000000	5202	03	4353	RPTX	,R\$DFVLN	LOOK FOR ABBREVIATION	580
002363	000000	1160	14	4354	CMPQ	R\$ABRR,Z		590
002364	002353	6010	00	4355	TNZ	PERI3	LOOP IF NO MATCH	600
002365	000003	1240	03	4356	SBLX	Z,R\$ABRR+R\$DEVLN,DU	*RESET INDEX	610
002366	000022	7270	11	4357	LXL	L,T\$TEMP6,T	RETRIEVE RETURN ADDRESS	620
		002367		4358	BUGL	(T\$TEMP6,T)	BUG IT	630
		525231			BUGBUG SET	BUGBUG+1		
002367	525231	2200	03		LDX	0,BUGBUG,DU		
002370	000022	4400	11		SXL	0,T\$TEMP6,T		
002371	000000	7100	17	4359	TRA	0,L	RETURN TO CALLER	640
				4360	*\$*	DISK	OPEXIT	650

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OPERATOR INTERFACE -- EXIT

		002372	4362	USE	CODE		110
			4363	HEAD	0		120
			4364 *				130
			4365 *				140
			4366 *		EXIT		150
			4367 *				160
			4368 *	VARIOUS WAYS OF EXITING THE OPERATOR INTERFACE			170
			4369 *				180
		002372	4370 OPX	BSS	0		190
		002372	4371	LOGC	MOK	LOG OK MESSAGE	200
END OF BINARY CARD	IOS00065			LDA	MOK		
002372	004740 2350 00			TSX	L,0\$LOGC		
002373	001543 7070 00			LOGX		SEND MESSAGE	210
	002374	4372		TSX	L,0\$LOGX		
002374	001560 7070 00			RELT		RELEASE TRAP BLOCK	220
	002375	4373		TSX	0,TSRELT		
002375	001477 7000 00			EXIT		AND EVAPORATE	230
	002376	4374		TRA	\$EXIT		
002376	003074 7100 00						240
			4375 *				250
			4376 *				260
		004740	4377	USE	CONST		270
004740	004741 0010 40		4378 MOK	TALLYB	*+1,8	OK MESSAGE TALLY	280
004741	177015012117		4379	OCT	177015012117,113015012177	RR CR LF 0 K CR LF RB	290
	002377	4380		USE	PREVIOUS		300
			4381 **	DISK	PERMAC		

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PERIPHERAL MANAGEMENT -- DESCRIPTION

002377	4383	USE	CODE		110
	4384	HEAD	R		120
	4385 *				130
	4386 *				140
	4387 *			DESCRIPTION	150
	4388 *				160
	4389 *	THESE MACROS GET A RELEASE A SINGLE PERIPHERAL			170
	4390 *				180
	4391 *				190
	4392 *			GETP	200
	4393 *				210
	4394 *	GET A PERIPHERAL			220
	4395 *				230
	4396 GETP	MACRO	TYPE/ 'A'		240
	4397	INE	'#1','A'		250
	4398	LDA	#1	GET TYPE IN AU	260
	4399	TSX	L,R\$GETP	CALL SUBROUTINE	270
	4400	ENDM	GETP		280
	4401 *				290
	4402 *				300
	4403 *			RELP MACRO	310
	4404 *				320
	4405 *	RELEASE A PERIPHERAL			330
	4406 *				340
	4407 RELP	MACRO	DEVICE NUMBER/ 'A'		350
	4408	INE	'#1','A'		360
	4409	LDA	#1	GET DEVICE NUMBER IN AL	370
	4410	TSX	L,R\$RELP	CALL SUBROUTINE	380
	4411	ENDM	RELP		390
	4412 **	DISK	GETP		400

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PERIPHERAL MANAGEMENT -- GETP

		002377	4414	USE	CODE		110	
			4415	HEAD	R		120	
			4416 *				130	
			4417 *				140	
			4418 *		GETP		150	
			4419 *				160	
			4420 *	GETP GETS A SINGLE PERIPHERAL UNIT OF A SPECIFIED TYPE.			170	
			4421 *	IF THERE ARE MORE UNITS OF THE SAME TYPE STILL AVAILABLE			180	
			4422 *	AFTER THE GET, THEN THE NEXT JOB WAITING FOR THE SAME			190	
			4423 *	PERIPHERAL TYPE IS AWAKENED.			200	
			4424 *				210	
			4425 *	CALL BY			220	
			4426 *		TSX L,R\$GETP		230	
			4427 *	CALL WITH			240	
			4428 *		C(J) = JOB NUMBER		250	
			4429 *		C(T) = TBLOCK ADDRESS		260	
			4430 *		C(L) = RETURN ADDRESS		270	
			4431 *		C(AU) = PERIPHERAL TYPE		280	
			4432 *	CALLS			290	
			4433 *		NONE		300	
			4434 *	RETURNS WITH			310	
			4435 *		C(J) = JOB NUMBER		320	
			4436 *		C(T) = TBLOCK ADDRESS		330	
			4437 *		C(AU) = PTR TO DEVICE HEADER		340	
			4438 *		C(AL) = DEVICE NUMBER		350	
			4439 *	EXIT TO	0,L		360	
			4440 *	USES			370	
			4441 *		NO LOCAL TEMPORARIES		380	
			4442 *		T\$TEMP1,T\$TEMP2		390	
			4443 *				400	
		002377	4444	GETP	BSS	0	ENTRY POINT	410
002377	000004	4470	11	4445	SXL	L,T\$TRA,T	SAVE RETURN ADDRESS	420
002400	000027	7550	11	4446	STA	T\$TEMP1,T	SAVE TYPE	430
002401	000027	2240	11	4447	LDX	Z,T\$TEMP1,T	GET PERIPHERAL TYPE	440
002402	000007	3640	03	4448	ANX	Z,7,DU	MASK TO TYPE ONLY	450
002403	004743	2240	14	4449	LDX	7,7,DU	POINT TO PERIPHERAL TYPE TABLE	460
002404	777777	6000	00	4450	TZE	\$ERROR	NO SUCH PERIPHERAL	470
002405	000027	7440	11	4451	STX	Z,T\$TEMP1,T	SAVE DEVICE HEADER PTR	480
				4452 *				490
				4453 *	LOOP TO LOOK FOR FREE DEVICE			500
				4454 *				510
002406	000003	3360	14	4455	LCQ	MAX,7	GET NUMBER TO CHECK COMPLEMENTED	520
002407	777777	6000	00	4456	TZE	\$ERROR	***BLEWIT	530
002410	000000	2200	14	4457	LDX	0,PTR,7	GET POINTER TO UNITS	540
002411	700000	2350	03	4458	LDA	BUSY+CLOSE+RSVE,DU	GET BITS TO CHECK	550
002412	000002	3150	10	4459	GETP1	FLAG,C	IS THIS UNIT FREE?	560
END OF BINARY CARD	IOS00066							
002413	002420	6000	00	4460	TZE	GETP2	YES, TAKE IT	570
002414	000003	0200	03	4461	ADLX	0,RS\$DEVLN,DU	STEP TO NEXT DEVICE	580
002415	000001	0760	07	4462	ADQ	1,DL	TEST FOR DONE	590

R			PERIPHERAL MANAGEMENT -- GETP				
002416	002412	5040 00	4463	TMI	GETP1	NO, SO CONTINUE SEARCH	600
002417	777777	7100 00	4464	TRA	\$ERROR	***SOMEBODY FORGOT TO RESERVE *EM1	610
			4465 *				620
			4466 *				630
			4467 *	FOUND THE REQUESTED UNIT			640
			4468 *				650
		002420	4469	GETP2	BSS	0	660
002420	000027	4400 11	4470	SXL	0,T\$TEMP1,T	SAVE POINTER TO DEVICE	670
002421	000002	4460 10	4471	SXL	J,ALLC,0	MARK IT ALLOCATED TO US	680
002422	400000	2220 03	4472	LDX	X,BUSY,DU	SET THE BUSY BIT ON	690
002423	000002	2420 10	4473	ORSX	X,FLAG,0		700
		002424	4474	DECRM	(AVAIL,Z)	DECREMENT NUMBER NOW FREE	710
002424	000001	3360 07		LCQ	1,DL		
002425	000005	0560 14		ASQ	AVAIL,Z		
002426	777777	6040 00	4475	TMI	\$ERROR	***BLEWIT	720
002427	000027	2350 11	4476	LDA	T\$TEMP1,T	GET RETURN WORDS FOR USER	730
		002430	4477	BUGXR	(X,Y,Z,Q)		740
		525232		BUGBUG	SET	BUGBUG+1	
002430	525232	2220 03		LDX	X,BUGBUG,DU		
002431	525232	2230 03		LDX	Y,BUGBUG,DU		
002432	525232	2240 03		LDX	Z,BUGBUG,DU		
002433	525232	2250 03		LDX	Q,BUGBUG,DU		
002434	000004	7270 11	4478	LXL	L,T\$TRA,T	RETRIEVE RETURN	750
		002435	4479	BUGL	(T\$TRA,T)	RUG IT	760
		525233		BUGBUG	SET	BUGBUG+1	
002435	525233	2200 03		LDX	Q,BUGBUG,DU		
002436	000004	4400 11		SXL	Q,T\$TRA,T		
002437	000027	2350 11	4480	LDA	T\$TEMP1,T	GET RETURN WORD FOR CALLER	770
		002440	4481	BUG	(T\$TEMP1,T)	BUG IT	780
		525234		BUGBUG	SET	BUGBUG+1	
002440	525234	2200 03		LDX	Q,BUGBUG,DU		
END	OF BINARY CARD	IOS00067					
002441	000027	7400 11		STX	0,T\$TEMP1,T		
002442	000027	4400 11		SXL	0,T\$TEMP1,T		
		002443	4482	BUG	(T\$TEMP2,T)		790
		525235		BUGBUG	SET	BUGBUG+1	
002443	525235	2200 03		LDX	Q,BUGBUG,DU		
002444	000026	7400 11		STX	0,T\$TEMP2,T		
002445	000026	4400 11		SXL	0,T\$TEMP2,T		
002446	000000	7100 17	4483	TRA	0,L	RETURN TO CALLER	800
			4484	*\$*	DISK	RES3	810

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PERIPHERAL MANAGEMENT -- RELP

		002447	4486	USE	CODE		110
			4487	HEAD	R		120
			4488 *				130
			4489 *				140
			4490 *		RELP		150
			4491 *				160
			4492 *	RELP	RELEASES A PERIPHERAL UNIT OF A SPECIFIED TYPE.		170
			4493 *				180
			4494 *	CALL WITH			190
			4495 *		TSX L,R\$RELP		200
			4496 *	CALL WITH			210
			4497 *		C(J) = JOB NUMBER		220
			4498 *		C(T) = TBLOCK ADDRESS		230
			4499 *		C(L) = RETURN ADDRESS		240
			4500 *		C(AU) = PTR TO DEVICE HEADER		250
			4501 *		C(AL) = DEVICE NUMBER ADDRESS		260
			4502 *	CALLS			270
			4503 *		\$CLOSE		280
			4504 *		\$NOTIF		290
			4505 *		\$CAUSE		300
			4506 *		O\$LOGS		310
			4507 *		O\$LOGC		320
			4508 *		O\$LOGX		330
			4509 *	RETURNS WITH			340
			4510 *		C(J) = JOB NUMBER		350
			4511 *		C(T) = TBLOCK ADDRESS		360
			4512 *	EXITS TO O,L			370
			4513 *				380
			4514 *	EXITS WITH PERIPHERAL DEALLOCATED.			390
			4515 *				400
		002447	4516	RELP	BSS	0	ENTRY POINT
002447	000004	4470	11		SXL	L,T\$TRA,T	SAVE RETURN ADDRESS
			4517				420
			4518 *				430
			4519 *	PERFORM	CONSISTANCY CHECKS		440
			4520 *				450
002450	000027	7550	11	4521	STA	T\$TEMP1,T	SAVE PASSED INFORMATION
002451	000027	7220	11	4522	LXL	X,T\$TEMP1,T	GET DEVICE ADDRESS
002452	000002	2340	12	4523	SZN	FLAG,X	SHOULD BE BUSY
002453	777777	6000	00	4524	TZE	\$ERROR	IT ISN'T I
002454	777777	2360	03	4525	LDQ	-1,DU	SET FOR LOWER HALF COMPARE
002455	000002	2350	12	4526	LDA	ALLC,X	GET ALLOCATED JOB NUMBER
002456	000006	2110	11	4527	CMK	T\$JOB,T	CHECK FOR CORRECT J NUMBER
002457	777777	6010	00	4528	TNZ	\$ERROR	SHOULD BE THE SAME
		002460		4529	BUGL	(ALLC,X)	OK, DESTROY IT
		525236			BUGBUG	SET	BUGBUG+1
002460	525236	2200	03		LDX	0,BUGBUG,DU	
002461	000002	4400	12		SXL	0,ALLC,X	
002462	000027	2240	11	4530	LDX	Z,T\$TEMP1,T	GET PTR TO DEVICE HEADER
002463	000005	0540	14	4531	AOS	AvAIL,7	BUMP THE NUMBER NOW FREE
				4532 *			570

R			PERIPHERAL MANAGEMENT -- RELP			
			4533 *	CHECK IF CLOSE REQUESTED		580
			4534 *			590
002464	000002	2350 12	4535	LDA FLAG,X	GET PERIPHERAL FLAG	600
002465	100000	3150 03	4536	CANA RSVE,DU	IS IT RESERVED?	610
END OF BINARY CARD	IOS00068					
002466	002545	6000 00	4537	TZE RELP3	NO, SO CONTINUE	620
			4538 *			630
			4539 *	CLOSE REQUESTED -- CLOSE AND LOG		640
			4540 *			650
002467	000006	0540 14	4541	AOS OPER,Z	BUMP OPER COUNT ONE	660
		002470	4542	DECRM (AVAIL,Z)	DECREMENT ONCE FOR THE 'NOW FREE'	670
002470	000001	3360 07		LCQ 1,DL		
002471	000005	0560 14		ASQ AVAIL,Z		
		002472	4543	DECRM (AVAIL,Z)	AND AGAIN FOR THE 'CLOSE'	680
002472	000001	3360 07		LCQ 1,DL		
002473	000005	0560 14		ASQ AVAIL,Z		
		002474	4544	DECRM (OMAX,Z)	AND OUR MAX TOO	690
002474	000001	3360 07		LCQ 1,DL		
002475	000004	0560 14		ASQ OMAX,Z		
002476	777777	6040 00	4545	TMI \$ERROR	***PROBLEM	700
002477	300000	6750 03	4546	ERA CLOSE+RSVE,DU	MARK IT CLOSED AND NOT RESERVED	710
002500	000002	7550 12	4547	STA FLAG,X	RESTORE FLAG WORD	720
002501	000001	2230 12	4548	LDX Y,FRN,X	GET FRN OF DEVICE	730
002502	000025	7430 11	4549	STX Y,T\$TEMP3,T	SAVE FOR CLOSE	740
		002503	4550	BUGU (FRN,X)	BUG FRN	750
		525237		BUGRUG SET BUGBUG+1		
002503	525237	2200 03		LDX 0,BUGBUG,DU		
002504	000001	7400 12		STX 0,FRN,X		
			4551 *			760
			4552 *	CLOSE PERIPHERAL		770
			4553 *			780
		002505	4554 RELP1	CLOSE (T\$TEMP3,T)	CLOSE THE DEVICE	790
002505	000704	7000 00		TSX 0,\$CLOSE		
002506	000025	0000 11		ARG T\$TEMP3,T		
		002507	4555	CHECK RELP2,\$BZ,RELP1		800
002507	000000	7200 11		LXL 0,T\$SRW1,T		
002510	000077	3600 03		ANX 0,\$\$STMK,DU		
002511	002515	6000 00		TZE RELP2		
002512	000003	1000 03		CMPX 0,\$BZ,DU		
002513	002505	6000 00		TZE RELP1		
END OF BINARY CARD	IOS00069					
002514	777777	7100 00		TRA \$ERROR		
		002515	4556 RELP2	BSS 0		810
			4557 *	SEE WHO HAS IT?		820
			4558 *			830
			4559 *	SEND RELEASE MESSAGE TO SUB-MODULE		840
			4560 *			850
002515	000027	2220 11	4561	LDX X,T\$TEMP1,T	RESTORE TYPE PTR	860
002516	000001	2350 12	4562	LDA STATE,X	GET STATE FOR CAUSE	870
002517	000025	7550 11	4563	STA T\$TEMP3,T		880

R			PERIPHERAL MANAGEMENT -- RELP						
002520	000027	7240 11	4564	LXL	Z,T\$TEMP1,T	RESTORE DEVICE UNIT PTR	890		
002521	000000	2350 14	4565	LDA	ADDR,Z	GET NAME	900		
002522	000007	3750 07	4566	ANA	7,DL	MASK TO UNIT NUMRER	910		
002523	000022	7350 00	4567	ALS	18	MOVE TO AU	920		
002524	006000	2750 03	4568	ORA	R\$,REL,DU	COMMAND: RELEASE	930		
002525	000024	7550 11	4569	STA	T\$TEMP4,T	SAVE FOR CAUSE	940		
		002526	4570	BRANCH	NO\$PASS,C\$MESSX,(T\$TEMP3,T),(T\$TEMP4,T)		950		
002526	001467	7000 00		TSX	0,T\$GETT				
002527	000000	6220 11		EAX	X,0,T				
002530	000005	2210 12		LDX	T,T\$LINK,X				
002531	000025	2360 11		LDQ	T\$TEMP3,T				
002532	000027	7560 12		STQ	T\$TEMP1,X				
002533	000024	2360 11		LDQ	T\$TEMP4,T				
002534	000026	7560 12		STQ	T\$TEMP2,X				
002535	000000	6210 12		EAX	T,0,X				
002536	003140	6200 00		EAX	0,C\$MESSX				
002537	000004	7400 11		STX	0,T\$TRA,T				
002540	000004	6200 11		EAX	0,Q\$OFFST,T				
002541	005162	7170 00		XED	Q\$XADD+Q\$TASK				
END OF BINARY CARD		10500070							
002542	000005	2210 12		LDX	T,T\$LINK,X				
		002543		BUGXR	(0,X)				
		525240		BUGRUG	SET	BUGRUG+1			
002543	525240	2200 03		LDX	0,BUGRUG,DU				
002544	525240	2220 03		LDX	X,BUGRUG,DU				
		002545	4571	REL\$P3	BSS	0	960		
			4572	*			970		
			4573	*	FINISH UP		980		
			4574	*			990		
002545	400000	2350 03	4575	LDA	BUSY,DU	UNSET THE BUSY BIT	1000		
002546	000027	7220 11	4576	LXL	X,T\$TEMP1,T	RESTORE PTR TO DEVICE UNIT	1010		
002547	000002	6550 12	4577	ERSA	FLAG,X		1020		
002550	000004	7270 11	4578	LXL	L,T\$TRA,T	RETRIEVE RETURN	1030		
		002551	4579	BUGL	(T\$TRA,T)	BUG IT	1040		
		525241		BUGRUG	SET	BUGRUG+1			
002551	525241	2200 03		LDX	0,BUGRUG,DU				
002552	000004	4400 11		SXL	0,T\$TRA,T				
		002553	4580	BUGXR	(X,Y,Z,Q)	BUG REGISTERS	1050		
		525242		BUGRUG	SET	BUGRUG+1			
002553	525242	2220 03		LDX	X,BUGRUG,DU				
002554	525242	2230 03		LDX	Y,BUGRUG,DU				
002555	525242	2240 03		LDX	Z,BUGRUG,DU				
002556	525242	2250 03		LDX	Q,BUGRUG,DU				
		002557	4581	BUGA			1060		
		525243		BUGRUG	SET	BUGRUG+1			
002557	525243	2350 03		LDA	BUGRUG,DU				
002560	525243	2750 07		ORA	BUGRUG,DL				
		002561	4582	BUGQ			1070		
		525244		BUGRUG	SET	BUGRUG+1			
002561	525244	2360 03		LDQ	BUGRUG,DU				

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INPUT/OUTPUT SCHEDULER -- IOS MONITOR

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R

PERIPHERAL MANAGEMENT -- RELP

002562	525244	2760	07		ORQ	BUGBUG,DL		
002563	000000	7100	17	4583	TRA	D,L	RETURN TO CALLER	1080
			002564	4584	EXIT		WAIT FOR REPLY	1090
002564	003074	7100	00		TRA	\$EXIT		
				4585	**	DISK	PERIPH	1100

R

RESOURCE ALLOCATION -- PERIPHERAL TYPE TABLE

		002565	4587	USE	CODE		110
			4588	HEAD	R		120
			4589	*			130
			4590	*			140
			4591	*			150
			4592	*			160
			4593	*			170
			4594	*			180
			4595	*			190
			4596	*			200
			4597	*			210
			4598	*			220
			4599	*			230
			4600	*			240
			4601	*			250
			4602	*			260
			4603	*			270
		004743	4604	USE	CONST		280
		004743	4605	TABLE	BSS	0	290
		004743	4606	ARG	0	0 = INVALID	300
		004743	4607	TABCP	ARG	CPTAB	1 = CARD PUNCH
		004743	4608	TABCR	ARG	CRTAB	2 = CARD READER
		004743	4609	TABLP	ARG	LPTAB	3 = LINE PRINTER
		004743	4610	TABMT	ARG	MTTAB	4 = MAGNETIC TAPE
		004743	4611	TABOP	ARG	OPTAB	5 = OPERATOR'S CONSOLE
		004743	4612	ARG	0	6 = INVALID	360
		004743	4613	ARG	0	7 = INVALID	370
		002565	4614	USE	PREVIOUS		380
			4615	*			390
			4616	*			400
		000001	4617	TYPCP	EQU	TABCP-TABLE	TYPE: CARD PUNCH
		000002	4618	TYPCR	EQU	TABCR-TABLE	TYPE: CARD READER
		000003	4619	TYPLP	EQU	TABLP-TABLE	TYPE: LINE PRINTER
		000004	4620	TYPMT	EQU	TABMT-TABLE	TYPE: MAG TAPE
		000005	4621	TYPOP	EQU	TABOP-TABLE	TYPE: OPERATOR'S CONSOLE
			4622	*			460
			4623	*			470
			4624	*			480
			4625	*			490
			4626	HEAD			500
		001000	4627	CPST	EQU	R\$TYPCP*512	CP STATE FOR COMMUNICATIONS
		002000	4628	CRST	EQU	R\$TYPCR*512	CR STATE
		003000	4629	LPST	EQU	R\$TYPLP*512	LP STATE
		004000	4630	MTST	EQU	R\$TYPMT*512	MT STATE
		005000	4631	OPST	EQU	R\$TYPOP*512	OP STATE
			4632	HEAD	R		560

END OF BINARY CARD IOS00071

R

RESOURCE ALLOCATION -- PERIPHERAL HEADER TABLE

002565	4634	USE	CODE		580
	4635	HEAD	R		590
	4636	*			600
	4637	*			610
	4638	*			620
	4639	*		PERIPHERAL HEADER TABLE	630
	4640	*			640
	4641	*		A DEVICE HEADER IS THE ITEM THAT AN ENTRY IN THE TABLE OF	650
	4642	*		PERIPHERAL TYPES POINTS TO. THE HEADER CONTAINS THE POINTER	660
	4643	*		TO THE DEVICES OF A CERTAIN TYPE (I.E. LINE PRINTERS).	670
	4644	*		IT ALSO CONTAINS SUCH INFORMATION AS THE CONFIGURATION OF THE	680
	4645	*		SYSTEM (MAXIMUM NUMBER OF DEVICES OF A CERTAIN TYPE).	690
	4646	*		LASTLY, IT CONTAINS A POINTER TO THE CORRESPONDING QUEUE.	700
	4647	*			710
	4648	*		FORMAT OF DEVICE HEADER	720
	4649	*			730
000000	4650	PTR	EQU	0	740
000001	4651	STATE	EQU	PTR+1	750
000002	4652	ELT	EQU	STATE+1	760
000002	4653	ACC	EQU	ELT	770
000003	4654	MAX	EQU	ELT+1	780
000004	4655	OMAX	EQU	MAX+1	790
000005	4656	AVAIL	EQU	OMAX+1	800
000006	4657	OPER	EQU	AVAIL+1	810
000007	4658	SPARE	EQU	OPER+1	820
000010	4659	DHLEN	EQU	SPARE+1-PTR	830
	4660	*			840
	4661	*			850
	4662	*		DEVICE HEADER GENERATING MACRO	860
	4663	*			870
	4664	DEVHDR	MACRO	NAME,ELT,ACC,MAX,OMAX,AVAIL,OPER	880
	4665	#1TAB	ARG	#1	890
	4666	ZERO		\$#1ST,0	900
	4667	ZERO		#2,#3	910
	4668	VFD		36/#4	920
	4669	VFD		36/#5	930
	4670	VFD		36/#6	940
	4671	VFD		36/#7	950
	4672	DEC		0	960
	4673	ENDM		DEVHDR	970

R RESOURCE ALLOCATION -- PERIPHERAL HEADER TABLE

		4675 *			990
		4676 *			1000
		4677 *	HERE IS THE LIST OF DEVICE HEADERS IN ALPHABETICAL ORDER.		1010
		4678 *			1020
	005543	4679	USE STORE		1030
	005550	4681	EIGHT		1050
	005550	4682	DEVHR BSS 0	START OF DEVICE HEADER LIST	1060
	005550	4683	DEVHDR CP,6,B\$AP,CPMAX,0,0,CPMAX		1070
005550	005620 0000 00		CPTAB ARG CP		
005551	001000 000000		ZERO \$CPST,0		
005552	000006 000004		ZERO 6,B\$AP		
005553	0000000000001		VFD 36/CPMAX		
005554	0000000000000		VFD 36/0		
005555	0000000000000		VFD 36/0		
005556	0000000000001		VFD 36/CPMAX		
005557	0000000000000		DEC 0		
	005560	4684	DEVHDR CR,6,B\$RD,CRMAX,0,0,CRMAX		1080
005560	005623 0000 00		CRTAB ARG CR		
005561	002000 000000		ZERO \$CRST,0		
005562	000006 000020		ZERO 6,B\$RD		
005563	0000000000001		VFD 36/CRMAX		
005564	0000000000000		VFD 36/0		
END OF BINARY CARD IOS00072					
005565	0000000000000		VFD 36/0		
005566	0000000000001		VFD 36/CRMAX		
005567	0000000000000		DEC 0		
	005570	4685	DEVHDR LP,6,B\$AP,LPMAX,0,0,LPMAX		1090
005570	005626 0000 00		LPTAB ARG LP		
005571	003000 000000		ZERO \$LPST,0		
005572	000006 000004		ZERO 6,B\$AP		
005573	0000000000002		VFD 36/LPMAX		
005574	0000000000000		VFD 36/0		
005575	0000000000000		VFD 36/0		
005576	0000000000002		VFD 36/LPMAX		
005577	0000000000000		DEC 0		
	005600	4686	DEVHDR MT,36,B\$RD+\$BAP,MTMAX,0,0,MTMAX		1100
005600	005634 0000 00		MTTAB ARG MT		
005601	004000 000000		ZERO \$MTST,0		
005602	000044 000024		ZERO 36,B\$PD+\$BAP		
005603	0000000000010		VFD 36/MTMAX		
005604	0000000000000		VFD 36/0		
005605	0000000000000		VFD 36/0		
005606	0000000000010		VFD 36/MTMAX		
005607	0000000000000		DEC 0		
	005610	4687	DEVHDR OP,9,B\$ALL,OPMAX,0,0,OPMAX		1110
005610	005664 0000 00		OPTAB ARG OP		
005611	005000 000000		ZERO \$OPST,0		
005612	000011 000037		ZERO 9,B\$ALL		
END OF BINARY CARD IOS00073					
005613	0000000000001		VFD 36/OPMAX		

R

RESOURCE ALLOCATION -- PERIPHERAL HEADER TABLE

005614	000000000000			VFD	36/0		
005615	000000000000			VFD	36/0		
005616	000000000001			VFD	36/OPMAX		
005617	000000000000			DEC	0		
	005620	4686	XDVHR	EQU	*	END OF DEVICE HEADER TABLE	1120
	000050	4689	DHRLN	EQU	*-DEVHR	LENGTH OF TABLE	1130
	002565	4690		USE	PREVIOUS		1140

R

RESOURCE ALLOCATION -- PERIPHERAL DEVICE TABLE

002565	4692	USE	CODE		1160
	4693	HEAD	R		1170
	4694 *				1180
	4695 *				1190
	4696 *			PERIPHERAL DEVICE TABLE	1200
	4697 *				1210
	4698 *			THERE IS A ONE-TO-ONE CORRESPONDENCE BETWEEN A *DEVICE* AND	1220
	4699 *			PHYSICAL DEVICE IN THE MACHINE ROOM. THE DEVICE CONTAINS THE	1230
	4700 *			NAME OF THE DEVICE, THE FRN WHEN OPEN, FLAG BITS TELLING ITS	1240
	4701 *			CURRENT STATUS, AND IF BUSY, WHO IS RESPONSIBLE.	1250
	4702 *				1260
	4703 *				1270
	4704 *			FORMAT OF PERIPHERAL DEVICE	1280
	4705 *				1290
	4706 *				1300
000000	4707	ABBR	EQU	0	FOUR CHARACTER ASCII ABBREVIATION FOR PERIPHERAL
000001	4708	FRN	EQU	ABBR+1	(UPPER) FRN OF PERIPHERAL WHEN OPEN
000002	4709	FLAG	EQU	FRN+1	(UPPER) FLAG BITS FOR THE PERIPHERAL
000002	4710	ALLC	EQU	FLAG	(LOWER) JOB NUMBER USING IT WHEN BUSY
000003	4711	DEVLN	EQU	ALLC+1-ABBR	DEVICE ENTRY LENGTH
	4712 *				1360
	4713 *				1370
	4714 *			BITS FOR FLAG	1380
	4715 *				1390
400000	4716	BUSY	EQU	B\$SIGN	ON IF NOT ALLOCATABLE
200000	4717	CLOSE	EQU	BUSY/2	ON IF CLOSED
100000	4718	RSVE	EQU	CLOSE/2	ON IF OPERATOR REQUESTED A CLOSE
	4719 *				1430
	4720 *				1440
	4721 *			DEVICE GENERATING MACRO	1450
	4722 *				1460
	4723	DEVICE	MACRO	NAME	1470
	4724		UASCI	1,#1	1480
	4725		ARG	0	1490
	4726		ZERO	BUSY+CLOSE,0	1500
	4727		ENDM	DEVICE	1510
	4728 *				1520
	4729 *				1530
	4730	DEVT	MACRO	NAME,(LIST OF DEVICE NUMBERS)	1540
	4731	#1	BSS	0	1550
	4732		IDRP	#2	1560
	4733	SET	SET	#2	1570
	4734		DEVICE	#1#2	1580
	4735		IDRP		1590
	4736	#1MAX	SET	SET+1	1600
	4737		ENDM	DEVT	1610

R

RESOURCE ALLOCATION -- PERIPHERAL DEVICE TABLE

		4739 *				1630
		4740 *				1640
		4741 *	HERE IS THE TABLE OF DEVICES IN ALPHABETICAL ORDER.			1650
		4742 *				1660
		4743 *				1670
	005620	4744	USE	STORE		1680
	005620	4745 PERT	BSS	0	START OF PERIPHERAL TABLE	1690
	005620	4746 DEVTB	BSS	0	START OF DEVICE TABLE	1700
	005620	4747	DEVT	CP,(00)	CARD PUNCHES	1710
	005620		CP	BSS	0	
	000000		SET	SET	00	
	005620		DEVICE	CP00		
005620	103120060060		UASCI	1,CP00		
005621	000000 0000 00		ARG	0		
005622	600000 000000		ZERO	BUSY+CLOSE,0		
	000001		CPMAX	SET	SET+1	
	005623	4748	DEVT	CR,(00)	CARD READERS	1720
	005623		CR	BSS	0	
	000000		SET	SET	00	
	005623		DEVICE	CR00		
	005623		UASCI	1,CR00		
005623	103122060060		ARG	0		
005624	000000 0000 00		ZERO	BUSY+CLOSE,0		
005625	600000 000000		CRMAX	SET	SET+1	
	000001		DEVT	LP,(00,01)	LINE PRINTERS	1730
	005626	4749	LP	BSS	0	
	005626		SET	SET	00	
	000000		DEVICE	LP00		
	005626		UASCI	1,LP00		
005626	114120060060		ARG	0		
005627	000000 0000 00		ZERO	BUSY+CLOSE,0		
005630	600000 000000		SET	SET	01	
	000001		DEVICE	LP01		
	005631		UASCI	1,LP01		
005631	114120060061		ARG	0		
005632	000000 0000 00		ZERO	BUSY+CLOSE,0		
005633	600000 000000		LPMAX	SET	SET+1	
	000002		DEVT	MT,(00,01,02,03,04,05,06,07)	MAG TAPES	1740
	005634	4750	MT	BSS	0	
	005634		SET	SET	00	
	000000		DEVICE	MT00		
	005634		UASCI	1,MT00		
005634	115124060060		ARG	0		
005635	000000 0000 00		ZERO	BUSY+CLOSE,0		
005636	600000 000000		SET	SET	01	
	000001		DEVICE	MT01		
	005637		UASCI	1,MT01		
005637	115124060061		ARG	0		
END OF BINARY CARD IOS00074			ZERO	BUSY+CLOSE,0		
005640	000000 0000 00					
005641	600000 000000					

R		RESOURCE ALLOCATION -- PERIPHERAL DEVICE TABLE			
	000002	SET	SET	02	
	005642		DEVICE	MT02	
005642	115124060062		UASCI	1,MT02	
005643	000000 0000 00		ARG	0	
005644	600000 000000		ZERO	BUSY+CLOSE,0	
	000003	SET	SET	03	
	005645		DEVICE	MT03	
005645	115124060063		UASCI	1,MT03	
005646	000000 0000 00		ARG	0	
005647	600000 000000		ZERO	BUSY+CLOSE,0	
	000004	SET	SET	04	
	005650		DEVICE	MT04	
005650	115124060064		UASCI	1,MT04	
005651	000000 0000 00		ARG	0	
005652	600000 000000		ZERO	BUSY+CLOSE,0	
	000005	SET	SET	05	
	005653		DEVICE	MT05	
005653	115124060065		UASCI	1,MT05	
005654	000000 0000 00		ARG	0	
005655	600000 000000		ZERO	BUSY+CLOSE,0	
	000006	SET	SET	06	
	005656		DEVICE	MT06	
005656	115124060066		UASCI	1,MT06	
005657	000000 0000 00		ARG	0	
005660	600000 000000		ZERO	BUSY+CLOSE,0	
	000007	SET	SET	07	
	005661		DEVICE	MT07	
005661	115124060067		UASCI	1,MT07	
005662	000000 0000 00		ARG	0	
005663	600000 000000		ZERO	BUSY+CLOSE,0	
	000010	MTMAX	SET	SET+1	
	005664	4751	DEVT	OP,(00)	OPERATOR'S CONSOLE
	005664	OP	BSS	0	
	000000	SET	SET	00	
	005664		DEVICE	OP00	
005664	117120060060		UASCI	1,OP00	
005665	000000 0000 00		ARG	0	
END OF BINARY CARD IOS00075			ZERO	BUSY+CLOSE,0	
005666	600000 000000		OPMAX	SET	
	000001	4752	TT	SET	PERT LENGTH
	000047	4753	PERTL	EQU	NUMBER OF ENTRIES
	000015	4754	*		
	005665	4755	OPFRN	EQU	OP FRN
	002565	4756	USE	PREVIOUS	
		4757	**	DISK	SCHED

		R	SCHEDULER -- MAIN				
		002565	4759	USE	CODE		110
			4760	HEAD	R		120
			4761 *				130
			4762 *				140
			4763 *		MAIN		150
			4764 *				160
			4765 *		THIS IS THE SCHEDULER FOR THE ENTIRE MONITOR JOB-RUN SYSTEMS.		170
			4766 *		IT CHECKS THE INPUT QUEUE LISTS FOR POSSIBLE JOB INITIATION.		180
			4767 *		TO DO THIS, IT POINTS TO A QUEUE LIST AND CALLS THE RUN		190
			4768 *		PART OF THE SCHEDULER. RUN WILL TRY TO START EVERY WAITING		200
			4769 *		JOB ON THE SPECIFIED QUEUE LIST. SO MAIN MERELY POINTS TO		210
			4770 *		LIST, AND THE SUBROUTINES DO THE REST.		220
			4771 *				230
			4772 *		CURRENTLY THERE ARE ONLY TWO INPUT QUEUE LIST:		240
			4773 *		Q\$INP1 -- GENERAL ALL JOBS GO HERE EXCEPT...		250
			4774 *		Q\$INP2 -- JOBS WHICH ARE SHORT PRINT JOBS.		260
			4775 *		Q\$INP3 -- JOBS WHICH ARE MEDIUM PRINTER JOBS (NOT IMPLEMENTED)		280
			4776 *				290
			4777 *		THIS IS AN ASYNCHRONOUS TASK		300
			4778 *				310
		002565	4779	SKED	BSS	0	320
002565	005240	6250 00	4780	EAX	Q,Q\$INP2	POINT TO SHORT PRINT JOB QUEUE LIST	330
002566	002575	7070 00	4781	TSX	L,SKEDR	RUN *EM	340
002567	005220	6250 00	4782	EAX	Q,Q\$INP1	POINT TO ALL OTHER JOB QUEUE LIST	350
002570	002575	7070 00	4783	TSX	L,SKEDR	LET *EM GO, TOO	360
002571	005260	6250 00	4784	EAX	Q,Q\$INP3	POINT TO MEDIUM PRINT JOB QUEUE LIST	370
002572	002575	7070 00	4785	TSX	L,SKEDR	LET HER RIP.	380
		002573	4786	RELT		DONE, RELEASE TCB	390
002573	001477	7000 00		TSX	0,T\$RELT		400
		002574	4787	EXIT		POOF.	
002574	003074	7100 00		TRA	\$EXIT		
			4788	**	DISK	SCHEDULE	

P

SCHEDULER -- RUN

		002575	4790	USE	CODE		110		
			4791	HEAD	R		120		
			4792 *				130		
			4793 *				140		
			4794 *		RUN		150		
			4795 *				160		
			4796 *				170		
			4797 *		THIS SUBROUTINE CHECKS A SPECIFIED QUEUE-LIST TO SEE IF		180		
			4798 *		ANY OF THE WAITING JOBS CAN HAVE ALL OF THEIR RESOURCE REQUESTS		190		
			4799 *		SATISFIED. IF SO, THEN THAT JOB IS REMOVED FROM THE LIST AND		200		
			4800 *		PLACED ON THE Q\$TASK QUEUE IN ORDER TO RUN.		210		
			4801 *				220		
			4802 *		ALL JOBS ARE ONE-STEP PROCESSES. THIS MEANS THAT THE JOBS		230		
			4803 *		CONSIST OF A SINGLE ACTIVITY. THUS IN ORDER TO AVOID DEAD-		240		
			4804 *		LOCK SITUATIONS, ALL RESOURCES ARE ALLOCATED COLLECTIVELY WHEN		250		
			4805 *		A JOB IS TO START; OTHERWISE THE JOB MUST WAIT TILL ALL HIS		260		
			4806 *		REQUESTS FOR SYSTEM RESOURCES CAN BE SATISFIED SIMULTANEOUSLY.		270		
			4807 *		CALL WITH		280		
			4808 *		C(XT) = TCR		290		
			4809 *		C(XJ) = JCR (PHONY)		300		
			4810 *		CALL BY		310		
			4811 *		TSX L,R\$SKEDR		320		
			4812 *		RETURNS D,L		330		
			4813 *		RETURNS WITH		340		
			4814 *		C(XT) = TCR		350		
			4815 *		USES		360		
			4816 *		TEMP3 (UPPER) ADDRESS		370		
			4817 *		TEMP3 (LOWER) RETURN ADDRESS		380		
			4818 *		TEMP4 PTR TO NEXT RESOURCE NEED		390		
			4819 *		TEMP5 CURRENT NEED		400		
			4820 *		TEMP6 COUNTER OF CURRENT NEED		410		
			4821 *		TEMP8 (UPPER) PTR TO PREDECESSOR (Q\$LINK)		420		
			4822 *		TEMP8 (LOWER) PTR TO CURRENT (Q\$OFFST)		430		
			4823 *				440		
		002575	4824	SKEDR	BSS	0	ENTRY POINT	450	
002575	000025	4470	11	4825	SXL	L,T\$TEMP3,T	SAVE RETURN ADDRESS	460	
002576	000025	7450	11	4826	STX	Q,T\$TEMP3,T	SAVE PTR TO QUEUE-LIST	470	
				4827 *				480	
				4828 *		IS THIS LIST EMPTY?		490	
				4829 *				500	
002577	000001	6200	15	4830	EAX	0,Q\$FIRST+1,Q	ADDRESS OF FIRST ELEMENT	510	
002600	000001	1000	15	4831	CMPX	0,Q\$LAST,Q	DOES LAST POINT TO IT?	520	
002601	002724	6000	00	4832	TZE	SKDRX	IF SO, EXIT	530	
002602	000000	6200	15	4833	EAX	0,Q\$FIRST,Q	GET PTR TO WHAT IS TO BE PREDECESSOR	540	
		002603		4834	SKDRQ	BSS	0	550	
002603	000020	7400	11	4835	STX	0,T\$TEMP8,T	SAVE IT FOR RE-LINKING	560	
002604	000000	2200	10	4836	LDX	0,0,0	OFFSET PTR TO BLOCK	570	
002605	777777	6000	00	4837	TZE	\$ERROR	***PROBLEM	580	
002606	000020	4400	11	4838	SKRQ.1	SXL	0,T\$TEMP8,T	SAVE PTR TO CURRENT BLOCK	590
002607	777774	6200	10	4839	EAX	0,-Q\$OFFST,Q	RELATE TO BEGINNING	600	

R		SCHEDULER -- RUN			
		4840 *			610
		4841 *	CHECK IF RESOURCE REQUIREMENTS CAN BE SATISFIED		620
		4842 *			630
END OF BINARY CARD	IOS00076				
002610	000006 7260 10	4843	LXL J,J\$JOB,0	GET PTR TO ASSOCIATED JOB DESCRIPTOR	640
002611	000013 6230 16	4844	EAX Y,J\$RES,J	GET PTR TO RESOURCE REQUEST BLOCK	650
002612	000024 7430 11	4845 SKDR1	STX Y,T\$TEMP4,T	SAVE IT	660
002613	000000 2240 13	4846	LDX Z,J,Y	GET TYPE OF NEXT RESOURCE REQUESTED	670
002614	002645 6040 00	4847	TMI SKDR4	END OF LIST--RUN THIS JOB	680
002615	004743 2220 14	4848	LDX X,TABLE,Z	GET PTR TO DEVICE HEADER	690
002616	777777 6000 00	4849	TZE \$ERROR	***PROBLEM	700
002617	000000 2350 13	4850	LDA O,Y	GET NUMBER REQUIRED	710
002620	777777 3750 07	4851	ANA -1,DL	MASK TO NUMBER ONLY	720
002621	000003 1150 12	4852	CMPA MAX,X	TEST AGAINST MAX	730
002622	002624 6000 00	4853	TZE **2	OK	740
002623	777777 6030 00	4854	TRC \$ERROR	***THAT'S SOMEBODY ELSE	750
002624	002625 7100 14	4855	TRA **1,7	BRANCH THRU TABLE ON TYPE	760
002625	777777 7100 00	4856	TRA \$ERROR	0 = ILLEGAL	770
002626	002735 7100 00	4857	TRA SCPCCK	1 = CARD PUNCH	780
002627	002735 7100 00	4858	TRA SCRCCK	2 = CARD READER	790
002630	002741 7100 00	4859	TRA SLPCCK	3 = LINE PRINTER	800
002631	002735 7100 00	4860	TRA SMTCK	4 = MAG TAPE	810
002632	777777 7100 00	4861	TRA \$ERROR	5 = OP CONSOLE (ALLOW NO ONE TO OWN IT)	820
002633	777777 7100 00	4862	TRA \$ERROR	6 = INVALID	830
002634	777777 7100 00	4863	TRA \$ERROR	7 = INVALID	840
	002635	4864 SKDR2	BSS 0	RETURN HERE IF RESOURCES AVAILABLE	850
002635	000000 6230 05	4865	EAX Y,O,AL	MOVE COUNT TO Y	860
END OF BINARY CARD	IOS00077				
002636	000024 0230 11	4866	ADLX Y,T\$TEMP4,T	ADD IN OLD POINTER	870
002637	002612 7100 00	4867	TRA SKDR1	AND LOOP	880
	002640	4868 SKDR3	BSS 0	RETURN HERE IF THIS JOB CAN'T START	890
002640	000004 6230 10	4869	EAX Y,Q\$OFFST,0	GET OFFSET PTR TO THIS BLOCK	900
002641	000001 1030 15	4870	CMPLX Y,Q\$LAST,0	IS IT THE LAST?	910
002642	002724 6000 00	4871	TZE SKDRX	IF SO, EXIT	920
002643	000003 6200 10	4872	EAX O,Q\$LINK,0	OTHERWISE GET PTR TO NEXT	930
		4873 *			940
002644	002603 7100 00	4874	TRA SKDR0	GET NEXT BLOCK	950
		4875 *			960
		4876 *	ALLOCATE RESOURCES		970
		4877 *			980
	002645	4878 SKDR4	BSS 0	START THIS JOB	990
002645	003064 7070 00	4879	TSX L,J\$JNUMR	GET A JOB NUMBER	1000
002646	000007 4420 16	4880	SXL X,J\$JOB,J	SAVE AS STATE FOR COMMUNICATIONS	1010
002647	000000 6350 12	4881	EAX O,X	MOVE JOB NUMBER	1020
002650	000022 7710 00	4882	ARL 18	INTO AL RIGHT JUSTIFIED	1030
002651	000010 2360 16	4883	LDQ J\$MESS,J	GET MESSAGE	1040
002652	000004 7360 00	4884	QLS 4	LEFT JUSTIFIED	1050
002653	000004 7730 00	4885	LRL 4	MOVE INTO QU	1060
002654	000010 7560 16	4886	STQ J\$MESS,J	COMPLETE MESSAGE, SAVE	1070
002655	000013 6230 16	4887	EAX Y,J\$RES,J	GET POINTER TO RESOURCE NEED BLOCK	1080

R		SCHEDULER -- RUN				
002656	000024 7430 11	4888	STX	Y,T\$TEMP4,T	SAVE	1090
		4889 *				1100
	002657	4890	SKDR5	BSS	0	1110
002657	000000 2350 13	4891	LDA	0,Y	GET NEXT RESOURC REQUIREMENT	1120
002660	002707 6040 00	4892	TMI	SKDR8	TEST FOR DONE	1130
002661	000023 7550 11	4893	STA	T\$TEMP5,T	SAVE IT	1140
002662	777777 3750 07	4894	ANA	-1,DL	MASK TO COUNT ONLY	1150
002663	777777 6000 00	4895	TZE	\$ERROR	***BLEWIT	1160
END OF BINARY CARD IOS00078						
002664	000000 5310 00	4896	NEG		COMPLEMENT	1170
002665	000022 7550 11	4897	STA	T\$TEMP6,T	SAVE AS LOOP COUNTER	1180
002666	000023 2350 11	4898	SKDR6	LDA	T\$TEMP5,T	GET BACK TYPE
002667	002670 7100 01	4899	TRA	**1,AU	BRANCH ON TYPE	1200
002670	777777 7100 00	4900	TRA	\$ERROR	0 = ILLEGAL	1210
002671	002751 7100 00	4901	TRA	SCPAL	1 = CARD PUNCH	1220
002672	002751 7100 00	4902	TRA	SCRAL	2 = CARD READER	1230
002673	002753 7100 00	4903	TRA	SLPAL	3 = LINE PRINTER	1240
002674	002751 7100 00	4904	TRA	SMTAL	4 = MAG TAPE	1250
002675	777777 7100 00	4905	TRA	\$ERROR	5 = OP CONSOLE (ALLOW NO ONE TO OWN IT)	1260
002676	777777 7100 00	4906	TRA	\$ERROR	6 = INVALID	1270
002677	777777 7100 00	4907	TRA	\$ERROR	7 = INVALID	1280
	002700	4908	SKDR7	BSS	0	1290
002700	000024 2230 11	4909	LDX	Y,T\$TEMP4,T	RESTORE RESOURCE PTR	1300
002701	000000 7550 13	4910	STA	0,Y	SAVE ALLOCATED RESOURCE IN JCB	1310
002702	000001 0230 03	4911	ADLX	Y,1,DU	BUMP ONE	1320
002703	000024 7430 11	4912	STX	Y,T\$TEMP4,T	SAVE	1330
002704	000022 0540 11	4913	AOS	T\$TEMP5,T	BUMP LOOP COUNTER	1340
002705	002666 6040 00	4914	TMI	SKDR6	LOOP IF MORE DEVICES OF THIS TYPE NEEDED	1350
002706	002657 7100 00	4915	TRA	SKDR5	OTHERWISE GET NEXT TYPE	1360
		4916 *				1370
		4917 *	RESOURCES ALLOCATED, REMOVE FROM LIST			1380
		4918 *				1390
	002707	4919	SKDR8	BSS	0	1400
002707	000020 7200 11	4920	LXL	0,T\$TEMP8,T	GET BACK PTR TO CURRENT BLOCK	1410
002710	777777 2220 10	4921	LDX	X,-Q\$OFFST+Q\$LINK,0	*PTR TO SUCCESSOR	1420
002711	000020 2230 11	4922	LDX	Y,T\$TEMP8,T	GET BACK PTR TO PREDECESSOR	1430
END OF BINARY CARD IOS00079						
002712	000000 7420 13	4923	STX	X,0,Y	REMOVE FROM LIST	1440
		4924 *				1450
		4925 *	ADD JOB TO Q\$TASK QUEUE			1460
		4926 *				1470
002713	005162 7170 00	4927	XED	Q\$XADD+Q\$TASK	PLACE ON Q\$TASK	1480
		4928 *				1490
		4929 *	TEST FOR DONE			1500
		4930 *				1510
002714	000025 2250 11	4931	LDX	Q,T\$TEMP3,T	RESTORE QADDRESS	1520
002715	000001 1000 15	4932	CMPX	0,Q\$LAST,Q	TEST FOR LAST	1530
002716	002722 6010 00	4933	TNZ	SKDR9	NOPE, CONTINUE	1540
002717	000001 6230 13	4934	EAX	Y,-Q\$LINK+Q\$OFFST,Y	*UPDATE	1550
002720	000001 7430 15	4935	STX	Y,Q\$LAST,Q	Q\$LAST PTR	1560

R		SCHEDULER -- RUN					
002721	002724	7100	00	4936	TRA SKDRX	AND EXIT	1570
				4937	*		1580
				4938	*	FUDGE IN ORDER TO LOOP THRU LIST AFTER REMOVAL	1590
				4939	*		1600
		002722		4940	SKDR9 BSS	0	1610
002722	777777	2200	10	4941	LDX	0,-Q\$OFFST+Q\$LINK,0 #MOVE TO NEXT BLOCK	1620
002723	002606	7100	00	4942	TRA SKR0.1	AND LOOP WITHOUT DISTURBING PREDECESSOR	1630
				4943	*		1640
				4944	*	DONE, BUG REGISTERS, AND RETURN	1650
				4945	*		1660
		002724		4946	SKDRX BSS	0	1670
		002724		4947	BUGXR	(X,Y,Z,0) BUG REGISTERS	1680
		525245			BUGBUG SET	BUGBUG+1	
002724	525245	2220	03		LDX	X,BUGBUG,DU	
002725	525245	2230	03		LDX	Y,BUGBUG,DU	
002726	525245	2240	03		LDX	Z,BUGBUG,DU	
002727	525245	2250	03		LDX	0,BUGBUG,DU	
002730	000025	7270	11	4948	LXL	L,T\$TEMP3,T RETRIEVE RETURN ADDRESS	1690
		002731		4949	BUG	(T\$TEMP3,T) BUG IT (BOTH HALVES)	1700
		525246			BUGBUG SET	BUGBUG+1	
002731	525246	2200	03		LDX	0,BUGBUG,DU	
002732	000025	7400	11		STX	0,T\$TEMP3,T	
002733	000025	4400	11		SXL	0,T\$TEMP3,T	
002734	000000	7100	17	4950	TRA	0,L RETURN TO CALLER	1710
				4951	*\$*	DISK SKEDCK	1720

R SCHEDULER -- CHECK

			002735	4953	USE	CODE		110
				4954	HEAD	R		120
				4955 *				130
				4956 *				140
				4957 *		CHECK		150
				4958 *				160
				4959 *		THESE ROUTINES DETERMINE WHETHER OR NOT ENOUGH RESOURCES		170
				4960 *		OF A SPECIFIC TYPE ARE AVAILABLE. THE DECISION IS BASED ON		180
				4961 *		UNVIOUSLY THE NUMBER OF FREE UNITS OF THE REQUESTED TYPE AND		190
				4962 *		ON A 'JOHN HAMM' POLICY RULE (SUBJECT TO CHANGE WITHOUT		200
				4963 *		NOTIFICATION). IF THE REQUEST CAN BE SATIFIED, THEN THE		210
				4964 *		ROUTINES EXIT TO R\$SKDR2; OTHERWISE R\$SKDR3.		220
				4965 *				230
				4966 *	ENTER WITH			240
				4967 *		C(A) = NUMBER REQUESTED		250
				4968 *		C(X) = PTR TO DEVICE HEADER OF TYPE REQUESTED		260
				4969 *	RETURNS			270
				4970 *		R\$SKDR2 IF SATIFIABLE		280
				4971 *		R\$SKDR3 OTHERWISE		290
				4972 *				300
			002735	4973	SCPCK	BSS 0	CARD PUNCH CHECK	310
			002735	4974	SCRCK	BSS 0	CARD READER CHECK	320
			002735	4975	SMTCK	BSS 0	MAG TAPE CHECK	330
			002735	4976	CMPA	AVAIL,X	ARE THAT MANY AVAILABLE?	340
			002735	4977	TZE	SKDR2	YES, NO POLICY RULE TO CHECK HERE	350
END	002735	000005	1150	12				
	002736	002635	6000	00				
	002737	002635	6040	00	4978	TMI SKDR2	YES, DITTO	360
	002740	002640	7100	00	4979	TRA SKDR3	NO, SO SOLLY PREASE	370
					4980 *			380
					4981 *			390
					4982 *	YE OLDE LINE PRINTERS		400
					4983 *			410
					4984 *	CURRENT IMPLEMENTED ALGORITHM:		420
					4985 *	GIVEN N PRINTERS, WHERE N>1, THEN WE'LL POTENTIALLY ALLOW N=1		430
					4986 *	LONG JOBS (SEE R\$LJOBS) TO RUN SIMULTANEOUSLY. IF N=1, THEN		440
					4987 *	WE'LL RUN SHORT, LONG, AND MEDIUM JOBS IN THAT ORDER.		450
					4988 *			460
			002741	4989	SLPCK	BSS 0	LINE PRINTER CHECK	470
	002741	000002	1150	07	4990	CMPA 2,DL	ALLOW ONLY ONE PRINTER PER JOB	480
	002742	777777	6030	00	4991	TRC \$ERROR	***PROBLEM	490
	002743	000004	2360	12	4992	LDQ OMAX,X	GET NUMBER OF PRINTERS WE OWN	500
	002744	000002	1760	07	4993	SBQ 2,DL	PART OF POLICY RULE	510
	002745	002735	6040	00	4994	TMI SCPCK	ALL RIGHT TO RUN LONG JOB	520
	002746	005674	1160	00	4995	CMPQ LJOBS	HAVE 2 OR MORE PRINTERS, RUN LJOR?	530
	002747	002640	6040	00	4996	TMI SKDR3	HAVE OMAX=1 LONG JOBS ALREADY GOING	540
	002750	002735	7100	00	4997	TRA SCPCK	OK TO TRY TO RUN A LONG JOB	550

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UTILITY -- SCRATCH INPUT QUEUE FILE

			002765	5043	USE	CODE		110
				5044	HEAD	R		120
				5045 *				130
				5046 *				140
				5047 *				150
				5048 *				160
				5049 *				170
				5050 *				180
				5051 *				190
				5052 *				200
				5053 *				210
				5054 *				220
				5055 *				230
				5056 *	ENTER BY			240
				5057 *		CREATED TASK R\$SCR		250
				5058 *	ENTER WITH			260
				5059 *		C(T\$TEMP1,T) = FRN OF INPUT QUEUE FILE		270
				5060 *		C(T\$TEMP2,T) = PTR TO NCB		280
				5061 *	RETURNS			290
				5062 *		TERMINATES -- ASYNCHRONOUS TASK		300
				5063 *				310
			002765	5064	SCR	BSS	0	320
			002765	5065	LOCK	(T\$TEMP1,T)	LOCK THE FILE	330
002765	000736	7000	00		TSX	0,\$LOCK		
002766	000027	0000	11		ARG	T\$TEMP1,T		
			002767	5066	CHECK	SCR1,\$B\$Z,SCR,\$B\$LOCK,SCR		340
002767	000000	7200	11		LXL	0,T\$SRW1,T		
002770	000077	3600	03		ANX	0,\$B\$STMK,DU		
002771	002777	6000	00		TZE	SCR1		
002772	000003	1000	03		CMPX	0,\$B\$Z,DU		
002773	002765	6000	00		TZE	SCR		
002774	000013	1000	03		CMPX	0,\$B\$LOCK,DU		
002775	002765	6000	00		TZE	SCR		
002776	777777	7100	00		TRA	\$ERROR		
			002777	5067	SCR1	BSS	0	FILE LOCKED
002777	000026	2220	11	5068	LDX	X,T\$TEMP2,T	GET PTR TO NCB	350
003000	000032	2340	12	5069	SZN	C\$BUSY,X	TEST COUNT	360
003001	003037	6010	00	5070	TNZ	SCR5	BUSY, EXIT	370
			003002	5071	GETC	(\$QBFSZ,DL)	GET A WORKING BUFFER	380
END OF BINARY CARD	IOS00082							390
003002	000100	2350	07		LDA	\$QBFSZ,DL		
003003	001152	7070	00		TSX	L,\$GETC		
003004	000025	7550	11	5072	STA	T\$TEMP3,T	SAVE FOR READ	400
			003005	5073	SCR2	READ	(T\$TEMP1,T),(T\$TEMP3,T),(1,DU),(0,DU) READ	410
003005	000536	7000	00		TSX	0,\$READ		
003006	000027	0000	11		ARG	T\$TEMP1,T		
003007	000025	0000	11		ARG	T\$TEMP3,T		
003010	000001	0000	03		ARG	1,DU		
003011	000000	0000	03		ARG	0,DU		
			003012	5074	CHECK	SCR7,\$B\$Z,SCR2,\$B\$EOF,SCR3		420

R		UTILITY -- SCRATCH INPUT QUEUE FILE				
003012	000000	7200	11	LXL	0,TSSRW1,T	
003013	000077	3600	03	ANX	0,B\$STMK,DU	
003014	003051	6000	00	TZE	SCR7	
003015	000003	1000	03	CMPX	0,B\$RZ,DU	
003016	003005	6000	00	TZE	SCR2	
003017	000016	1000	03	CMPX	0,B\$FOF,DU	
003020	003022	6000	00	TZE	SCR3	
003021	777777	7100	00	TRA	\$ERROR	
		003022		0		
		003022	5075	SCR3	BSS	430
			5076	SCR	(T\$TEMP1,T),(0,DU) SCRATCH THE FILE	440
003022	000612	7000	00	TSX	0,\$SCR	
003023	000027	0000	11	ARG	T\$TEMP1,T	
003024	000000	0000	03	ARG	0,DU	
		003025	5077	CHECK	SCR4,B\$BZ,SCR3	450
003025	000000	7200	11	LXL	0,TSSRW1,T	
003026	000077	3600	03	ANX	0,B\$STMK,DU	
003027	003033	6000	00	TZE	SCR4	
END	OF BINARY CARD	IOS00083				
003030	000003	1000	03	CMPX	0,B\$RZ,DU	
003031	003022	6000	00	TZE	SCR3	
003032	777777	7100	00	TRA	\$ERROR	
		003033	5078	SCR4	BSS	0
003033	000026	2220	11	5079	LDX	X,T\$TEMP2,T
003034	000031	4500	12	5080	STZ	C\$OFLOC,X
		003035	5081	RELC	(T\$TEMP3,T)	RELEASE BUFFER
003035	000025	2350	11	LDA	T\$TEMP3,T	
003036	001252	7070	00	TSX	L,R\$RELC	
		003037	5082	SCR5	BSS	0
		003037	5083	UNLCK	(T\$TEMP1,T)	UNLOCK IT
003037	000746	7000	00	TSX	0,\$UNLCK	
003040	000027	0000	11	ARG	T\$TEMP1,T	
		003041	5084	CHECK	SCR6,B\$BZ,SCR5	
003041	000000	7200	11	LXL	0,TSSRW1,T	
003042	000077	3600	03	ANX	0,B\$STMK,DU	
003043	003047	6000	00	TZE	SCR6	
003044	000003	1000	03	CMPX	0,B\$RZ,DU	
003045	003037	6000	00	TZE	SCR5	
003046	777777	7100	00	TRA	\$ERROR	
		003047	5085	SCR6	BSS	0
		003047	5086	RELT		DONE
003047	001477	7000	00	TSX	0,T\$RELT	RELEASE TCB
		003050	5087	EXIT		EXIT
003050	003074	7100	00	TRA	\$EXIT	
			5088	*		
			5089	*		
			5090	*		
		003051	5091	SCR7	BSS	0
003051	000000	2200	11	5092	LDX	0,TSSRW1,T
003052	003022	6000	00	5093	TZE	SCR3
		003053	5094	SCR8	BSS	0
						FILE NOT EMPTY
						GET NUMBER OF UNITS TRANSFERRED
						NONE, THEN SCRATCH THE FILE

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UTILITY -- SCRATCH INPUT QUEUE FILE

		003053	5095	SPTR	(T\$TEMP1,T),(-1,DU)	SET POINTER BACK ONE	630
003053	000623	7000 00		TSX	0,\$SPTP		
003054	000027	0000 11		ARG	T\$TEMP1,T		
003055	777777	0000 03		ARG	-1,DU		
		003056	5096	CHECK	SCR4,B\$B7,SCR8		640
END OF BINARY CARD	IOS00084						
003056	000000	7200 11		LXL	0,T\$SRW1,T		
003057	000077	3600 03		ANX	0,B\$STMK,DU		
003060	003033	6000 00		TZE	SCR4		
003061	000003	1000 03		CMPX	0,B\$RZ,DU		
003062	003053	6000 00		TZE	SCR8		
003063	777777	7100 00		TRA	\$ERROR		
			5097 **	DISK	JOBTAB		650

R JOB NUMBER ASSIGNMENT AND TABLE

			003064	5099	USE	CODE		110
				5100	HEAD	J		120
				5101	*			130
				5102	*			140
				5103	*			150
				5104	*		JOB NUMBER ASSIGNMENT	160
				5105	*		THE JOB NUMBER IS RETURNED IN XR=X	170
				5106	*		CLOBEPS C(X0)	180
				5107	*			190
			003064	5108	JNUMB	BSS	0	200
003064	000000	2220	03	5109	LDX	X,0,DU	INITIALIZE FOR SEARCH	210
003065	000000	2350	07	5110	LDA	0,DL	CLEAR AL	220
003066	040300	5202	01	5111	RPT	MAXJB,1,TZE		230
003067	005700	1150	12	5112	CMPA	JTAB,X	SEARCH FOR FREE JOB NUMBER	240
003070	777777	6010	00	5113	TNZ	SERROR	***OY VEHI	250
003071	005701	1220	03	5114	SBLX	X,JTAB+1,DU	COMPUTE JOB NUMBER	260
003072	005700	7460	12	5115	STX	J,JTAB,X	MARK IT ALLOCATED TO US	270
003073	000000	7100	17	5116	TRA	0,L	RETURN TO CALLER	280
				5117	*			290
				5118	*			300
				5119	*			310
				5120	*		JOB TABLE	320
				5121	*			330
				5122	*		THE JOB TABLE IS A TABLE OF ONE WORD ENTRIES, INDEXED	340
				5123	*		BY THE JOB NUMBER. INFORMATION IN THE JOB TABLE IS	350
				5124	*		THAT WHICH MUST BE LOCATED OR MATCHED FOR CONSISTENCY	360
				5125	*		CHECKS AND FOR DEBUGGING PURPOSES. THE TABLE CONTAINS	370
				5126	*		MAXJB ENTRIES.	380
				5127	*			390
			005675	5128	USE	STORE		400
			000020	5129	MAXJB	EQU	16	410
							MAXIMUM NUMBER OF JOBS IN THE SYSTEM	420
				5130	*			430
				5131	*			440
			005700	5132	EIGHT		TO MAKE DEBUGGING EASIER	450
			005700	5133	JTAB	BSS	0	460
			005700	5134	DUP	1,MAXJB	(UPPER) PTR TO JCB/ (LOWER) PTR TO DEVICE	470
005700	000000	000000		5135	ZERO	0,0	INITIALLY OFF	480
END OF BINARY CARD	10500085							
			003074	5136	USE	PREVIOUS		490
				5137	*\$*	DISK	XIT	490

J		EXIT			
	003074	5139	USE	CODE	110
		5140	HEAD		120
		5141 *			130
		5142 *			140
		5143 *		EXIT	150
		5144 *			160
		5145 *	COMPLETE ONE TASK AND BEGIN ANOTHER FROM Q\$TASK QUEUE		170
		5146 *			180
		5147 -	ENTER BY		190
		5148 *	TRA \$EXIT		200
		5149 *	RETURN TO NEXT TASK		210
		5150 *	RETURNS WITH		220
		5151 *	C(J) = JOB NUMBER		230
		5152 *	C(T) = TRAP BLOCK		240
		5153 *	C(L) = TASK-START-ADDRESS		250
		5154 *			260
	003074	5155	EXIT	BSS 0 ENTRY POINT	270
	003074	5156		CKPT DEBUGGING	280
003074	000474 7170 00		XED	X\$CKPT	
		5157	INHIB	SAVE,ON LOCK OUT UNWANTED INTERRUPTS	290
	003075	5158	EXIT1	BSS 0 TIME TO FOOL WITH THE Q\$TASK QUEUE	300
003075	005161 6202 00	5159	EAX	0,Q\$FIRST+1,Q\$TASK ADDRESS OF FIRST ELEMENT	310
003076	005161 1002 00	5160	CMPIX	0,Q\$LAST+Q\$TASK *DOES LAST POINT TO IT?	320
003077	003135 6002 00	5161	TZE	WAIT	330
003100	005160 2212 00	5162	LDX	T,Q\$FIRST+Q\$TASK *OFFSET POINTER TO BLOCK	340
003101	777777 6002 00	5163	TZE	\$ERROR ***PROBLEM	350
003102	005161 1012 00	5164	CMPIX	T,Q\$LAST+Q\$TASK *IS THIS LAST?	360
003103	003106 6012 00	5165	TNZ	*+3 NO	370
003104	005161 6202 00	5166	EAX	0,Q\$FIRST+1+Q\$TASK YES, SET THIS QUEUE	380
END OF BINARY CARD	IOS00086				
003105	005161 7402 00	5167	STX	0,Q\$LAST+Q\$TASK *TO EMPTY STATUS	390
003106	000004 1212 03	5168	SBLX	T,Q\$OFFST,DU RELATE THE BEGINNING OF BLOCK	400
003107	777777 6002 00	5169	TZE	\$ERROR ***DBG	410
003110	777777 6042 00	5170	TMI	\$ERROR ***DBG	420
003111	000003 2222 11	5171	LDX	X,Q\$LINK,T GET OFFSET POINTER TO NEXT BLOCK	430
003112	005160 7422 00	5172	STX	X,Q\$FIRST+Q\$TASK *AND MAKE IT NOW FIRST	440
		5173	INHIB	RESTORE RESUME NORMAL TELECAST	450
003113	000006 7260 11	5174	LXL	J,T\$JCR,T RESTORE JCR POINTER	460
003114	777777 6000 00	5175	TZE	\$ERROR ***DBG	470
003115	777777 6040 00	5176	TMI	\$ERROR ***DBG	480
003116	000004 2270 11	5177	LDX	L,T\$TRA,T AND TRANSFER ADDRESS	490
003117	777777 6000 00	5178	TZE	\$ERROR ***DBG	500
003120	777777 6040 00	5179	TMI	\$ERROR ***DBG	510
	003121	5180	BUGU	(T\$TRA,T) BUG RETURN	520
	525247		BUGBUG	SET BUGBUG+1	
003121	525247 2200 03		LDX	0,BUGBUG,DU	
003122	000004 7400 11		STX	0,T\$TRA,T	
	003123	5181	BUGXR	(0,X,Y,Z,Q) BUG THE REGISTERS	530
	525250		BUGBUG	SET BUGBUG+1	
003123	525250 2200 03		LDX	0,BUGBUG,DU	

EXIT

003124	525250	2220	03		LDX	X,BUGBUG,DU		
003125	525250	2230	03		LDX	Y,BUGBUG,DU		
003126	525250	2240	03		LDY	Z,BUGBUG,DU		
003127	525250	2250	03		LDX	Q,BUGBUG,DU		
		003130		5182	BUGA			540
		525251			BUGBUG	SET	BUGBUG+1	
003130	525251	2350	03		LDA	BUGRUG,DU		
003131	525251	2750	07		ORA	BUGRUG,DL		
		003132		5183	BUGQ			550
		525252			BUGRUG	SET	BUGRUG+1	
003132	525252	2360	03		LDQ	BUGRUG,DU		
END	OF BINARY CARD	IOS00087						
003133	525252	2760	07		ORQ	BUGRUG,DL		
003134	000000	7100	17	5184	EXIT2	TRA	0,L AND AWAAAY WE GO!	560
				5185	*			570
				5186	*			580
				5187	*		WAIT FOR SOMETHING TO HAPPEN	590
				5188	*			600
				5189	*			610
				5190		INHIB	SAVE,ON	620
003135	000017	2202	03	5191	WAIT	LDX	0,%,PAUSE,DU PAUSE AND START AT	630
003136	000000	0012	00	5192		MME	ANY INTERRUPT	640
				5193		INHIB	RESTORE	650
003137	003075	7100	00	5194		TRA	EXIT1 SKIP CHECKPOINT	660
				5195	**	DISK	NOTC	690

COMMUNICATIONS -- DESCRIPTION

003140	5197	USE	CODE	110
	5198	HEAD	C	120
	5199 *			130
	5200 *			140
	5201 *	COMMUNICATIONS NETWORK STRUCTURE		150
	5202 *			160
	5203 *	THERE EXISTS A PRIVATE COMMUNICATIONS NETWORK AMONG THE		170
	5204 *	THE MONITOR AND ALL OF ITS SUB-MODULES (I.E. PERIPHERAL DRIVERS).		180
	5205 *	AT STARTUP TIME FOR THE MONITOR, IT CREATES THE NETWORK BY		190
	5206 *	OPENING THREE SCRATCH EVENTS AND PASSING A FRN OF EACH TO EACH		200
	5207 *	SUB-MODULES SPAWNED. FOR THE SUB-MODULES, THESE EVENTS ARE		210
	5208 *	REFERENCED BY CANONICAL NUMBERS:		220
	5209 *			230
	5210 *	FRN0 --		240
	5211 *	THIS IS THE COMMAND EVENT FOR THE DRIVERS. EACH DRIVER IS		250
	5212 *	ALLOWED NOTIFY ACCESS ONLY. COMMANDS ARE CHANNLED TO THE		260
	5213 *	SPECIFIED SUB-MODULE BY THE STATE WHEN CAUSED.		270
	5214 *			280
	5215 *	FRN1 --		290
	5216 *	THIS IS THE COMMAND REPLY EVENT FOR THE DRIVERS. EACH DRIVER		300
	5217 *	IS ALLOWED CAUSE ACCESS ONLY. IN ORDER TO INFORM THE		310
	5218 *	MONITOR THE PERIPHERAL DRIVER CAUSES THIS EVENT WITH ITS STATE.		320
	5219 *			330
	5220 *	FRN2 --		340
	5221 *	AS IMPLIED ABOVE, FRN0 AND FRN1 ARE AN INPUT/OUTPUT PAIR.		350
	5222 *	FRN2 HOWEVER IS NOT PAIRED AT ALL. THE MONITOR USES THIS		360
	5223 *	EVENT AS A PASS EVENT SENDING FILES TO BE PROCESSED AND DEVICES		370
	5224 *	DOWN TO ITS SONS. THE SONS NEVER EVER PASS ANYTHING BACK TO		380
	5225 *	THE FATHER. THEY SIMPLY CLOSE FILES.		390
	5226 *			410
	5227 *	MESSAGE FORMATS: RETURNED IN T\$SRW2,T (UPPER)		420
	5228 *			430
	5229 *	FOR FRN2 --		440
	5230 *	BITS 0 - 3 = JOB NUMBER (WITH 0 ILLEGAL)		450
	5231 *	BITS 4 = BANNER (ON MEANS SUPPLY BANNER)		460
	5232 *	BITS 5 = OUTPUT MODE: 512/ 320 (ON MEANS 320)		470
	5233 *	BITS 6 -17 = START ADDRESS (IN ELEMENTS)		480
	5234 *			490
	5235 *	FOR FRN1 --		500
	5236 *	BITS 0 - 3 = MUST BE ZERO		510
	5237 *	BITS 4 - 7 = COMMAND		520
	5238 *	BITS 8 -14 = <NOT USED>		530
	5239 *	BITS 15-17 = DEVICE UNIT NUMBER (0-7)		540
	5240 *			550
	5241 *			560
	5242 *	FOR FRN0 --		570
	5243 *	BITS 0 - 3 = JOB NUMBER (MUST BE ZERO)		580
	5244 *	BITS 4 - 7 = COMMAND		590
	5245 *	BITS 8 -14 = <NOT USED>		600
	5246 *	BITS 15-17 = DEVICE UNIT NUMBER (0 THRU 7)		610

C			COMMUNICATIONS -- SEND MESSAGE						
		003140	5248	USE	CODE		630		
			5249	HEAD	C		640		
			5250 *				650		
			5251 *				660		
			5252 *		SEND A MESSAGE		670		
			5253 *				680		
			5254 *		THIS TASK SENDS A 36 BIT MESSAGE, WHICH IS BIT CODED TO THE		690		
			5255 *		MONITOR ON THE OUTPUT EVENT FILE FRN1. THE STATE AND		700		
			5256 *		MESSAGE ARE PASSED TO IT IN T\$TEMP1,T AND T\$TEMP2,T,		710		
			5257 *		RESPECTIVELY. AFTER SUCCESSFULLY TRANSMITTING THE MESSAGE		720		
			5258 *		THE TASK EVAPORATES.		730		
			5259 *				740		
			5260 *	ENTER WITH			750		
			5261 *		C(XT) = TBLOCK-ADDRESS		760		
			5262 *		C(T\$TEMP1,T) = STATE		770		
			5263 *		C(T\$TEMP2,T) = MESSAGE		780		
			5264 *		SINCE AN ASYNCHRONOUS TASK, IT CAN USE ALL TEMP'S		790		
			5265 *		CALLS ONLY R\$RELT WHEN DONE.		800		
			5266 *				810		
		003140	5267	MESSX	BSS	0	820		
		003140	5268	CAUSE	FRNO,(1,DU),(T\$TEMP1,T),(T\$TEMP2,T),(0,DU),(0,DU)	ENTRY POINT	830		
003140	000767	7000	00	TSX	0,\$CAUSE				
003141	006217	0000	00	ARG	FRNO				
003142	000001	0000	03	ARG	1,DU				
003143	000027	0000	11	ARG	T\$TEMP1,T				
003144	000026	0000	11	ARG	T\$TEMP2,T				
003145	000000	0000	03	ARG	0,DU				
003146	000000	0000	03	ARG	0,DU				
		003147	5269	CHECK	MESS1,B\$BZ,MESSX		840		
003147	000000	7200	11	LXL	0,T\$SRW1,T				
003150	000077	3600	03	ANX	0,R\$STMK,DU				
003151	003155	6000	00	TZE	MESS1				
003152	000003	1000	03	CMPX	0,B\$BZ,DU				
003153	003140	6000	00	TZE	MESSX				
003154	777777	7100	00	TRA	\$ERROR				
003155	000000	2220	11	5270	MESS1	LDX	X,T\$SRW1,T	GET NUMBER OF PEOPLE NOTIFIED	850
003156	003140	6000	00	5271	TZE	MESSX	NONE, RE-SEND	860	
			5272 *				870		
			5273 *	MESSAGE SENT			880		
			5274 *				890		
		003157	5275	RELT		RELEASE TRAP BLOCK	900		
003157	001477	7000	00	TSX	0,T\$RELT				
		003160	5276	EXIT		EVAPORATE	910		
END OF BINARY CARD	IOS00088								
003160	003074	7100	00	TRA	\$EXIT				

C

COMMUNICATIONS -- CTRAP SERVICE

			5279	USE	CODE		940
		003161	5280	HEAD	C		950
			5281 *				960
			5282 *			CTRAP SERVICE	970
			5283 *				980
			5284 *			THIS SUBROUTINE IS ENTERED WHENEVER AN OUTSTANDING	990
			5285 *			NOTIFY IS CAUSED. THE ROUTINE ASCERTAINS THE REASON	1000
			5286 *			FOR THE CAUSE AND TRANSFERS CONTROL TO THE AP-	1010
			5287 *			PROPRIATE SUBROUTINE.	1020
			5288 *				1030
			5289 *	CALL WITH			1040
			5290 *		C(XT) = CTRAP ADDRESS		1050
			5291 *	CALLS			1060
			5292 *		C\$NSRVX (TO RE-ISSUE NOTIFY)		1070
			5293 *		OR APPROPRIATE SUBROUTINE		1080
			5294 *				1090
		003161	5295 NSRV	BSS	0		1100
		003161	5296	CHECK	NSRV1,B\$TLE,NSRVX		1110
003161	000000	7200	11	LXL	0,T\$SRW1,T		
003162	000077	3600	03	ANX	0,B\$STMK,DU		
003163	003167	6000	00	TZE	NSRV1		
003164	000011	1000	03	CMPX	0,B\$TLE,DU		
003165	003171	6000	00	TZE	NSRVX		
003166	777777	7100	00	TRA	\$ERROR		
		003167	5297 NSRV1	BSS	0	IT WAS REALLY CAUSED	1120
003167	000005	7270	11	5298	L,C\$RLINK,T	WELL WHO GETS IT?	1130
003170	000000	7100	17	5299	0,L	LET HIM HANDLE IT	1140

C

COMMUNICATIONS -- RE-ISSUE NOTIFY

			003171	5301	USE	CODE		1160
				5302	HEAD	C		1170
				5303 *				1180
				5304 *				1190
				5305 *		RE-ISSUE NOTIFY		1200
				5306 *				1210
				5307 *	THIS SUBROUTINE SIMPLY RE-ISSUES THE NOTIFY FOR			1220
				5308 *	ANY NCB (NOTIFY CONTROL BLOCK). AFTER A SUCCESSFUL OPERATION			1230
				5309 *	THE TASK EVAPORATES.			1240
				5310 *				1250
				5311 *	CALL WITH			1260
				5312 *	C(XT) = NCB ADDRESS			1270
				5313 *	CALLS			1280
				5314 *	T\$GETT			1290
				5315 *	\$SETUP			1300
				5316 *	\$NOTIF			1310
				5317 *	T\$RELT			1320
				5318 *				1330
			003171	5319	NSRVX	BSS	0	1340
			003171	5320	GETT		GET A TRAP BLOCK	1350
003171	001467	7000	00	TSX	0,T\$GETT			
003172	000000	6220	11	EAX	X,0,T		SAVE A PTR TO IT	1360
003173	000005	2210	11	LDX	T,T\$LINK,T		LET T POINT TO ORIGINAL TCB	1370
003174	003161	6200	00	EAX	0,NSRV		RESTART ADDRESS	1380
			003175	5324	SETUP		RESTART CTRAP	1390
003175	000510	7170	00	XED	\$SETUP			
003176	000000	6210	12	5325	EAX	T,0,X	GET BACK PTR TO NEW TCB	1400
003177	000005	2230	11	5326	NSX1	LDX	3,T\$LINK,T	MAKE X3 POINT TO NCB
			003200	5327	NOTIF	(ERN,3),(STATE,3)		1420
003200	000756	7000	00	TSX	0,\$NOTIF			
003201	000024	0000	13	ARG	ERN,3			
003202	000025	0000	13	ARG	STATE,3			
			003203	5328	CHECK	NSX2,\$BZ,NSX1		1430
003203	000000	7200	11	LXL	0,T\$SRW1,T			
END	OF BINARY CARD	10500089						
003204	000077	3600	03	ANX	0,\$\$TMK,DU			
003205	003211	6000	00	TZE	NSX2			
003206	000003	1000	03	CMPX	0,\$BZ,DU			
003207	003177	6000	00	TZE	NSX1			
003210	777777	7100	00	TRA	\$ERROR			
			003211	5329	NSX2	BSS	0	SUCCESSFULLY RE-ISSUED NOTIFY
			003211	5330	RELT		RELEASE TBLOCK	1450
003211	001477	7000	00	TSX	0,T\$RELT			
			003212	5331	EXIT		EXIT	1460
003212	003074	7100	00	TRA	\$EXIT			

C

COMMUNICATIONS -- NOTIFY CONTROL BLOCKS

			5333 *			1480
			5334 *			1490
			5335 *		NOTIFY CONTROL BLOCKS	1500
			5336 *			1510
			5337 *		PRINT-FILE-EVENT	1520
			5338 *			1530
		003213	5339	NCB	LPE,LPE,\$INIT,-1,0,R\$LPAB+\$SOMAX,0,\$INP1,\$STYPL,1	1540
		005720		USE	STORE	
		005720		EIGHT		
		005720	LPE	BSS	0	
005720	000000	000035		ZERO	0,\$STRO	
005721	000000	000000		ZERO	0,0	
005722	000000	000000		ZERO	0,0	
005723	000000	000000		ZERO	0,0	
005724	000000	000000		ZERO	**0	
005725	000000	003301		ZERO	0,\$INIT	
005726	005720	005720		ZERO	*-C\$NC9,*-C\$JCB	
005727	114120105040			ASCII	1,LPE	ASCII ABBREVIATION
		005730		DUP	1,16-4	
		005730		DEC	0	
END	OF BINARY CARD	IOS00090				
005744	777777	0000 00	FRLPE	ARG	-1	
005745	000000	000000		ZERO	0,0	
		005746		DUP	1,2	
005746	000000000000			DEC	0	
005750	777777	0000 00		ARG	-1	
005751	000000000000			DEC	0	
005752	000000000000			DEC	0	
005753	005574	0000 00		ARG	R\$LPAB+\$SOMAX	
005754	005220	0000 00		ARG	0\$INP1	
005755	000003	000001		ZERO	\$STYPL,1	
005756	777777777777			DEC	-1	
		003213		USE	PREVIOUS	
		005750	5340	FRLPQ	EQU	LPE+QFRN
						INPUT FILE RN
			5341 *			1550
			5342 *			1560
			5343 *		PUNCH-FILE-EVENT	1570
			5344 *			1580
		003213	5345	NCB	CPE,CPE,\$INIT,-1,0,R\$CPAB+\$SOMAX,0,\$INP1,\$STYPC,1	1590
		005757		USE	STORE	1600
		005760		EIGHT		
		005760	CPE	BSS	0	
005760	000000	000035		ZERO	0,\$STRO	
005761	000000	000000		ZERO	0,0	
END	OF BINARY CARD	IOS00091				
005762	000000	000000		ZERO	0,0	
005763	000000	000000		ZERO	0,0	
005764	000000	000000		ZERO	**0	
005765	000000	003301		ZERO	0,\$INIT	
005766	005760	005760		ZERO	*-C\$NCB,*-C\$JCB	


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C
COMMUNICATIONS -- NOTIFY CONTROL BLOCKS

005767 103120105040          UASCII 1,CPE          ASCII ABBREVIATION
              005770          DUP 1,16-4
005770 000000000000          DEC 0
006004 777777 0000 00          FRCPE ARG -1
006005 000000 000000          ZERO 0,0
              006006          DUP 1,2
006006 000000000000          DEC 0
END OF BINARY CARD IOS00092
006010 777777 0000 00          ARG -1
006011 000000000000          DEC 0
006012 000000000000          DEC 0
006013 005554 0000 00          ARG R$CPTAB+$SOMAX
006014 005220 0000 00          ARG Q$INP1
006015 000001 000001          ZERO R$TYPCP,1
006016 777777777777          DEC -1
              003213          USE PREVIOUS
              006010          EQU CPE+QFRN          INPUT FILE RN          1610
5347 *          1620
5348 *          1630
5349 *          1640
              JOB-STREAM-SCHEDULER-EVENT
5350 *          1650
              525253          5351 BUGBUG SET BUGBUG+1          ***DBG          1660
              003213          5352 JSS,JSS,$SENDER,-1,0,$RUGBUG,$RUGBUG,$RUGBUG,$RUGBUG          1670
              006017          USE STORE
              006020          EIGHT
              006020          JSS BSS 0
006020 000000 000035          ZERO 0,BSTRO
006021 000000 000000          ZERO 0,0
006022 000000 000000          ZERO 0,0
006023 000000 000000          ZERO 0,0
006024 000000 000000          ZERO **0
006025 000000 001775          ZERO 0,$SENDER
006026 006020 006020          ZERO *-C$NCB,**-C$JCR
006027 112123123040          UASCII 1,JSS          ASCII ABBREVIATION
              006030          DUP 1,16-4
006030 000000000000          DEC 0
END OF BINARY CARD IOS00093
006044 777777 0000 00          FRJSS ARG -1
006045 000000 000000          ZERO 0,0
              006046          DUP 1,2
006046 000000000000          DEC 0
006050 777777 0000 00          ARG -1
006051 000000000000          DEC 0
006052 000000000000          DEC 0
006053 525253 0000 00          ARG $RUGBUG
006054 525253 0000 00          ARG $RUGRUG
006055 525253 525253          ZERO $RUGRUG,$RUGBUG
006056 777777777777          DEC -1
              003213          USE PREVIOUS
5353 *          1680

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C

COMMUNICATIONS -- NOTIFY CONTROL BLOCKS

```

5354 *                                     1690
5355 *                                     FRN1-EVENT-LP (INPUT) 1700
5356 *                                     1710
525254 BUGRUG SET BUGRUG+1 ***DBG 1720
003213 5358 NCB LP1,LP1,C$COMD,-1,$LPST,$BUGRUG,$BUGBUG,$BUGBUG,$BUGRUG 1730
006057 USE STORE
006060 EIGHT
LP1 BSS 0
ZERO 0,B$TRO
ZERO 0,0
ZERO 0,0
ZERO 0,0
ZERO **0
ZERO 0,C$COMD
ZERO *-C$NCB, *-C$JCB
UASCII 1,LP1 ASCII ABBREVIATION
DUP 1,16-4
DEC 0
FRLP1 ARG -1
ZERO $LPST,0
DUP 1,2
DEC 0
ARG -1
DEC 0
DEC 0
ARG $BUGBUG
ARG $BUGBUG
ZERO $BUGBUG,$BUGRUG
DEC -1
USE PREVIOUS
5359 *                                     1740
5360 *                                     1750
5361 *                                     $FRN1-EVENT-CP (INPUT) 1760
5362 *                                     1770
525255 BUGRUG SET BUGRUG+1 ***DBG 1780
003213 5364 NCB CP1,CP1,C$COMD,-1,$CPST,$BUGRUG,$BUGRUG,$BUGBUG,$BUGRUG 1790
006117 USE STORE
006120 EIGHT
CP1 BSS 0
ZERO 0,B$TRO
ZERO 0,0
ZERO 0,0
ZERO 0,0
ZERO **0
ZERO 0,C$COMD
ZERO *-C$NCB, *-C$JCB
UASCII 1,CP1 ASCII ABBREVIATION
DUP 1,16-4

```

END OF BINARY CARD IOS00094

006060 000000 000035

006061 000000 000000

006062 000000 000000

006063 000000 000000

006064 000000 000000

006065 000000 003213

006066 006060 006060

006067 114120061040

006070 006070

006070 000000000000

006104 777777 0000 00

006105 003000 000000

006106

END OF BINARY CARD IOS00095

006106 000000000000

006110 777777 0000 00

006111 000000000000

006112 000000000000

006113 525254 0000 00

006114 525254 0000 00

006115 525254 525254

006116 777777777777

003213

006120 000000 000035

006121 000000 000000

006122 000000 000000

006123 000000 000000

006124 000000 000000

006125 000000 003213

006126 006120 006120

006127 103120061040

006130

C
 006130 000000000000
 END OF BINARY CARD IOS00096
 006144 777777 0000 00
 006145 001000 000000
 006146
 006146 000000000000
 006150 777777 0000 00
 006151 000000000000
 006152 000000000000
 006153 525255 0000 00
 006154 525255 0000 00
 006155 525255 525255
 006156 777777777777
 003213

COMMUNICATIONS -- NOTIFY CONTROL BLOCKS

DEC 0
 FRCP1 ARG -1
 ZERO \$CPST,0
 DUP 1,2
 DEC 0
 ARG -1
 DEC 0
 DEC 0
 ARG \$BUGRUG
 ARG \$BUGRUG
 ZERO \$BUGRUG,\$BUGRUG
 DEC -1
 USE PREVIOUS

5365 * 1800
 5366 * 1810
 5367 * \$FRN1=EVENT=CR (INPUT) 1820
 525256 BUGRUG SET BUGRUG+1 ***DRG 1830
 003213 5369 NCB CR1,CR1,C\$COMD,-1,\$CRST,\$BUGRUG,\$BUGRUG,\$BUGRUG,\$BUGRUG 1840
 006157 USE STORE

END OF BINARY CARD IOS00097
 006160
 006160
 006160 000000 000035
 006161 000000 000000
 006162 000000 000000
 006163 000000 000000
 006164 000000 000000
 006165 000000 003213
 006166 006160 006160
 006167 103122061040
 006170

CR1 EIGHT
 BSS 0
 ZERO 0,B\$TR0
 ZERO 0,0
 ZERO 0,0
 ZERO 0,0
 ZERO **0
 ZERO 0,C\$COMD
 ZERO *-C\$NCB, *-C\$JCB
 UASCII 1,CR1 ASCII ABBREVIATION
 DUP 1,16-4
 DEC 0

006170 000000000000
 END OF BINARY CARD IOS00098
 006204 777777 0000 00
 006205 002000 000000
 006206
 006206 000000000000
 006210 777777 0000 00
 006211 000000000000
 006212 000000000000
 006213 525256 0000 00
 006214 525256 0000 00
 006215 525256 525256
 006216 777777777777
 003213

FRCR1 ARG -1
 ZERO \$CRST,0
 DUP 1,2
 DEC 0
 ARG -1
 DEC 0
 DEC 0
 ARG \$BUGRUG
 ARG \$BUGRUG
 ZERO \$BUGRUG,\$BUGRUG
 DEC -1
 USE PREVIOUS

5370 * 1850
 5371 * 1860
 5372 * COMMUNICATIONS FRN'S 1870
 5373 * 1880

C

COMMUNICATIONS -- NOTIFY CONTROL BLOCKS

		006217	5374	USE	STORE		1890
006217	777777	0000 00	5375	FRN0 ARG	-1	MON: COMMAND OUTPUT	1900
006220	777777	0000 00	5376	FRN1 ARG	-1	MON: COMMAND INPUT	1910
006221	777777	0000 00	5377	FRN2 ARG	-1	MON: COMMAND PASS	1920
			5378	*			1930
			5379	*			1940
			5380	*	PROCESS-ONE-EVENT (LPCP MODULE)		1950
			5381	*			1960
		525257	5382	BUGBUG SET	BUGBUG+1	***DBG	1970
		006222	5383	NCB	PR1,LPCP,\$CRASH,-1,0,BUGBUG,BUGBUG,BUGBUG,BUGBUG		1980
		006222		USE	STORE		
		006230		EIGHT			
		006230	PR1	BSS	0		
006230	000000	000035		ZERO	0,\$B\$TRO		
006231	000000	000000		ZERO	0,0		
006232	000000	000000		ZERO	0,0		
006233	000000	000000		ZERO	0,0		
END OF BINARY CARD	IOS00099						
006234	000000	000000		ZERO	***0		
006235	000000	004074		ZERO	0,\$CRASH		
006236	006230	006230		ZERO	*-C\$NCR,*-C\$JCB		
006237	114120103120			UASCII	1,LPCP	ASCII ABBREVIATION	
		006240		DUP	1,16=4		
006240	000000000000			DEC	0		
006254	777777	0000 00	FRPR1	ARG	-1		
006255	000000	000000		ZERO	0,0		
		006256		DUP	1,2		
006256	000000000000			DEC	0		
006260	777777	0000 00		ARG	-1		
006261	000000000000			DEC	0		
END OF BINARY CARD	IOS00100						
006262	000000000000			DEC	0		
006263	525257	0000 00		ARG	BUGBUG		
006264	525257	0000 00		ARG	BUGBUG		
006265	525257	525257		ZERO	BUGBUG,BUGBUG		
006266	777777777777			DEC	-1		
		006267		USE	PREVIOUS		
		006260	5384	FRPP	EQU	PR1+QFRN	1990
			5385	*			2000
			5386	*			2010
			5387	*	PROCESS-TWO-EVENT (CR MODULE)		2020
			5388	*			2030
		525260	5389	BUGBUG SET	BUGBUG+1	***DBG	2040
		006267	5390	NCB	PR2,CDRD,\$CRASH,-1,0,BUGBUG,BUGBUG,BUGBUG,BUGBUG		2050
		006267		USE	STORE		
		006270		EIGHT			
		006270	PR2	BSS	0		
006270	000000	000035		ZERO	0,\$B\$TRO		
006271	000000	000000		ZERO	0,0		
006272	000000	000000		ZERO	0,0		

C

COMMUNICATIONS -- NOTIFY CONTROL BLOCKS

006273	000000	000000		ZERO	0,0		
006274	000000	000000		ZERO	***0		
006275	000000	004074		ZERO	0, \$CRASH		
006276	006270	006270		ZERO	*-C\$NCR, *-C\$JCB		
006277	103104122104			UASCII	1, CDRD	ASCII ABBREVIATION	
		006300		DUP	1, 16-4		
006300	000000000000			DEC	0		
END	OF BINARY CARD	IOS00101					
006314	777777	0000 00	FRPR2	ARG	-1		
006315	000000	000000		ZERO	0,0		
		006316		DUP	1,2		
006316	000000000000			DEC	0		
006320	777777	0000 00		ARG	-1		
006321	000000000000			DEC	0		
006322	000000000000			DEC	0		
006323	525260	0000 00		ARG	BUGBUG		
006324	525260	0000 00		ARG	BUGBUG		
006325	525260	525260		ZERO	BUGBUG,BUGBUG		
006326	777777777777			DEC	-1		
		006327		USE	PREVIOUS		
		006320	5391 FRCR	EQU	PR2+QFRN	SPAWNED FRN	2060
			5392 **	DISK	COMD		2070

C

COMMUNICATIONS -- GCOMMAND GET

	003232	5431	USE	CODE		480
		5432	HEAD	C		490
		5433 *				500
		5434 *				510
		5435 *		COMMAND GET		520
		5436 *				530
		5437 *	THIS SUBROUTINE 'GETS' THE SPECIFIED PERIPHERAL.			540
		5438 *				550
		5439 *				560
	003232	5440 GET	BSS	U	IGNORE	570
003232	003231 7100 00	5441	TRA	COMDX	EXPECT NO REPLY--IGNORE	580

C

COMMUNICATIONS -- COMMAND KILL

		003233	5443	USE	CODE		600
			5444	HEAD	C		610
			5445 *				620
			5446 *				630
			5447 *		COMMAND KILL		640
			5448 *				650
			5449 *		THIS ROUTINE TELLS THE OPERATOR THAT A PARTICULAR DEVICE		660
			5450 *		HAS BEEN STOPPED AND RELEASED -- KILLED.		670
			5451 *				680
			5452 *	ENTER WITH			690
			5453 *		C(XT) = NCB-ADDRESS		700
			5454 *				710
		003233	5455 KILL	BSS	0		720
003233	004753	2350 00	5456	LDA	KLMS	GET MESSAGE TO SEND	730
003234	000027	7550 11	5457	STA	T\$TEMP1,T	SAVE FOR LOGGING	740
		004753	5458	USE	CONST		750
004753	004754	0024 40	5459 KLMS	TALLYB	*+1,19+1,0		760
004754	120105122111		5460	UASCI	5,PERIPHERAL KILLED:		770
END OF BINARY CARD IOS00103							
		003235	5461	USE	PREVIOUS		780
003235	003243	7100 00	5462	TRA	RDY1	HANDLE LIKE READY	790

C			COMMUNICATIONS -- COMMAND RELEASE			
		003236	5464	USE	CODE	810
			5465	HEAD	C	820
			5466 *			830
			5467 *			840
			5468 *		COMMAND RELEASE	850
			5469 *			860
			5470 *		THIS ROUTINE RELEASES A PERIPHERAL AS SOON AS IT IS FREE	870
			5471 *			880
			5472 *	ENTER WITH		890
			5473 *		C(XT) = NCB-ADDRESS	900
			5474 *			910
		003236	5475	REL	BSS 0	920
003236	004761	2350 00	5476	LDA	CLMS GET MESSAGE TO SEND	930
003237	000027	7550 11	5477	STA	T\$TEMP1,T SAVE FOR LOGGING	940
		004761	5478	USE	CONST	950
004761	004762	0024 40	5479	CLMS	TALLYH *+1,19+1,0	960
004762	120105122111		5480	UASCI	5,PERIPHERAL CLOSED:	970
		003240	5481	USE	PREVICUS	980
003240	003243	7100 00	5482	TRA	RDY1 HANDLE LIKE READY	990

C

COMMUNICATIONS -- COMMAND READY

			5484	HEAD	C		1010
			5485	*			1020
			5486	*			1030
			5487	*		COMMAND READY	1040
			5488	*			1050
			5489	*		THIS COMMAND TELLS THE OPERATOR TO PERFORM CERTAIN ACTIONS.	1060
			5490	*			1070
			5491				1080
		003241	5492	READY	BSS	C	1090
003241	004767	2350 00	5493	LDA	RDYMS	GET MESSAGE TO SEND	1100
003242	000027	7550 11	5494	STA	T\$TEMP1,T	SAVE FOR LOGGING	1110
		004767	5495	USE	CONST		1120
004767	004770	0011 40	5496	RDYMS	TALLYB	*+1,8+1,0	1130
004770	12210510	1104	5497	UASCI	2,READY:		1140
		003243	5498	USE	PREVIOUS		1150
		003243	5499	RDY1	BSS	0	1160
END OF BINARY CARD	I0S00104					PRINT NAME	
003243	003262	7070 00	5500	TSX	L,PERI	GET PERIPHERAL INFORMATION	1170
003244	000026	7420 11	5501	STX	X,T\$TEMP2,T	SAVE DEVICE HEADER PTR	1180
003245	000026	4440 11	5502	SXL	Z,T\$TEMP2,T	SAVE UNIT PTR	1190
003246	003250	7100 00	5503	TRA	LOG	SEND BUILT UP MESSAGES	1200

C

COMMUNICATIONS -- COMMAND RESTART

003247	5505	USE	CODE	1220
	5506	HEAD	C	1230
	5507 *			1240
	5508 *			1250
	5509 *		COMMAND RESTART	1260
	5510 *			1270
	5511 *	THIS COMMAND RESTARTS THE JOB ON THE SPECIFIED PER-		1280
	5512 *	IPHERAL TO THE SPECIFIED ELEMENT NUMBER.		1290
	5513 *			1300
003247	5514 RSTRT	BSS	0	1310
003247 003231 7100 00	5515	TRA	COMDx	1320
			EXPECT NO REPLY--IGNORE	

C			COMMUNICATIONS -- LOG MESSAGE TO OP				
		003250	5517	USE	CODE		1340
			5518	HEAD	C		1350
			5519 *				1360
			5520 *				1370
			5521 *		LOG MESSAGE TO OP		1380
			5522 *				1390
		003250	5523	LOG	BSS	0	1400
		003250	5524	LOGS		LOG A MESSAGE TO THE OP SAYING	1410
003250	001517	7070 00		TSX	L,0\$LOGS		
		003251	5525	LOGC	(T\$TEMP1,T)	THE CANNED MESSAGE	1420
003251	000027	2350 11		LDA	T\$TEMP1,T		
003252	001543	7070 00		TSX	L,0\$LOGC		
003253	000026	7240 11	5526	LXL	Z,T\$TEMP2,T	FROM -- GET PTR TO UNIT	1430
003254	000000	6350 14	5527	EAA	R\$ABBR,Z	POINT TO ABBREVIATION	1440
003255	000540	2750 07	5528	ORA	4*\$TAL+\$TAL+\$TALYB,DL	ABBREVIATIONS ARE 4 CHARACTERS	1450
		003256	5529	LOGC	A	PERIPHERAL NAME	1460
003256	001543	7070 00		TSX	L,0\$LOGC		
		003257	5530	LCRLF		NEATNESS	1470
003257	001600	7070 00		TSX	L,0\$LCRLF		
		003260	5531	LOGX		FINISH UP	1480
003260	001560	7070 00		TSX	L,0\$LOGX		
003261	003231	7100 00	5532	TRA	COMDX	RE-ISSUE THE NOTIFY	1490

C

COMMUNICATIONS -- PERIPHERAL INFORMATION (PERI)

			003262	5534	USE	CODE		1510	
				5535	HEAD	C		1520	
				5536	*			1530	
				5537	*			1540	
				5538	*			1550	
				5539	*			1560	
				5540	*			1570	
				5541	*			1580	
				5542	*			1590	
				5543	*			1600	
				5544	*			1610	
				5545	*			1620	
				5546	*			1630	
				5547	*			1640	
				5548	*			1650	
				5549	*			1660	
			003262	5550	PERI	BSS	0	1670	
	003262	000025	2350	11	5551	LDA	CSSTATE,T	1680	
	003263	000011	7710	00	5552	ARL	18-9	1690	
	003264	004743	2200	01	5553	LDX	0,RSTABLE,AU	1700	
	003265	000000	2240	10	5554	LDX	Z,RSPT,R,0	1710	
END OF BINARY CARD			IOS00105						
	003266	000001	2350	11	5555	LDA	TSSRW2,T	1720	
	003267	000022	7710	00	5556	ARL	36-18	1730	
	003270	000007	3750	07	5557	ANA	7,DL	1740	
	003271	000000	2750	14	5558	ORA	RSABRR,Z	1750	
	003272	000003	3360	10	5559	LCQ	RSMAX,0	1760	
	003273	000000	1150	14	5560	PERI1	CMPL	1770	
	003274	000000	6000	17	5561	TZE	0,L	1780	
	003275	000003	0240	03	5562	ADLX	Z,RSDEVLN,DU	1790	
	003276	000001	0760	07	5563	ADQ	1,DL	1800	
	003277	003273	6040	00	5564	TMI	PERI1	1810	
	003300	003231	7100	00	5565	TRA	COMDX	1820	
					5566	**	DISK	INIT	1830

C

JOB INITIALIZATION -- DESCRIPTION

00J301	5568	USE	CODE	110
	5569	HEAD		120
	5570	*		130
	5571	*		140
	5572	*	THE INITIALIZATION ROUTINE IS CHARGED WITH THE RESPONSIBILITY	150
	5573	*	OF COORDINATING THE ACTIVITIES OF THE EXTERNAL SYSTEMS' USERS	160
	5574	*	AND THE ENTIRE MONITOR STRUCTURE, FROM THE EXTERNAL SIDE,	170
	5575	*	AS USERS PLACE DESCRIPTOR ITEMS OF FILES TO BE PRINTED OR PUNCHED	180
	5576	*	IN THE PRINT AND PUNCH FILE QUEUES, RESPECTIVELY, AND CAUSE THE	190
	5577	*	CORRESPONDING EVENT TO INDICATE THAT ACTION, THE 'INIT' ROUTINE	200
	5578	*	IS NOTIFIED. IT THEN MUST READ IN THE DESCRIPTOR FROM THE EXTERNAL	210
	5579	*	FILES, CREATE A CORRESPONDING INTERNAL REPRESENTATION, AND QUEUE	220
	5580	*	IT UP TO BE RUN.	230
	5581	*		240
	5582	*	WITH REGARDS TO ACTUAL IMPLEMENTATION, THE FOLLOWING HAPPENS:	250
	5583	*	CHECK TO SEE IF WE HAVE ANY PERIPHERALS NEEDED FOR THIS	260
	5584	*	INPUT FILE QUEUE. IF NOT, THERE IS NO NEED TO READ THE DES-	270
	5585	*	CRIPTOR BECAUSE WE CAN'T RUN HIM (OWN NO PERIPHERALS).	280
	5586	*	OTHERWISE GET A TCB AND A JCB FOR THE NEW JOB, MOVE IN DATA FROM	290
	5587	*	THE NOTIFY CONTROL BLOCK (NCB) TO THE JOB CONTROL BLOCK (JCB).	300
	5588	*	LOCK THE INPUT FILE QUEUE	310
	5589	*	UPDATE THE INPUT FILE QUEUE	320
	5590	*	GET A READ BUFFER	330
	5591	*	READ DESCRIPTOR ITEM INTO CORE	340
	5592	*	OPEN NAMED FILE	350
	5593	*	RELEASE BUFFER	360
	5594	*	FINISH DESCRIBING INPUT IN JCB	370
	5595	*	PLACE THE NEW REQUEST ON A WAIT-WAITING-TO-RUN QUEUE	380
	5596	*	UNLOCK THE INPUT FILE QUEUE	390
	5597	*	WAKE UP THE SCHEDULER	400
	5598	*	LOOP TO HIT EOF OF INPUT FILE QUEUE	410
	5599	*	UPON HITTING THE END, RE-ISSUE THE NOTIFY	420

JOB -- SET UP

Job ID	Code	Address	Length	Op	Op2	Op3	Description	Line No.					
	003301	5601		USE		CODE		440					
		5602		HEAD				450					
		5603	*					460					
		5604	*					470					
		5605	*			SETUP		480					
		5606	*					490					
	003301	5607	INIT	BSS		0		500					
003301	000033	2340	31	5608	SZN	C\$RUN,T*	TEST TO SEE IF ANY PERIPHERAL OPENED	510					
003302	003171	6000	00	5609	TZE	C\$NSRVX	EXIT IF NONE OPENED	520					
		5610	*					530					
		5611	*	LOCK	THE	INPUT	FILE	QUEUE					
		5612	*					540					
	003303	5613	INIT0	BSS		0		550					
	003303	5614	LOCK	(C\$QFRN,T)			LOCK	THE	INPUT	QUEUE			
003303	000736	7000	00	TSX		0,\$LOCK							
003304	000030	0000	11	ARG		C\$QFRN,T							
		003305		5615	CHECK	INTO.0,B\$BZ,INIT0,B\$LOCK,INIT0		580					
003305	000000	7200	11	LXL		0,T\$SRW1,T							
003306	000077	3600	03	ANX		0,B\$STMK,DU							
003307	003315	6000	00	TZE		INTO.0							
003310	000003	1000	03	CMPX		0,B\$BZ,DU							
003311	003303	6000	00	TZE		INIT0							
003312	000013	1000	03	CMPX		0,B\$LOCK,DU							
END	OF	BINARY	CARD	10800106									
003313	003303	6000	00	TZE		INIT0							
003314	777777	7100	00	TRA		\$ERROR							
		003315		5616	INTO.0	BSS		0					
		003315		5617	GETT		GET	A	NEW	TASK	BLOCK		
003315	001467	7000	00	TSX		0,T\$GETT							
		003316		5618	GETJ		GET	A	JOB	CONTROL	BLOCK	(JCB)	
003316	001505	7000	00	TSX		0,J\$GETJ							
003317	000006	4460	11	5619	SXL	J,T\$JCB,T	SAVE	PTR	TO	JCB			
003320	000005	4410	16	5620	SXL	T,J\$TCB,J	SAVE	PTR	TO	TCB***DBG			
003321	000005	2220	11	5621	LDX	X,T\$LINK,T	GET	BACK	PTR	TO	NCB		
003322	000006	7420	11	5622	STX	X,T\$NCB,T	SAVE	PTR	TO	NCB			
003323	000005	7420	16	5623	STX	X,J\$NCB,J	SAVE	PTR	TO	NCB***DBG			
		5624	*						670				
		5625	*	MOVE	DATA	FROM	NCB	TO	NEW	TBLOCK			
		5626	*						680				
		5627	*						690				
003324	000030	2200	12	5627	LDX	0,C\$QFRN,X	GET	FRN	OF	FILE	CORRESPONDING	TO	
003325	000000	7400	16	5628	STX	0,J\$QFRN,J	EVENT	NOTIFIED					
003326	000031	7200	12	5629	LXL	0,C\$QFLOC,X	GET	FILE	SOURCE	(READ)	PTR		
003327	000001	7400	16	5630	STX	0,J\$QFLOC,J	AND	SAVE	IT				
003330	000017	2200	03	5631	LDX	0,A\$LP,DU	GET	ACCOUNTING	NUMBER	FOR	LINE	PRINTER	
003331	005720	1020	03	5632	CMPX	X,C\$LPE,DU	SEE	IF	IT	IS	THE	LINE	PRINTER
003332	003334	6000	00	5633	TZE	*+2	YES						
003333	000016	2200	03	5634	LDX	0,A\$CP,DU	NO,	IT	IS	THE	CARD	PUNCH	
003334	000004	4400	16	5635	SXL	0,J\$ACODE,J	SAVE	FOR	ACCOUNTING				
003335	000035	6230	12	5636	EAX	Y,C\$RES,X	PTR	TO	RESOURCE	NEEDS			
003336	000013	6240	16	5637	EAX	Z,J\$RES,J	PTR	TO	WHERE	THEY	GO	IN	JCB

JOB -- SET UP

	003337	000035	2350	12	5638	LDA	C\$RES,X	COMPUTE STATE	810
	003340	777777	3750	03	5639	ANA	-1,DU	FOR JOB INITIATION	820
END OF BINARY CARD	IOS00107								
	003341	000011	7350	00	5640	ALS	9	***WHEW!?!!	830
	003342	000006	7550	16	5641	STA	J\$STATI,J	SAVE FOR RUN	840
			003343		5642	INTO.1	BSS	0	850
	003343	000027	7440	11	5643	STX	Z,T\$TEMP1,T	SAVE PTR	860
	003344	000000	2350	13	5644	LDA	0,Y	GET NEXT NEED	870
	003345	000000	7550	14	5645	STA	0,Z	SAVE IN JCB	880
	003346	003353	6040	00	5646	TMI	INIT1	TEST FOR DONE	890
	003347	000000	6240	05	5647	EAX	Z,0,AL	GET NUMBER REQUESTED IN Z	900
	003350	000027	0240	11	5648	ADLX	Z,T\$TEMP1,T	PLUS OLD PTR	910
	003351	000001	0230	03	5649	ADLX	Y,1,DU	BUMP Y PTR TOO	920
	003352	003343	7100	00	5650	TRA	INTO.1	LOOP	930
					5651 *				940
					5652 *	UPDATE FILE			950
					5653 *				960
			003353		5654	INIT1	UPDATE (J\$QFRN,J)	UPDATE INPUT QUEUE	970
	003353	000726	7000	00		TSX	0,\$UPDATE		
	003354	000000	0000	16		ARG	J\$QFRN,J		
			003355		5655	CHECK	INIT2,B\$BZ,INIT1		980
	003355	000000	7200	11		LXL	0,T\$SRW1,T		
	003356	000077	3600	03		ANX	0,B\$STMK,DU		
	003357	003363	6000	00		TZE	INIT2		
	003360	000003	1000	03		CMPX	0,B\$BZ,DU		
	003361	003353	6000	00		TZE	INIT1		
	003362	777777	7100	00		TRA	\$ERROR		
					5656 *				990
					5657 *	GET A BUFFER TO READ IN NEXT JOB			1000
					5658 *				1010
			003363		5659	INIT2	BSS	0	1020
			003363		5660	GETC	(QBFSZ,DL)	GET A BUFFER	1030
	003363	000100	2350	07		LDA	QBFSZ,DL		
	003364	001152	7070	00		TSX	L,R\$GETC		
	003365	000011	7550	16	5661	STA	J\$BUF,J	SAVE BUFFER POINTER	1040
					5662 *				1050
					5663 *				1060
					5664 *	READ INFORMATION INTO CORE			1070
					5665 *				1080
			003366		5666	INIT3	BSS	0	1090
			003366		5667	READ	(J\$QFRN,J),(J\$BUF,J),(1,DU),(0,DU)		1100
END OF BINARY CARD	IOS00108								
	003366	000536	7000	00		TSX	0,\$READ		
	003367	000000	0000	16		ARG	J\$QFRN,J		
	003370	000011	0000	16		ARG	J\$BUF,J		
	003371	000001	0000	03		ARG	1,DU		
	003372	000000	0000	03		ARG	0,DU		
			003373		5668	CHECK	INIT4,B\$BZ,INIT3,B\$EOF,INITX		1110
	003373	000000	7200	11		LXL	0,T\$SRW1,T		
	003374	000077	3600	03		ANX	0,B\$STMK,DU		

JOB -- SET UP

003375	003403	6000	00	TZE	INIT4			
003376	000003	1000	03	CMPX	0,BBRZ,DU			
003377	003366	6000	00	TZE	INIT3			
003400	000016	1000	03	CMPX	0,B\$EOF,DU			
003401	003531	6000	00	TZE	INITX			
003402	777777	7100	00	TRA	\$ERROR			
				5669 *				1120
				5670 *	INFORMATION READ			1130
				5671 *				1140
	003403			5672	INIT4 BSS	0		1150
003403	000011	2220	16	5673	LDX	X,J\$RUF,J	GET BACK BUFFER POINTER	1160
003404	000001	2200	12	5674	LDX	0,TNGZ,X	GET TREE-SIZE	1170
003405	003413	6010	00	5675	TNZ	INT4,2	GOT A TREE-SIZE	1180
003406	000001	2220	03	5676	INT4.1 LDX	X,1,DU	BUMP READ PTR TO NEXT	1190
003407	000001	0420	16	5677	ASX	X,J\$OFLOC,J	IN JCB	1200
003410	000006	2220	11	5678	LDX	X,T\$NCB,T	AND IN THE NCB	1210
003411	000031	0540	12	5679	AOS	C\$OFLOC,X		1220
003412	003366	7100	00	5680	TRA	INITJ	AND READ AGAIN	1230
	003413			5681	INT4.2 BSS	0		1240
003413	000026	7400	11	5682	STX	0,T\$TEMP2,T	SAVE IN TASK BLOCK	1250
003414	000002	2240	12	5683	LDX	Z,TYPE,X	GET TYPE	1260
END	OF BINARY CARD	IOS00109						
003415	000001	3640	03	5684	ANX	Z,TYPMK,DU	MASK TO TYPE ONLY	1270
003416	000003	7440	16	5685	STX	Z,J\$TYPE,J	SAVE IN JBLOCK	1280
003417	000002	7240	12	5686	LXL	Z,DISP,X	GET DISPOSITION	1290
003420	000003	3640	03	5687	ANX	Z,DISMK,DU	MASK TO DISPOSITION	1300
003421	000003	4440	16	5688	SXL	Z,J\$DISP,J	SAVE IN JBLOCK, ALSO	1310
003422	000003	2240	12	5689	LDX	Z,ACODE,X	GET ACODE FOR ACCOUNTING	1320
003423	000004	7440	16	5690	STX	Z,J\$ACODE,J	SAVE IT	1330
003424	000004	6200	12	5691	EAX	0,TN,X	GET POINTER TO TREE-NAME	1340
003425	000027	7400	11	5692	STX	0,T\$TEMP1,T	SAVE ONLY FOR OPEN	1350
003426	000003	2200	16	5693	LDX	0,J\$TYPE,J	GET BACK TYPE	1360
003427	000001	3600	03	5694	ANX	0,TYPMK,DU	ISOLATE TYPE	1370
003430	004772	2200	10	5695	LDX	0,TABLE,0	GET CORRESPONDING ELEMENT SIZE	1380
003431	000025	7400	11	5696	STX	0,T\$TEMP3,T	SAVE ONLY FOR OPEN	1390
				5697 *				1400
				5698 *				1410
	004772			5699	USE	CONSI		1420
	004772			5700	TABLE BSS	0		1430
004772	044000	000000		5701	ZERO	512*36,	512 ELEMENT SIZE	1440
004773	026400	000000		5702	ZERO	320*36,	320 ELEMENT SIZE	1450
	003432			5703	USE	PREVIOUS		1460
				5704 *				1470
				5705 *				1480
				5706 *	OPEN FILE			1490
				5707 *				1500
	003432			5708	INIT5 BSS	0		1510
	003432			5709	OPEN	(T\$TEMP1,T),(T\$TEMP2,T),(1,DU),(T\$TEMP3,T),(0,DU)		1520
003432	000667	7000	00		TSX	0,\$OPEN		
003433	000027	0000	11		ARG	T\$TEMP1,T		

JOB -- SET UP

003434	000026	0000	11	ARG	T\$TEMP2,T		
003435	000001	0000	03	ARG	1,DU		
003436	000025	0000	11	ARG	T\$TEMP3,T		
END OF BINARY CARD	10S00110						
003437	000000	0000	03	ARG	0,DU		
	003440			CHECK	INIT6,B\$BZ,INIT5,B\$ITN,INT4.1		1530
003440	000000	7200	11	LXL	0,T\$SRW1,T		
003441	000077	3600	03	ANX	0,B\$STMK,DU		
003442	003450	6000	00	TZE	INIT6		
003443	000003	1000	03	CMPX	0,B\$RZ,DU		
003444	003432	6000	00	TZE	INIT5		
003445	000005	1000	03	CMPX	0,B\$ITN,DU		
003446	003406	6000	00	TZE	INT4.1		
003447	777777	7100	00	TRA	\$ERROR		
				5711 *			1540
				5712 *	FILE OPENED, FINISH UP		1550
				5713 *			1560
	003450			5714	INIT6 BSS 0		1570
003450	000000	2200	11	5715	LDX 0,T\$SRW1,T	GET FRN OF FILE	1580
003451	000002	7400	16	5716	STX 0,J\$FRN,J	SAVE IT	1590
003452	000001	2200	11	5717	LDX 0,T\$SRW2,T	GET NUMBER OF UNITS	1600
003453	000012	4500	16	5718	STZ J\$SIZE,J	CLEAR STORAGE AREA	1610
003454	000012	4400	16	5719	SXL 0,J\$SIZE,J	SAVE SIZE FOR LATER DECISIONS	1620
003455	000006	2200	11	5720	LDX 0,T\$NCB,T	GET BACK POINTER TO NCB	1630
003456	000031	0540	10	5721	AOS C\$QFLOC,0	BUMP R/W PTR TO NEXT	1640
003457	000032	0540	10	5722	AOS C\$BUSY,0	BUMP THE BUSY COUNT	1650
003460	000011	2220	16	5723	LDX X,J\$BUF,J	GET PTR TO INPUT BUFFER	1660
003461	000000	2350	07	5724	LDA 0,DL	BUILD MESSAGE WORD	1670
003462	000001	7200	12	5725	LXL 0,BANR,X	GET BANNER BITS	1680
003463	003465	6010	00	5726	TNZ *+2	NO BANNER	1690
003464	020000	2750	03	5727	ORA B\$HDMK,DU	OR IN BANNER BIT	1700
END OF BINARY CARD	10S00111						
003465	000002	2200	12	5728	LDX 0,TYPE,X	GET OUTPUT FORMAT	1710
003466	003470	6000	00	5729	TZE *+2	OK, 512 FORMAT	1720
003467	010000	2750	03	5730	ORA B\$OUTMK,DU	OR IN 320 FORMAT	1730
003470	000010	7550	16	5731	STA J\$MESS,J	MESSAGE BUILT EXCEPT FOR JOB NUMRER	1740
	003471			5732	RELC (J\$BUF,J)	RELEASE THE BUFFER	1750
003471	000011	2350	16		LDA J\$BUF,J		
003472	001252	7070	00		YSX L,R\$RELC		
				5733 *			1760
				5734 *	FAKE THE RESTART ADDRESS		1770
				5735 *			1780
003473	003577	6200	00	5736	EAX 0,RUN	RESTART ADDRESS	1790
003474	000004	7400	11	5737	STX 0,T\$TRA,T	PLACE IN TCB	1800
				5738 *			1810
				5739 *	PLACE ON PROPER WAIT QUEUE		1820
				5740 *			1830
003475	000004	6200	11	5741	EAX 0,Q\$OFFST,T	GET OFFSET POINTER	1840
003476	000006	2210	11	5742	LDX T,T\$NCB,T	GET BACK PTR TO NCB	1850
003477	000034	2250	11	5743	LDX Q,C\$QUEUE,T	GET PTR TO QUEUE LIST	1860

JOB -- SET UP

003500	005720	1010	03	5744	CMPX	T,C\$LP,DU	TEST FOR LINE PRINTER JOB	1870
003501	003517	6010	00	5745	TNZ	INIT9	NO, NOT A LP JOB	1880
003502	000002	2230	12	5746	LDX	Y,TYPE,X	GET ITS TYPE (512/320 FORMAT)	1890
003503	003507	6010	00	5747	TNZ	INIT7	SKIP IF 320	1900
003504	005670	2350	00	5748	LDA	R\$S,512		1910
003505	005672	2360	00	5749	LDQ	R\$M,512		1920
003506	003511	7100	00	5750	TRA	INIT8	DO COMPARE WITH THESE LIMITS	1930
003507	005671	2350	00	5751	INIT7	LDA	R\$S,320	1940
003510	005673	2360	00	5752	LDQ	R\$M,320		1950
003511	000012	1110	16	5753	INIT8	CWL	J\$SIZE,J	TEST WITH Q GETS HIM
003512	003517	6040	00	5754	TMI	INIT9	LARGE, ON QSINP1	1960
END OF BINARY CARD IOS00112								1970
003513	003516	6010	00	5755	TNZ	++3	SHORT?	1980
003514	005260	2250	03	5756	LDX	Q,Q\$INP3,DU	MEDIUM	1990
003515	003517	7100	00	5757	TRA	INIT9	PLACE ON Q	2000
003516	005240	2250	03	5758	LDX	Q,Q\$INP2,DU	SHORT	2010
			003517	5759	INIT9	BSS	0	2020
003517	000002	7170	15	5760	XED	Q\$XADD,Q	PLACE ON QUEUE LIST	2030
				5761	*			2040
				5762	*	UNLOCK	THE QUEUE FILE	2050
				5763	*			2060
		003520		5764	INT10	BSS	0	2070
		003520		5765	UNLCK	(C\$QFRN,T)	UNLOCK THE FILE QUEUE	2080
003520	000746	7000	00		TSX	Q,\$UNLCK		
003521	000030	0000	11		ARG	C\$QFRN,T		
			003522	5766	CHECK	INT11,\$BZ,INT10		2090
003522	000000	7200	11		LXL	Q,\$SRW1,T		
003523	000077	3600	03		ANX	Q,\$STMK,DU		
003524	003530	6000	00		TZE	INT11		
003525	000003	1000	03		CMPX	Q,\$RZ,DU		
003526	003520	6000	00		TZE	INT10		
003527	777777	7100	00		TRA	\$ERROR		
			003530	5767	INT11	BSS	0	2100
003530	003303	7100	00	5768	TRA	INIT0	SEE IF MORE	2110
				5769	*			2120
				5770	*			2130
				5771	*	EOF REACHED		2140
				5772	*			2150
		003531		5773	INITX	BSS	0	2160
		003531		5774	UNLCK	(J\$QFRN,J)	UNLOCK THE QUEUE FILE	2170
003531	000746	7000	00		TSX	Q,\$UNLCK		
003532	000000	0000	16		ARG	J\$QFRN,J		
			003533	5775	CHECK	INTX1,\$BZ,INITX		2180
003533	000000	7200	11		LXL	Q,\$SRW1,T		
003534	000077	3600	03		ANX	Q,\$STMK,DU		
003535	003541	6000	00		TZE	INTX1		
003536	000003	1000	03		CMPX	Q,\$RZ,DU		
003537	003531	6000	00		TZE	INITX		
003540	777777	7100	00		TRA	\$ERROR		
			003541	5776	INTX1	BSS	0	2190

JOB -- SET UP

003541	5777	RELC	(J\$BUF,J)	RELEASE THE BUFFFFER	2200
END OF BINARY CARD 10S00113		LDA	J\$BUF,J		
003541 000011 2350 16		TSX	L,R\$RELC		
003542 001252 7070 00					
	5778 *				2210
	5779 *	WAKE UP	THE SCHEDULER		2220
	5780 *				2230
	5781	BRANCH	NOPASS,R\$SKED	TRY TO START THE DUDES	2240
003543		TSX	0,T\$GETT		
003544 000000 6220 11		EAX	X,0,T		
003545 000005 2210 12		LDX	T,T\$LINK,X		
003546 000000 6210 12		EAX	T,0,X		
003547 002565 6200 00		EAX	0,R\$SKED		
003550 000004 7400 11		STX	0,T\$TRA,T		
003551 000004 6200 11		EAX	0,Q\$OFFST,T		
003552 005162 7170 00		XED	Q\$XADD+Q\$TASK		
003553 000005 2210 12		LDX	T,T\$LINK,X		
	003554	BUGXR	(0,X)		
	525261	BUGRUG	SET	BUGRUG+1	
003554 525261 2200 03		LDX	0,BUGRUG,DU		
003555 525261 2220 03		LDX	X,BUGRUG,DU		
	003556	5782	RELJ	RELEASE JCH	2250
003556 001511 7000 00		TSX	0,J\$RELJ		
003557 000000 6230 11	5783	EAX	Y,0,T	SAVE PTR TO CURRENT TCR	2260
003560 000006 2210 11	5784	LDX	T,T\$NCB,T	GET BACK NCB	2270
	5785 *				2280
	5786 *	RE-ISSUE	NOTIFY		2290
	5787 *				2300
	5788	BRANCH	PASS,C\$NSRVX	SETUP TASK TO RE-ISSUE NOTIFY	2310
003561 001467 7000 00		TSX	0,T\$GETT		
003562 000000 6220 11		EAX	X,0,T		
003563 000005 2210 12		LDX	T,T\$LINK,X		
003564 003171 6200 00		EAX	0,C\$NSRVX		
003565 000004 7400 11		STX	0,T\$TRA,T		
003566 000004 6200 11		EAX	0,Q\$OFFST,T		
END OF BINARY CARD 10S00114		XED	Q\$XADD+Q\$TASK		
003567 005162 7170 00		EAX	T,0,X		
003570 000000 6210 12		BUGXR	(0,X)		
	003571	BUGRUG	SET	BUGRUG+1	
003571 525262 2200 03		LDX	0,BUGRUG,DU		
003572 525262 2220 03		LDX	X,BUGRUG,DU		
	5789 *				2320
	5790 *	RELEASE	REMAINING RESOURCE		2330
	5791 *				2340
	5792	RELT		RELEASE NEW TCR	2350
003573 001477 7000 00		TSX	0,T\$RELT		
003574 000000 6210 13	5793	EAX	T,0,Y	RESTORE TO OLD TCR	2360
	003575	5794	RELT	RELEASE THE TRAP BLOC	2370
003575 001477 7000 00		TSX	0,T\$RELT		

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INPUT/OUTPUT SCHEDULER -- IOS MONITOR

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JOB -- SET UP

003576
003576 003074 7100 00

5795

EXIT

EVAPORATE

2380

TRA

SEXIT

5796 *\$*

DISK

RUN

2390

JOB -- RUN

Job ID	Resource	Usage	Code	Description	Address	
003577	5798	5799	USE HEAD		110	
	5800 *				120	
	5801 *			RUN	130	
	5802 *				140	
	5803 *			THIS ROUTINE RUNS THE NEXT JOB	150	
	5804 *			IT IS ENTERED ONLY AFTER A JOB HAS HAD ALL ITS RESOURCE	160	
	5805 *			REQUESTS SATISFIED (I.E. ALL RESOURCES ALLOCATED TO IT).	170	
	5806 *			ALL THAT NEED BE DONE IS PASS THE INFORMATION TO THE SUBMODULE.	180	
	5807 *			THAT MEANS ISSUING A CAUSE PASSING THE FRN OF THE INPUT FILE	190	
	5808 *			AND A MESSAGE WORD DESCRIBING WHAT TO DO WITH IT. THE	200	
	5809 *			STATE TELLS WHAT PERIPHERAL TYPE IS TO BE THE OUTPUT DEVICE TYPE.	210	
	5810 *			AFTER A SUCCESSFUL CAUSE, WE PUT OUT A NOTIFY ON THE JOB.	220	
	5811 *			STATE CORRESPONDS TO THE JOB NUMBER WHICH ARE UNIQUE. WHEN	230	
	5812 *			CAUSED, WE GO TO JOB TERMINATION.	240	
	5813 *				250	
	5814 *			CALL WITH	260	
	5815 *			C(XT) = JOB-CONTROL-BLOCK ADDRESS	270	
	5816 *			ALL RESOURCES ALLOCATED	280	
	5817 *				290	
003577	5818	RUN	BSS	0	300	
	5819 *				310	
	5820 *			ISSUE CAUSE TO START JOB	320	
	5821 *				330	
003577	5822		CAUSE	C\$FRN2,(1,DU),(J\$STATI,J),(J\$MESS,J)	340	
003577	5823		ETC	(J\$FRN,J),(B\$RD+B\$LK,DU)	350	
003577	000767	7000	00	TSX	0,\$CAUSE	
003600	006221	0000	00	ARG	C\$FRN2	
003601	000001	0000	03	ARG	1,DU	
003602	000006	0000	16	ARG	J\$STATI,J	
003603	000010	0000	16	ARG	J\$MESS,J	
003604	000002	0000	16	ARG	J\$FRN,J	
003605	000021	0000	03	ARG	B\$RD+B\$LK,DU	
	003606			5824	CHECK RUN1,B\$BZ,RUN	370
003606	000000	7200	11	LXL	0,T\$SRW1,T	
003607	000077	3600	03	ANX	0,B\$STMK,DU	
003610	003614	6000	00	TZE	RUN1	
003611	000003	1000	03	CMPX	0,B\$BZ,DU	
003612	003577	6000	00	TZE	RUN	
003613	777777	7100	00	TRA	\$ERROR	
	003614			5825	RUN1 BSS 0	380
END OF BINARY CARD	I0S00115					
003614	000000	2220	11	LDX	X,T\$SRW1,T GET NUMBER OF PEOPLE CAUSED	390
003615	003577	6000	00	TZE	RUN NO ONE, TRY AGAIN	400
				5828 *		410
				5829 *	PUT OUT A NOTIFY ON THIS JOB	420
				5830 *		430
003616	003640	6200	00	EAX	0,TERM GET RESTART ADDRESS	440
003617	000005	4400	11	SXL	0,C\$RLINK,T SAVE IN TCB	450
003620	000007	2360	16	LDQ	J\$STATI,J GET THE TERMINATE STATE	460

JOB -- RUN

003621	000025	7560	11	5834	STQ	T\$TEMP3,T	SAVE FOR COMMUNICATIONS	470
003622	006220	2360	00	5835	LDQ	C\$FRN1	GET ERN FOR NOTIFY	480
003623	000024	7560	11	5836	STQ	T\$TEMP4,T	SAVE FOR COMMUNICATIONS	490
		003624		5837	BRANCH	PASS,C\$NSRVX	PUT OUT A NOTIFY	500
003624	001467	7000	00		TSX	0,T\$GETT		
003625	000000	6220	11		EAX	X,0,T		
003626	000005	2210	12		LDX	T,T\$LINK,X		
003627	003171	6200	00		EAX	0,C\$NSRVX		
003630	000004	7400	11		STX	0,T\$TRA,T		
003631	000004	6200	11		EAX	0,Q\$OFFST,T		
003632	005162	7170	00		XED	Q\$XADD+Q\$TASK		
003633	000000	6210	12		EAX	T,0,X		
		003634			BUGXR	(0,X)		
		525263		BUGBUG	SET	BUGBUG+1		
003634	525263	2200	03		LDX	0,BUGBUG,DU		
003635	525263	2220	03		LDX	X,BUGBUG,DU		
				5838 *				510
				5839 *	FINISH UP			520
				5840 *				530
		003636		5841	RELT		RELEASE TRAP BLOCK	540
003636	001477	7000	00		TSX	0,T\$RELT		
		003637		5842	EXIT			550
003637	003074	7100	00		TRA	\$EXIT		
				5843 *\$*	DISK	TERM1		560

JOB -- TERMINATION

		003640	5845	USE	CODE		110
			5846	HEAD			120
			5847	*			130
			5848	*			140
			5849	*	TERMINATION		150
			5850	*			160
			5851	*	JOB TERMINATION DOES THE FOLLOWING:		170
			5852	*	DOES ACCOUNTING REPORT		180
			5853	*	ALLOCATES A WORKING BUFFER		190
			5854	*	ACTS ON DISPOSITION CODE		200
			5855	*	MARKS INPUT FILE DESCRIPTOR AS DONE		210
			5856	*	TRIES TO SCRATCH THE INPUT FILE QUEUE		220
			5857	*	RELEASES JOBS RESOURCES		230
			5858	*	AWAKENS SCHEDULER		240
			5859	*			250
			5860	*			260
		003640	5861	TERM	BSS	0	270
			5862	LDG	TSSRW2,T	GET MESSAGE FROM SUBMODULE	280
003640	000001	2360	11				
END OF BINARY CARD	IOS00116						
003641	000012	7720	00	5863	GRL	18-4-4	290
003642	000017	3760	03	5864	ANQ	=017,DU	300
003643	000010	1160	03	5865	CMPQ	CMAX,DU	310
003644	777777	6030	00	5866	TRC	\$ERROR	320
003645	003646	7100	22	5867	TRA	*->1,QU*	330
003646	777777	0000	00	5868	CMDTB	ARG \$ERROR	0 = ILLEGAL
003647	777777	0000	00	5869	ARG	\$ERROR	1 = GET (ILLEGAL)
003650	003670	0000	00	5870	ARG	TERM1	2 = KILL
003651	777777	0000	00	5871	ARG	\$ERROR	3 = RELEASE (ILLEGAL)
003652	777777	0000	00	5872	ARG	\$ERROR	4 = XXXX (ILLEGAL)
003653	777777	0000	00	5873	ARG	\$ERROR	5 = RESTART (ILLEGAL)
003654	003670	0000	00	5874	ARG	TERM1	6 = DONE
003655	777777	0000	00	5875	ARG	\$ERROR	7 = READY (ILLEGAL)
		000010		5876	CMAX	EQU *-CMDTB	NUMBER OF COMMANDS
				5877	*		420
				5878	*	ACCOUNTING REPORT	430
				5879	*		440
		003656		5880	TERMO	BSS	0
		003656		5881	ACCT	(J\$ACCODE,J),(J\$SIZE,J),(1,DU)	WRITE BILLING INFO
003656	001017	7000	00		TSX	0,\$ACCT	460
003657	000004	0000	16		ARG	J\$ACCODE,J	470
003660	000012	0000	16		ARG	J\$SIZE,J	
003661	000001	0000	03		ARG	1,DU	
		003662		5882	CHECK	TERM1,\$B\$Z,TERMO	480
003662	000000	7200	11		LXL	0,T\$SRW1,T	
003663	000077	3600	03		ANX	0,\$B\$TMK,DU	
003664	003670	6000	00		TZE	TERM1	
003665	000003	1000	03		CMPX	0,\$B\$Z,DU	
003666	003656	6000	00		TZE	TERMO	
END OF BINARY CARD	IOS00117						
003667	777777	7100	00		TRA	\$ERROR	

JOB -- TERMINATION

		003670	5883	TERM1	BSS	0		490	
			5884	*				500	
			5885	*	ALLOCATE A WORKING BUFFER			510	
			5886	*				520	
		003670	5887	TRM1	BSS	0		530	
		003670	5888		GETC	(QBFSZ,DL)	GET A WORKING BUFFER	540	
003670	000100	2350			LDA	QBFSZ,DL			
003671	001152	7070			TSX	L,R\$GETC			
003672	000011	7550	5889		STA	J\$BUF,J	SAVE BUFFER POINTER	550	
003673	000003	7200	5890		LXL	0,J\$DISP,J	GET DISPOSITION RULE	560	
003674	000003	3600	5891		ANX	0,DISMK,T	ISOLATE DISPOSITION RULE	570	
003675	003676	7100	5892		TRA	**1,0*	FOLLOW THE RULES	580	
003676	003701	0000	5893		ARG	TRMD	DESTROY	590	
003677	003733	0000	5894		ARG	TRMS	SCRATCH	600	
003700	003744	0000	5895		ARG	TRMC	CLOSE	610	
			5896	*				620	
			5897	*	DESTROY SOURCE FILE			630	
			5898	*				640	
		003701	5899	TRMD	BSS	0		650	
		003701	5900		RRF	(J\$QFRN,J),(J\$QFLOC,J),(J\$BUF,J),(1,DU)		660	
003701	000564	7000			TSX	0,\$RRF			
003702	000000	0000			ARG	J\$QFRN,J			
003703	000001	0000			ARG	J\$QFLOC,J			
003704	000011	0000			ARG	J\$BUF,J			
003705	000001	0000			ARG	1,DU			
		003706	5901		CHECK	TRMD1,B\$BZ,TRMD		670	
003706	000000	7200			LXL	0,T\$SRW1,T			
003707	000077	3600			ANX	0,B\$STMK,DU			
003710	003714	6000			TZE	TRMD1			
003711	000003	1000			CMPX	0,B\$BZ,DU			
003712	003701	6000			TZE	TRMD			
003713	777777	7100			TRA	\$ERROR			
003714	000011	2220	5902	TRMD1	LDX	2,J\$BUF,J	GET POINTER TO BUFFER AREA	680	
END OF BINARY CARD IOS00118									
003715	000004	6350	5903		EAA	TN,2	GET PTR TO TREE NAME	690	
003716	000027	7550	5904		SYA	T\$TEMP1,T	SAVE FOR DESTROY	700	
		003717	5905		DESTRO	(T\$TEMP1,T),(TNSZ,2),(1,DU)		710	
003717	000714	7000			TSX	0,\$DESTRO			
003720	000027	0000			ARG	T\$TEMP1,T			
003721	000001	0000			ARG	TNSZ,2			
003722	000001	0000			ARG	1,DU			
		003723	5906		CHECK	TRMS,B\$BZ,TRMD1,B\$ITN,TRMC		720	
003723	000000	7200			LXL	0,T\$SRW1,T			
003724	000077	3600			ANX	0,B\$STMK,DU			
003725	003733	6000			TZE	TRMS			
003726	000003	1000			CMPX	0,B\$BZ,DU			
003727	003714	6000			TZE	TRMD1			
003730	000005	1000			CMPX	0,B\$ITN,DU			
003731	003744	6000			TZE	TRMC			
003732	777777	7100			TRA	\$ERROR			

JOB -- TERMINATION

			5907 *			730
			5908 *	SCRATCH THE SOURCE FILE		740
			5909 *			750
		003733	5910 TRMS	BSS 0		760
		003733	5911	SCR (J\$FRN,J),(0,DU) SCRATCH THE FILE		770
003733	000612	7000 00		TSX 0,\$SCR		
003734	000002	0000 16		ARG J\$FRN,J		
003735	000000	0000 03		ARG 0,DU		
		003736	5912	CHECK TRM0,\$B\$Z,TRMS		780
003736	000000	7200 11		LXL 0,\$SRW1,T		
003737	000077	3600 03		ANX 0,\$STMK,DU		
003740	003744	6000 00		TZE TRMC		
003741	000003	1000 03		CMFX 0,\$B\$Z,DU		
003742	003733	6000 00		TZE TRMS		
END OF BINARY CARD	IOS00119			TRA \$ERROR		
003743	777777	7100 00				
			5913 *			790
			5914 *	CLOSE SOURCE FILE		800
			5915 *			810
		003744	5916 TRMC	BSS 0		820
		003744	5917	CLOSE (J\$FRN,J)		830
003744	000704	7000 00		YSX 0,\$CLOSE		
003745	000002	0000 16		ARG J\$FRN,J		
		003746	5918	CHECK TRM2,\$B\$Z,TRMC		840
003746	000000	7200 11		LXL 0,\$SRW1,T		
003747	000077	3600 03		ANX 0,\$STMK,DU		
003750	003754	6000 00		TZE TRM2		
003751	000003	1000 03		CMFX 0,\$B\$Z,DU		
003752	003744	6000 00		TZE TRMC		
003753	777777	7100 00		TRA \$ERROR		
			5919 *			850
			5920 *	MARK SOURCE DONE		860
			5921 *			870
		003754	5922 TRM2	BSS 0		880
003754	000011	2220 16	5923	LDX X,J\$BUF,J POINT TO BUFFER		890
003755	000001	0220 03	5924	ADLX X,1,DU BUMP ONE		900
003756	000001	3360 07	5925	LCQ 1,DL MARK BUFFER DONE		910
003757	777777	7560 12	5926	STQ -1,X WITH CHECKSUM = -1		920
003760	176300	5202 01	5927	RPT QBFSZ-1,1,TZE AND WITH THE REST		930
003761	000000	4500 12	5928	STZ 0,X OF THE BLOCK ZERO		940
		003762	5929 TRM3	BSS 0		950
		003762	5930	WRF (J\$QFRN,J),(J\$QFLOC,J),(J\$BUF,J),(1,DU)		960
003762	000577	7000 00		TSX 0,\$WRF		
003763	000000	0000 16		ARG J\$QFRN,J		
003764	000001	0000 16		ARG J\$QFLOC,J		
003765	000011	0000 16		ARG J\$BUF,J		
003766	000001	0000 03		ARG 1,DU		
		003767	5931	CHECK TRM4,\$B\$Z,TRM3		970
003767	000000	7200 11		LXL 0,\$SRW1,T		
003770	000077	3600 03		ANX 0,\$STMK,DU		

JOB -- TERMINATION

END OF BINARY CARD IOS00120

003771	003775	6000	00	TZE	TRM4				
003772	000003	1000	03	CMPX	0,B\$BZ,DU				
003773	003762	6000	00	TZE	TRM3				
003774	777777	7100	00	TRA	\$ERROR				
	003775			5932	TRM4	BSS	0		980
				5933	*				990
				5934	*	CLEAN UP			1000
				5935	*				1010
003775	000005	2220	16	5936	LDX	X,J\$NCB,J	GET PTR TO NCB		1020
	003776			5937	DECRM	(C\$BUSY,X)	DECREMENT THE BUSY COUNT		1030
003776	000001	3360	07		LCQ	1,DL			
003777	000032	0560	12		ASQ	C\$BUSY,X			
004000	777777	6040	00	5938	TMI	\$ERROR	***PROBLEM		1040
004001	004024	6010	00	5939	TNZ	TRM5	SKIP IN NON-ZERO		1050
				5940	*				1060
				5941	*	SETUP TASK TO SCRATCH INPUT Q FILE			1070
				5942	*				1080
004002	000030	2200	12	5943	LDX	0,C\$QFRN,X	GET INPUT QUEUE FRN		1090
004003	000027	7400	11	5944	STX	0,T\$TEMP1,T	SAVE FOR PASS		1100
004004	000026	7420	11	5945	STX	X,T\$TEMP2,T	PASS PTR TO NCB		1110
	004005			5946	BRANCH	NOPASS,R\$SCR,(T\$TEMP1,T),(T\$TEMP2,T)	SCRATCH INPUT QUEUE FILE		
004005	001467	7000	00		TSX	0,T\$GETT			
004006	000000	6220	11		EAX	X,0,T			
004007	000005	2210	12		LDX	T,T\$LINK,X			
004010	000027	2360	11		LDQ	T\$TEMP1,T			
004011	000027	7560	12		STQ	T\$TEMP1,X			
004012	000026	2360	11		LDQ	T\$TEMP2,T			
004013	000026	7560	12		STQ	T\$TEMP2,X			
004014	000000	6210	12		EAX	T,0,X			
004015	002765	6200	00		EAX	0,R\$SCR			
004016	000004	7400	11		STX	0,T\$TRA,T			
END OF BINARY CARD IOS00121									
004017	000004	6200	11		EAX	0,Q\$OFFST,T			
004020	005162	7170	00		XED	Q\$XADD+Q\$TASK			
004021	000005	2210	12		LDX	T,T\$LINK,X			
	004022				BUGXR	(0,X)			
	525264				BUGBUG	SET	BUGBUG+1		
004022	525264	2200	03		LDX	0,BUGBUG,DU			
004023	525264	2220	03		LDX	X,BUGBUG,DU			
				5947	*				1130
				5948	*	RELEASE RESOURCES			1140
				5949	*				1150
	004024			5950	TRM5	BSS	0		1160
004024	000013	6200	16	5951	EAX	0,J\$RES,J	GET PTR TO RESOURCES		1170
004025	000026	7400	11	5952	TRM6	STX	0,T\$TEMP2,T	SAVE PTR	1180
004026	000000	2350	10	5953	LDA	0,0	GET NEXT RESOURCE TO RELEASE		1190
004027	004050	6040	00	5954	TMI	TRM8	TEST FOR DONE		1200
004030	777777	3750	03	5955	ANA	-1,DU	MASK TO TYPE		1210
004031	005570	1150	03	5956	CMPA	R\$LPTAB,DU	TEST FOR A LINE PRINTER		1220

JOB -- TERMINATION

	004032	004043	6010	00	5957	TNZ	TRM7	IF NOT, SKIP	1230	
	004033	000012	2360	16	5958	LDQ	J\$SIZE,J	GET SIZE	1240	
	004034	000003	2220	16	5959	LDX	X,J\$TYPE,J	AND TYPE	1250	
	004035	004774	7160	12	5960	XEC	LPTB1,X	TEST FOR LONG JOB	1260	
				004774	5961	USE	CONST		1270	
	004774	005672	1160	00	5962	LPTB1	CMPO	R\$M,512	LONG 512?	1280
	004775	005673	1160	00	5963	CMPO	R\$M,320	LONG 320?	1290	
				004036	5964	USE	PREVIOUS		1300	
	004036	004043	6000	00	5965	TZE	TRM7	IF NOT, SKIP	1310	
	004037	004043	6020	00	5966	TNC	TRM7	IF NOT, SKIP	1320	
				004040	5967	DECRM	R\$LJOBS	YES, SO DECREMENT COUNTER	1330	
	004040	000001	3360	07		LCQ	1,DL			
END	OF BINARY CARD	IOS00122								
	004041	005674	0560	00		ASQ	R\$LJOBS			
	004042	777777	6040	00	5968	TMI	\$ERROR	***PROBLEM	1340	
				004043	5969	TRM7	RELP	(0,0)	RELEASE THE PERIPHERAL	1350
	004043	000000	2350	10		LDA	0,0			
	004044	002447	7070	00		TSX	L,R\$RELP			
	004045	000001	2200	03	5970	LDX	0,1,DU	ADD ONE	1360	
	004046	000026	0200	11	5971	ADLX	0,T\$TEMP2,T	TO BUMP TO NEXT RESOURCE	1370	
	004047	004025	7100	00	5972	TRA	TRM6	LOOP	1380	
				004050	5973	TRM6	BSS	0	1390	
				004050	5974	RELC	(J\$BUF,J)	RELEASE BUFFER	1400	
	004050	000011	2350	16		LDA	J\$BUF,J			
	004051	001252	7070	00		TSX	L,R\$RELC			
					5975	*			1410	
					5976	*	AWAKEN THE SCHEDULER		1420	
					5977	*			1430	
				004052	5978	BRANCH	NOPASS,R\$SKED	AWAKEN SCHEDULER	1440	
	004052	001467	7000	00		TSX	0,T\$GETT			
	004053	000000	6220	11		EAX	X,0,T			
	004054	000005	2210	12		LDX	T,T\$LINK,X			
	004055	000000	6210	12		EAX	T,0,X			
	004056	002565	6200	00		EAX	0,R\$SKED			
	004057	000004	7400	11		STX	0,T\$TRA,T			
	004060	000004	6200	11		EAX	0,Q\$OFFST,T			
	004061	005162	7170	00		XED	Q\$XADD+Q\$TASK			
	004062	000005	2210	12		LDX	T,T\$LINK,X			
				004063		BUGXR	(0,X)			
				525265		BUGBUG	SET	BUGBUG+1		
	004063	525265	2200	03		LDX	0,BUGBUG,DU			
	004064	525265	2220	03		LDX	X,BUGBUG,DU			
	004065	000007	7220	16	5979	LXL	X,J\$JOB,J	RELEASE JOB NUMBER	1450	
	004066	005700	2340	12	5980	SZN	J\$JTAB,X	CHECK FOR GOOD BOOKKEEPING	1460	
END	OF BINARY CARD	IOS00123								
	004067	777777	6000	00	5981	TZE	\$ERROR	***PROBLEM	1470	
	004070	005700	4500	12	5982	STZ	J\$JTAB,X	DEALLOCATE NUMBER	1480	
				004071	5983	RELJ		RELEASE JCB	1490	
	004071	001511	7000	00		TSX	0,J\$RELJ			
				004072	5984	RELT		RELEASE TRAP BLOCK	1500	

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INPUT/OUTPUT SCHEDULER -- IOS MONITOR

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JOB -- TERMINATION

004072 001477 7000 00
 004073
004073 003074 7100 00

5985 TSX D,T\$RELT
 EXIT
 TRA SEXIT
5986 ** DISK CRASH

POOF1

1510

1520

SUB-MODULE ABNORMAL TERMINATION -- CRASH

		004074	5988	USE	CODE		110
			5989	HEAD			120
			5990	*			130
			5991	*			140
			5992	*	CRASH		150
			5993	*			160
			5994	*	THIS ROUTINE IS ENTERED WHENEVER ANY SUB-MODULE TERMINATES.		170
			5995	*	THAT IS TO SAY, THE NOTIFY ON ITS PROCESSS EVENT IS CAUSED.		180
			5996	*	IT LOGS A MESSAGE TO THE OPERATOR POINTING THE FINGER AT		190
			5997	*	THE CULPRIT AND THEN DOES A SOFT CRASH. THE 'LISTENER' WILL		200
			5998	*	DO AN INSTANT RESTART AND THEN WE'RE BACK IN THE OLE BALL GAME.		210
			5999	*			220
			6000	*	ENTER WITH		230
			6001	*	C(XT) = NCB		240
			6002	*			250
		004074	6003	CRASH	BSS 0		260
			6004	*			270
			6005	*	TELL OPER THE NEWS		280
			6006	*			290
		004074	6007	LOGS		TELL THE OP THE BAD NEWS	300
004074	001517	7070 00		TSX	L,0\$LOGS		
			004075	6008	LOGC	CRMS	THAT A SUB-MODULE CRASHED
004075	004776	2350 00		LDA	CRMS		310
004076	001543	7070 00		TSX	L,0\$LOGC		
			004776	6009	USE	CONST	320
004776	004777	0031 40	6010	CRMS	TALLYB	**1,24+1,0	*PERIPHERAL CRASHED: *
004777	1771770	15012	6011	OCT	177177015012		330
005000	1201051	22111	6012	UASCI	5,PERIPHERAL CRASHED:		340
			004077	6013	USE	PREVIOUS	350
004077	000007	6350 11	6014	EAA	CSABBR,T	AND WHICH ONE IT WAS	360
004100	000540	2750 07	6015	ORA	4*\$TAL+\$TAL+\$TALYB,DL		370
			004101	6016	LOGC	A	380
004101	001543	7070 00		TSX	L,0\$LOGC		390
			004102	6017	LOGC	BYEMS	SAY SO LONG,FARE-THEE=WELL,AU REVOIR
004102	005005	2350 00		LDA	BYEMS		400
END	OF BINARY CARD	IOS00124		TSX	L,0\$LOGC		
004103	001543	7070 00		USE	CONST		410
			005005	6018	BYEMS	TALLYB	**1,19+1,0
005005	005006	0024 40	6019	OCT	007007015012	*IOS*OVER & OUT!	420
005006	0070070	15012	6020	UASCI	4,IOS*OVER & OUT!		430
005007	1111171	23052	6021	USE	PREVIOUS		440
			004104	6022	LOGX	SEND IT	450
004104	001560	7070 00	6023	TSX	L,0\$LOGX		460
			6024	*			470
			6025	*	DO A SOFT-CRASH		480
			6026	*			490
004105	000470	7100 00	6027	TRA	X\$TERM	BYE-BYE TOOTS	500
			6028	*\$*	DISK	ANIT	510

INITIALIZATION

		004106	6030	USE	CODE		110
			6031	HEAD			120
			6032	*			130
			6033	*			140
			6034	*		INITIALIZATION	150
			6035	*			160
			6036	*	THIS ROUTINE INITIALIZES THE PERIPHERAL SCHEDULER		170
			6037	*	MONITOR BY PERFORMING THE FOLLOWING FUNCTIONS.		180
			6038	*			190
			6039	*	FUNCTIONS		200
			6040	*	INITIALIZE REGISTERS AND LOCATION ZERO		210
			6041	*	SET FAULT VECTOR		220
			6042	*	OPEN VARIOUS SYSTEM FILES		230
			6043	*	OPEN PERIPHERALS		240
			6044	*	OPEN COM FILES		250
			6045	*	SPAWN SUBMODULES		260
			6046	*	SET UP NOTIFIES		270
			6047	*			280
		004106	6048	*	USE	STORE	PUT THIS CODE IN THE STORAGE AREA FUDGE
			6049	NIT	BSS	0	INITIALIZATION ENTRY
		004106	6050	UP	EQU	NIT	300
			6051	*			310
			6052	*	CATCH WILD TRANSFERS TO ZERO		320
			6053	*			330
004106	000000	4500 00	6054	STZ	0	BURN OUR BRIDGES BEHIND US	340
		004107	6055	CKPT		SAVE REGISTERS	350
004107	000474	7170 00		XED	X\$CKPT		360
004110	006350	2210 03	6056	LDX	T,SPTCB,DU	INITIALIZE T	370
004111	006350	7260 07	6057	LXL	J,SPTCB,DL	AND J TO THE SAME	380
004112	000006	4460 11	6058	SXL	J,T\$JCB,T	SAVE FOR \$EXIT	390
004113	004400	6340 07	6059	LDI	B\$OVM+B\$PAM,DL	MASK OFF OVERFLOWAND PARITY ERRORS	400
004114	000400	2350 03	6060	LDA	ZZ1,DU	INITIALIZE TO AVAILABLE MEMORY	410
004115	005300	7550 00	6061	STA	\$AVAIL		420
004116	005700	5540 00	6062	STC1	JSJTAB	MAKE JOB NUMBER 0 ILLEGAL	430
			6063	*			440
			6064	*	SET FAULT VECTOR		450
			6065	*			460
		004117	6066	NIT1	SETFV	(X\$FV,DU)	SET THE FAULT VECTOR
					TSX	0,\$SETFV	470
004117	000526	7000 00		ARG	X\$FV,DU		
END OF BINARY CARD		INS00125		CHECK	NIT2,B\$BZ,NIT1		480
004120	000000	0000 03	6067	LXL	0,T\$SRW1,T		
		004121		ANX	0,B\$STMK,DU		
004121	000000	7200 11		TZE	NIT2		
004122	000077	3600 03		CMPX	0,B\$BZ,DU		
004123	004127	6000 00		TZE	NIT1		
004124	000003	1000 03		TRA	\$ERROR		
004125	004117	6000 00		DISK	ANIT2		490
004126	777777	7100 00					
			6068	*\$*			

INITIALIZATION -- OPEN SYSTEM FILES

			6070 *			110
			6071 *			120
			6072 *		OPEN SYSTEM FILES	130
			6073 *			140
			6074 *			150
			6075 *	OPEN	'PRINT=FILE-QUEUE' FILE	160
			6076 *			170
			6077 NIT2	BSS	0	180
		004127	6078	OPEN	(LPQ,DU),(LPQTS,DU),(1,DU),(LPQES,DU),(0,DU)	190
004127	000667	7000 00		TSX	0,\$OPEN	
004130	005013	0000 03		ARG	LPQ,DU	
004131	000014	0000 03		ARG	LPQTS,DU	
004132	000001	0000 03		ARG	1,DU	
004133	004400	0000 03		ARG	LPQES,DU	
004134	000000	0000 03		ARG	0,DU	
		004135	6079	CHECK	NIT3,B\$BZ,NIT2	200
004135	000000	7200 11		LXL	0,T\$SRW1,T	
004136	000077	3600 03		ANX	0,B\$STMK,DU	
004137	004143	6000 00		TZE	NIT3	
004140	000003	1000 03		CMPX	0,B\$BZ,DU	
004141	004127	6000 00		TZE	NIT2	
004142	777777	7100 00		TRA	\$ERROR	
		004143	6080 NIT3	BSS	0	210
004143	000000	2200 11	6081	LDX	0,T\$SRW1,T GET FRN	220
004144	005750	7400 00	6082	STX	0,\$CFRLPG SAVE IT TABLE	230
			6083 *			240
			6084 *	OPEN	'PUNCH=FILE-QUEUE' FILE	250
			6085 *			260
		004145	6086 NIT4	BSS	0	270
		004145	6087	OPEN	(CPQ,DU),(CPQTS,DU),(1,DU),(CPQES,DU),(0,DU)	280
004145	000667	7000 00		TSX	0,\$OPEN	
END OF BINARY CARD	10S00126					
004146	005027	0000 03		ARG	CPQ,DU	
004147	000014	0000 03		ARG	CPQTS,DU	
004150	000001	0000 03		ARG	1,DU	
004151	004400	0000 03		ARG	CPQES,DU	
004152	000000	0000 03		ARG	0,DU	
		004153	6088	CHECK	NIT4.1,B\$BZ,NIT4	290
004153	000000	7200 11		LXL	0,T\$SRW1,T	
004154	000077	3600 03		ANX	0,B\$STMK,DU	
004155	004161	6000 00		TZE	NIT4.1	
004156	000003	1000 03		CMPX	0,B\$BZ,DU	
004157	004145	6000 00		TZE	NIT4	
004160	777777	7100 00		TRA	\$ERROR	
		004161	6089 NIT4.1	BSS	0	300
004161	000000	2200 11	6090	LDX	0,T\$SRW1,T GET FRN	310
004162	006010	7400 00	6091	STX	0,\$CFRCPQ SAVE IN TABLE	320

INITIALIZATION -- OPEN SYSTEM EVENTS

			6093 *			340
			6094 *			350
			6095 *	OPEN SYSTEM EVENTS		360
			6096 *			370
			6097 *			380
			6098 *	OPEN 'PRINT-FILE-QUEUE' EVENT		390
			6099 *			400
		004163	6100 NIT5	BSS	0	410
		004163	6101	OPEN	(LPE,DU),(LPETS,DU),(1,DU),(LPEES,DU),(0,DU)	420
004163	000667	7000 00		TSX	0,\$OPEN	
004164	005043	0000 03		ARG	LPE,DU	
004165	000014	0000 03		ARG	LPETS,DU	
004166	000001	0000 03		ARG	1,DU	
004167	002200	0000 03		ARG	LPEES,DU	
004170	000000	0000 03		ARG	0,DU	
		004171	6102	CHECK	NIT6,B\$BZ,NIT5	430
004171	000000	7200 11		LXL	0,T\$SRW1,T	
004172	000077	3600 03		ANX	0,B\$STMK,DU	
004173	004177	6000 00		TZE	NIT6	
END	OF BINARY CARD	IOS00127				
004174	000003	1000 03		CMPX	0,B\$BZ,DU	
004175	004163	6000 00		TZE	NIT5	
004176	777777	7100 00		TRA	\$ERROR	
		004177	6103 NIT6	BSS	0	440
004177	000000	2200 11		6104	LDX	0,T\$SRW1,T GET FRN
004200	005744	7400 00		6105	STX	0,C\$FRLPE SAVE IN TABLE
			6106 *			460
			6107 *	OPEN 'PUNCH-FILE-QUEUE' EVENT		470
			6108 *			480
		004201	6109 NIT7	BSS	0	490
		004201	6110	OPEN	(CPE,DU),(CPETS,DU),(1,DU),(CPEES,DU),(0,DU)	500
004201	000667	7000 00		TSX	0,\$OPEN	510
004202	005057	0000 03		ARG	CPE,DU	
004203	000014	0000 03		ARG	CPETS,DU	
004204	000001	0000 03		ARG	1,DU	
004205	002200	0000 03		ARG	CPEES,DU	
004206	000000	0000 03		ARG	0,DU	
		004207	6111	CHECK	NIT8,B\$BZ,NIT7	520
004207	000000	7200 11		LXL	0,T\$SRW1,T	
004210	000077	3600 03		ANX	0,B\$STMK,DU	
004211	004215	6000 00		TZE	NIT8	
004212	000003	1000 03		CMPX	0,B\$BZ,DU	
004213	004201	6000 00		TZE	NIT7	
004214	777777	7100 00		TRA	\$ERROR	
		004215	6112 NIT8	BSS	0	530
004215	000000	2200 11		6113	LDX	0,T\$SRW1,T GET FRN
004216	006004	7400 00		6114	STX	0,C\$FRCPE SAVE IN TABLE

INITIALIZATION -- OPEN SYSTEM EVENTS

			6116 *			570
			6117 *			580
			6118 *	OPEN 'JOB-STREAM-SCHEDULER' EVENT		590
			6119 *			600
		004217	6120 NIT9	BSS 0		610
		004217	6121	OPEN (JSS,DU),(JSSTS,DU),(1,DU),(JSSES,DU),(0,DU)		620
	004217	000667		TSX 0,\$OPEN		
	004220	005073		ARG JSS,DU		
	004221	000014		ARG JSSTS,DU		
END	OF BINARY CARD	10500128				
	004222	000001		ARG 1,DU		
	004223	002200		ARG JSSES,DU		
	004224	000000		ARG 0,DU		
		004225	6122	CHECK NIT10,\$SBZ,NIT9		630
	004225	000000		LXL 0,\$SRW1,T		
	004226	000077		ANX 0,\$STMK,DU		
	004227	004233		TZE NIT10		
	004230	000003		CMPX 0,\$SBZ,DU		
	004231	004217		TZE NIT9		
	004232	777777		TRA \$ERROR		
		004233	6123 NIT10	BSS 0		640
	004233	000000		LDX 0,\$SRW1,T	GET FRN	650
	004234	006044		6125	STX 0,\$FRJSS	SAVE IN TABLE
						660

INITIALIZATION -- OPEN SYSTEM SUB-MODULES

			6127 *			680
			6128 *			690
			6129 *	OPEN SYSTEM SUB-MODULES		700
			6130 *			710
			6131 *	OPEN 'LINE PRINTER/CARD PUNCH' MODULE		720
			6132 *			730
		004235	6133 NIT11	BSS	0	740
		004235	6134	OPEN	(PP,DU),(PPTS,DU),(1,DU),(PPES,DU),(0,DU)	750
004235	000667	7000		TSX	0,%OPEN	
004236	005107	0000		ARG	PP,DU	
004237	000014	0000		ARG	PPTS,DU	
004240	000001	0000		ARG	1,DU	
004241	002200	0000		ARG	PPES,DU	
004242	000000	0000		ARG	0,DU	
		004243	6135	CHECK	NIT12,B%BZ,NIT11	760
004243	000000	7200		LXL	0,T%SRW1,T	
004244	000077	3600		ANX	0,B%STMK,DU	
004245	004251	6000		TZE	NIT12	
004246	000003	1000		CMPX	0,B%BZ,DU	
004247	004235	6000		TZE	NIT11	
END	OF BINARY CARD	IOS00129				
004250	777777	7100		TRA	%ERROR	
		004251	6136 NIT12	BSS	0	770
004251	000000	2200		6137	LDX	0,T%SRW1,T GET FRN
004252	006260	7400		6138	STX	0,C%FRPP SAVE IN TABLE
004253	004272	7100		6139	TRA	NIT15 *****
						800

INITIALIZATION -- OPEN SYSTEM SUB-MODULES

			6141 *			820
			6142 *			830
			6143 *	OPEN 'CARD READER' MODULE		840
			6144 *			850
		004254	6145 NIT13	BSS	0	860
		004254	6146	OPEN	(CR,DU),(CRTS,DU),(1,DU),(CRES,DU),(0,DU)	870
004254	000667	7000		TSX	0,\$OPEN	
004255	005123	0000		ARG	CR,DU	
004256	000014	0000		ARG	CRTS,DU	
004257	000001	0000		ARG	1,DU	
004260	002200	0000		ARG	CRES,DU	
004261	000000	0000		ARG	0,DU	
		004262	6147	CHECK	NIT14,\$BZ,NIT13	880
004262	000000	7200		LXL	0,TSSRW1,T	
004263	000077	3600		ANX	0,\$\$STMK,DU	
004264	004270	6000		TZE	NIT14	
004265	000003	1000		CMPL	0,\$\$BZ,DU	
004266	004254	6000		TZE	NIT13	
004267	777777	7100		YRA	\$ERROR	
		004270	6148 NIT14	BSS	0	890
004270	000000	2200	6149	LDX	0,TSSRW1,T GET FRN	900
004271	006320	7400	6150	STX	0,C\$FRCR SAVE IN TABLE	910

INITIALIZATION -- OPEN COMMUNICATIONS NETWORK EVENTS

		6152 *		930
		6153 *		940
		6154 *	OPEN COMMUNICATIONS NETWORK EVENTS	950
		6155 *		960
		6156 *	OPEN 'CSFRN0' EVENT	970
		6157 *		980
	004272	6158 NIT15	BSS 0	990
	004272	6159	OPSCE (0,DU),(BSTRANS,DU),(0,DU)	1000
004272	001005		TSX 0,%OPSCE	
004273	000000		ARG 0,DU	
004274	000001		ARG BSTRANS,DU	
004275	000000		ARG 0,DU	
	004276	6160	CHECK NIT16,B\$BZ,NIT15	1010
END OF BINARY CARD	IOS00130			
004276	000000		LXL 0,T\$SRW1,T	
004277	000077		ANX 0,B\$STMK,DU	
004300	004304		TZE NIT16	
004301	000003		CMPX 0,B\$BZ,DU	
004302	004272		TZE NIT15	
004303	777777		TRA \$ERROR	
	004304	6161 NIT16	BSS 0	1020
004304	000000	6162	LDX 0,T\$SRW1,T GET FRN	1030
004305	006217	6163	STX 0,CSFRN0 SAVE IN TABLE	1040
004306	006345	6164	STX 0,PFIL0 SAVE IN PASS LIST	1050
		6165 *		1060
		6166 *	OPEN 'CSFRN1' EVENT	1070
		6167 *		1080
	004307	6168 NIT17	BSS 0	1090
	004307	6169	OPSCE (0,DU),(BSTRANS,DU),(0,DU)	1100
004307	001005		TSX 0,%OPSCE	
004310	000000		ARG 0,DU	
004311	000001		ARG BSTRANS,DU	
004312	000000		ARG 0,DU	
	004313	6170	CHECK NIT18,B\$BZ,NIT17	1110
004313	000000		LXL 0,T\$SRW1,T	
004314	000077		ANX 0,B\$STMK,DU	
004315	004321		TZE NIT18	
004316	000003		CMPX 0,B\$BZ,DU	
004317	004307		TZE NIT17	
004320	777777		TRA \$ERROR	
	004321	6171 NIT18	BSS 0	1120
004321	000000	6172	LDX 0,T\$SRW1,T GET FRN	1130
004322	006220	6173	STX 0,CSFRN1 SAVE IN TABLE	1140
004323	006346	6174	STX 0,PFIL1 SAVE IN PASS LIST	1150
END OF BINARY CARD	IOS00131			
004324	006104	6175	STX 0,CSFRLP1 SAVE FOR LP INPUT	1160
004325	006144	6176	STX 0,CSFRCP1 SAVE FOR CP INPUT	1170
004326	006204	6177	STX 0,CSFRCR1 SAVE FOR CR INPUT	1180
		6178 *		1190
		6179 *	OPEN 'CSFRN2' EVENT	1200

INITIALIZATION -- OPEN COMMUNICATIONS NETWORK EVENTS

			6180 *					1210
		004327	6181 NIT19	BSS	0			1220
		004327	6182	OPSCE	(0,DU),(B\$TRANS+B\$PASS,DU),(0,DU)			1230
004327	001005	7000 00		TSX	0,\$OPSCE			
004330	000000	0000 03		ARG	0,DU			
004331	000003	0000 03		ARG	B\$TRANS+B\$PASS,DU			
004332	000000	0000 03		ARG	0,DU			
		004333	6183	CHECK	NIT20,B\$BZ,NIT19			1240
004333	000000	7200 11		LXL	0,T\$SRW1,T			
004334	000077	3600 03		ANX	0,B\$STMK,DU			
004335	004341	6000 00		TZE	NIT20			
004336	000003	1000 03		CMPX	0,B\$BZ,DU			
004337	004327	6000 00		TZE	NIT19			
004340	777777	7100 00		TRA	\$ERROR			
		004341	6184 NIT20	BSS	0			1250
004341	000000	2200 11	6185	LDX	0,T\$SRW1,T	GET FRN		1260
004342	006221	7400 00	6186	STX	0,C\$FRN2	SAVE IN TABLE		1270
004343	006347	7400 00	6187	STX	0,PFIL2	SAVE IN PASS LIST		1280

INITIALIZATION -- SPAWN ALL SUB-MODULES

			6189 *			1300
			6190 *			1310
			6191 *			1320
			6192 *	SPAWN ALL SUB-MODULES		1330
			6193 *			1340
			6194 *	SPAWN *LPCP* MODULE		1350
			6195 *			1360
004344	006260	2200 00	6196	LDX 0,C\$FRPP	GET LP/CP FRN	1370
004345	006341	7400 00	6197	STX 0,P\$FG0	STICK IT IN PARAMETER LIST	1380
		004346	6198	NIT21 BSS 0		1390
		004346	6199	SPAWN (PLOC,DU),(PLEN,DU),(0,DU)		1400
004346	000644	7000 00		TSX 0,\$\$SPAWN		
004347	006327	0000 03		ARG PLOC,DU		
004350	000021	0000 03		ARG PLEN,DU		
004351	000000	0000 03		ARG 0,DU		
		004352	6200	CHECK NIT22,B\$BZ,NIT21,B\$RNA,NIT21		1410
END OF BINARY CARD	IOS00132					
004352	000000	7200 11		LXL 0,T\$SRW1,T		
004353	000077	3600 03		ANX 0,B\$STMK,DU		
004354	004362	6000 00		TZE NIT22		
004355	000003	1000 03		CMPX 0,B\$BZ,DU		
004356	004346	6000 00		TZE NIT21		
004357	000011	1000 03		CMPX 0,B\$RNA,DU		
004360	004346	6000 00		TZE NIT21		
004361	777777	7100 00		TRA \$ERROR		
		004362	6201	NIT22 BSS 0		1420
004362	000000	2200 11	6202	LDX 0,T\$SRW1,T	GET PROCESS EVENT	1430
004363	006254	7400 00	6203	STX 0,C\$FRPR1	SAVE IN TABLE	1440
004364	004405	7100 00	6204	TRA NIT29	*****	1450
			6205 *			1460
			6206 *	SPAWN *CR* MODULE		1470
			6207 *			1480
004365	006320	2200 00	6208	LDX 0,C\$FRCR	GET CR FRN	1490
004366	006341	7400 00	6209	STX 0,P\$EG0	STICK IT IN PARAMETER LIST	1500
		004367	6210	NIT25 BSS 0		1510
		004367	6211	SPAWN (PLOC,DU),(PLEN,DU),(0,DU)		1520
004367	000644	7000 00		TSX 0,\$\$SPAWN		
004370	006327	0000 03		ARG PLOC,DU		
004371	000021	0000 03		ARG PLEN,DU		
004372	000000	0000 03		ARG 0,DU		
		004373	6212	CHECK NIT26,B\$BZ,NIT25,B\$RNA,NIT25		1530
004373	000000	7200 11		LXL 0,T\$SRW1,T		
004374	000077	3600 03		ANX 0,B\$STMK,DU		
004375	004403	6000 00		TZE NIT26		
004376	000003	1000 03		CMPX 0,B\$BZ,DU		
004377	004367	6000 00		TZE NIT25		
END OF BINARY CARD	IOS00133					
004400	000011	1000 03		CMPX 0,B\$RNA,DU		
004401	004367	6000 00		TZE NIT25		
004402	777777	7100 00		TRA \$ERROR		

INITIALIZATION -- SPAWN ALL SUB-MODULES

		004403	6213	NIT26	BSS	0		1540
004403	000000	2200 11	6214	LDX	0,T\$SRW1,T		GET PROCESS EVENT	1550
004404	006314	7400 00	6215	STX	0,C\$FRPR2		SAVE IN TABLE	1560
			6216	*				1570
			6217	*			SPAWN PARAMETER LIST	1580
			6218	*				1590
		006327	6219	USE	STORE			1600
		006327	6220	PLOC	BSS	0	PARAMETER LIST	1610
		006327	6221	DUP	1,0		REGISTERS	1620
006327	000000000000		6222	DEC	0			1630
006337	400000000000		6223	OCT	-0		TIME LIMIT	1640
006340	000000 000010		6224	ZERO	0,B\$CEND		OPTION BITS	1650
006341	000000 000002		6225	PSEG0	ZERO	***,B\$PF	SEGMENT ZERO	1660
006342	000000 000000		6226	PSEG1	ZERO	0,B\$VOID	SEGMENT ONE	1670
006343	000000 000000		6227	PSEG2	ZERO	0,B\$VOID	SEGMENT TWO	1680
006344	000000 000000		6228	PSEG3	ZERO	0,B\$VOID	SEGMENT THREE	1690
006345	000000 000004		6229	PFIL0	ZERO	***,B\$NO	SUB-SYSTEM INPUT EVENT	1700
006346	000000 000002		6230	PFIL1	ZERO	***,B\$CA	SUB-SYSTEM OUTPUT EVENT	1710
END OF BINARY CARD	IOS00134							
006347	000000 000004		6231	PFIL2	ZERO	***,B\$NO	SUB-SYSTEM INPUT FILE EVENT	1720
		000021	6232	PLEN	EQU	*-PLOC		1730
		004405	6233	USE	PREVIOUS			1740

INITIALIZATION -- OPEN PERIPHERALS

			6235 *			1760
			6236 *			1770
			6237 *	OPEN PERIPHERALS		1780
			6238 *			1790
			6239 *	OPEN OPERATOR'S CONSOLE		1800
			6240 *			1810
		004405	6241 NIT29	BSS	0	1820
		004405	6242	OPEN	(0\$PTN,DU),(0\$PTS,DU),(1,DU),(9,DU),(0,DU)	1830
004405	000667	7000 00		TSX	0,\$OPEN	
004406	005527	0000 03		ARG	0\$PTN,DU	
004407	000014	0000 03		ARG	0\$PTS,DU	
004410	000001	0000 03		ARG	1,DU	
004411	000011	0000 03		ARG	9,DU	
004412	000000	0000 03		ARG	0,DU	
		004413	6243	CHECK	NIT30,\$B\$BZ,NIT29,\$B\$LOCK,NIT29	1840
004413	000000	7200 11		LXL	0,\$TSRW1,T	
004414	000077	3600 03		ANX	0,\$B\$STMK,DU	
004415	004423	6000 00		TZE	NIT30	
004416	000003	1000 03		CMPX	0,\$B\$BZ,DU	
004417	004405	6000 00		TZE	NIT29	
004420	000013	1000 03		CMPX	0,\$B\$LOCK,DU	
004421	004405	6000 00		TZE	NIT29	
004422	777777	7100 00		TRA	\$ERROR	
		004423	6244 NIT30	BSS	0	1850
004423	000000	2200 11		6245	LDX 0,\$TSRW1,T GET FRN	1860
004424	005665	7400 00		6246	STX 0,\$RSOPFRN SAVE IN TABLE	1870
			6247 *			1880
			6248 *	SEIZE OPERATOR'S CONSOLE TTY		1890
			6249 *			1900
		004425	6250 NIT31	BSS	0	1910
		004425	6251	LOGS	SEIZE OP CON/ LOCK IT/ SAY HELLO	1920
004425	001517	7070 00		TSX	L,0\$LOGS	
			6252 *			1930
			6253 *	OPEN THE REST OF THE PERIPHERALS		1940
			6254 *			1950
		004426	6255	BRANCH	NOPASS,0\$INP4 SETUP UP TASK TO READ DUMMY INPUT BUFFER	1960
004426	001467	7000 00		TSX	0,\$T\$GETT	
004427	000000	6220 11		EAX	X,0,T	
004430	000005	2210 12		LDX	T,\$LINK,X	
END OF BINARY CARD	IOS00135					
004431	000000	6210 12		EAX	T,0,X	
004432	002041	6200 00		EAX	0,0\$INP4	
004433	000004	7400 11		STX	0,\$T\$TRA,T	
004434	000004	6200 11		EAX	0,\$\$OFFST,T	
004435	005162	7170 00		XED	0\$XADD+0\$TASK	
004436	000005	2210 12		LDX	T,\$LINK,X	
		004437		BUGXR	(0,X)	
		525266	BUGBUG	SET	BUGBUG+1	
004437	525266	2200 03		LDX	0,BUGBUG,DU	
004440	525266	2220 03		LDX	X,BUGBUG,DU	

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INPUT/OUTPUT SCHEDULER -- IOS MONITOR

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INITIALIZATION -- OPEN PERIPHERALS

004441 004441
004441 001477 7000 00

6256

RELT
TSX

0,T\$RELT

RELEASE TCB

1970

INITIALIZATION -- SET UP NOTIFIES

			6258 *		1990
			6259 *		2000
			6260 *		2010
			6261 *	SET UP NOTIFIES	2020
			6262 *		2030
004442	005720	2210 03	6263	LDX T,C\$LP,E,DU	LINE PRINTER EVENT
004443	005720	7260 07	6264	LXL J,C\$LP,E,DL	
		004444	6265	BRANCH PASS,C\$NSRVX	RE-ISSUE NOTIFY
004444	001467	7000 00		TSX O,T\$GETT	
004445	000000	6220 11		EAX X,O,T	
004446	000005	2210 12		LDX T,T\$LINK,X	
004447	003171	6200 00		EAX O,C\$NSRVX	
004450	000004	7400 11		STX O,T\$TRA,T	
004451	000004	6200 11		EAX O,Q\$OFFST,T	
004452	005162	7170 00		XED Q\$XADD+Q\$TASK	
004453	000000	6210 12		EAX T,O,X	
		004454		BUGXR (O,X)	
		525267	BUGBUG	SET BUGBUG+1	
004454	525267	2200 03		LDX O,BUGBUG,DU	
004455	525267	2220 03		LDX X,BUGBUG,DU	
		004456	6266	RELT	RELEASE TCB AND JCB
004456	001477	7000 00		TSX O,T\$RELT	2070
			6267 *		2080
			6268 *		2090
END OF BINARY CARD	IOS00136				
004457	005760	2210 03	6269	LDX T,C\$CPE,DU	CARD PUNCH FILE QUEUE
004460	005760	7260 07	6270	LXL J,C\$CPE,DL	
		004461	6271	BRANCH PASS,C\$NSRVX	RE-ISSUE NOTIFY
004461	001467	7000 00		TSX O,T\$GETT	
004462	000000	6220 11		EAX X,O,T	
004463	000005	2210 12		LDX T,T\$LINK,X	
004464	003171	6200 00		EAX O,C\$NSRVX	
004465	000004	7400 11		STX O,T\$TRA,T	
004466	000004	6200 11		EAX O,Q\$OFFST,T	
004467	005162	7170 00		XED Q\$XADD+Q\$TASK	
004470	000000	6210 12		EAX T,O,X	
		004471		BUGXR (O,X)	
		525270	BUGBUG	SET BUGBUG+1	
004471	525270	2200 03		LDX O,BUGBUG,DU	
004472	525270	2220 03		LDX X,BUGBUG,DU	
		004473	6272	RELT	RELEASE TCB
004473	001477	7000 00		TSX O,T\$RELT	2130
			6273 *		2140
			6274 *		2150
004474	006020	2210 03	6275	LDX T,C\$JSS,DU	JOB STREAM SCHEDULER EVENT
004475	006020	7260 07	6276	LXL J,C\$JSS,DL	
		004476	6277	BRANCH PASS,C\$NSRVX	RE-ISSUE NOTIFY
004476	001467	7000 00		TSX O,T\$GETT	
004477	000000	6220 11		EAX X,O,T	
004500	000005	2210 12		LDX T,T\$LINK,X	

INITIALIZATION -- SET UP NOTIFIES.

004501	003171	6200	00		EAX	0,C\$NSRVX		
004502	000004	7400	11		STX	0,T\$TRA,T		
004503	000004	6200	11		EAX	0,Q\$OFFST,T		
004504	005162	7170	00		XED	Q\$XADD+Q\$TASK		
END OF BINARY CARD	IOS00137							
004505	000000	6210	12		EAX	T,0,X		
	004506				BUGXR	(0,X)		
	525271			BUGBUG	SET	BUGBUG+1		
004506	525271	2200	03		LDX	0,BUGBUG,DU		
004507	525271	2220	03		LDX	X,BUGBUG,DU		
	004510			6278	RELT		RELEASE TCR	2190
004510	001477	7000	00		TSX	0,T\$RELT		
				6279 *				2200
				6280 *				2210
004511	006060	2210	03		LDX	T,C\$LP1,DU	COMMUNICATIONS INPUT FROM LP	2220
004512	006060	7260	07		LXL	J,C\$LP1,DL		2230
	004513			6282	BRANCH	PASS,C\$NSRVX		2240
004513	001467	7000	00		TSX	0,T\$GETT		
004514	000000	6220	11		EAX	X,0,T		
004515	000005	2210	12		LDX	T,T\$LINK,X		
004516	003171	6200	00		EAX	0,C\$NSRVX		
004517	000004	7400	11		STX	0,T\$TRA,T		
004520	000004	6200	11		EAX	0,Q\$OFFST,T		
004521	005162	7170	00		XED	Q\$XADD+Q\$TASK		
004522	000000	6210	12		EAX	T,0,X		
	004523				BUGXR	(0,X)		
	525272			BUGBUG	SET	BUGBUG+1		
004523	525272	2200	03		LDX	0,BUGBUG,DU		
004524	525272	2220	03		LDX	X,BUGBUG,DU		
	004525			6284	RELT			2250
004525	001477	7000	00		TSX	0,T\$RELT		
				6285 *				2260
004526	006120	2210	03		LDX	T,C\$CP1,DU	COMMUNICATIONS INPUT FROM CP	2270
004527	006120	7260	07		LXL	J,C\$CP1,DL		2280
	004530			6288	BRANCH	PASS,C\$NSRVX	RE-ISSUE NOTIFY	2290
004530	001467	7000	00		TSX	0,T\$GETT		
004531	000000	6220	11		EAX	X,0,T		
004532	000005	2210	12		LDX	T,T\$LINK,X		
END OF BINARY CARD	IOS00138							
004533	003171	6200	00		EAX	0,C\$NSRVX		
004534	000004	7400	11		STX	0,T\$TRA,T		
004535	000004	6200	11		EAX	0,Q\$OFFST,T		
004536	005162	7170	00		XED	Q\$XADD+Q\$TASK		
004537	000000	6210	12		EAX	T,0,X		
	004540				BUGXR	(0,X)		
	525273			BUGBUG	SET	BUGBUG+1		
004540	525273	2200	03		LDX	0,BUGBUG,DU		
004541	525273	2220	03		LDX	X,BUGBUG,DU		
004542	004561	7100	00	6289	TRA	NITXX	*****	2300
	004543			6290	RELT			2310

INITIALIZATION -- SET UP NOTIFY

004543	001477	7000	00		TSX	0,TSRELT		
				6291	*			2320
				6292	*			2330
004544	006160	2210	03	6293	LDX	T,C\$CRI,DU	COMMUNICATIONS INPUT FROM CR	2340
004545	006160	7260	07	6294	LXL	J,C\$CRI,DL		2350
			004546	6295	BRANCH	PASS,C\$NSRVX	RE-ISSUE THE NOTIFY	2360
004546	001467	7000	00		TSX	0,TSGETT		
004547	000000	6220	11		EAX	X,0,T		
004550	000005	2210	12		LDX	T,TSLINK,X		
004551	003171	6200	00		EAX	0,C\$NSRVX		
004552	000004	7400	11		STX	0,TS\$TRA,T		
004553	000004	6200	11		EAX	0,Q\$OFFST,T		
004554	005162	7170	00		XED	Q\$XADD+Q\$TASK		
004555	000000	6210	12		EAX	T,0,X		
			004556		BUGXR	(0,X)		
			525274		BUGBUG	SET	BUGBUG+1	
004556	525274	2200	03		LDX	0,BUGBUG,DU		
004557	525274	2220	03		LDX	X,BUGBUG,DU		
			004560	6296	RELT			2370
004560	001477	7000	00		TSX	0,TS\$RELT		
				6297	*			2380
				6298	*			2390
				6299	*			2400
				6300	*			2410
				6301	*****			2420
				6302	*			2430
				6303	*			2440
			004561	6304	NITXX	BSS	0	2450
END OF BINARY CARD	IOS00139						*****	
004561	006230	2210	03	6305	LDX	T,C\$PRI,DU	LPCP MODULE	2460
004562	006230	7260	07	6306	LXL	J,C\$PRI,DL		2470
			004563	6307	BRANCH	PASS,C\$NSRVX	RE-ISSUE NOTIFY	2480
004563	001467	7000	00		TSX	0,TSGETT		
004564	000000	6220	11		EAX	X,0,T		
004565	000005	2210	12		LDX	T,TSLINK,X		
004566	003171	6200	00		EAX	0,C\$NSRVX		
004567	000004	7400	11		STX	0,TS\$TRA,T		
004570	000004	6200	11		EAX	0,Q\$OFFST,T		
004571	005162	7170	00		XED	Q\$XADD+Q\$TASK		
004572	000000	6210	12		EAX	T,0,X		
			004573		BUGXR	(0,X)		
			525275		BUGBUG	SET	BUGBUG+1	
004573	525275	2200	03		LDX	0,BUGBUG,DU		
004574	525275	2220	03		LDX	X,BUGBUG,DU		
			004575	6308	RELT		RELEASE TCB	2490
004575	001477	7000	00		TSX	0,TSRELT		
			004576	6309	EXIT		*****	2500
004576	003074	7100	00		TRA	\$EXIT		
				6310	*			2510
				6311	*			2520

INITIALIZATION -- SET UP NOTIFIES

004577	006270	2210	03	6312	LDX	T,C\$PR2,DU	CR MODULE	2530
004600	006270	7260	07	6313	LXL	J,C\$PR2,DL		2540
			004601	6314	BRANCH	PASS,C\$NSRVX		2550
004601	001467	7000	00		TSX	O,T\$GETT		
004602	000000	6220	11		EAX	X,O,T		
004603	000005	2210	12		LDX	T,T\$LINK,X		
004604	003171	6200	00		EAX	O,C\$NSRVX		
004605	000004	7400	11		STX	O,T\$TRAT		
004606	000004	6200	11		EAX	C,\$\$OFFST,T		
END	OF BINARY CARD	IOS00140						
004607	005162	7170	00		XED	Q\$XADD+Q\$TASK		
004610	000000	6210	12		EAX	T,O,X		
			004611		BUGXR	(O,X)		
			525276	BUGBUG	SET	BUGBUG+1		
004611	525276	2200	03		LDX	O,BUGBUG,DU		
004612	525276	2220	03		LDX	X,BUGBUG,DU		
			004613	6315	RELT		RELEASE TCB	2560
004613	001477	7000	00		TSX	O,T\$RELT		
			004614	6316	EXIT		AND WE'RE OFF!	2570
004614	003074	7100	00		TRA	\$EXIT		
				6317	*\$*	DISK	TREENAME	2580

INITIALIZATION -- TREE-NAMES & CONSTANTS

Address	Label	Value	Usage	Constant	Page
005013		6319	USE	CONST	110
		6320	HEAD		120
		6321	*		130
		6322	*		140
		6323	*	TREE-NAMES & CONSTANTS	150
		6324	*		160
		6325	*	PRINT-FILE-QUEUE	170
		6326	*		180
005013	123131123117	6327	LPQ	UASCI 6,SYSOUT	190
005021	120122111116	6328		UASCI 6,PRINT-FILE-QUEUE	200
	000014	6329	LPQTS	EQU *-LPQ TREE-SIZE	210
	004400	6330	LPQES	EQU 36*64 ELEMENT SIZE	220
		6331	*		230
		6332	*		240
		6333	*	PUNCH-FILE-QUEUE	250
		6334	*		260
	005027	6335	CPQ	BSS 0 TREE-NAME	270
005027	123131123117	6336		UASCI 6,SYSOUT	280
END OF BINARY CARD	IOS00141				
005035	120125116103	6337		UASCI 6,PUNCH-FILE-QUEUE	290
	000014	6338	CPQTS	EQU *-CPQ TREE-SIZE	300
	004400	6339	CPQES	EQU 36*64 ELEMENT SIZE	310
		6340	*		320
		6341	*		330
		6342	*	PRINT-FILE-EVENT	340
		6343	*		350
	005043	6344	LPE	BSS 0 TREE-NAME	360
005043	123131123117	6345		UASCI 6,SYSOUT	370
005051	120122111116	6346		UASCI 6,PRINT-FILE-EVENT	380
	000014	6347	LPETS	EQU *-LPE TREE-SIZE	390
	002200	6348	LPEES	EQU 36*32 ELEMENT SIZE	400
		6349	*		410
		6350	*		420
		6351	*	PUNCH-FILE-EVENT	430
		6352	*		440
	005057	6353	CPE	BSS 0 TREE-NAME	450
005057	123131123117	6354		UASCI 6,SYSOUT	460
END OF BINARY CARD	IOS00142				
005065	120125116103	6355		UASCI 6,PUNCH-FILE-EVENT	470
	000014	6356	CPETS	EQU *-CPE TREE-SIZE	480
	002200	6357	CPEES	EQU 36*32 ELEMENT SIZE	490
		6358	*		500
		6359	*		510
		6360	*	JOB STREAM SCHEDULER EVENT	520
		6361	*		530
	005073	6362	JSS	BSS 0 TREE-NAME	540
005073	122125102105	6363		UASCI 6,RUBENS	550
005101	105126105116	6364		UASCI 6,EVENT1	560
END OF BINARY CARD	IOS00143				
	000014	6365	JSSTS	EQU *-JSS TREE-SIZE	570

INITIALIZATION -- TRFE-NAMES & CONSTANTS

	002200	6366	JSES	EQU	36*32	ELEMENT SIZE	580
		6367	*				590
		6368	*				600
		6369	*		LINE PRINTER/CARD PUNCH		610
		6370	*				620
	005107	6371	PP	BSS	0	TREE-NAME	630
005107	122125102105	6372		UASCI	6,RUBENS		640
005115	130114120103	6373		UASCI	6,XLPCPA		650
	000014	6374	PPTS	EQU	*-PP	TREE-SIZE	660
	002200	6375	PPES	EQU	36*32	ELEMENT SIZE	670
		6376	*				680
		6377	*				690
		6378	*		CARD READ MODULE		700
		6379	*				710
	005123	6380	Cr	BSS	0	TREE-NAME	720
005123	055055055055	6381		UASCI	6,----		730
005131	055055055055	6382		UASCI	6,----		740
END OF BINARY CARD	IOS00144						
	000014	6383	CRTS	EQU	*-CR	TREE-SIZE	750
	002200	6384	CRES	EQU	36*32	ELEMENT SIZE	760
		6385	*\$*	DISK	END		770

ASSEMBLY CONTROL CARDS

	6387		HEAD			110
	6388	*				120
	6389	*				130
	6390	*		HOUSEKEEPING CARDS		140
	6391	*				150
	6392	*		HERE WE CLEAN ANY AND ALL ASSEMBLER BUGS.		160
	6393	*		THE JOB'S ARE PRE-ALLOCATED, THE LAST USED LOCATION OF		170
	6394	*		CORE IS CALCULATED AND WHAT REMAINS UP TO THE END OF THE		180
	6395	*		BAR IF ANY IS LINKED ON THE FREE MEMORY LIST.		190
	6396	*				200
004615	6397		USE	CODE		210
	6398		HEAD			220
004620	6399		EIGHT			230
004620	6400	ZCODEL	EQU	*-ZCODE	CODE UNDER CODE	240
	6401	*				250
	6402	*				260
005137	6403		USE	CONST		270
	6404		LIT		FORCE LITERAL POOL HERE	280
005140	6405		EIGHT			290
000320	6406	ZCONSL	EQU	*-ZCONS	CODE UNDERCONST	300
	6407	*				310
	6408	*				320
005300	6409		USE	QSTOR		330
005300	6410		EIGHT			340
000140	6411	ZQSTRL	EQU	*-ZQSTR	CODE UNDER QSTOR	350
	6412	*				360
	6413	*				370
006350	6414		USE	STORE		380
000037	6415	TLEN	EQU	T\$LEN+7	ROUND T\$LEN TO	390
000030	6416	TLENR	EQU	TLEN/8*8	MULTIPLE OF EIGHT	400
006350	6417		EIGHT		START OF DYNAMIC BUFFER AREA	410
006350	6418	SPTCB	BSS	TLENR-1	FIRST TCB (START OF DYNAMIC BUFFERS	420
006377 000000000000	6419		DEC	0	FOR THE CRUMMY LOADER	430
006400	6420		EIGHT			440
001100	6421	ZSTORL	EQU	*-ZSTOR	CODE UNDER STORE	450
	6422	*				460
	6423	*				470
006400	6424	LASTC	EQU	ZCODEL+ZCONSL+ZQSTRL+ZSTORL	LAST CARD	480

ASSEMBLY CONTROL CARDS

			6426 *			500	
			6427 *			510	
	006350		6428 ZTOP0 EQU	SPTCB	START OF DYNAMIC BUFFER AREA	520	
	007377		6429 ZZ EQU	LASTC+MQUAN-1	ROUND UP TO NEXT MULTIPLE	530	
	007000		6430 ZTOP EQU	ZZ/MQUAN*MQUAN	OF CORE PAGE SIZE	540	
	000400		6431 ZZ1 EQU	ZTOP-LASTC	LENGTH OF LEFT OVER CORE	550	
			6432 *			560	
	006400		6437 NEXTF EQU	LASTC	STICK EXTRA CORE ON FREE LIST	610	
	006400		6438 NEXTB EQU	NEXTF		620	
006400	005346	000400	6439	ZERO	R\$LAST,ZZ1	INITIALIZE HEADER DATA FOR LINKED LIST	630
006401	005344	000000	6440	ZERO	R\$FIRST,0		640
			6441 *				650
			6442 *				660
END OF BINARY CARD	IOS00145						
	004106		6443	TCD	\$UP	MARK END OF BINARY DECK	670
END OF BINARY CARD	IOS00146						
			6444 *				680
			6445	DCARD	2,\$		690
			6446 \$	DKEND			700
			6447 \$EOD				710
			6448 *				720
			6449 *				730
			6450 *				740
	004106		6451 THIS	END	UP		750

END OF BINARY CARD IOS00149
 6402 IS THE NEXT AVAILABLE LOCATION. GMAP VERSION JMPA/062770 JMPB/062770 JMPC/062770
 THERE WERE NO WARNING FLAGS IN THE ABOVE ASSEMBLY

OCTAL	SYMBOL	REFERENCES BY ALTER NO.							
2000	1K	771	771	772	2822				
24	A AD	442	442	443					
16	A CP	436	436	437	5634				
15	A CR	355	355	3532					
100060	A DO	365	365	3604	4023				
56	A DP	363	363						
3	A ID	430	430	431					
101	A LA	369	369	3750					
12	A LF	354	354	3534					
17	A LP	437	437	438	5631				
23	A MT	441	441	442					
77	A QM	368	368						
35	A RL	450	450	451					
200040	A SP	356	356	383	3547	3748	3792		
52	A AST	361	361						
22	A CLM	440	440	441					
72	A COL	366	366						
32	A CPU	448	448	449					
177	A DEL	370	370						
200044	A DOL	359	359	387					
600004	A EOT	352	352	3745	3746	3961			
0	A FAC	427	427	428					
27	A MIP	445	445	446					
0	A NUL	351	351						
7	A BELL	353	353						
20	A CDRD	438	438	439					
33	A CORE	449	449	450					
13	A DISK	433	433	434					
14	A DRUM	434	434	435					
177	A MASK	373	373	3560	3743				
200073	A SCOL	367	367	410	4159	4211	4256	4301	
7	A SHOP	431	431	432					
600043	A TERM	358	358	386					
600046	AAMPER	360	360	389	3958				
40	AASCLN	415	415						
4620	AASCTB	382	382	415	3752				
40	ABLANK	357	357						
1017	ACCT	2201	2201	2204	5881				
5343	ACCTT	2213	2201	2205	2206	2213			
3	ACODE	644	644	5689					
200054	ACOMMA	362	362	395					
2	ADATES	429	429	430					
15	AOPCON	435	435	436					
551	APEND	1360	1360	3629					
5322	APNDT	1373	1360	1363	1364	1365	1366	1373	
240057	ASLASH	364	364	398					
37	ATIMEO	451	451						
1	ATIMES	428	428	429					
5300	AVAIL	775	775	2829	2964	3053	3096	6061	
4	B AP	230	230	231	233	4683	4685	4686	

OCTAL	SYMBOL	REFERENCES BY ALTER NO.
3	B BZ	266 266 906 927 952 3134 3433 3515 3630 3911 4106 4137 4555 5066 5074 5077 5084 5096 5269 5328 5615 5655 5668 5710 5766 5775 5824 5882 5901 5906 5912 5918 5931 6067 6079 6088 6102 6111 6122 6135 6147 6160 6170 6183 6200 6212 6243
2	B CA	235 235 6230
2	B EX	231 231 232 233
4	B IO	291 291 3134
1	B LK	232 232 233 5823
4	B NO	234 234 235 6229 6231
0	B OK	263 263
2	B PF	247 247 6225
1	B PS	246 246
20	B RD	228 228 229 233 4684 4686 5823
0	B SS	252 252
400000	B WP	244 244
10	B WT	229 229 230 233
37	B ALL	233 233 763 4687
100000	B CAR	215 215
13	B DBZ	273 273
16	B EOF	274 274 5074 5668
20000	B EOY	217 217
10000	B EUN	218 218
2000	B GET	327 327
4	B IOP	267 267
5	B ITN	281 281 5710 5906
200	B MOD	223 223
200000	B NEG	214 214
40000	B OPR	306 306 394 396 411 413 3856 3863 4330
40000	B OVF	216 216
4000	B OVM	219 219 6059
400	B PAM	222 222 6059
1000	B PAR	221 221
16000	B RDY	333 333
6000	B REL	329 329
11	B RNA	296 296 6200 6212
2000	B RPT	198 198
2000	B TAL	220 220
11	B TLE	283 283 5296
20	B TMI	204 204
2	B TNC	207 207
40	B TNZ	203 203
1	B TOV	208 208
10	B TPL	205 205
4	B TRC	206 206
35	B TRO	276 276 3911 5339 5345 5352 5358 5364 5369 5383 5390
100	B TZE	202 202 4352
400000	B ZER	213 213
1000	B ABIT	199 199 2815 4352
400	B BBIT	200 200 2815
1	B BHDR	318 318

OCTAL SYMBOL REFERENCES BY ALTER NO.

200	B CBIT	201	201																	
10	B CEND	242	242	6224																
14000	B DONE	332	332																	
12	B HDWE	272	272																	
2	B IACC	265	265																	
7	B IELT	270	270																	
1	B IFRN	264	264																	
11	B IMOD	271	271																	
5	B IPTR	268	268																	
16	B IPWD	285	285																	
6	B IREQ	269	269																	
2000	B .GET	339	339	4130																
6000	B .REL	341	341	4568																
4000	B KILL	328	328																	
13	B LOCK	284	284	3433	4106	5066	5615	6243												
26	B NSTR	275	275																	
2	B PASS	255	255	6182																
400000	B SIGN	302	302	4716																
77	B STMK	301	301	3134	3433	3515	3630	3911	4106	4137	4555	5066	5074	5077	5084	5096	5269			
			5296	5328	5615	5655	5668	5710	5766	5775	5824	5882	5901	5906	5912	5918	5931			
			6067	6079	6088	6102	6111	6122	6135	6147	6160	6170	6183	6200	6212	6243				
400000	B TERM	303	303	304	3745	3746	3758	3830	3856	3863	3964	4330	4339							
7	B UERR	282	282																	
0	B VOID	245	245	6226	6227	6228														
10000	B XXXX	330	330																	
1	BANR	636	636	5725																
17	BBJBMK	316	316																	
1	BBOTMK	320	320																	
7777	BBSTMK	322	322																	
4	BBSTRD	243	243																	
200000	BDELIM	304	304	305	352	356	358	359	360	362	364	367	3745	3746	3748	3758	3826			
			3832	3856	3863	4330	4339													
100000	BDIGIT	305	305	306	365	399	400	401	402	403	404	405	406	407	408	3856	3863			
20000	BHDMK	317	317	5727																
0	B .ABRT	338	338																	
4000	B .KILL	340	340	4203																
12000	B .STRT	343	343	4293																
10000	B .XXXX	342	342																	
740000	BJOBMK	315	315																	
1700	BMODMK	314	314																	
0	BNPASS	254	254																	
10000	BOUTMK	319	319	5730																
12000	BRSTRT	331	331																	
7777	BSRTMK	321	321																	
1	BTRANS	253	253	6159	6169	6182														
0	BUFSEG	769	769	3133																
525200	BUG	742	742	743																
525276	BUGBUG	6314	743	2348	2349	2350	2380	2522	2975	2978	3137	3218	3296	3440	3482	3483	3519			
			3537	3550	3609	3795	3836	3869	3891	3982	3983	4205	4295	4358	4477	4479	4481			
			4482	4529	4550	4570	4579	4580	4581	4582	4947	4949	5180	5181	5182	5183	5351			

OCTAL SYMBOL REFERENCES BY ALTER NO.

OCTAL	SYMBOL	REFERENCES BY ALTER NO.
17	C TEM9	560 560
767	CAUSE	2096 2096 4136 5268 5823
5741	CAUST	2112 2096 2099 2100 2101 2102 2103 2104 2112
3221	CCMDTB	5412 5412 5420
3231	CCOMDX	5428 5410 5412 5416 5428 5441 5515 5532 5565
6144	CFRCP1	5364 5364 6176
6004	CFRCPE	5345 5345 6114
6010	CFRCPO	5346 5346 6091
6204	CFRCR1	5369 5369 6177
6044	CFRJSS	5352 5352 6125
6104	CFRLP1	5358 5358 6175
5744	CFRLPE	5339 5339 6105
5750	CFRLPO	5340 5340 6082
6254	CFRPR1	5383 5383 6203
6314	CFRPR2	5390 5390 6215
656	CHSEG	1710 1710 3133
5331	CHSGT	1721 1710 1713 1714 1721
777	CKMK	744 744
0	CKSM	634 634
704	CLOSE	1812 1812 4554 5917
5333	CLOST	1822 1812 1815 1822
10	CMAx	5876 5865 5876
3646	CMDTB	5868 5868 5876
3155	CMESS1	5270 5269 5270
3140	CMESSX	5267 4205 4295 4570 5267 5269 5271
3167	CNSRV1	5297 5296 5297
3171	CNSRVX	5319 3892 5296 5319 5429 5609 5788 5837 6265 6271 6277 6283 6288 6295 6307 6314
5057	CPE	6353 6110 6353 6356
2200	CPEES	6357 6110 6357
3273	CPERI1	5560 5560 5564
14	CPETS	6356 6110 6356
5027	CPQ	6335 6087 6335 6338
4400	CPQES	6339 6087 6339
14	CPQTS	6338 6087 6338
1000	CPST	4627 4627 4683 5364
31	CQFLOC	571 571 572 5080 5629 5679 5721
34	CQUEUE	577 577 578 5743
5123	CR	6380 6146 6380 6383
4074	CRASH	6003 5383 5390 6003
4767	CRDYMS	5496 5493 5496
3241	CREADY	5492 5419 5492
2200	CRES	6384 6146 6384
5	CRLINK	548 548 5298 5832
4776	CRMS	6010 6008 6010
2000	CRST	4628 4628 4684 5369
3247	CRSTRT	5514 5417 5514
14	CRTS	6383 6146 6383
25	CSTATE	569 569 5327 5551
16	CTEM10	561 561
15	CTEM11	562 562

OCTAL	SYMBOL	REFERENCES BY ALTER NO.				
35	GEIUSE					
45	GEINFO					
1	GEINOS					
6	GELAPS					
37	GELBAR					
33	GELoop					
11	GEMORE					
25	GEMREL					
43	GENEWS					
42	GEPRIO					
17	GERELC					
4	GERELS					
15	GERETS					
2	GEROAD					
31	GEROLL					
30	GEROUT					
24	GERSTR					
23	GESAVE					
14	GESETS					
5	GESNAP					
44	GESNUM					
20	GESPEC					
26	GESYOT					
21	GETIME					
32	GEUSER					
34	GEWAKE					
3303	INIT0	5613	5613	5615	5768	
3353	INIT1	5654	5646	5654	5655	
3363	INIT2	5659	5655	5659		
3366	INIT3	5666	5666	5668	5680	
3403	INIT4	5672	5668	5672		
3432	INIT5	5708	5708	5710		
3450	INIT6	5714	5710	5714		
3507	INIT7	5751	5747	5751		
3511	INIT8	5753	5750	5753		
3517	INIT9	5759	5745	5754	5757	5759
3301	INIT	5607	5339	5345	5607	
3531	INITX	5773	5668	5773	5775	
3315	INT0.0	5616	5615	5616		
3343	INT0.1	5642	5642	5650		
3520	INT10	5764	5764	5766		
3530	INT11	5767	5766	5767		
3406	INT4.1	5676	5676	5710		
3413	INT4.2	5681	5675	5681		
3541	INTX1	5776	5775	5776		
5	.APEND	143	143	1367		
32	.CATDR	164	164			
26	.CATLG	160	160			
42	.CATLK	172	172			
47	.CAUSE	177	177	2106		

OCTAL	SYMBOL	REFERENCES BY ALTER NO.				
22	.CHSEG	156	156	1715		
25	.CLOSE	159	159	1816		
21	.CLSEG	155	155			
57	.CRSG	183	183			
50	.DELET	178	178			
27	.DESTR	161	161	1866		
36	.EMM					
23	.EXSEG	157	157			
44	.LOCK	174	174	1954		
55	.MSTA	181	181			
46	.NOTIF	176	176	2046		
24	.OPEN	158	158	888	1772	
30	.OPENS	162	162			
36	.OPENW	168	168			
52	.OPSCE	180	180	2156		
20	.OPSEG	154	154			
17	.PAUSE	153	153	899	921	946 5191
0	.PRIV	138	138			
34	.RDA CL	166	166			
37	.RDBRN	169	169			
35	.RDDIR	167	167			
40	.RDLK	170	170			
56	.RDME	182	182	2207		
4	.READ	142	142	1312		
13	.RQDT	149	149	881		
14	.RGERT	150	150			
12	.RQST	148	148	1617		
63	.RQWD	186	186			
6	.RRF	144	144	1422		
10	.SCR	146	146	913	1524	
1	.SETFV	139	139	1258		
2	.SETSQ	140	140			
15	.SPAWN	151	151	1667		
11	.SPTR	147	147	1572		
3	.SQUEZ	141	141			
16	.TERM	152	152	955		
51	.UNCAU	179	179			
45	.UNLCK	175	175	1998		
31	.UPDAT	163	163	1910		
61	.WAMI	185	185			
43	.WBRAN	173	173			
33	.WRACL	165	165			
7	.WRF	145	145	934	1477	
41	.WSINF	171	171			
60	.WTME	184	184			
6	J J		3267	3296	5115	
7	J L		3266	3295		
5	J Q					
1	J T					
2	J X		5109	5114		

OCTAL	SYMBOL	REFERENCES BY ALTER NO.
5303	MTOP	778 778 2857 2859 2862 2900 2965 3050 3095 3129 3132 3133
4000	MTST	4630 4630 4686
6400	NEXTB	6438 2679 6438
6400	NEXTF	6437 2674 6437 6438
4233	NIT10	6123 6122 6123
4235	NIT11	6133 6133 6135
4251	NIT12	6136 6135 6136
4254	NIT13	6145 6145 6147
4270	NIT14	6148 6147 6148
4272	NIT15	6158 6139 6158 6160
4304	NIT16	6161 6160 6161
4307	NIT17	6168 6168 6170
4321	NIT18	6171 6170 6171
4327	NIT19	6181 6181 6183
4117	NIT1	6066 6066 6067
4341	NIT20	6184 6183 6184
4346	NIT21	6198 6198 6200
4362	NIT22	6201 6200 6201
4367	NIT25	6210 6210 6212
4403	NIT26	6213 6212 6213
4405	NIT29	6241 6204 6241 6243
4127	NIT2	6077 6067 6077 6079
4423	NIT30	6244 6243 6244
4425	NIT31	6250 6250
4143	NIT3	6080 6079 6080
4145	NIT4	6086 6086 6088
4161	NIT4.1	6089 6088 6089
4163	NIT5	6100 6100 6102
4177	NIT6	6103 6102 6103
4201	NIT7	6109 6109 6111
4215	NIT8	6112 6111 6112
4217	NIT9	6120 6120 6122
4106	NIT	6049 6049 6050
4561	NITXX	6304 6289 6304
5340	NOTFT	2052 2041 2044 2045 2052
756	NOTIF	2041 2041 5327
6	0 J	4247 4248
7	0 L	3430 3439 3469 3475 3481 3511 3512 3513 3518 3531 3533 3535 3536 3546 3548
		3549 3601 3605 3608 3790 3791 3794 3824 3829 3835 3850 3853 3855 3862 3868
		3903 3904 3963 3968 3998 3999 4000 4020 4024 4025 4042 4090 4145 4154 4158
		4185 4209 4228 4250 4255 4273 4300 4325 4327 4329 4338 4357 4371 4372
5	0 Q	
1	0 T	3891 4205 4295
2	0 X	3891 3970 3974 3975 4091 4111 4139 4186 4205 4229 4274 4295 4344 4346 4347
3	0 Y	3852 3860 4326 4334
4	0 Z	4092 4112 4151 4187 4230 4275 4349 4356
2110	0 GET	4089 4065 4089 4160
4676	0 LM1	3647 3437 3647
4677	0 LM2	3648 3648
1645	0 LOG	3620 3513 3563 3620 3904 4025

OCTAL	SYMBOL	REFERENCES BY ALTER NO.
1611	0 LSP	3545 3545
4740	0 MOK	4378 4371 4378
2372	0 OPX	4370 4063 4370
5527	0 PTN	4165 4102 4105 4165 4169 6242
14	0 PTS	4169 4105 4169 6242
2253	0 REL	4227 4069 4227 4257
4705	0 CERR	4029 4020 4029
4713	0 FERR	4046 4042 4046
2124	0 GET1	4105 4105 4106
2142	0 GET2	4110 4106 4110
2167	0 GET3	4134 4134 4137 4140
2204	0 GET4	4138 4106 4137 4138
2206	0 GET5	4141 4098 4101 4141
5434	0 IBUF	3935 3910 3921 3931 3935 3938
1674	0 ICHR	3739 3739 3749 3791 3855 3862 4329
1747	0 ICMD	3849 3849 3968
5	0 ILEN	3938 3931 3938 3939
2012	0 INP1	3904 3904 4001
2015	0 INP2	3910 3910 3911 3918
2032	0 INP3	3915 3911 3915
2041	0 INP4	3923 3923 6255
5462	0 ITAL	3931 3742 3922 3931
2220	0 KILL	4184 4067 4184 4212
1620	0 LCHR	3559 3475 3533 3535 3548 3559 3605 4024
1624	0 LOCF	3570 3570
1632	0 LOCT	3600 3572 3581 3600
1655	0 LOG1	3629 3629 3630
1670	0 LOG2	3634 3626 3630 3634
1543	0 LOGC	3469 3469 4000 4020 4042 4145 4154 4371 5525 5529 6008 6016 6017
1517	0 LOGS	3429 3429 3903 3999 5524 6007 6251
1560	0 LOGX	3510 3510 3998 4372 5531 6023
4731	0 OPMS	4147 4145 4147
2332	0 PERI	4324 4090 4185 4228 4273 4324
2265	0 REL1	4244 4244
2273	0 REL2	4254 4236 4240 4254
5460	0 RFLG	3763 3740 3756 3763 3828 3834 3865 3926 3956 3966 4337
2277	0 STRT	4272 4071 4272 4302
4700	OBLNKS	3874 3851 3874
2075	OCMDER	4019 3973 4019 4043
5	OCMDLN	4073 3971 4073
12	OCMDLX	4072 4072 4073
4717	OCMDTB	4061 3972 4061 4072
1775	OENTER	3890 3890 5352
5526	OERCMD	3984 3924 3969 3984 4021
0	OFF	37 37
2105	OFORER	4041 3827 4041 4340 4348
40	OIBFSZ	3930 3905 3930 3939
1713	OICHR1	3755 3741 3755
1716	OICHR2	3758 3747 3758
1711	OICHR3	3753 3753 3759

OCTAL	SYMBOL	REFERENCES BY ALTER NO.
6327	PLOC	6220 6199 6211 6220 6232
5107	PP	6371 6134 6371 6374
2200	PPES	6375 6134 6375
14	PPTS	6374 6134 6374
6341	PSEGO	6225 6197 6209 6225
6342	PSEG1	6226 6226
6343	PSEG2	6227 6227
6344	PSEG3	6228 6228
6	Q J	
7	Q L	2352 2386 2585 2601 2612 2623 2634 2646
5	Q Q	2350 2585 2601 2612 2623 2634 2646
1	Q T	2346 2379
2	Q X	2350 2380 2388 2389 2395 2397 2451 2453 2454 2497 2498 2503 2504 2513 2517
		2522
3	Q Y	2350 2509 2512 2515 2522
4	Q Z	2350
5140	Q OP	2586 2586 3431 3517
1103	Q DEQ	2440 2440 2585 2601 2612 2623 2634 2646
1031	Q ENQ	2338 2338 2585 2601 2612 2623 2634 2646
1130	Q INV	2493 2493 2585 2601 2612 2623 2634 2646
20	Q LEN	678 678
13	Q MAX	673 673 674 2341 2343
17	Q ABBR	677 677 678 4100
12	Q BUSY	671 671 673 2344 2346 2377 2379 2442 2444 2452 2971
5200	Q CORE	2612 2612 2767 2807 2971 3049 3099
1053	Q ENQ1	2352 2345 2352
1061	Q ENQF	2376 2376
5220	Q INP1	2623 2623 4782 5339 5345
5240	Q INP2	2634 2634 4780 5758
5260	Q INP3	2646 2646 4784 5756
1146	Q INV1	2521 2499 2505 2521
1	Q LAST	666 666 667 1173 1175 2389 2405 2446 2456 2459 2498 2504 2515 2517 2585 2601
		2612 2623 2634 2646 4831 4870 4932 4935 5160 5164 5167
3	Q LINK	682 682 2397 2453 2509 2511 2513 2514 4872 4921 4934 4941 5171
5140	Q OPOD	2585 2585 2586 4100
5160	Q TASK	2601 1173 1175 2455 2601 2975 3891 4205 4295 4570 4927 5159 5160 5162 5164 5166
		5167 5172 5781 5788 5837 5946 5978 6255 6265 6271 6277 6283 6288 6295 6307
		6314
2	Q XADD	667 667 668 2356 2455 2975 3891 4205 4295 4570 4927 5760 5781 5788 5837 5946
		5978 6255 6265 6271 6277 6283 6288 6295 6307 6314
6	Q XDEQ	669 669 670 2807 3099 3517
4	Q XENG	668 668 669 2767 3049 3431
10	Q XINV	670 670 671
5	Q	733 733 2339 2340 2341 2343 2344 2346 2356 2377 2379 2388 2389 2395 2396 2404
		2405 2441 2442 2444 2445 2446 2448 2452 2454 2456 2458 2459 2497 2498 2503
		2504 2512 2515 2517 4830 4831 4833 4870 4932 4935 5181 5743 5756 5758 5760
14	QAVAIL	674 674 675 2339 2340 2441
100	QBFSZ	652 652 653 5071 5660 5888 5927
4400	QELSZ	653 653
1067	QENQFO	2385 2378 2385

OCTAL	SYMBOL	REFERENCES BY ALTER NO.									
1245	PMORE1	2861	2861								
1450	RMREQ1	3133	3133	3134							
1463	RMREQ2	3135	3134	3135							
10	RMTMAX	4750	4686	4750							
5600	RMTTAB	4686	4610	4686							
5665	ROPFRN	4755	3432	3514	3629	3910	4755	6246			
1	ROPMAX	4751	4687	4751							
5610	ROPTAB	4687	4611	4687							
15	RPERTL	4753	4753								
4001	RQMAX	772	772	2773							
634	RQST	1613	1613								
5327	RQSTT	1623	1613	1616	1623						
1276	RRELC1	2924	2924	2940							
1306	RRELC2	2938	2916	2938							
1310	RRELC3	2952	2934	2952							
1350	RRELC4	2994	2953	2957	2994						
1344	RRELCX	2976	2968	2970	2972	2976					
2505	RRELP1	4554	4554	4555							
2515	RRELP2	4556	4555	4556							
2545	RRELP3	4571	4537	4571							
564	RRF	1415	1415	5900							
5323	RRFT	1428	1415	1418	1419	1420	1421	1428			
2751	RSCPAL	5010	4901	5010							
2735	RSCPCK	4973	4857	4973	4994	4997					
2751	RSCRAL	5011	4902	5011							
2735	RSCRCK	4974	4858	4974							
5671	RS.320	5035	5035	5751							
5670	RS.512	5034	5034	5748							
2603	RSKDR0	4834	4834	4874							
2612	RSKDR1	4845	4845	4867							
2635	RSKDR2	4864	4864	4977	4978						
2640	RSKDR3	4868	4868	4979	4996						
2645	RSKDR4	4878	4847	4878							
2657	RSKDR5	4890	4890	4915							
2666	RSKDR6	4898	4898	4914							
2700	RSKDR7	4908	4908	5014							
2707	RSKDR8	4919	4892	4919							
2722	RSKDR9	4940	4933	4940							
2724	RSKDRX	4946	4832	4871	4936	4946					
2575	RSKEDR	4824	4781	4783	4785	4824					
2753	RSLPAL	5017	4903	5017							
2741	RSLPCK	4989	4859	4989							
2751	RSMTAL	5012	4904	5012	5022	5023	5025				
2735	RSMTCK	4975	4860	4975							
7	RSPARE	4658	4658	4659							
1	RSTATE	4651	4125	4198	4288	4562	4651	4652			
4744	RTABCP	4607	4607	4617							
4745	RTABCR	4608	4608	4618							
4743	RTABLE	4605	4449	4605	4617	4618	4619	4620	4621	4848	5553
4746	RTABLP	4609	4609	4619							

OCTAL	SYMBOL	REFERENCES BY ALTER NO.
160	X REG	852 852 1113 1117 1122
36	X XEC	822 822
5304	X CKIC	1128 1110 1124 1128
474	X CKPT	1109 1109 1259 1313 1368 1423 1479 1525 1573 1618 1668 1716 1773 1817 1867 1911
52	X DATE	838 838 883
170	X DBGQ	855 852 855 1119 1121
16	X LOCK	806 806
40	X REGS	832 832 877
55	X SBAR	841 841 939 940
30	X STRT	816 816
470	X TERM	954 908 929 951 954 6027
53	X TIME	839 839
2	X	730 730 2453 2509 2514 2802 2805 2806 2818 2820 2907 2915 2926 2931 2975 3002
		3005 3078 3082 3093 3891 3972 3974 3976 4103 4116 4117 4118 4125 4132 4198
		4205 4288 4295 4349 4350 4523 4526 4529 4535 4547 4548 4550 4562 4570 4577
		4852 4881 4976 4992 5021 5069 5080 5112 5115 5171 5172 5181 5325 5621 5622
		5623 5627 5629 5632 5636 5638 5673 5674 5676 5677 5678 5679 5683 5686 5689
		5691 5723 5725 5728 5746 5781 5788 5826 5837 5923 5924 5926 5928 5936 5937
		5943 5945 5946 5959 5960 5978 5979 5980 5982 6255 6265 6271 6277 6283 6288
		6295 6307 6314
5310	XCKREG	1130 1112 1123 1130
12	XCOMND	802 802
20	XCONCT	808 808
20	XDBGON	853 853 855 1119
370	XFAULT	874 793 795 797 799 801 803 805 807 809 811 813 815 817 819 821
		823 874
32	XOFLOW	818 818
22	XPARTY	810 810
56	XPATCH	846 846
10	XTIMER	800 800
414	XTRAP1	902 889 902 903 905 915 936
435	XTRAP2	924 914 924 925
461	XTRAP3	949 935 949 950
3	Y	731 731 2511 2513 2804 2821 2932 2955 3866 4846 4850 4891 4910 4923 4934 5181
		5636 5644 5649 5746 5783 5793
4	Z	732 2783 2786 2793 2794 2795 2800 2801 2803 2928 2929 2930 2954 2995 2997
		3003 3007 3009 3067 3070 3076 3083 3088 3090 3092 4096 4099 4114 4121 4127
		4152 4191 4200 4234 4238 4246 4247 4279 4290 4354 4449 4455 4457 4474 4531
		4541 4542 4543 4544 4565 4848 4855 5181 5527 5558 5560 5637 5643 5645 5647
		5648 5683 5684 5685 5686 5687 5688 5689 5690
0	ZCODE	108 108 6400
4620	ZCODEL	6400 6400 6424
4620	ZCONS	112 112 6406
320	ZCONSL	6406 6406 6424
5140	ZQSTR	116 116 6411
140	ZQSTRL	6411 6411 6424
5300	ZSTOR	120 120 6421
1100	ZSTORL	6421 6421 6424
6350	ZTOPO	6428 777 6428

OCTAL SYMBOL REFERENCES BY ALTER NO.

7000	ZTOP	6430	778	6430	6431		
400	ZZ1	6431	775	6060	6431	6433	6439
7377	ZZ	6429	6429	6430			

** 22862 WORDS OF MEMORY WERE USED BY GMAP FOR THIS ASSEMBLY.

APPENDIX III

Listing of peripheral driver prototype -
Line Printer and Card Punch module are
implemented.

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PAGE	7	BIT AND STATUS DEFINITIONS
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PAGE	12	JOB CONTROL BLOCK DESCRIPTION
PAGE	14	NOTIFY BLOCK DESCRIPTION
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PAGE	43	NOTIFY MACRO
PAGE	44	CAUSE MACRO
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PAGE	56	QUEUE MANAGEMENT -- V (UP)
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PAGE	59	QUEUES -- QSCORE QUEUE
PAGE	60	CORE MANAGEMENT -- GENERAL INTRODUCTION
PAGE	61	CORE MANAGEMENT -- MACROS
PAGE	62	CORE MANAGEMENT -- ALLOCATION
PAGE	65	CORE MANAGEMENT -- REQUEST MORE
PAGE	66	CORE MANAGEMENT -- DE-ALLOCATION
PAGE	70	CORE MANAGEMENT -- MEMORY RELEASE
PAGE	72	CORE MANAGEMENT -- MEMORY REQUESTS
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PAGE	75	TRAP MANAGEMENT -- RELT

PAGE	76	JOB CONTROL BLOCK MANAGEMENT -- DESCRIPTION
PAGE	77	JOB CONTROL BLOCK MANAGEMENT -- GETJ
PAGE	78	JOB CONTROL BLOCK MANAGEMENT -- RELJ
PAGE	79	PERIPHERAL MANAGEMENT -- DESCRIPTION
PAGE	80	PERIPHERAL MANAGEMENT -- GETP
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PAGE	89	RESOURCE ALLOCATION -- PERIPHERAL DEVICE TABLE
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PAGE	96	COMMUNICATIONS -- CTAP SERVICE
PAGE	97	COMMUNICATIONS -- RE-ISSUE NOTIFY
PAGE	98	COMMUNICATIONS -- NOTIFY CONTROL BLOCKS
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PAGE	101	COMMUNICATIONS -- COMMAND GET
PAGE	102	COMMUNICATIONS -- COMMAND KILL
PAGE	103	COMMUNICATIONS -- COMMAND RELEASE
PAGE	104	COMMUNICATIONS -- COMMAND RESTART
PAGE	106	COMMUNICATIONS -- PERIPHERAL INFORMATION (PERI)
PAGE	107	JOB INITIALIZATION ROUTINE
PAGE	111	JOB PROCESSING -- DIJKSTRA IMPLEMENTATION
PAGE	112	READ TASK -- FILL
PAGE	115	READ TASK -- BANNERS

PAGE 117 READ TASK -- MAIN
PAGE 118 WRITE TASK -- WRITE
PAGE 122 WRITE TASK -- 320 OUTPUT MODE
PAGE 125 WRITE TASK -- 320 FORMAT CONVERSIONS
PAGE 126 WRITE TASK -- 320 OUTPUT PARAMETER ROUTINES
PAGE 128 WRITE TASK -- 512 OUTPUT MODE
PAGE 129 WRITE TASK -- EMPTY
PAGE 132 WRITE TASK -- MAIN
PAGE 135 INITIALIZATION
PAGE 138 ASSEMBLY CONTROL CARDS
PAGE 139 THERE WERE NO WARNING FLAGS IN THE ABOVE ASSEMBLY

ASSEMBLY CONTROL CARDS

	1	TTLs	ASSEMBLY CONTROL CARDS
	2	TTL	LINE PRINTER/ CARD PUNCH MODULE
	3	HEAD	
	4 *		
	5 *		
	6 *		HOUSEKEEPING CARDS
	7 *		
	8 *		
	9	PCC	ON PRINT CONTROL CARDS
	10	PMC	ON EXPAND MACROS
	11	DETAIL	ON EXPAND OCT,DEC,BCI,ASCII,DUP,ETC.
	12	DETAIL	OFF
000000	13	PCC	OFF
	19	ORG	0 SET ORIGIN
	20	HEAD	AND UNHEADED
	21	LBL	LPCP,LPCP LINE P-RINTER/ C-ARD P-UNCH MODIILE
	22 *		
	23 *		
	24 *		
	25 *	SQUASH	ASSEMBLER BUG
001000 400013	26 *		
	27	MME OPD	012/0010.6/,02/2.6/,6/,02/2.02/3
	28 *		
	29 *		
	30 *		
	31 *		DEBUGGING AIDS
	32 *		
	33 *		IF THE DEBUG SWITCH IS SET ON, THEN THE DEBUG MACROS
	34 *		WILL BE EXPANDED. OTHERWISE NOT.
	35 *		
000000	36	OFF EQU	0 OFF SWITCH
000001	37	ON EQU	1 ON SWITCH
	38 *		
000001	39	DBG SET	ON INITIALLY ON
	40 *		
	41 *		
	43 *		
	44	TTL OPSYN TTLs	FOR SYSPROG PPRNT

ASSEMBLY DECK SETUP

```

46          HEAD
47 *
48 *
49 *
50 *
51 *
52 *
53 *
54 *
55 *
56 *
57 *
58 *
59 *
60 *
61 *
62 *
63 *
64 *S      SNUMB  MBR
65 *S      IDENT  RUBENS,MICHAEL
66 *S      GMAP   COMDK,DECK
67 *S      TAPE   G*,X1D,,TAPE#A,,LPCPA
68 *S      TAPE   *1,X2R
69 *S      TAPE   K*,X3D,,,,LPCPB
70 *S      TAPE   P*,X4S,,,,LPCPPRINT
71 *S      ASCII  A*,BCDT,RUBENS/ALTERS
72 *S      SYSPROG PPRNT
73 *S      TAPE   IN,X4D,,,,LPCPPRINT
74 *S      PRMFL  A1,S,R,RUBENS/LPCP-SYM-TAB-A
75 *S      COMMENT PLEASE PRINT P* 'LPCPPRINT' FOR MBR.  THANK.
76 *S      ENDJOB
77 *SEOD
    
```

ASSEMBLY DECK SETUP

```

GMAP ASSEMBLY DECK SETUP -- THE DECK SETUP FOR ASSEMBLING THE
THIS PROGRAM (HEREAFTER LPCP) IS AS FOLLOWS.  ALL CONTROL
CARDS BEGIN WITH A 'S'.  THE FOLLOWING DECK WILL PRODUCE
A NEW K* TAPE, A P* TAPE FOR THE LISTING AND IS ALSO USED
TO PRODUCE A SYMBOL TABLE FOR DDT.  THE SECOND ACTIVITY,
SYSPROG PPRNT, CREATES THE SYMBOL TABLE AND STORES THE
RESULTS IN THE FILE 'RUBENS/LPCP-SYM-TAB.  TO DO AN ASSEMBLY
SIMPLY SUBMIT THE FOLLOWING PROGRAM TO TECOS:
SOURCE RUBENS/<NAME-OF-DECK>]RUN]EXIT
ALTER CARDS SHOULD BE SAVED PREVIOUSLY IN FILE 'RUBENS/ALTERS'
    
```


BINDER DECK SETUP

```
79          HEAD
80 *
81 *
82 *
83 *          BINDER DECK SETUP
84 *
85 *          THE BINDER DECK SETUP -- THE DECK SETUP FOR LOADING THE
86 *          ASSEMBLED PROGRAM INTO THE R & DC SYSTEM IS AS FOLLOWS. NOTE
87 *          THAT THE DECK IS LOADED VIA 'MULTIBINDER' AND NOT THE USUAL
88 *          'BINDER'. THIS IS NECESSARY IF BOTH MULTI-SEGMENTS AND USE
89 *          COUNTERS ARE TO BE USED.
90 *
91          DCARD 3,S
92 $BUILD SPAWNSYS,RURENS,MULTIBINDER
93 $      FNAME  */XLPCPA
94 $      OBJECT
```

LOCATION COUNTERS

96 HEAD

97 *

98 *

99 *

100 *

101 *

102 *

103 *

104 *

105 *

LOCATION COUNTERS

THE FOLLOWING PREDEFINES THE ORDER IN WHICH THE LOCATION COUNTERS WILL OCCUR, INDEPENDENT OF THE ORDER IN WHICH THEY ARE USED WITHIN THE PROGRAM.

000000

000000

003060

003060

003260

003260

003320

003320

000000

106 USE

107 ZCODE BSS

108 *

109 *

110 USE

111 ZCONS BSS

112 *

113 *

114 USE

115 ZQSTR BSS

116 *

117 *

118 USE

119 ZSTOR BSS

120 *

121 *

122 USE

CODE

0

CONST

0

QSTOR

0

STORE

0

CODE

MAIN PROGRAM SEGMENT

STORAGE FOR CONSTANTS, TABLES

FOR ALL QUEUES

FOR ALL DYNAMIC STORAGE

CODE LOCATION COUNTER INITIALLY

DEFINITIONS OF EXECUTIVE SYSTEM CONSTANTS

124 HEAD

125 *

126 *

127 *

EXECUTIVE CONSTANTS

128 *

129 *

130 * THE DEFINITIONS OF SYSTEM PARAMETERS SUCH AS MME NUMBERS
 131 * ARE INDICATED BY SYMBOLS WHICH START WITH A DECIMAL POINT.
 132 * THEY ARE NOT HEADED SO A TYPICAL REFERENCE IS LDX X0,\$.OPEN,DU.

133 *

134 * DEFINITION OF MME NUMBERS

135 *

000000	136 .PRIV EQU	0	MICRO-CODED PRIVILEGED PRIMITIVE
000001	137 .SETFV EQU	1	SET UP FAULT VECTOR
000002	138 .SETSQ EQU	2	SET UP SQUEEZE MODE
000003	139 .SQUEZ EQU	3	ENTER SQUEEZE MODE
000004	140 .READ EQU	4	READ
000005	141 .APPEND EQU	5	APPEND
000006	142 .RRF EQU	6	READ RANDOM FILE
000007	143 .WRF EQU	7	WRITE RANDOM FILE
000010	144 .SCR EQU	8	SCRATCH
000011	145 .SPTR EQU	9	SET POINTER
000012	146 .RQST EQU	10	REQUEST STATUS
000013	147 .RQDT EQU	11	REQUEST DATE AND TIME
000014	148 .RQRT EQU	12	REQUEST ELAPSED RUN TIME
000015	149 .SPAWN EQU	13	SPAWN
000016	150 .TERM EQU	14	TERMINATE
000017	151 .PAUSE EQU	15	PAUSE
000020	152 .OPSEG EQU	16	OPEN SEGMENT
000021	153 .CLSEG EQU	17	CLOSE SEGMENT
000022	154 .CHSEG EQU	18	CHANGE SEGMENT LENGTH
000023	155 .EXSEG EQU	19	EXCHANGE TWO SEGMENTS
000024	156 .OPEN EQU	20	OPEN
000025	157 .CLOSE EQU	21	CLOSE
000026	158 .CATLG EQU	22	CATALOGUE
000027	159 .DESTR EQU	23	DESTROY
000030	160 .OPENS EQU	24	OPEN SCRATCH
000031	161 .UPDAT EQU	25	UPDATE
000032	162 .CATDR EQU	26	CATALOGUE DIRECTORY
000033	163 .WRACL EQU	27	WRITE ACCESS CONTROL LIST
000034	164 .RDAQL EQU	28	READ ACCESS CONTROL LIST
000035	165 .RDDIR EQU	29	READ DIRECTORY
000036	166 .OPENW EQU	30	OPEN WORKING DIRECTORY
000037	167 .RDBRN EQU	31	READ BRANCH
000040	168 .RDLK EQU	32	READ LINK
000041	169 .WSINF EQU	33	WRITE SYSTEM INFORMATION
000042	170 .CATLK EQU	34	CATALOGUE LINK
000043	171 .WBRAN EQU	35	WRITE BRANCH
000044	172 .LOCK EQU	36	LOCK
000045	173 .UNLCK EQU	37	UNLOCK

DEFINITIONS OF EXECUTIVE SYSTEM CONSTANTS

000046	174	•NOTIF EQU	38	NOTIFY
000047	175	•CAUSE EQU	39	CAUSE
000050	176	•DELET EQU	40	DELETE EVENT
000051	177	•UNCAU EQU	41	UNCAUSE EVENT
000052	178	•OPSCE EQU	42	OPEN SCRATCH EVENT
000055	179	•MSTA EQU	45	SYSTEM STATUS MEASUREMENTS
000056	180	•RDME EQU	46	MEASURE READ ME
000057	181	•CRSG EQU	47	CREATE SEGMENT
000060	182	•WTME EQU	48	WRITE ME
000061	183	•WAMI EQU	49	WHO AM I
000063	184	•RGWD EQU	51	REQUEST WORKING DIRECTORY

BIT AND STATUS DEFINITIONS

	186	HEAD	B	
	187 *			
	188 *			
	189 *			
	190 *			BIT DEFINITIONS
	191 *			
	192 *			
	193 *			DEFINITIONS FOR REPEAT INSTRUCTIONS
	194 *			
002000	195 RPT	BOOL	002000	COUNT FIELD FOR RPT INSTRUCTIONS
001000	196 ABIT	BOOL	001000	INCREMENT FIRST INDEX REGISTER
000400	197 BBIT	BOOL	000400	INCREMENT SECOND INDEX REGISTER
000200	198 CBIT	BOOL	000200	LOAD C(X0) FROM REPEAT INSTRUCTION
000100	199 TZE	BOOL	000100	ZERO
000040	200 TNZ	BOOL	000040	NON ZERO
000020	201 TMI	BOOL	000020	NEGATIVE
000010	202 TPL	BOOL	000010	POSITIVE
000004	203 TRC	BOOL	000004	CARRY
000002	204 TNC	BOOL	000002	NO CARRY
000001	205 TOV	BOOL	000001	OVERFLOW
	206 *			
	207 *			
	208 *			DEFINITIONS FOR INDICATOR REGISTER
	209 *			
400000	210 ZER	BOOL	400000	ZERO INDICATOR BIT
200000	211 NEG	BOOL	200000	NEGATIVE
100000	212 CAR	BOOL	100000	CARRY
040000	213 OVF	BOOL	040000	OVERFLOW
020000	214 EQV	BOOL	020000	EXPONENT OVERFLOW
010000	215 EUN	BOOL	010000	EXPONENT UNDERFLOW
004000	216 OVM	BOOL	004000	OVERFLOW MASK -- ON PREVENTS OVERFLOW FAULTS
002000	217 TAL	BOOL	002000	TALLY RUNOUT
001000	218 PAR	BOOL	001000	PARITY ERROR
000400	219 PAM	BOOL	000400	PARITY ERROR MASK
000200	220 MOD	BOOL	000200	MASTER MODE
	221 *			
	222 *			
	223 *			DEFINITION FOR ACCESS BITS
	224 *			
000020	225 RD	BOOL	20	READ ACCESS
000010	226 WT	EQU	RD/2	WRITE
000004	227 AP	EQU	WT/2	APPEND
000002	228 EX	EQU	AP/2	EXECUTE
000001	229 LK	EQU	EX/2	LOCK
000037	230 ALL	EQU	RD+WT+AP+EX+LK	ALL
000004	231 NO	BOOL	4	NOTIFY
000002	232 CA	EQU	NO/2	CAUSE
	233 *			

B

BIT AND STATUS DEFINITIONS

	235 *				
	236 *				
	237 *			DEFINITIONS FOR EXEC STATUS RETURNS	
	238 *				
	239 *			LOGICAL STATUS CODE FOR I/O PRIMITIVES	
	240 *				
000000	241 OK	BOOL	00	OK	
000001	242 IFRN	BOOL	01	INVALID FRN	
000002	243 IACC	BOOL	02	INVALID ACCESS SPECIFIED	
000003	244 BZ	BOOL	03	EXECUTIVE TOO BUSY	
000004	245 IOP	BOOL	04	INVALID OPERATION FOR THIS DEVICE	
000005	246 IPTR	BOOL	5	COPY POINTER IS OUT OF BOUNDS	
000006	247 IREQ	BOOL	6	AMOUNT REQUESTED GREATER THAN FILE LENGTH	
000007	248 IELT	BOOL	07	ELEMENT SIZE IS NOT A MULTIPLE OF UNIT SIZE	
000011	249 IMOD	BOOL	11	INVALID MODE	
000012	250 HDWE	BOOL	12	HARDWARE ERROR -- OPERATION NOT COMPLETE	
000013	251 DBZ	BOOL	13	DEVICE UNAVAILABLE (HARDWARE)	
000016	252 EOF	BOOL	16	END-OF-FILE ENCOUNTERED	
000026	253 NSTR	BOOL	26	NO FILE STORAGE AVAILABLE	
000035	254 TRO	BOOL	35	TIMER RUN OUT ON OPERATOR'S CONSOLE	
	255 *				
	256 *				
	257 *			LOGICAL STATUS CODE FOR FILE AND EVENT PRIMITIVES	
	258 *				
000005	259 ITN	BOOL	5	INVALID NAME	
000007	260 UERR	BOOL	7	UNRECOVERABLE ERROR	
000011	261 TLE	BOOL	11	TIME LIMIT EXCEEDED	
000013	262 LOCK	BOOL	13	ITEM LOCKED	
000016	263 IPWD	BOOL	16	INVALID PASSWORD	
	264 *				
	265 *				
	266 *			LOGICAL STATUS CODES FOR CONTROL PRIMITIVES	
	267 *				
	268 *				
000004	269 IO	BOOL	4	I/O ACTIVITY IN PROGRESS	
	270 *				
	271 *				
	272 *			MISCELLANEOUS BITS	
	273 *				
000077	274 STMK	BOOL	77	STATUS BIT MASK	
400000	275 SIGN	BOOL	400000	SIGN BIT	
400000	276 TERM	BOOL	400000	TERMINATOR BIT	
200000	277 DELIM	EQU	TERM/2	DELIMINATOR	
100000	278 DIGIT	EQU	DELIM/2	DIGIT	
040000	279 OPR	EQU	DIGIT/2	OPERATOR	
	280 *				
	281 *				

B

BIT AND STATUS DEFINITIONS

Address	Bit	Type	Value	Description
	283 *			
	284 *			
	285 *			DEFINITIONS FOR JSFLAG
	286 *			
	287 *			JSFLAGS (LOWER)
	288 *			
400000	289 KILL	BOOL	BSSIGN	KILL THIS JOB
200000	290 RHDR	EQU	KILL/2	FAKE A READ OF A HEADER BUFFER
100000	291 WHDR	EQU	RHDR/2	WRITE A HEADER BUFFER
040000	292 ENDR	EQU	WHDR/2	END OF FILE REACHED ON READING
020000	293 ENDW	EQU	ENDR/2	END OF FILE REACHED ON WRITING
010000	294 HALT	EQU	ENDW/2	HALT READING TO DO SPTR (RESTART)
	295 *			
000400	296 LP	BOOL	400	LINE PRINTER
000200	297 CP	BOOL	200	CARD PUNCH
	298 *			
	299 *			JSFLAGS (UPPER)
	300 *			
001700	301 MODMK	BOOL	1700	MEDIA MASK FOR RCW (BITS 26 - 29)
740000	302 JOBMK	BOOL	740000	JOB NUMBER MASK (TOP 4 BITS)
000017	303 BJBMK	BOOL	17	4 BIT MASK
020000	304 HDRMK	BOOL	020000	HEADER MASK (ON = OUTPUT BANNER)
000001	305 BHDR	BOOL	1	HEADER BIT MASK
010000	306 OUTMK	BOOL	010000	OUTPUT TYPE MASK (OFF = 512; ON = 320)
000001	307 BOTMK	BOOL	1	OUTPUT BIT MASK
007777	308 SRTMK	BOOL	007777	START ADDRESS IN ELEMENTS OF JSBijFsz
007777	309 BSTMK	BOOL	7777	BIT MASK
	310 *			
	311 *			
	312 *			BITS RETURNED TO MONITOR
	313 *			
002000	314 GET	BOOL	002000	PERIPHERAL GOTTEN (NOT SENT)
004000	315 ABORT	BOOL	004000	PERIPHERAL ABORTED
006000	316 REL	BOOL	006000	PERIPHERAL RELEASED
010000	317 XXXX	BOOL	010000	NOT USED
012000	318 RSTRT	BOOL	012000	PERIPHERAL RESTARTED
014000	319 DONE	BOOL	014000	JOB FINISHED SUCCESSFULLY
016000	320 RDY	BOOL	016000	READY PERIPHERAL
	321 *			
	322 *			
	323 *			CARD PUNCH MODES
	324 *			
000000	325 CP0	BOOL	0	STATUS
000001	326 CP1	BOOL	1	ATTENTION
000002	327 CP2	BOOL	2	WRITE CARD BINARY
000003	328 CP3	BOOL	3	WRITE CARD HOLLERITH
000004	329 CP4	BOOL	4	WRITE CARD HOLLERITH EDITED

B

BIT AND STATUS DEFINITIONS

Bit	Label	Type	Value	Description
331 *				
332 *				
333 *				LINE PRINTER MODES
334 *				
000000	335 LP0	BOOL	0	STATUS
000001	336 LP1	BOOL	1	ATTENTION
000002	337 LP2	BOOL	2	WRITE EDITED CONTINOUS, NO SLEW
000003	338 LP3	BOOL	3	EDITED, NO SLEW
000004	339 LP4	BOOL	4	EDITED, SLEW 1 LINE
000005	340 LP5	BOOL	5	EDITED, SLEW 2 LINES
000006	341 LP6	BOOL	6	EDITED, SLEW TOP OF FORM
000007	342 LP7	BOOL	7	NON EDITED, NO SLEW
000010	343 LP10	BOOL	10	NON EDITED, SLEW 1 LINE
000011	344 LP11	BOOL	11	NON EDITED, SLEW 2 LINES
000012	345 LP12	BOOL	12	NON EDITED, SLEW TOP OF FORM
000013	346 LP13	BOOL	13	SLEW 1 LINE
000014	347 LP14	BOOL	14	SLEW 2 LINES
000015	348 LP15	BOOL	15	SLEW TOP OF FORM

B

TRAP BLOCK DESCRIPTION

	350	HEAD	T	
	351 *			
	352 *			
	353 *			TCB
	354 *			
	355 *			
	356 *	THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS		
	357 *	IN THE TRAP BLOCK (TBLOCK).		
	358 *			
000000	359 SRW1	EQU	0	FIRST STATUS RETURN WORD FROM EXEC
000001	360 SRW2	EQU	1	SECOND STATUS RETURN WORD
000002	361 RET	EQU	2	SAVED IC/IR WHEN EXEC SPRINGS TRAP
000003	362 XED	EQU	3	CONTROL IS TRANSFERRED HERE WHEN EXEC
	363 *			SPRINGS THE TRAP. IT CONTAINS AN XED
	364 *			OF A CHAIN WHICH LINKS THE TRAP TO THE
	365 *			MASTER TASK QUEUE.
000004	366 TRA	EQU	4	(UPPER) RESTART ADDRESS FOR TASKS ON
	367 *			ON A QUEUE (SUCH AS THE QSTASK)
	368 *			(LOWER) MAY BE USED TO SAVE RETURN
	369 *			FROM A REENTRANT ROUTINE
000005	370 LINK	EQU	5	(UPPER) LINK TO PREVIOUS TCB
000006	371 NCB	EQU	6	(UPPER) POINTER TO NCB
000006	372 JCB	EQU	NCB	(LOWER) POINTER TO JCB
000007	373 SPARE	EQU	7	SPARE
000030	374 LEN	EQU	24	LENGTH OF TCB (NICE IF MULTIPLE OF 8)
	375 *			
	376 *			
000027	377 TEMP1	EQU	LEN-1	TEMPORARY STORAGE AT END OF BLOCK
000026	378 TEMP2	EQU	TEMP1-1	MORE TEMPORARY STORAGE
000025	379 TEMP3	EQU	TEMP2-1	
000024	380 TEMP4	EQU	TEMP3-1	
000023	381 TEMP5	EQU	TEMP4-1	
000022	382 TEMP6	EQU	TEMP5-1	
000021	383 TEMP7	EQU	TEMP6-1	
000020	384 TEMP8	EQU	TEMP7-1	
	385 *			
	386 *	NO ONE EXCEPT RSGETC SHOULD USE TEMP9 - TEM16		
	387 *			
000017	388 TEM9	EQU	TEMP8-1	
000016	389 TEM10	EQU	TEM9-1	
000015	390 TEM11	EQU	TEM10-1	
000014	391 TEM12	EQU	TEM11-1	
000013	392 TEM13	EQU	TEM12-1	
000012	393 TEM14	EQU	TEM13-1	
000011	394 TEM15	EQU	TEM14-1	
000010	395 TEM16	EQU	TEM15-1	

T

JOB CONTROL BLOCK DESCRIPTION

```

397      HEAD      J
398 *
399 *
400 *
401 *
402 *
403 *
404 *
405 *
406 *
407 *
408 *
409 *
410 *
411 *
412 *
413 *
414 *
415 *
416 *
417 *
418 *
419 *
420 *
421 *
422 *

```

JCB

THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS
IN THE JOB CONTROL BLOCK (HERAFTER CALL JCB).

PLEASE NOTE THAT THE NUMBER OF JCB'S IS (AND MUST BE) EQUAL
TO THE NUMBER OF DEVICES AT MAX. THEREFORE IN ORDER TO
MAKE MORE EFFICIENT USAGE OF THE DYNAMIC BUFFER AREA
THE JCB'S ARE ALL PRE-ALLOCATED. THIS IS DONE FOR THE FOL-
REASONS:

- (1) SINCE A JCB IS ALLOCATED AT JOB INITIALIZATION AND IS
NOT RELEASED UNTIL JOB COMPLETION, WE DON'T WANT MEMORY
TIED DOWN THAT LONG.
- (2) ALSO WE DON'T WANT TO CREATE LARGE HOLES IN MEMORY (E.G.
A JCB GRABBING A RELEASED 320 WORD BLOCK
- (3) THERE ARE ONLY AS MANY JCB'S AS DEVICE (CURRENTLY 3)

THE FOLLOWING MACROS CAN BE USED TO MANAGE JCB'S:

GETJ	GETS A JCB	(C(XJ) = JCB-ADDRESS)
RELJ	RELEASES A JCB	(C(XJ) = DESTROYED)

J JOB CONTROL BLOCK DESCRIPTION

	424 *				
	425 *				
	426 *				
777777	427 ALLC	EQU	-1		ALLOCATED FLAG WORD (0 = FREE)
000000	428 FLAGS	EQU	0		FLAG BITS
000001	429 FLAG	EQU	1		TSSRW2 WHEN CAUSED
000002	430 IFRN	EQU	2		(UPPER) INPUT FRN
000003	431 OFRN	EQU	3		(UPPER) OUTPUT FRN
	432				(LOWER) PERIPHERAL UNIT NUMBER
000004	433 CARD1	EQU	4		FIRST PUNCH CARD (0 = MESSAGE TO OPER)
000005	434 BUFSZ	EQU	5		(LOWER) WORKING BUFFER SIZE
000006	435 RSTR	EQU	6		(LOWER) RESTART ELEMENT ADDRESS
000007	436 RCW	EQU	7		(UPPER) PTR TO NEXT RCW
	437				(LOWER) MUST BE ZERO
000010	438 XDATA	EQU	8		(UPPER) PTR TO END OF INPUT DATA
000011	439 EMPTY	EQU	9		(UPPER) RESTART ADDRESS FOR END
	440				(LOWER) SEMAPHORE -- NUMBER OF BUFFERS TO FILL
000012	441 FULL	EQU	10		(UPPER) RESTART ADDRESS FOR END
	442				(LOWER) SEMAPHORE -- NUMBER OF BUFFERS TO EMPTY
000013	443 RPTR	EQU	11		(UPPER) PTR TO NEXT INPUT BUFFER INDIRECT
	444				(LOWER) MUST BE ZERO
000014	445 WPTR	EQU	12		(UPPER) PTR TO NEXT OUTPUT BUFFER INDIRECT
	446				(LOWER) MUST BE ZERO
000015	447 RTASK	EQU	13		(UPPER) POINTER TO READ TASK
000015	448 WTASK	EQU	RTASK		(LOWER) POINTER TO WRITE TASK
000016	449 RES	EQU	WTASK+1		TYPE OF RESOURCE REQUIRED
000017	450 RTRY	EQU	RES+1		RETRY COUNTER
000020	451 RESET	EQU	RTRY+1		(UPPER) PTR TO END OF BUFFER QUEUE
	452				(LOWER) PTR TO START OF BUFFER QUEUE
	453				RESET MUST BE JUST BEFORE BUFFER QUEUE.
	454 *				
000002	455 N	EQU	2		NUMBER OF WORKING BUFFERS
000024	456 LEN	EQU	RESET+1+N-ALLC		JCB LENGTH
	457				NOT NECESSARY TO BE EVEN

J

NOTIFY BLOCK DESCRIPTION

463 HEAD C
 464 *
 465 *
 466 * NCB
 467 *
 468 *
 469 * THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS
 470 * IN THE NOTIFY BLOCK (ALIAS NCB).
 471 *
 472 * A NCB IS A TCB WITH EXTRAS.
 473 *

000000	474	SRW1	EQU	TSSRW1
000001	475	SRW2	EQU	TSSRW2
000002	476	RET	EQU	TSRET
000003	477	XED	EQU	TSXED
000004	478	TRA	EQU	TSTRA
000005	479	LINK	EQU	TSLINK
000005	480	RLINK	EQU	LINK
000006	481	NCB	EQU	TSNCB
000006	482	JCB	EQU	TSJCB
000007	483	SPARE	EQU	TSSPARE
000027	484	TEMP1	EQU	TSTEMP1
000026	485	TEMP2	EQU	TSTEMP2
000025	486	TEMP3	EQU	TSTEMP3
000024	487	TEMP4	EQU	TSTEMP4
000023	488	TEMP5	EQU	TSTEMP5
000022	489	TEMP6	EQU	TSTEMP6
000021	490	TEMP7	EQU	TSTEMP7
000020	491	TEMP8	EQU	TSTEMP8
000017	492	TEM9	EQU	TSTEM9
000016	493	TEM10	EQU	TSTEM10
000015	494	TEM11	EQU	TSTEM11
000014	495	TEM12	EQU	TSTEM12
000013	496	TEM13	EQU	TSTEM13
000012	497	TEM14	EQU	TSTEM14
000011	498	TEM15	EQU	TSTEM15
000010	499	TEM16	EQU	TSTEM16
000030	500	ERN	EQU	TSLEN
000031	501	STATE	EQU	ERN+1
000032	502	MESS	EQU	STATE+1
000033	503	RES	EQU	MESS+1

(LOWER) RESTART AFTER NOTIFY

(UPPER) EVENT FRN
 STATE
 36 BITS MESSAGE (OPTIONAL)
 TYPE OF RESOURCE REQUIRED (LP/ CP)

C

NOTIFY BLOCK DESCRIPTION

```

505 *
506 *
507 *      THIS MACRO IS USED TO GENERATE THE NECESSARY NOTIFY CONTROL
508 *      BLOCKS FOR THE COMMUNICATIONS NETWORK.
509 *
510 *
511 *
512 NCB    MACRO LABEL,RESTART-ADD,ERN,STATE,MESSAGE,RESOURC-TYPE,PER-BITS
513 USE    STORE
514      EIGHT
515 #1     BSS      0          LABEL
516      ZERO     0,B$TRO    SRW1; SIMULATE A TIMER RUNOUT
517      ZERO     0,0        SRW2
518      ZERO     0,0        RET
519      ZERO     0,0        XED
520      ZERO     **,0       TRA
521      ZERO     0,#2       LINK/ RESTART
522      ZERO     *-C$NCB,+-C$JCB *NCB/ JCB POINT TO THEMSELVES
523      DEC      0
524      DUP      1,16      TEMP1 THRU TEM16
525      DEC      0
526      ZERO     #3,        ERN
527      ZERO     #4,0       STATE
528      VFD      36/#5     MESSAGE
529      ZERO     #6,#7     RESOURCE TYPE / PERIPHERAL BIT
530      USE      PREVIOUS
531      ENDM    NCB
    
```

C

QUEUE MANAGEMENT DEFINITIONS

	533	HEAD	Q		
	534 *				
	535 *				
	536 *			QCB	
	537 *				
	538 *				
	539 *	THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS			
	540 *	IN A QBLOCK GENERATED BY THE QUEUE MACRO.			
	541 *				
000000	542	FIRST	EQU	0	POINTER TO FIRST BLOCK OF QUEUE
000001	543	LAST	EQU	FIRST+1	POINTER TO LAST BLOCK OF QUEUE
000002	544	XADD	EQU	LAST+1	INSTRUCTION PAIR FOR ADDING A BLOCK
000004	545	XENQ	EQU	XADD+2	INSTRUCTION PAIR FOR ENQUEUEING
000006	546	XDEQ	EQU	XENQ+2	INSTRUCTION PAIR FOR DEQUEUEING
000010	547	BUSY	EQU	XDEQ+2	RESPONSIBLE BLOCK IF QUEUE IS BUSY
	548				ZERO OTHERWISE
000011	549	MAX	EQU	BUSY+1	MAXIMUM NUMBER OF ITEMS ASSOCIATED WITH Q
000012	550	AVAIL	EQU	MAX+1	NUMBER OF ITEMS CURRENTLY AVAILABLE
000013	551	SPAR1	EQU	AVAIL+1	SPAR1
000014	552	SPAR2	EQU	SPAR1+1	SPAR2
000015	553	ABBR	EQU	SPAR2+1	ASCII ABBREVIATION OF QUEUE
000016	554	LEN	EQU	ABBR+1	LENGTH OF QUEUE (WISE TO KEEP EVEN)
	555 *				
	556 *				
000004	557	OFFST	EQU	4	OFFSET FOR QUEUE POINTER
000003	558	LINK	EQU	OFFST-1	FORWARD LINK POINTER

Q

CORE MANAGEMENT DEFINITIONS

560 HEAD R

561 *

562 *

563 *

564 *

565 *

566 *

567 *

568 *

CCB

THESE SYMBOLS ARE USED IN REFERENCING SPECIFIED WORDS IN A BLOCK ON THE FREE MEMORY LIST.

000000

000000

000001

569 LINKF EQU 0

570 LEN EQU LINKF

571 LINKB EQU LINKF+1

POINTER TO SUCCESSOR (UPPER)
TOTAL LENGTH (IN WORDS) OF BLOCK (LOWER)
POINTER TO PREDECESSOR

R

GLOBAL DEFINITIONS

573 HEAD
 574 *
 575 *
 576 * GLOBALS
 577 *
 578 * HEAD SYMBOL USAGE
 579 *
 580 HEAD GLOBAL AND EXEC CONSTANTS
 581 HEAD B GENERAL PURPOSE BITS
 582 HEAD C COMMUNICATIONS ROUTINES
 583 HEAD J JCB SYMBOLS AND ROUTINES
 584 HEAD Q QUEUE SYMBOLS AND ROUTINES
 585 HEAD R RESOURCE ALLOCATION
 586 HEAD S STATISTICS COUNTERS
 587 HEAD T TRAP SYMBOLS AND ROUTINES
 588 HEAD X DIAGNOSTIC ROUTINES
 589 HEAD

590 *
 591 *
 592 * INDEX REGISTER DEFINITIONS

593 *
 594 * THE SYMBOLIC INDEX REGISTERS USED IN THIS PROGRAM ARE
 595 * ONE CHARACTER SYMBOLS, DEFINED UNDER EACH HEAD SYMBOL
 596 * IN USE IN THE PROGRAM. INDEX REGISTER 0 IS SPECIAL,
 597 * SINCE IT IS USED FOR REPEAT INSTRUCTIONS, SO IT IS NOT
 598 * SYMBOLIC.
 599 *

600 HEAD O,C,J,Q,R,T,X
 601 *

000001	602 T	EQU	1	TRAP BLOCK POINTER
000002	603 X	EQU	2	TEMP
000003	604 Y	EQU	3	TEMP
000004	605 Z	EQU	4	TEMP
000005	606 Q	EQU	5	QUEUES AND GENERAL USE
000006	607 J	EQU	6	JOB NUMBER
000007	608 L	EQU	7	LINK REGISTER FOR SUBROUTINE CALLS

609 *
 610 *
 611 * OTHER GLOBAL SYMBOLS
 612 *

777777	613	HEAD		
525200	614 ERROR	EQU	-1	USED TO GENERATE MEMORY FAULTS
525200	615 BUG	BOOL	525200	BUGGING QUANTITY
001000	616 BUGBUG	SET	BUG	
000777	617 MQUAN	EQU	512	QUANTUM INCREMENT FOR MEMORY REQUEST
000040	618 CKMK	BOOL	777	STATUS MASK (9 BITS)
777700	619 TALYB	BOOL	40	TALYB BIT
000100	620 TALMK	BOOL	777700	MASK FOR TALLY COUNT FIELD
000005	621 TAL	BOOL	100	TALLY DISPLACEMENT
	622 RTMAX	EQU	5	RETRY ERROR ONLY 5 TIMES

GLOBAL CONSTANTS

```

624          HEAD
625 *
626 *
627 *
628 *
629 *
630 *
631 *
632 FRNO    EQU    0          INPUT EVENT FRN
633 FRN1    EQU    1          OUTPUT EVENT FRN
634 FRN2    EQU    2          INPUT PASS EVENT FRN
635 *
636 *
637 *
638 CPST    BOOL   1000      CP STATE C(AU)
639 LPST    BOOL   3000      LP STATE C(AU)
640 *
641 *
642 *
643 *
644 *
645          USE    CONST
646 DTN      UASCI   6,RUBENS
647          UASCI   6,LPCPDUMP
648 DTSZ     EQU    *-DTN     TREE-SIZE
649 DESZ     EQU    36*512    ELEMENT SIZE (ONE PAGE OF CORE)
650 DACC     BOOL
651          USE    PREVIOUS
652 *
653 *
654 *
655 *
656 BUFSEG   EQU    0          SEGMENT WHERE DYNAMIC BUFFER IS LOCATED
657 1K       EQU    1024      ONE K DECIMAL
658 RMAX     EQU    2*1K+1    MAX MEMORY REQUEST/SHOT
659 *
660          USE    STORE
661 AVAIL    ZERO    ZZ1.0     MEMORY AVAILABLE
662 MEMRQ    ZERO    MQUAN.0   MEMORY REQUIREMENT/ NEED ONE UNIT ALWAYS
663 MTOPO    ARG     ZTOPO     END OF PROGRAM
664 MTOP     ARG     ZTOP      TOP OF MEMORY
665          USE    PREVIOUS

```

GLOBALS

COMMUNICATIONS FRN'S

COMMUNICATIONS STATES

DUMPFILe PARAMETERS

MEMORY MANAGEMENT PARAMETERS

LOW CORE ALLOCATION -- FAULT VECTOR

000000	000000	667	USE	CODE	
		668	HEAD	X	
		669 *			
		670 *			
		671 *			FAULT VECTOR
		672 *			
		673 *	CONSIDER ALL FAULTS FATAL		
		674 *			
	000000	675	ORG	0	IN CASE OF PRECEEDING ERRORS, FORCE ZERO
000000	002750	676	FV	TRA	ANITIALIZATION; 0 = SHUTDOWN
000001	000370	677		XED	
000002	000000	678	MEM	ZERO	1 = MEMORY
000003	000370	679		XED	
END OF BINARY CARD	LPCP0003				
000004	000000	680	MME	ZERO	2 = MASTER MODE ENTRY
000005	000370	681		XED	
000006	000000	682	FT	ZERO	3 = FAULT TAG
000007	000370	683		XED	
000010	000000	684	TIMER	ZERO	4 = TIMER RUNOUT
000011	000370	685		XED	
000012	000000	686	COMND	ZERO	5 = COMMAND
000013	000370	687		XED	
000014	000000	688	DRL	ZERO	6 = DERAIL
000015	000370	689		XED	
000016	000000	690	LOCK	ZERO	7 = LOCKUP
000017	000370	691		XED	
000020	000000	692	CONCT	ZERO	8 = CONNECT
000021	000370	693		XED	
000022	000000	694	PARTY	ZERO	9 = PARITY
000023	000370	695		XED	
000024	000000	696	DP	ZERO	10 = ILLEGAL OP CODE
000025	000370	697		XED	
000026	000000	698	ONC	ZERO	11 = OPERATION NOT COMPLETE
000027	000370	699		XED	
000030	000000	700	STRT	ZERO	12 = STARTUP
000031	000370	701		XED	
END OF BINARY CARD	LPCP0004				
000032	000000	702	OFLOW	ZERO	13 = OVERFLOW
000033	000370	703		XED	
000034	000000	704	DIV	ZERO	14 = DIVIDE CHECK
000035	000370	705		XED	
000036	000000	706	XEC	ZERO	15 = EXECUTE
000037	000370	707		XED	

X

LOW CORE ALLOCATION -- DEBUG STORAGE

			709 *		
			710 *		
			711 *		DEBUG STORAGE
			712 *		
			713 *		STORAGE FOR REGISTERS AND IC ON CRASH
			714 *		
		000040	715	EIGHT	
		000040	716 REGS	BSS 8	STORAGE FOR CRASH REGISTERS
000050	000000	000000	717 IC1	ZERO	IC BEFORE FAULT
000051	000000	000000	718 IC	ZERO	IC AT FAULT
			719 *		
			720 *		DATE AND TIME OF CRASH
			721 *		
		000052	722 DATE	BSS 1	DATE OF CRASH
		000053	723 TIME	BSS 1	TIME OF CRASH
000054	001101070701		724 DATEA	DATE	ASSEMBLY DATE
000055	000000 0000 00		725 SBAR	ARG 0	BAR SETTING WHEN CRASHED
			726 *		
			727 *		PATCH AREA
			728 *		
		000056	729	EVEN	
		000056	730 PATCH	BSS 64	LEAVE LOTS OF ROOM
			731 *		
			732 *		STORAGE FOR DEBUGGING QUEUE
			733 *		
		000160	735	EIGHT	QUASH STUPID ASSEMBLER BUG.
000160	000170	0000 00	736 REG	ARG DBGQ	POINTER TO NEXT ENTRY
		000020	737 DBGQN	EQU 16	NUMBER OF ENTRIES
		000170	738	EIGHT	
		000170	739 DBGQ	BSS 8*DBGQN	RESERVE SPACE

X

DIAGNOSTICS

		000370	741	USE	CODE	
			742	HEAD	X	
			743 *			
			744 *			
			745 *			DIAGNOSTICS
			746 *			
			747 *			THIS SECTION IS ENTERED FROM THE FAULT VECTOR IN THE
			748 *			EVENT OF A PROGRAMMING ERROR. THEN REGISTERS ARE
			749 *			PRESERVED. THE ENTIRE PROGRAM IS WRITTEN OUT INTO
			750 *			THE FILE RUBENS/LPCPDUMP.
			751 *			
			752 *			CONSIDER ALL FAULTS FATAL.
			753 *			
			754 *			ENTER FROM THE FAULT VECTOR BY
			755 *			XED X\$FAULT
			756 *			
		000370	757	EVEN		
		000370	758	FAULT	BSS	0
			759		STC1	IC
		000370	760		TRA	*+1
		000051	761		SREG	REGS
		5540	762		LDX	0,IC
		00				ENTRY POINT
		000371				SAVE IC AND IR
		000372				BREAK XED
		000040				SAVE REGISTERS
		7530				FIND IC+1 AT FAULT
		00				
END	OF	BINARY	CARD	LPCP0005		
		000374	763		LDA	-2,0
		777776	764		STA	IC1
		2350	765		LDX	0,\$.RGDT,DU
		10	766		MME	
		000375	767		STAQ	DATE
		000050				SAVE DATE AND TIME
		7550				
		00				
		000376				
		000013				
		2200				
		03				
		000377				
		000000				
		0010				
		00				
		000400				
		000052				
		7570				
		00				
			768 *			
			769 *			OPEN DUMP FILE
			770 *			
		000401	771	FT1	BSS	0
		000024	772		LDX	0,\$.OPEN,DU
		2200	773		LDX	1,TRAP1,DU
		03	774		LDX	4,\$DTN,DU
		000402	775		LDX	5,\$DTSZ,DU
		000414	776		LDX	6,1,DU
		2210	777		LDX	7,\$DESZ,DU
		03	778		LDQ	\$DACC,DL
		000403	779		MME	
		003060				OPEN THE DUMP FILE
		2240				MME NUMBER
		03				TRAP
		000404				TREE-NAME
		000014				TREE-SIZE
		2250				BEHALF
		03				ELEMENT-SIZE
		000405				ACCESSES
		000001				TRY OPENING
		2260				
		03				
		000406				
		044000				
		2270				
		03				
		000407				
		000037				
		2360				
		07				
		000410				
		000000				
		0010				
		00				
			780 *			
			781 *			PAUSE TILL OPENED
			782 *			
		000411	783		LDX	0,\$.PAUSE,DU
		000017	784		MME	
		2200	785		TRA	*-2
		03	786	TRAP1	BSS	3
		000412	787		LDX	2,TRAP1
		000000	788		TNZ	FT2
		0010	789		LXL	0,TRAP1
		00				PAUSE INDEFINITELY
		000413				WAIT FOR TRAP
		000411				KEEP WAITING
		7100				TRAP ON DUMPFIL OPEN
		00				GET FRN
						DID WE REALLY GET IT OPEN?
		000414				NO, SEE WHY NOT
		2220				
		00				
		000420				
		000425				
		6010				
		00				
		000421				
		000414				
		7200				
		00				

X
 000422 000003 1000 03
 000423 000401 6000 00
 END OF BINARY CARD LPCP0006
 000424 000470 7100 00

DIAGNOSTICS

790 CMPX 0,B\$BZ,DU WAS THE EXEC TOO BUSY?
 791 TZE FT1 YES, SO RETRY
 792 TRA TERM NOPE, WELL THAT'S IT
 793 *
 794 * SCRATCH DUMP FILE
 795 *
 796 FT2 BSS 0
 797 LDX 0,S,SCR,DU MME NUMBER
 798 LDX 1,TRAP2,DU TRAP ADDRESS
 799 LDX 2,TRAP1 FRN OF FILE
 800 LDX 3,0,DU SCRATCH IT
 801 MME
 802 *
 803 * PAUSE TILL SCRATCHED
 804 *
 805 LDX 0,S,PAUSE,DU ALWAYS PAUSE
 806 MME WAIT
 807 TRA *-2 FOREVER
 808 TRAP2 BSS 3
 809 LXL 0,TRAP2 CHECK EXEC STATUS RETURN
 810 TZE FT3 OK, CONTINUE
 811 CMPX 0,B\$BZ,DU NO, WELL WAS THE EXEC TOO BUSY?
 812 TZE FT2 YES, JUST RETRY
 813 TRA TERM NOPE, JUST BLEWIT
 814 *
 815 * START CORE TO FILE DUMP
 816 *
 817 FT3 BSS 0
 818 LDX 0,S,WRF,DU WRITE RANDON FILE
 819 LDX 1,TRAP3,DU TRAP ADDRESS
 820 LDX 2,TRAP1 FRN OF DUMPFIL
 821 LDX 3,0,DU TO: FILE LOCATION
 822 LDX 4,0,DU FROM: CORE LOCATION
 823 SBAR SBAR GET CORE-SIZE
 824 LDX 5,SBAR LOAD NUMBER OF ELEMENTS
 825 ANX5 =0377,DU MASK TO NUMBER OF ELEMENTS
 826 MME INITIATE THE COPY
 827 *
 828 * PAUSE TILL DUMP IS DONE
 829 *
 830 LDX 0,S,PAUSE,DU ALWAYS PAUSE
 831 MME WAIT
 832 TRA *-2 FOREVER
 833 TRAP3 BSS 3 COPY TO DUMP FILE
 834 LXL 0,TRAP3 GET STATUS
 835 TZE TERM NOW IT IS TIME TO TERMINATE
 836 CMPX 0,B\$BZ,DU WAS THE EXEC TOO BUSY
 837 TZE FT3 YES, SO JUST RETRY

000425 000010 2200 03
 000426 000435 2210 03
 000427 000414 2220 00
 000430 000000 2230 03
 000431 000000 0010 00
 000432 000017 2200 03
 000433 000000 0010 00
 000434 000432 7100 00
 000440 000435 7200 00
 000441 000445 6000 00
 000442 000003 1000 03
 000443 000425 6000 00
 000444 000470 7100 00
 000445 000007 2200 03
 000446 000461 2210 03
 000447 000414 2220 00
 000450 000000 2230 03
 000451 000000 2240 03
 000452 000055 5500 00
 000453 000055 2250 00
 END OF BINARY CARD LPCP0007
 000454 000377 3650 03
 000455 000000 0010 00
 000456 000017 2200 03
 000457 000000 0010 00
 000460 000456 7100 00
 000464 000461 7200 00
 000465 000470 6000 00
 000466 000003 1000 03
 000467 000445 6000 00

X

DIAGNOSTICS

000470 000016 2200 03 000470
 000471 000000 0010 00
 000472 000470 7100 00

838 TERM BSS 0
 839 LDX 0,S.TERM,DU
 840 MME
 841 TRA *-2

TIME TO SAY SO LONG
 TERMINATE
 BYE-BYE
 TAKE NO CHANCES

X

EXIT MACRO

000473

843 USE CODE

844 HEAD

845 *

846 *

847 *

EXIT

848 *

849 *

850 *

EXIT TERMINATES A THREAD OF CONTROL BY RETURNING TO THE
TASK DISTRIBUTOR.

851 *

852

EXIT

MACRO

NO ARGUMENTS

853

854

TRA

ENDM

SEXIT

EXIT

BUGGING MACROS

000473

```

856 USE CODE
857 HEAD
858 *
859 *
860 * BUGGING MACROS PLANT ADDRESSES IN INVALID DATA AREAS SO THAT
861 * ANY UNAUTHORIZED USE OF SUCH DATA WILL RESULT IN A MEMORY FAULT
862 * OR EXECUTIVE CALL REJECT.
863 *
864 *
865 * BUG
866 *
867 * BUG FILLS BOTH UPPER AND LOWER HALVES OF A STORAGE WORD WITH
868 * THE BUG PATTERN $BUGBUG.
869 *
870 BUG MACRO STORAGE-ADDRESS
871 IFE $DBG,$ON,4
872 BUGBUG SET BUGBUG+1
873 LDX 0,BUGBUG,DU
874 STX 0,#1
875 SXL 0,#1
876 ENDM BUG
877 *
878 *
879 * BUGU
880 *
881 * BUGU FILLS THE UPPER HALF OF A STORAGE WORD WITH THE BUG
882 * PATTERN $BUGBUG
883 *
884 BUGU MACRO STORAGE-ADDRESS
885 IFE $DBG,$ON,3
886 BUGBUG SET BUGBUG+1
887 LDX 0,BUGBUG,DU
888 STX 0,#1
889 ENDM BUGU
890 *
891 *
892 * BUGL
893 *
894 * BUGL FILL THE LOWER HALF OF A STORAGE WORD WITH THE BUG
895 * PATTERN $BUGBUG.
896 *
897 BUGL MACRO STORAGE-ADDRESS
898 IFE $DBG,$ON,3
899 BUGBUG SET BUGBUG+1
900 LDX 0,BUGBUG,DU
901 SXL 0,#1
902 ENDM BUGL
    
```


BUGGING MACROS

```

904 *
905 *
906 *                               BUGXR
907 *
908 * ..... BUGXR LOADS THE SPECIFIED INDEX REGISTER(S) WITH THE
909 *
910 BUGXR MACRO INDEX-REGISTER(S)
911     IFE $DBG,$ON,4
912 BUGBUG SET BUGBUG+1
913     IDRP #1
914     LDX #1,BUGBUG,DU
915     IDRP
916     ENDM BUGXR
917 *
918 *
919 *                               BUGA
920 *
921 * ..... BUGA LOADS THE CONTENTS OF THE THE A REGISTER WITH THE BUG
922 * ..... PATTERN $BUGBUG.
923 *
924 BUGA MACRO <NO ARGUMENTS>
925     IFE $DBG,$ON,3
926 BUGBUG SET BUGBUG+1
927     LDA BUGBUG,DU
928     ORA BUGBUG,DL
929     ENDM BUGA
930 *
931 *
932 *                               BUGQ
933 *
934 * ..... BUGQ LOADS THE CONTENTS OF THE Q REGISTER WITH THE BUG
935 * ..... PATTERN $BUGBUG.
936 *
937 BUGQ MACRO <NO ARGUMENTS>
938     IFE $DBG,$ON,3
939 BUGBUG SET BUGBUG+1
940     LDQ BUGBUG,DU
941     ORQ BUGBUG,DL
942     ENDM BUGQ
943 *
944 *
945 *                               DECRM MACRO
946 *
947 * ..... DECREMENT A COUNTER
948 *
949 DECRM MACRO COUNTER=ADDRESS
950     LCQ 1,DL
951     ASQ #1
952     ENDM DECRM

```

CHECKPOINT MACRO

```

000473 954 USE CODE
955 HEAD X
956 *
957 *
958 *
959 *
960 * CHECKPOINTS
961 * THIS MACRO CAUSES THE REGISTERS TO BE STORED IN 8-WORD
962 * BLOCKS IN A CIRCULAR QUEUE FOR DEBUGGING USE. INFORMATION
963 * IS STORED IN THE FOLLOWING FORMAT:
964 *
965 * C(0) = C(X0) (UPPER)
966 * C(X1) (LOWER)
967 * C(1) = C(X2) (UPPER)
968 * C(X3) (LOWER)
969 * C(2) = C(X4) (UPPER)
970 * C(X5) (LOWER)
971 * C(3) = C(X6) (UPPER)
972 * C(X7) (LOWER)
973 * C(4) = C(A)
974 * C(5) = C(Q)
975 * C(6) = C(E) (0-7 BITS)
976 * C(7) = C(TR) (0-23 BITS)
977 *
978 *
979 * CKPT
980 *
981 CKPT MACRO <NO ARGUMENTS>
982 IFE $DBG,$ON,1
983 XED X$CKPT
984 ENDM CKPT
985 *
986 *
987 * CKPT -- SUBROUTINE
988 *
000474 989 EVEN
000474 990 CKPT BSS 0 ENTRY POINT
000474 003324 5540 00 991 STC1 CKIC SAVE IC
000475 000476 7100 00 992 TRA **1 BREAK XED
000476 003330 7530 00 993 SREG CKREG SAVE REGISTERS FOR A RELOAD
000477 000160 7530 51 994 SREG REG,I SAVE IN 8-WORD BLOCK
995 *
996 * UPDATE POINTER FOR CIRCULAR QUEUE
997 *
000500 000160 2200 00 998 LDX 0,REG GET CURRENT ADDRESS
000501 000010 0200 03 999 ADLX 0,8,DU BUMP TO NEXT ENTRY
000502 000370 1000 03 1000 CMPX 0,DBGQ+8*DBGON,DU OVER THE END?
END OF BINARY CARD LPCP0008
000503 000505 6020 00 1001 TNC **2 NO, THIS IS VALID
000504 000170 2200 03 1002 LDX 0,DBGQ,DU YES, RESET TO BEGINNING

```

X

CHECKPOINT MACRO

000505 000160 7400 00
000506 003330 0730 00
000507 003324 6300 00

1003 STX 0,REG
1004 LREG CKREG
1005 RET CKIC

SAVE FOR NEXT TIME
RESTORE REGISTERS
RESTORE IC

1006 *
1007 *

003324
003324
003330
003330
000510

1008 USE STORE
1009 CKIC BSS 1
1010 EIGHT
1011 CKREG BSS 8
1012 USE PREVIOUS

TEMP STORAGE FOR IC/IR
TEMP STORAGE FOR REGISTERS

X

TRAP SETUP MACRO

```

000510      1014      USE      CODE
              1015      HEAD
              1016 *
              1017 *
              1018 *                SETUP MACRO
              1019 *
              1020 SETUP  MACRO                NO ARGUMENTS
              1021      XED      $SETUP
              1022      ENDM     SETUP
              1023 *
              1024 *
              1025 *                SETUP -- SUBROUTINE TO SET UP A TRAP
              1026 *
              1027 *                CALL WITH
              1028 *                C(XT) = TBLOCK-ADDRESS
              1029 *                C(XJ) = JBLOCK ADDRESS
              1030 *                C(XO) = TRANSFER ADDRESS FOR JSTRA
              1031 *                ENTER BY
              1032 *                XED T$SETUP
              1033 *                DESTROYS C(A), C(Q), C(XO)
              1034 *                USES NO TEMPORARIES
              1035 *
              1036 *
000510      1037      EVEN
000510      1038 SETUP  BSS      0
000510 000004 7400 11      1039      STX      0,T$TRA,T      SET T$TRA = RESTART ADDRESS
000511 000512 7000 00      1040      TSX      0,**+1      BREAK XED
000512 000000 4310 03      1041      FLD      0,DU      ZERO OUT A AND Q
000513 000000 7570 11      1042      STAQ     T$SRW1,T      ZERO STATUS WORDS
000514 000520 2370 00      1043      LDAQ     TRAP-1      GET ZERO, XED WORDS
000515 000002 7570 11      1044      STAQ     T$XED-1,T      SAVE ZERO, XED
000516 000000 7100 10      1045      TRA      0,0      RETURN
              1046 *
              1047 *
              1048 *                TRAP -- XED SEQUENCE TO PUT BLOCK ON Q$TASK
              1049 *
000520      1050      EVEN
000520 000000 000000      1051      ZERO
000521 000522 7170 00      1052 TRAP  XED      **1      CAN BE USED FOR CLEARING RET WORDS
000522 003261 5540 54      1053      STC1     Q$LAST+Q$TASK,DI *UPDATE PREVIOUS LAST POINTER
000523 000524 7170 00      1054      XED      **1      CONTINUE WITHOUT AFFECTING IC
000524 003261 5540 00      1055      STC1     Q$LAST+Q$TASK      UPDATE POINTER TO LAST
000525 777777 6300 04      1056      RET      -1,IC      RETURN TO POINT OF INTERRUPTION

```

SYSTEM CALL MACRO DESCRIPTIONS

```

000526 1058      USE      CODE
        1059      HEAD
        1060 *
        1061 *
        1062 *
        1063 *
        1064 *      ALL SYSTEM CALLS ARE DONE THRU PRE-DEFINED MACROS. THOSE
        1065 *      MACROS ARE LISTED IN THE FOLLOWING PAGES. EACH OF THESE
        1066 *      MACROS IS CODED TO SPECIFIC CONVENTIONS:
        1067 *      ENTER BY
        1068 *      TSX  0,$<MACRO-NAME>
        1069 *      ENTERED WITH
        1070 *      C(XT) = TBLOCK-ADDRESS
        1071 *      C(XJ) = JBLOCK-ADDRESS
        1072 *      CALLS
        1073 *      SETUP
        1074 *      ISSUES MME AND THEN 'EXITS'
        1075 *      RETURNS TO FIRST LOCATION AFTER MACRO
        1076 *      RETURNS WITH
        1077 *      C(XT) = TBLOCK-ADDRESS
        1078 *      C(XJ) = JBLOCK-ADDRESS
        1079 *      C(XL) = RESTART-ADDRESS
        1080 *
        1081 *      IN ACTUALITY, THE FOLLOWING HAPPENS: THE MACRO DOES
        1082 *      DO THE 'TSX0'. HOWEVER, FOLLOWING THAT INSTRUCTION IS
        1083 *      THE SET OF ARGUMENTS FOR THE MME. THE SUBROUTINE ENTERED
        1084 *      WHICH ALWAYS HAS THE SAME NAME AS THE MACRO KNOWS THE
        1085 *      NUMBER OF ARGUMENTS IT IS PASSED. THEREFORE IT SIMPLY CAL-
        1086 *      CULATES THE RESTART ADDRESS. IT THEN CALLS 'SETUP' WHICH
        1087 *      RESETS THE TRAP BLOCK (C(XT) = X1) WITH THE FIRST THREE WORDS
        1088 *      BEING ZEROED AND THE LINK WORD FOR THE EXEC SET TO THE
        1089 *      XED INSTRUCTION TO PLACE THE BLOCK ON THE QSTASK QUEUE. HENCE
        1090 *      WHEN TRAPPED, THE BLOCK WILL BE LINKED ON THE QSTASK QUEUE
        1091 *      AND THE INTERRUPT WILL BE TRANSPARENT TO THE CURRENTLY RUN-
        1092 *      NING TASK UNLESS THE CURRENT TASK IS 'PAUSE'. THE PARAMETERS
        1093 *      ARE THEN FETCHED (SEE PROGRAMMER'S REFERENCE MANUAL IF YOU
        1094 *      DON'T FULLY UNDERSTAND 'IDC' MODIFICATIONS; CAUSE IF YOU DON'T
        1095 *      YOU WILL BE LOST) AND THE MME EXECUTED. 'EXIT' IS CALLED
        1096 *      THUS PLACING THE CURRENTLY RUNNING TASK IN A BLOCKED STATE
        1097 *      WHILE STARTING THE NEXT READY-TO-RUN TASK. THIS IS HOW WE
        1098 *      MULTI-PROGRAM. WHEN THE EXEC TRAPS THIS OPERATION, AS
        1099 *      WE HAVE SAID, THE XED INSTRUCTION PLACES THE TASK BACK
        ON THE QSTASK QUEUE.
    
```

SETUP FAULT VECTOR MACRO

```

000526      1101      USE      CODE
              1102      HEAD
              1103 *
              1104 *
              1105 *
              1106 *
              1107 SETFV  MACRO   CORELOC
              1108      TSX     0,%SETFV
              1109      ARG     #1      ADDRESS OF SLAVE FAULT VECTOR
              1110      ENDM    SETFV
              1111 *
              1112 *
              1113 *
              1114 *
              1115 *      THIS SUBROUTINE IS CALLED BY THE SETFV MACRO.  IT ISSUES THE
              1116 *      COMMAND TO LOCATE THE SLAVE FAULT VECTOR.
              1117 *
              1118 *      CALL WITH
              1119 *      C(XT) = TBLOCK-ADDRESS
              1120 *      C(XJ) = JBLOCK ADDRESS
              1121 *      ENTER BY
              1122 *      TSX 0,%SETFV
              1123 *      ARG CORE LOCATION TO FAULT VECTOR
              1124 *      RETURNS TO FIRST LOC AFTER MACRO EXPANSION
              1125 *      RETURNS WITH
              1126 *      C(XT) = TBLOCK-ADDRESS
              1127 *      C(XJ) = JBLOCK ADDRESS
              1128 *      C(XL) = RESTART ADDRESS
              1129 *      USES LOCAL TEMPORARY ONLY
              1130 *
              1131 *
END OF BINARY CARD LPCP0009
000526 003340 7400 00      1132 SETFV  STX     0,%SETFT      POINTER TO ARGUMENT LIST
000527 000001 0200 03      1133      ADLX    0,1,DU      RESTART ADDRESS
              000530
              1134      SETUP
              1135      XED     %SETUP
              1136      LDX     2,%SETFT, IDC      LOAD ADDRESS OF FAULT VECTOR
              1137      LDX     0,%SETFV,DU      LOAD MME NUMBER
              000533      CKPT
              1138      XED     X3CKPT      CHECKPOINT
              000534 000474 7170 00      1139      MME
              000000 0010 00      1140 *
              000535      EXIT
              001547 7100 00      1141 *
              003340      TRA     %EXIT
              000000 0000 20      1142 *
              000536      USE     STORE
              1143      SETFT  ARG     0,*      POINTER TO ARGUMENT LIST
              1144      USE     PREVIOUS

```

READ MACRO

```

000536      1145      USE      CODE
              1146      HEAD
              1147 *
              1148 *
              1149 *
              1150 *
              1151 READ  MACRO  FRN,CORELOC,N,MODE
              1152      TSX    0,$READ
              1153      ARG    #1      FRN ADDRESS
              1154      ARG    #2      ADDRESS OF CORE LOC
              1155      ARG    #3      NUMBER OF ELEMENTS
              1156      ARG    #4      MODE
              1157      ENDM   READ
              1158 *
              1159 *
              1160 *
              1161 *
              1162 *
              1163 *
              1164 *
              1165 *
              1166 *
              1167 *
              1168 *
              1169 *
              1170 *
              1171 *
              1172 *
              1173 *
              1174 *
              1175 *
              1176 *
              1177 *
              1178 *
              1179 *
              1180 *
              1181 *
              1182 READ  STX    0,READY      POINTER TO ARGUMENT LIST
000536      003341 7400 00
000537      000004 0200 03
              000540
              1183      ADLX   0,4,DU      RESTART ADDRESS
              1184      SETUP
000540      000510 7170 00
000541      003341 2220 57
000542      003341 2240 57
000543      003341 2250 57
000544      003341 2260 57
000545      000004 2200 03
              000546
000546      000474 7170 00
000547      000000 0010 00
              000550
              1185      LDX    2,READY,1DC   LOAD FRN
              1186      LDX    4,READY,1DC   LOAD CORE LOC
              1187      LDX    5,READY,1DC   LOAD N
              1188      LDX    6,READY,1DC   LOAD MODE
              1189      LDX    0,READ,DU     LOAD MME NUMBER
              1190      CKPT
              1191      X$CKPT
              1192      EXIT
              1193      MME
              1194      READ
              1195      READ
  
```

READ MACRO

000550	001547	7100	00						
			003341		1193 *	TRA		\$EXIT	
END OF BINARY CARD					1194	USE		STORE	
			LPCP0010						
003341	000000	0000	20		1195	READT	ARG	0,*	POINTER TO ARGUMENT LIST
			000551		1196	USE		PREVIOUS	


```

                                APEND MACRO
000551      1198      USE      CODE
            1199      HEAD
            1200 *
            1201 *
            1202 *
            1203 *
            1204 APEND MACRO FRN,CORELOC,N,MODE
            1205      TSX      0,$APEND
            1206      ARG      #1          FRN ADDRESS
            1207      ARG      #2          ADDRESS OF CORE LOC
            1208      ARG      #3          NUMBER OF ELEMENTS
            1209      ARG      #4          MODE
            1210      ENDM      APEND
            1211 *
            1212 *
            1213 *
            1214 *
            1215 *
            1216 *
            1217 *
            1218 *
            1219 *
            1220 *
            1221 *
            1222 *
            1223 *
            1224 *
            1225 *
            1226 *
            1227 *
            1228 *
            1229 *
            1230 *
            1231 *
            1232 *
            1233 *
            1234 *
            1235 *
            1236 APEND STX      0,APNDT          POINTER TO ARGUMENT LIST
            1237      ADLX     0,4,DU          RESTART ADDRESS
            1238      SETUP
            1239      XED      $$SETUP
            1240      LDX      2,APNDT,1DC      LOAD FRN
            1241      LDX      4,APNDT,1DC      LOAD CORE LOC
            1242      LDX      5,APNDT,1DC      LOAD NUMBER OF ELEMENTS
            1243      LDX      6,APNDT,1DC      LOAD MODE
            1244      LDX      0,,APEND,DU      LOAD MME NUMBER
            1245      CKPT
            1246      XED      X$CKPT          CHECKPOINT
            1247      MME
            1248      APEND
000551 003342 7400 00
000552 000004 0200 03
            000553
000553 000510 7170 00
000554 003342 2220 57
000555 003342 2240 57
000556 003342 2250 57
000557 003342 2260 57
000560 000005 2200 03
            000561
000561 000474 7170 00
000562 000000 0010 00

```

APEND MACRO

000563	001547	000563 7100 00	1246	EXIT		
				TRA	\$EXIT	
			1247 *			
003342	000000	000342 0000 20	1248	USE	STORE	
		000564	1249	APNDT ARG	0,*	POINTER TO ARGUMENT LIST
			1250	USE	PREVIOUS	

SET POINTER MACRO

```

000564      1252      USE      CODE
              1253      HEAD
              1254 *
              1255 *
              1256 *
              1257 *
              1258 SPTR  MACRO  FRN,N
              1259      TSX    0,$SPTR
              1260      ARG    #1      FRN ADDRESS
              1261      ARG    #2      NUMBER OF ELEMENTS TO MOVE POINTER
              1262      ENDM   SPTR
              1263 *
              1264 *
              1265 *
              1266 *
              1267 *
              1268 *
              1269 *
              1270 *
              1271 *
              1272 *
              1273 *
              1274 *
              1275 *
              1276 *
              1277 *
              1278 *
              1279 *
              1280 *
              1281 *
              1282 *
              1283 *
              1284 SPTR  STX    0,$SPTR      POINTER TO ARGUMENT LIST
              1285      ADLX   0,2,DU      RESTART ADDRESS
              1286      SETUP
              1287      XED    $SETUP
              1288      LDX    2,$SPTR,IDC   LOAD FRN
              1289      LDX    3,$SPTR,IDC   LOAD N
              1290      CKPT   0,$SPTR,DU    LOAD MME NUMBER
              1291      XED    X$CKPT
              1292      MME
              1293 *
              1294      USE    STORE
              1295 SPTR  ARG    0,*      POINTER TO ARGUMENT LIST
              1296      USE    PREVIOUS

```

000564 003343 7400 00
 000565 000002 0200 03
 000566
 000566 000510 7170 00
 000567 003343 2220 57
 000570 003343 2230 57
 000571 000011 2200 03
 000572
 END OF BINARY CARD LPCP0011
 000572 000474 7170 00
 000573 000000 0010 00
 000574
 000574 001547 7100 00
 003343
 003343 000000 0000 20
 000575

REQUEST STATUS MACRO

```

000575 1298 USE CODE
1299 HEAD
1300 *
1301 *
1302 * REQUEST STATUS
1303 *
1304 RQST MACRO FRN
1305 TSX 0,%RQST
1306 ARG #1 FRN ADDRESS
1307 ENDM RQST
1308 *
1309 *
1310 * REQUEST STATUS -- SUBROUTINE
1311 *
1312 * THIS SUBROUTINE IS CALLED BY THE RQST MACRO. IT ISSUES
1313 * THE COMMAND TO REQUEST STATUS ON THE FRN SPECIFIED.
1314 *
1315 * CALL WITH
1316 * C(XT) = TCB
1317 * C(XJ) = JCB
1318 * ENTER BY
1319 * TSX 0,%RQST
1320 * ARG FRN
1321 * RETURNS TO FIRST LOC AFTER MACRO EXPANSION
1322 * RETURNS WITH
1323 * C(XT) = TCB
1324 * C(XJ) = JCB
1325 * USES LOCAL TEMPORARY ONLY
1326 *
000575 003344 7400 00 1328 RQST STX 0,%RQST POINTER TO ARGUMENT LIST
000576 000001 0200 03 1329 ADLX 0,%1,DU RESTART ADDRESS
000577 000510 7170 00 1330 SETUP
000600 003344 2220 57 1331 LDX 2,%RQST,%IDC LOAD FRN
000601 000012 2200 03 1332 LDX 0,%RQST,%DU LOAD MME NUMBER
000602 000474 7170 00 1333 CKPT CHECKPOINT
000603 000000 0010 00 1334 XED X%CKPT
000604 001547 7100 00 1335 MME REQUEST STATUS
1336 * EXIT %EXIT
003344 000000 0000 20 1337 USE STORE
000605 000605 1338 RQST ARG 0,%* POINTER TO ARGUMENT LIST
1339 USE PREVIOUS
    
```

CHANGE SEGMENT MACRO

```

000605      1341      USE      CODE
              1342      HEAD
              1343 *
              1344 *
              1345 *
              1346 *
              1347 CHSEG MACRO  SEGMENT-NUMBER,LENGTH
              1348      TSX      0,%CHSEG
              1349      ARG      #1          NUMBER-OF-SEGMENT
              1350      ARG      #2          NEW-LENGTH
              1351      ENDM    CHSEG
              1352 *
              1353 *
              1354 *
              1355 *
              1356 *
              1357 *
              1358 *
              1359 *
              1360 *
              1361 *
              1362 *
              1363 *
              1364 *
              1365 *
              1366 *
              1367 *
              1368 *
              1369 *
              1370 *
              1371 *
              1372 *
              1373 CHSEG  STX      0,%CHSGT    POINTER TO ARGUMENT LIST
              1374      ADLX     0,2,DU      RESTART ADDRESS
              1375      SETUP
              1376      XED      $SETUP
              1377      LDX      2,%CHSGT, IDC  LOAD SEGMENT NUMBER
              1378      LDX      3,%CHSGT, IDC  LOAD SEGMENT LENGTH
              1379      LDX      0,%CHSEG,DU    LOAD MME NUMBER
              1380      CKPT
              1381      XED      X$CKPT        CHECKPOINT
              1382 *
              1383 *
              1384 CHSGT ARG      0,*          CHANGE SEGMENT
              1385      USE      PREVIOUS    POINTER TO ARGUMENT LIST

000605 003345 7400 00
000606 000002 0200 03
              000607
000607 000510 7170 00
000610 003345 2220 57
000611 003345 2230 57
END OF BINARY CARD LPCP0012
000612 000022 2200 03
              000613
000613 000474 7170 00
000614 000000 0010 00
              000615
000615 001547 7100 00
              003345
003345 000000 0000 20
              000616

```

CLOSE MACRO

```

000616      1387      USE      CODE
              1388      HEAD
              1389 *
              1390 *
              1391 *
              1392 *
              1393 CLOSE MACRO FRN
              1394      TSX      0,%CLOSE
              1395      ARG      #1      FILE REFERENCE ADDRESS
              1396      ENDM      CLOS
              1397 *
              1398 *
              1399 *
              1400 *
              1401 *      THIS SUBROUTINE IS CALLED BY THE CLOS MACRO. IT ISSUES THE
              1402 *      MME TO CLOSE A FILE.
              1403 *
              1404 *      CALL WITH
              1405 *      C(XT) = TBLOCK-ADDRESS
              1406 *      C(XJ) = JBLOCK ADDRESS
              1407 *      ENTER BY
              1408 *      TSX 0,%CLOS
              1409 *      ARG FILE=REFERENCE=ADDRESS
              1410 *      RETURNS TO FIRST LOC AFTER MACRO EXPANSION
              1411 *      RETURNS WITH
              1412 *      C(XT) = TBLOCK-ADDRESS
              1413 *      C(XJ) = JBLOCK ADDRESS
              1414 *      C(XL) = RESTART ADDRESS
              1415 *      USES LOCAL TEMPORARY ONLY
              1416 *
000616 003346 7400 00      1417 CLOS STX      0,%CLOST      POINTER TO ARGUMENT LIST
000617 000001 0200 03      1418 ADLX      0,1,DU      RESTART ADDRESS
              000620      1419      SETUP
000620 000510 7170 00      XED      $SETUP
000621 003346 2220 57      1420 LDX      2,%CLOST, IDC      LOAD FILE REFERENCE
000622 000025 2200 03      1421 LDX      0,%CLOSE, DU      LOAD MME NUMBER
              000623      1422 CKPT      CHECKPOINT
000623 000474 7170 00      XED      X$CKPT
000624 000000 0010 00      1423 MME      CLOS
              000625      1424 EXIT
000625 001547 7100 00      TRA      $EXIT
              1425 *
              1426 USE      STORE
003346 000000 0000 20      1427 CLOST ARG      0,*      POINTER TO ARGUMENT LIST
              000626      1428 USE      PREVIOUS
    
```

LOCK MACRO

	000626	1430	USE	CODE				
		1431	HEAD					
		1432 *						
		1433 *						
		1434 *			LOCK			
		1435 *						
		1436	LOCK	MACRO	FRN			
		1437	TSX	0,\$LOCK				
		1438	ARG	#1	FILE-REFERENCE ADDRESS			
		1439	ENDM	LOCK				
		1440 *						
		1441 *						
		1442 *			LOCK -- SUBROUTINE			
		1443 *						
		1444 *			THIS SUBROUTINE IS CALLED BY THE LOCK MACRO. IT ISSUES THE			
		1445 *			MME TO LOCK A FILE.			
		1446 *						
		1447 *			CALL WITH			
		1448 *			C(XT) = TBLOCK-ADDRESS			
		1449 *			C(XJ) = JBLOCK ADDRESS			
		1450 *			ENTER BY			
		1451 *			TSX 0,\$LOCK			
		1452 *			ARG FILE-REFERENCE-ADDRESS			
		1453 *			RETURNS TO FIRST LOC AFTER MACRO EXPANSION			
		1454 *			RETURNS WITH			
		1455 *			C(XT) = TBLOCK-ADDRESS			
		1456 *			C(XJ) = JBLOCK ADDRESS			
		1457 *			C(XL) = RESTART ADDRESS			
		1458 *			USES LOCAL TEMPORARY ONLY			
		1459 *						
	000626	003347	7400	00	1460 LOCK	STX	0,\$LOCKT	POINTER TO ARGUMENT LIST
	000627	000001	0200	03	1461	ADLX	0,\$DU	RESTART ADDRESS
				000630	1462	SETUP		
	000630	000510	7170	00		XED	\$SETUP	
	000631	003347	2220	57	1463	LDX	2,\$LOCKT,\$IDC	LOAD FILE REFERENCE
END		OF BINARY CARD	LPC0013					
	000632	000044	2200	03	1464	LDX	0,\$LOCK,\$DU	MME NUMBER
				000633	1465	CKPT		CHECKPOINT
	000633	000474	7170	00		XED	X\$CKPT	
	000634	000000	0010	00	1466	MME		LOCK
				000635	1467	EXIT		
	000635	001547	7100	00		TRA	\$EXIT	
				003347	1468 *			
				000000	1469	USE	STORE	
	003347	000000	0000	20	1470 LOCKT	ARG	0,*	POINTER TO ARGUMENT LIST
				000636	1471	USE	PREVIOUS	

UNLOCK MACRO

```

000636      1473      USE      CODE
              1474      HEAD
              1475 *
              1476 *
              1477 *
              1478 *
              1479 UNLCK  MACRO  FRN
              1480      TSX    0,%UNLCK
              1481      ARG    #1      FILE REFERENCE ADDRESS
              1482      ENDM   UNLK
              1483 *
              1484 *
              1485 *
              1486 *
              1487 *
              1488 *
              1489 *
              1490 *
              1491 *
              1492 *
              1493 *
              1494 *
              1495 *
              1496 *
              1497 *
              1498 *
              1499 *
              1500 *
              1501 *
              1502 *
000636 003350 7400 00      1503 UNLCK  STX    0,%UNLKT      POINTER TO ARGUMENT LIST
000637 000001 0200 03      1504      ADLX   0,%1,%DU      RESTART ADDRESS
              000640
000640 000510 7170 00      1505      SETUP
000641 003350 2220 57      1506      XED    $SETUP
000642 000045 2200 03      1507      LDX    2,%UNLKT,%IDC      LOAD FILE REFERENCE
              000643
000643 000474 7170 00      1508      LDX    0,%UNLCK,%DU      MME NUMBER
000644 000000 0010 00      1509      CKPT   X$CKPT      CHECKPOINT
              000645
000645 001547 7100 00      1510      MME
              1511 *
              1512 *
003350 000000 0000 20      1513 UNLKT  USE    STORE
              000646
              1514      ARG    0,%*      POINTER TO ARGUMENT LIST
              USE    PREVIOUS
    
```


NOTIFY MACRO

```

000646      1516      USE      CODE
              1517      HEAD
              1518 *
              1519 *
              1520 *
              1521 NOTIF  MACRO  ERN,STATE
              1522      TSX    0,$NOTIF
              1523      ARG    #1      FRN ADDRESS
              1524      ARG    #2      STATE ADDRESS
              1525      ENDM   NOTIF
              1526 *
              1527 *
              1528 *
              1529 *
              1530 *
              1531 *   THIS SUBROUTINE IS CALLED BY THE NOTIF MACRO. IT ISSUES
              1532 *   THE NOTIFY ON THE SPECIFIED EVENT. NOTE THAT C(X3) POINT TO
              1533 *   CTRAP.
              1534 *
              1535 *   CALL WITH
              1536 *       C(XT) = TBLOCK-ADDRESS
              1537 *       C(XJ) = JBLOCK ADDRESS
              1538 *       C(X3) = CTRAP ADDRESS
              1539 *
              1540 *   ENTER BY
              1541 *       TSX 0,$NOTIF
              1542 *       ARG EVENT=FRN
              1543 *       ARG STATE ADDRESS
              1544 *
              1545 *   RETURNS TO FIRST LOC AFTER MACRO EXPANSION
              1546 *   RETURNS WITH
              1547 *       C(XT) = TBLOCK-ADDRESS
              1548 *       C(XJ) = JBLOCK ADDRESS
              1549 *       C(XL) = RESTART ADDRESS
              1550 *
              1551 *   USES LOCAL TEMPORARY ONLY
              1552 *
000646 003351 7400 00      1549 NOTIF  STX    0,$NOTFT      POINTER OF ARGUMENT LIST
000647 000002 0200 03      1550      ADLX   0,2,DU      RESTART ADDRESS
              000650
000650 000510 7170 00      1551      SETUP
000651 003351 2220 57      1552      XED    $$SETUP
END OF BINARY CARD LPCP0014
000652 003351 2350 57      1553      LDX    2,$NOTFT, IDC   LOAD EVENT FRN
000653 000046 2200 03      1554      LDA    NOTFT, IDC   LOAD STATE
              000654
000654 000474 7170 00      1555      LDX    0,$NOTIF, DU   LOAD MME NUMBER
000655 000000 0010 00      1556      CKPT
              000656
000656 001547 7100 00      1557      XED    X$CKPT      CHECKPOINT
              1558 *
              1559 *
003351 000000 0000 20      1559      MME
              000657
              1560      EXIT
              1561      TRA    $EXIT
              1562 *
              1563 *
              1564 *
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```

CAUSE MACRO

```

000657 1563 USE CODE
1564 HEAD
1565 *
1566 *
1567 * CAUSE
1568 *
1569 CAUSE MACRO ERN,NUMBER,STATE,MESSAGE,ACCESSES,FRN (N.B. ORDERING)
1570 TSX 0,$CAUSE
1571 ARG #1 FILE REFERENCE ADDRESS
1572 ARG #2 NUMBER WHICH ARE TO NOTIFIED
1573 ARG #3 STATE
1574 ARG #4 MESSAGE
1575 ARG #5 ACCESSES ON PASSED ITEM
1576 ARG #6 FRN OF ITEM TO PASS
1577 ENDM CAUS
1578 *
1579 *
1580 * CAUSE -- SUBROUTINE
1581 *
1582 * THIS SUBROUTINE IS CALLED BY THE CAUS MACRO. IT ISSUES THE
1583 * MME TO CAUSE THE SPECIFIED FILE.
1584 *
1585 * CALL WITH
1586 * C(XJ) = JBLOCK ADDRESS
1587 * C(XT) = TBLOCK-ADDRESS
1588 * ENTER BY
1589 * TSX 0,$CAUS
1590 * ARG FILE-REFERENCE-NUMBER OF EVENT
1591 * ARG NUMBER
1592 * ARG PASSED-FILE-REFERENCE
1593 * ARG ACCESSES-ON-PASSED-ITEMS
1594 * ARG STATE
1595 * ARG MESSAGE
1596 * RETURNS TO FIRST LOC AFTER THE MACRO EXPANSION
1597 * RETURNS WITH
1598 * C(XJ) = JBLOCK ADDRESS
1599 * C(XT) = TBLOCK-ADDRESS
1600 * C(XL) = RESTART ADDRESS
1601 * USES LOCAL TEMPORARY ONLY
1602 *
000657 003352 7400 00 1603 CAUSE STX 0,CAUST POINTER TO ARGUMENT LIST
000660 000006 0200 03 1604 ADLX 0,6,DU RESTART ADDRESS
000661 000510 7170 00 1605 SETUP
000661 000510 7170 00 XED $SETUP
000662 003352 2220 57 1606 LDX 2,CAUST,IDC LOAD FILE REFERENCE
000663 003352 7230 57 1607 LXL 3,CAUST,IDC NUMBER
000664 003352 2350 57 1608 LDA CAUST,IDC STATE
000665 003352 2360 57 1609 LDQ CAUST,IDC MESSAGE
000666 003352 7270 57 1610 LXL 7,CAUST,IDC LOAD ACCESSES
000667 003352 2260 57 1611 LDX 6,CAUST,IDC LOAD FRN -- CLOBBER XR-J NOW

```

CAUSE MACRO

000670	000047	2200 03	1612	LDX	0, CAUSE, DU	MME NUMBER
		000671	1613	CKPT		CHECKPOINT
000671	000474	7170 00		XED	X\$CKPT	
000672	000000	0010 00	1614	MME		CAUSE
		000673	1615	EXIT		
000673	001547	7100 00		TRA	\$EXIT	
		003352	1616 *			
END OF BINARY CARD	LPC0015		1617	USE	STORE	
003352	000000	0000 20	1618 CAUST	ARG	0,*	POINTER TO ARGUMENT LIST
		000674	1619	USE	PREVIOUS	

CHECK MACRO

000674

```

1621      USE      CODE
1622      HEAD
1623 *
1624 *
1625 *
1626 *
1627 *
1628 *
1629 *
1630 *
1631 *
1632 *
1633 *
1634 *
1635 *
1636 CHECK MACRO ZEROSTATADD,BOOLPAT,XFERADD,BOOLPAT,XFERADD,ETC.
1637      LXL      0,T$SRW1,T      PICK UP LOGICAL STATUS
1638      ANX      0,R$STMK,DU      ISOLATE STATUS
1639      INE      '#1',,,'
1640      TZE      #1                ZERO STATUS TEST
1641      INE      '#2',,,'23
1642      CMPX     0,#2,DU
1643      TZE      #3                FIRST PAIR OF TESTS
1644      INE      '#4',,,'20
1645      CMPX     0,#4,DU
1646      TZE      #5                SECOND PAIR OF TESTS
1647      INE      '#6',,,'17
1648      CMPX     0,#6,DU
1649      TZE      #7                THIRD PAIR OF TESTS
1650      INE      '#8',,,'14
1651      CMPX     0,#8,DU
1652      TZE      #9                FOURTH PAIR OF TESTS
1653      INE      '#10',,,'11
1654      CMPX     0,#10,DU
1655      TZE      #11               FIFTH PAIR OF TESTS
1656      INE      '#12',,,'8
1657      CMPX     0,#12,DU
1658      TZE      #13               SIXTH PAIR OF TESTS
1659      INE      '#14',,,'5
1660      CMPX     0,#14,DU
1661      TZE      #15               SEVENTH PAIR OF TESTS
1662      INE      '#16',,,'2
1663      CMPX     0,#16,DU
1664      TZE      #17               EIGHTH PAIR OF TESTS
1665      TRA      $ERROR           DIE ON UNEXPECTED RETURN
1666      ENDM     CHECK
    
```

QUEUE MANAGEMENT -- GENERAL INTRODUCTION

```

000674 1668      USE      CODE
        1669      HEAD   Q
        1670 *
        1671 *           QUEUE MANAGEMENT -- GENERAL INTRODUCTION
        1672 *
        1673 *           EACH QUEUE IN THE PROGRAM HAS A SIMILAR STRUCTURE. A QUEUE
        1674 *           CONSISTS OF A (POSSIBLY EMPTY) LINKED LIST OF BLOCK. THE
        1675 *           POINTERS POINT TO WORD 4 (Q$OFFST) OF A BLOCK. THE LINK
        1676 *           POINTERS ARE STORED IN WORD 3 (Q$LINK) OF A BLOCK. THE WORD AT
        1677 *           LOCATION Q$FIRST POINTS TO Q$OFFST OF THE FIRST BLOCK OF THE
        1678 *           QUEUE. THE LOCATION Q$LAST POINTS TO Q$OFFST OF THE LAST BLOCK
        1679 *           OF THE QUEUE. THE EMPTY QUEUE IS DENOTED BY THE WORD AT Q$LAST
        1680 *           POINTING TO Q$FIRST+1.
        1681 *
        1682 *
        1683 *                               QUEUE
        1684 *
        1685 *           THIS GENERATES A QBLOCK. THIS STRUCTURE MUST AGREE
        1686 *           WITH THE STRUCTURE DEFINED FOR QUEUE MANAGEMENT.
        1687 *
        1688 QUEUE  MACRO  QBLOCK=LOCATION=SYMBOL,ASCII=NAME
        1689      USE      Q$TOR          PUT ALL QUEUES CONTIGUOUS
        1690      EVEN
        1691 #1    BSS      0              NAME OF QUEUE
        1692      ARG      $ERROR          FIRST
        1693      ARG      Q$#1+Q$FIRST+1 LAST
        1694      STX      0,Q$#1+Q$LAST,DI *XADD
        1695      STX      0,Q$#1+Q$LAST
        1696      EAX      Q,Q$#1          END
        1697      TSX      L,Q$END
        1698      EAX      Q,Q$#1          DEQ
        1699      TSX      L,Q$DEQ
        1700      ARG      0              BUSY
        1701      DEC      0              MAX
        1702      DEC      0              AVAIL
        1703      DEC      0              SPARE1
        1704      DEC      0              SPARE2
        1705      UASCI  1,#1          ABBREVIATION
        1706      ENDM   QUEUE
    
```

Q

QUEUE MANAGEMENT -- END

```

000674      1708      USE      CODE
              1709      HEAD      Q
              1710 *
              1711 *
              1712 *
              1713 *
              1714 *      ENQ
              1715 *      ENQ SUSPENDS A TASK UNTIL THE SPECIFIED QUEUE CAN BE MADE
              1716 *      AVAILABLE.
              1717 ENQ      MACRO      QADDRESS
              1718      XED      Q$#1+Q$XENQ
              1719      ENUM      ENQ
              1720 *
              1721 *      ENQ -- SUBROUTINE TO SERIALIZE RESOURCE USE
              1722 *
              1723 *      THIS SUBPROGRAM RETURNS IMMEDIATELY IF THERE IS NO NEED
              1724 *      TO QUEUE. IF NECESSARY TO DO SO, IT SUSPENDS EXECUTION OF
              1725 *      THE CURRENT TASK UNTIL A WAKE-UP IS GENERATED BY A DEQ FOR
              1726 *      THIS QUEUE AND THE RESOURCE CAN BE ALLOCATED TO THIS TASK.
              1727 *
              1728 *      CALL WITH
              1729 *      C(XT) = TBLOCK-ADDRESS
              1730 *      C(XJ) = JBLOCK-ADDRESS
              1731 *      C(XQ) = QBLOCK-ADDRESS
              1732 *      ENTER BY
              1733 *      TSX L,Q$ENQ
              1734 *      CLOBBERS ALL BUT C(XT), C(XJ), AND C(XL)
              1735 *      USES NO LOCAL TEMPORARIES
              1736 *
              1737      INHIB      SAVE,ON
              1738 ENQ      BSS      0
              1739      AOS      AVAIL,Q      ONE MORE BLOCK CURRENTLY ON QUEUE
              1740      LDA      AVAIL,Q      UPDATE MAX LENGTH OF QUEUE
              1741      CMPA      MAX,Q
              1742      TNC      *+2
              1743      STA      MAX,Q
              1744      SZN      BUSY,Q      IS THIS QUEUE BUSY?
              1745      TNZ      ENQ1      YES, WILL HAVE TO QUEUE FOR IT
              1746      STX      T,BUSY,Q      NO, MARK WHO IS RESPONSIBLE
              1747      INHIB      RESTORE
              1748      BUGA
              1749      BUGBUG SET      BUGBUG+1
              1750      LDA      BUGBUG,DU
              1751      ORA      BUGBUG,DL
              1752      BUGQ
              1753      BUGBUG SET      BUGBUG+1
              1754      LDQ      BUGBUG,DU
              1755      ORQ      BUGBUG,DL
              1756      BUGXR      (0,X,Y,Z,Q)
              1757      BUGBUG SET      BUGBUG+1
000674      000012 0542 15
000675      000012 2352 15
000676      000011 1152 15
000677      000701 6022 00
000700      000011 7552 15
000701      000010 2342 15
000702      000716 6012 00
000703      000010 7412 15
              000704
              525201
000704      525201 2350 03
000705      525201 2750 07
              000706
              525202
000706      525202 2360 03
000707      525202 2760 07
              000710
              525203

```

Q

QUEUE MANAGEMENT -- ENQ

000710	525203	2200	03		LDX	0,BUGBUG,DU	
000711	525203	2220	03		LDX	X,BUGBUG,DU	
000712	525203	2230	03		LDX	Y,BUGBUG,DU	
000713	525203	2240	03		LDX	Z,BUGBUG,DU	
000714	525203	2250	03		LDX	Q,BUGBUG,DU	
000715	000000	7100	17	1751	TRA	0,L	RETURN TO CALLER
000716	000004	7470	11	1752 ENQ1	STX	L,T\$TRA,T	SAVE RESTART ADDRESS
000717	000004	6200	11	1753	EAX	0,OFFST,T	STORE POINTER WITH OFFSET
END OF BINARY CARD LPCP0016							
000720	777777	6000	00	1754	TZE	\$ERROR	***DBG
000721	777777	6040	00	1755	TMI	\$ERROR	***DBG
000722	000002	7170	15	1756	XED	XADD,Q	IN QUEUE LINKED LIST OF BLOCKS
			000723	1757	EXIT		
000723	001547	7100	00		TRA	\$EXIT	

Q

QUEUE MANAGEMENT -- DEQ

```

000724      1759      USE      CODE
              1760      HEAD      Q
              1761 *
              1762 *
              1763 *
              1764 *
              1765 *
              1766 *
              1767 *
              1768 DEQ      MACRO      QADDRESS
              1769      XED      QS#1+Q*XDEQ
              1770      ENDM      DEQ
              1771 *
              1772 *
              1773 *
              1774 *
              1775 *
              1776 *
              1777 *
              1778 *
              1779 *
              1780 *
              1781 *
              1782 *
              1783 *
              1784 *
              1785 *
              1786 *
              1787 *
              1788      INHIB      SAVE,ON
              1789 DEQ      BSS      0
              1790      DECRM      (AVAIL,Q)      ONE LESS BLOCK ON QUEUE
000724      000001 3362 07      LCQ      1,DL
000725      000012 0562 15      ASQ      AVAIL,Q
000726      000010 2342 15      1791      SZN      BUSY,Q      IF QUEUE IS NOT BUSY
000727      777777 6002 00      1792      TZE      $ERROR      WE ARE IN BAD TROUBLE
000730      000010 4502 15      1793      STZ      BUSY,Q      MARK QUEUE NOT BUSY
000731      000001 6202 15      1794      EAX      0,FIRST+1,Q      ADDRESS OF FIRST ELEMENT
000732      000001 1002 15      1795      CMPX     0,LAST,Q      DOES LAST POINT TO IT?
000733      000000 6002 17      1796      TZE      0,L      YES, QUEUE EMPTY -- RETURN
000734      000000 2202 15      1797      LDX      0,FIRST,Q      GET OFFSET POINTER TO BLOCK
000735      777777 6002 00      1798      TZE      $ERROR      ***BLEWIT
000736      777777 6042 00      1799      TMI      $ERROR      ***JBG
000737      777774 6222 10      1800      EAX      X,-OFFST,Q      RELATE TO THE BEGINNING OF BLOCK
000740      000010 7402 15      1801      STX      0,BUSY,Q      REMEMBER WHO IS RESPONSIBLE
000741      000003 2222 12      1802      LDX      X,LINK,X      GET NEXT ELEMENT ON QUEUE
000742      000000 7422 15      1803      STX      X,FIRST,Q      NOW MAKE IT FIRST
000743      003262 7172 00      1804      XED      XADD,TASK      ADD TO TASK QUEUE
000744      000001 1002 15      1805      CMPX     0,LAST,Q      LAST BLOCK ON QUEUE?

000724      000724
000724
END OF BINARY CARD LPCP0017

```


Q

QUEUE MANAGEMENT -- DEQ

000745 000000 6012 17
000746 000001 6202 15
000747 000001 7402 15
000750 000000 7100 17

1806 INZ 0,L
1807 EAX 0,FIRST+1,Q
1808 STX 0,LAST,Q
1809 INHIB RESTORE
1810 TRA 0,L

NO, RETURN
YES, SET UP QUEUE TO APPEAR EMPTY
BY MAKING LAST POINT TO FIRST+1
RETURN

G

QUEUE MANAGEMENT -- BRANCH MACRO

```

000751 1812 USE CODE
1813 HEAD G
1814 *
1815 *
1816 * BRANCH MACRO
1817 *
1818 * THIS MACRO CREATES AN ASYNCHRONOUS TASK TO BE PERFORMED AT A
1819 * LATER TIME. IT CAN GIVE THE CREATED TASK THE CURRENT TBLOCK
1820 * OR GIVE IT A NEW TBLOCK. EITHER WAY FOUR PARAMETERS MAY BE
1821 * BETWEEN THE TWO TBLOCKS.
1822 *
1823 * PAST INFORMATION IS PLACED IN TEMP1 THRU TEMP4 OF
1824 * THE TASK PLACED ON THE Q$TASK QUEUE.
1825 *
1826 * CALLS
1827 * T$GETT
1828 * CLOBBERS C(XX), C(X0)
1829 *
1830 *
1831 BRANCH MACRO PASS,XFER ADD,C(TEMP1),C(TEMP2),C(TEMP3),C(TEMP4)
1832 TSX 0,T$GETT GET A NEW TBLOCK
1833 EAX X,0,T C(XX) POINTS TO NEW TBLOCK
1834 LDX T,$SLINK,X C(XT) POINTS TO OLD TBLOCK
1835 INE '#3',',',',2
1836 LDQ #3 SAVE FIRST PARAMETER
1837 STQ T$TEMP1,X
1838 INE '#4',',',',2
1839 LDQ #4 SAVE SECOND PARAMETER
1840 STQ T$TEMP2,X
1841 INE '#5',',',',2
1842 LDQ #5 SAVE THIRD PARAMETER
1843 STQ T$TEMP3,X
1844 INE '#6',',',',2
1845 LDQ #6 SAVE FOURTH PARAMETER
1846 STQ T$TEMP4,X
1847 INE '#1',',',PASS',',1 T IS THE BLOCK THAT IS PASSED
1848 EAX T,0,X PASS NEW BLOCK
1849 EAX 0,#2 POINT TO TRANSFER ADDRESS
1850 STX 0,T$TRA,T INTO QUEUE BLOCK
1851 EAX 0,Q$OFFST,T PREPARE TO QUEUE
1852 XED Q$XADD+Q$TASK GET ON THE TASK QUEUE
1853 IFE '#1',',',PASS',',1 WHICH BLOCK TO WE GIVE BACK AS CURRENT
1854 EAX T,0,X GIVE BACK NEW BLOCK
1855 INE '#1',',',PASS',',1
1856 LDX T,$SLINK,X NO, GIVE BACK OLD BLOCK
1857 BUGXR (0,X)
1858 ENDM BRANCH
    
```

Q

QUEUE MANAGEMENT -- DIJKSTRA'S DESIGN

000751

1860 USE CODE
1861 HEAD G

1862 *
1863 *
1864 *
1865 *
1866 *
1867 *
1868 *
1869 *
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1871 *
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1893 *

DIJKSTRA DESIGN

DR. E.W. DIJKSTRA IN HIS PAPER ON "COOPERATING SEQUENTIAL PROCESSES" EXPLAINS A METHODOLOGY FOR SYNCHRONIZING ASYNCHRONOUS TASKS BY MEANS OF "SEMAPHORES" AND TWO PRIMITIVES--THE "P-OPERATION" AND THE "V-OPERATION". THESE PRIMITIVES OPERATE UPON SEMAPHORES AND REPRESENT THE ONLY WAY IN WHICH THE CONCURRENT PROCESSES (TASKS) MAY ACCESS THE SEMAPHORES.

DEFINITION: THE V-OPERATION IS AN OPERATION WITH ONE ARGUMENT, WHICH MUST BE THE IDENTIFICATION OF A SEMAPHORE. ITS FUNCTION IS TO INCREASE THE VALUE OF ITS ARGUMENT SEMAPHORE BY 1 AND AWAKEN ANY TASK WAITING ON THAT SEMAPHORE. THIS INCREASE AND AWAKENING IS TO BE REGARDED AS AN INDIVISIBLE OPERATION.

DEFINITIONS: THE P-OPERATION IS AN OPERATION WITH ONE ARGUMENT, WHICH MUST BE THE IDENTIFICATION OF A SEMAPHORE. ITS FUNCTION IS TO DECREASE THE VALUE OF ITS ARGUMENT SEMAPHORE AND IF THE ARGUMENT SEMAPHORE VALUE SHOULD GO NEGATIVE THE TASK DOING THE P-OPERATION IS BLOCKED WAITING FOR THE SEMAPHORE TO GO NON-NEGATIVE. THIS DECREASE AND POTENTIAL BLOCKING IS TO BE REGARDED AS AN INDIVISIBLE OPERATION.

IT IS THE P-OPERATION, WHICH REPRESENTS THE POTENTIAL DELAY, VIZ. WHEN A PROCESS INITIATES A P-OPERATION ON A SEMAPHORE, THAT AT THAT MOMENT IS = 0. IN THIS CASE THE TASK EXECUTING THE P-OPERATION IS BLOCKED AND CAN NOT BE RESTARTED UNTIL A V-OPERATION IS DONE ON THE SAME SEMAPHORE.

Q

QUEUE MANAGEMENT -- P (DOWN)

```

000751 1895 USE CODE
1896 HEAD Q
1897 *
1898 *
1899 *
1900 *
1901 * P (DOWN)
1902 * P DECREASES A SEMAPHORE COUNT AND IF THE COUNT SHOULD
1903 * GO NEGATIVE SUSPENDS THE CURRENT TASK UNTIL THE SEMAPHORE
1904 * IS RAISED.
1905 P MACRO SEMAPHORE-ADDRESS
1906 EAX Q,#1 MAKE Q POINT TO SEMAPHORE
1907 TSX L,QSP CALL SUBROUTINE
1908 ENDM P
1909 *
1910 *
1911 * P -- SUBROUTINE
1912 *
1913 * THIS SUBROUTINE RETURNS IMMEDIATELY IF THERE IS NO NEED
1914 * TO QUEUE. IF NECESSARY TO DO SO, IT SUSPENDS EXECUTION OF
1915 * THE CURRENT TASK UNTIL A WAKE-UP IS GENERATED BY A V (UP)
1916 * FOR THIS QUEUE AND THE RESOURCE CAN BE ALLOCATED TO THIS TASK.
1917 *
1918 * CALL WITH
1919 * C(XQ) = SEMAPHORE-ADDRESS
1920 * C(XT) = TBLOCK-ADDRESS
1921 * C(XL) = RETURN-ADDRESS
1922 * ENTER BY
1923 * TSX L,QSP
1924 * DESTROYS ALL BUT C(XT), C(XJ), AND C(XL)
1925 * USES NO LOCAL TEMPORARIES
1926 *
000751 1927 INHIB SAVE,ON
1928 P BSS 0
1929 LXL Z,0,Q GET SEMAPHORE
000751 000000 7242 15 1930 SBLX Z,1,DU LET SEM:=SEM-1
000752 000001 1242 03 1931 SXL Z,0,Q AND SAVE IT
000753 000000 4442 15 1932 TNC P1 WILL HE HAVE TO HANG?
000754 000767 6022 00 1933 BUGA NO. BUG REGISTERS
000755 000755 525204 BUGBUG SET BUGBUG+1
000755 525204 2352 03 LDA BUGBUG,DU
000756 525204 2752 07 ORA BUGBUG,DL
000757 000757 525205 1934 BUGQ AND RETURN
000757 525205 2362 03 BUGRUG SET BUGBUG+1
000760 525205 2762 07 LDQ BUGBUG,DU
000761 000761 525206 1935 BUGXR (0,X,Y,Z,Q) ORQ BUGBUG,DL
000761 525206 2202 03 BUGRUG SET BUGBUG+1
000762 525206 2222 03 LDX 0,BUGBUG,DU
LDX X,BUGBUG,DU
    
```

Q

QUEUE MANAGEMENT -- P (DOWN)

000763	525206	2232	03		LDX	Y,BUGBUG,DU	
000764	525206	2242	03		LDX	Z,BUGBUG,DU	
000765	525206	2252	03		LDX	G,BUGBUG,DU	
000766	000000	7102	17	1936	TRA	O,L	
000767	000004	7472	11	1937	STX	L,TSTRA,T	SAVE RESTART ADDRESS
000770	000000	7412	15	1938	STX	T,O,Q	STORE POINTER TO TBLOCK
				1939	EXIT		
000771	001547	7102	00		TRA	\$EXIT	
				1940	INHIB	RESTORE	

Q QUEUE MANAGEMENT -- V (UP)

```

000772      1942      USE      CODE
              1943      HEAD      Q
              1944 *
              1945 *
              1946 *
              1947 *
              1948 *
              1949 *
              1950 *
              1951 V      MACRO      SEMAPHORE-ADDRESS
              1952      EAX      Q,#1      MAKE Q POINT TO SEMAPHORE
              1953      TSX      L,Q$V      CALL SUBROUTINE
              1954      ENDM      V
              1955 *
              1956 *
              1957 *
              1958 *
              1959 *
              1960 *
              1961 *
              1962 *
              1963 *
              1964 *
              1965 *
              1966 *
              1967 *
              1968 *
              1969 *
              1970 *
              1971 *
              1972 *
              1973 *
              1974 *
              1975 *
              1976 *
              1977
              1978 V      INHIB      SAVE,QV
              BSS      Q
000772      1979      LXL      X,Q,Q      GET SEMAPHORE
000772 000000 7222 15      1980      ADLX     X,1,DU      SEM:=SEM+1
000773 000001 0222 03      1981      SXL      X,Q,Q      SAVE IT
000774 000000 4422 15      1982      TZE      *+2      IF SEM<=0, THEN AWAKEN
000775 000777 6002 00      1983      TPL      Q,L      NO, JUST RETURN TO CALLER
000776 000000 6052 17      1984      LDX      Q,Q,Q      GET POINTER TO TBLOCK
000777 000000 2202 15      1985      EAX      Q,OFFST,Q      GET OFFSET POINTER
001000 000004 6202 10      1986      TZE      $ERROR      ***DBG
001001 777777 6002 00      1987      TMI      $ERROR      ***DBG
001002 777777 6042 00      1988      XED      XADD,TASK      ADD IT TO TASK GUEUE
001003 003262 7172 00      1989      BUGU     (Q,Q)      BUG IT
              001004
              525207      BUGBUG SET      BUGBUG+1
    
```

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LINE PRINTER/ CARD PUNCH MODULE

G

QUEUE MANAGEMENT -- V (UP)

001004	525207	2202	03		LDX	0,BUGBUG,DU
001005	000000	7402	15		STX	0,0,0
		001006		1990	BUGXR	(0,X)
		525210			BUGBUG SET	BUGBUG+1
001006	525210	2202	03		LDX	0,BUGBUG,DU
001007	525210	2222	03		LDX	X,BUGBUG,DU
001010	000000	7102	17	1991	TRA	0,L
				1992	INHIB	RESTORE

RETURN TO CALLER

Q

QUEUES -- Q\$TASK QUEUE

1994 HEAD Q

1995 *

1996 *

1997 *

1998 *

1999 *

2000 *

2001 *

2002 *

2003 *

2004 *

2005 *

2006

Q\$TASK QUEUE

THIS QUEUE IS USED TO SCHEDULE THE ACTIVITY OF THE PROCESSOR
 ENTRIES ARE MADE BY THE USE OF THE NORMAL XED Q\$ADD
 FEATURE AND ALSO BY AN XED CHAIN INSTIGATED BY THE ACTION OF
 A TRAP BEING SPRUNG BY THE EXECUTIVE. ENTRIES ARE REMOVED BY
 A SPECIAL PROGRAM MODULE WHICH IS CALLED BY THE EXIT MACRO
 WHENEVER THE PROCESSOR IS FREE TO WORK ON A NEW TASK.

001011
 003260
 003260
 003260

QUEUE TASK
 USE Q\$TOR
 EVEN

TASK

BSS 0
 ARG \$ERROR
 ARG Q\$TASK+Q\$FIRST+1
 STX 0,Q\$TASK+Q\$LAST,DI
 STX 0,Q\$TASK+Q\$LAST
 EAX Q,Q\$TASK
 TSX L,Q\$ENQ

END OF BINARY CARD LPCP0019

003260 777777 0000 00
 003261 003261 0000 00
 003262 003261 7400 54
 003263 003261 7400 00
 003264 003260 6250 00
 003265 000674 7070 00
 003266 003260 6250 00
 003267 000724 7070 00
 003270 000000 0000 00
 003271 000000000000
 003272 000000000000
 003273 000000000000
 003274 000000000000
 003275 124101123113

EAX Q,Q\$TASK
 TSX L,Q\$DEQ
 ARG 0
 DEC 0
 DEC 0
 DEC 0
 DEC 0
 UASCI 1,TASK

ABBREVIATION

Q

QUEUES -- QSCORE QUEUE

2008 HEAD Q
 2009 *
 2010 *
 2011 * QSCORE QUEUE
 2012 *
 2013 * THIS QUEUE HOLDS THOSE TASKS WHICH REQUIRE MEMORY ALLOCATIONS
 2014 * WHEN A PRIOR MEMORY REQUEST IS IN OPERATION. THIS IS NOT
 2015 * TO BE CONFUSED WITH THE THE FREE MEMORY LIST.
 2016 *
 2017 *

	003276		QUEUE	CORE	
	003276		USE	QSTOR	
	003276		EVEN		
	003276	CORE	BSS	0	
003276	777777	0000 00	ARG	\$ERROR	
003277	003277	0000 00	ARG	QSCORE+QSFIRST+1	
003300	003277	7400 54	STX	0,QSCORE+QSLAST,DI	
003301	003277	7400 00	STX	0,QSCORE+QSLAST	
003302	003276	6250 00	EAX	Q,QSCORE	
003303	000674	7070 00	TSX	L,QSEQ	
003304	003276	6250 00	EAX	Q,QSCORE	
003305	000724	7070 00	TSX	L,QSEQ	
003306	000000	0000 00	ARG	0	
003307	000000	000000	DEC	0	
003310	000000	000000	DEC	0	
003311	000000	000000	DEC	0	
003312	000000	000000	DEC	0	
END OF BINARY CARD	LPC0020				
003313	103117	122105	UASCI	1,CORE	ABBREVIATION

0

CORE MANAGEMENT -- GENERAL INTRODUCTION

		001011	2019	USE	CODE	
			2020	HEAD	R	
			2021 *			
			2022 *			GENERAL INTRODUCTION
			2023 *			
			2024 *			BELOW IS THE FREE MEMORY LIST. THE LIST CONSISTS OF A
			2025 *			POSSIBLY EMPTY LINKED LIST OF BLOCKS. THE FORWARD/BACKWARD
			2026 *			POINTERS OF A BLOCK POINT TO THE FIRST WORD OF THE SUCCEEDING/
			2027 *			PRECEEDING BLOCKS, RESPECTIVELY. THE LINK POINTERS ARE
			2028 *			STORED IN WORDS 0 AND 1, RESPECTIVELY, OF THE BLOCK.
			2029 *			HENCE THE MINIMAL SIZE OF A BLOCK IS TWO (2) WORDS. THE
			2030 *			TOTAL LENGTH OF THE BLOCK IS ALSO KEPT IN WORD 0. BY
			2031 *			DESIGN CONVENTIONS, THE POINTERS ARE UPPER HALF QUANTITIES
			2032 *			AND THE LENGTH IS A LOWER HALF QUANTITY. THE EMPTY LIST
			2033 *			IS DENOTED BY THE FORWARD LINK OF THE 'FIRST' POINTING TO
			2034 *			'LAST' AND BY THE BACKWARD LINK OF 'LAST' POINTING TO 'FIRST'.
			2035 *			
			2036 *			
			2037 *			NOTE THAT ALL ALLOCATIONS ARE DONE IN MULTIPLES OF EIGHT.
			2038 *			A BLOCK MUST BE DE-ALLOCATED AS ONE ENTITY. NO PARTIAL RELEASES
			2039 *			ARE ALLOWED.
			2040 *			
			2041 *			FREE MEMORY LIST
			2042 *			
		003353	2043	USE	STORE	
003353	003770	000000	2044	FIRST	ZERO	\$NEXTF,0 FORWARD LINK/ LENGTH OF BLOCK
003354	000000	000000	2045		ZERO	0, BACKWARD LINK/ <NOT USED>
			2046 *			
			2047 *			
			2048	LAST	ZERO	0,0 FORWARD LINK/ LENGTH OF BLOCK
003355	000000	000000	2049		ZERO	\$NEXTB, BACKWARD LINK/ <NOT USED>
003356	003770	000000	2050		USE	PREVIOUS
		001011				

R

CORE MANAGEMENT -- MACROS

```

001011 2052 USE CODE
        2053 HEAD R
        2054 *
        2055 *
        2056 *
        2057 *
        2058 *
        2059 *
        2060 *
        2061 *
        2062 *
        2063 *
        2064 *
        2065 *
        2066 *
        2067 *
        2068 *
        2069 *
        2070 *
        2071 *
        2072 *
        2073 *
        2074 *
        2075 *
        2076 *
        2077 *
        2078 *
        2079 *
        2080 *
        2081 *
        2082 *
        2083 *
        2084 *
        2085 *
        2086 *
        2087 *
        2088 *
        2089 *
        2090 *
        2091 *
        2092 *
        2093 *
        2094 *
        2095 *
        2096 *
    
```

MACROS

THE FOLLOWING MACROS ARE USED TO ALLOCATE/DEALLOCATE BLOCKS OF CORE. ONLY INDEX REGISTER J IS GUARANTEED ACROSS THESE CALLS.

GETC MACRO

CALL WITH
 C(AL) = NUMBER-OF-WORDS-REQUESTED
 ENTER BY
 TSX L,R\$GETC
 RETURNS TO O,L
 RETURNS WITH
 C(AU) = BUFFER-ADDRESS
 C(AL) = BUFFER-LENGTH

```

2074 GETC MACRO WORD-COUNT-ADDRESS/ 'A'
2075     INE     '#1','A'
2076     LDA     #1
2077     TSX     L,R$GETC
2078     ENDM    GETC
    
```

RELC MACRO

CALL WITH
 C(AU) = BUFFER-ADDRESS
 C(AL) = BUFFER-ADDRESS
 ENTER BY
 TSX L,R\$RELC
 RETURNS TO O,L
 RETURNS WITH
 DESTROYS C(A)

```

2092 RELC MACRO RELEASE-ADDRESS&COUNT/ 'A'
2093     INE     '#1','A'
2094     LDA     #1
2095     TSX     L,R$RELC
2096     ENDM    RELC
    
```

R

CORE MANAGEMENT -- ALLOCATION

```

001011      2098      USE      CODE
                2099      HEAD      R
                2100 *
                2101 *
                2102 *
                2103 *
                2104 *
                2105 *
                2106 *
                2107 *
                2108 *
                2109 *
                2110 *
                2111 *
                2112 *
                2113 *
                2114 *
                2115 *
                2116 *
                2117 *
                2118 *
                2119 *
                2120 *
                2121 *
                2122 *
                2123 *
                2124 *
                2125 *
                2126 *
                2127 *
                2128 *
                2129 *
                2130 *
                2131 *
                2132 *
                2133      USE      CODE
                2134      BSS      0
                2135      SREG    T$STEM16,T      SAVE ALL REGISTERS
                2136      ENQ     CORE            AND GET ON THE CORE QUEUE
                2137      XED     Q$SCORE+Q$XENQ
                2138      BSS      0
                2139      LDA     T$STEM12,T      RESTORE A AND
                2140      ADLA    7,DL            ROUND UP LENGTH
                2141      ANA     =0777770,DL    TO A MULTIPLE OF 8
                2142      TZE     $ERROR        ***BLEWIT
                2143      CMPA    $RQMAX,DL     HOW MUCH IS REQUESTED
                2144      TRC     $ERROR        TOO MUCH!
                2145      STA     T$STEM12,T      SAVE NEW LENGTH
                2146      EAA     0,AL          MOVE LENGTH TO AU
                STA     T$STEM10,T      SAVE FOR INDEX REGISTER OPERATIONS
001011      000010 7530 11
                001011
001012      003302 7170 00
                001012
                001013
001013      000014 2350 11
001014      000007 0350 07
001015      777770 3750 07
001016      777777 6000 00
001017      004001 1150 07
001020      777777 6030 00
001021      000014 7550 11
001022      000000 6350 05
001023      000016 7550 11
    
```

ALLOCATION

THIS SUBROUTINE REMOVES A BLOCK OF N WORDS FROM THE FREE MEMORY LIST. IF THERE IS NO BLOCK OF N WORDS OR GREATER ON THE FREE LIST, THERE A REQUEST FOR MORE MEMORY IS MADE. AND THE PROCESS IS REPEATED. SINCE MEMORY REQUESTS ARE OF THE TRAPPING MME BRAND, IT IS NECESSARY TO FIRST CHECK TO SEE IF A MEMORY REQUEST IS CURRENTLY IN OPERATION. IF SO, THEN THIS REQUEST IS PUT TO SLEEP BY QUEUEING IT ON THE Q\$CORE QUEUE. WHEN THE MEMORY REQUEST COMPLETES THE NEXT ITEM (IF ANY) ON Q\$CORE IS AWAKENED. CORE ALLOCATION IS ON A FIRST FIT BASIS. THIS IS TO ALLOW HOLES TO FLOW TOWARD HIGHER MEMORY.

CALL BY

TSX L,R\$GETC

CALL WITH

C(AL) = NUMBER-OF-WORDS-REQUESTED

C(T) = TRAP BLOCK

CALLS

ENQ CORE

R\$MORE

R\$RELC

DEQ

CORE

RETURNS WITH

C(AU) = ADDRESS

C(AL) = NUMBER OF WORDS

EXIT TO 0,L

PRESERVES ALL REGISTERS EXCEPT C(A)

USES TEMPORARIES T\$STEM9 THRU T\$STEM16

R CORE MANAGEMENT -- ALLOCATION

```

001024 000014 2350 11 001024 2147 GETC3 BSS 0
001025 003353 2240 00 2148 LDA T$STEM12,T GET BACK REQUEST
001026 000302 5002 00 2149 LDX Z,FIRST GET PTR TO FIRST FREE BLOCK
001027 000000 1150 14 2150 GETC6 BSS 0
END OF BINARY CARD LPCP0021 2151 RPL ,TNC,T7E RUN DOWN LINKED LIST
001030 001035 6000 00 2152 CMPA 0,Z FOR A BLOCK BIG ENOUGH
001031 001035 6020 00 2153 TZE GETC7 FOUND A BLOCK THAT IS 'JUST RIGHT'
001032 000000 2240 14 2154 TNC GETC7 MORE THAT BIG ENOUGH
001033 001026 6010 00 2155 LDX Z,0,Z GET BACK PTR OF WHERE WE LEFT OFF
001034 001077 7100 00 2156 TNZ GETC6 IF NOT AT END OF LIST, KEEP SEARCHING
001035 000014 7440 11 2157 TRA MORE WON'T FIT ANYWHERE, GET MORE CORE
2158 GETC7 STX Z,T$STEM12,T SAVE PTR TO BLOCK TO RETURN TO CALLER
2159 *
2160 * Z POINTS TO BLOCK TO DELINK
2161 *
001036 000001 2230 14 2162 LDX Y,LINKB,Z ASSIGN Y TO PREDECESSOR
001037 000000 2220 14 2163 LDX X,LINKF,Z AND X TO SUCCESSOR
001040 000000 7200 14 2164 LXL 0,LEN,Z GET THE LENGTH OF THIS BLOCK
001041 000016 1200 11 2165 SBLX 0,T$STEM10,T MINUS THE AMOUNT REQUIRED
001042 001051 6000 00 2166 TZE GETC4 AH HA, BLOCK JUST FITS
001043 777777 6040 00 2167 TMI $ERROR ***BLEWIT
001044 000016 0240 11 2168 ADLX Z,T$STEM10,T SET Z TO START OF EXCESS OF BLOCK
001045 000000 4400 14 2169 SXL 0,LEN,Z SET THE LENGTH OF THE REMAINING BLOCK
001046 000000 7420 14 2170 STX X,LINKF,Z SET FORWARD LINK OF REMAINING BLOCK
001047 000001 7440 12 2171 STX Z,LINKB,X ' BACKWARD ' ' ' '
001050 000000 6220 14 2172 EAX X,0,Z COPY Z INTO X FOR FUDGE
001051 000000 7420 13 2173 GETC4 STX X,LINKF,Y SET FORWARD LINK OF PREDECESSOR
001052 000001 4500 12 2174 STZ LINKB,X ***DBG
001053 000001 7430 12 2175 STX Y,LINKB,X SET BACKWARD LINK OF SELF
001054 003304 7170 00 2176 DEQ CORE SEE IF ANYONE ELSE WANTS CORE
XED Q$CORE+Q$XDEQ
2177 *
2178 * ZERO BLOCK FOR USER
2179 *
001055 000014 2350 11 2180 LDA T$STEM12,T GET LENGTH
END OF BINARY CARD LPCP0022
001056 777777 3750 07 2181 ANA -1,DL MASK TO COUNT
001057 777777 6240 05 2182 EAX Z,-1,AL SAVE COUNT MINUS ONE IN Z
001060 000010 7350 00 2183 ALS 10-2 PUT COUNT IN REPEAT FIELD
001061 001400 6200 05 2184 EAX 0,$$ABIT+$$$BIT,AL TERMINATE CONDITIONS AND COUNT
001062 000000 4310 07 2185 FLD 0,DL CLEAR AQ
001063 000014 2220 11 2186 LDX X,T$STEM12,T GET STARTING ADDRESS
001064 000002 6230 12 2187 EAX Y,2,X SET SET INDEX REGISTER
001065 000000 5602 04 2188 GETC8 RPDx ,4 ZERO MEMORY LIKE MAD
001066 000000 7570 12 2189 STAQ 0,X CHONK
001067 000000 7570 13 2190 STAQ 0,Y CHONK
001070 002000 1240 03 2191 SBLX Z,$1K,DU REMOVE A K AS DONE
001071 001065 6030 00 2192 TRC GETC8 TEST FOR MORE
2193 *

```

R

CORE MANAGEMENT -- ALLOCATION

			2194 *	FINISH UP	
			2195 *		
		001072	2196	GETC5 BSS 0	
001072	000016	3220 11	2197	LCX X,\$STEM10,T	GET COMPLEMENT OF LENGTH TO RETURN
001073	003320	0420 00	2198	ASX X,\$AVAIL	REDUCE AVAIL BY THAT AMOUNT
001074	777777	6040 00	2199	TMI \$ERROR	***BLEWIT
001075	000010	0730 11	2200	LREG T\$STEM16,T	RESTORE REGISTERS
001076	000000	7100 17	2201	TRA 0,L	RETURN

R CORE MANAGEMENT -- REQUEST MORE

```

001077 2203 USE CODE
2204 HEAD R
2205 *
2206 *
2207 * REQUEST MORE
2208 *
2209 * THIS SUBROUTINE DOES A REQUEST FOR MORE MEMORY. IT ASKS
2210 * FOR ONE CORE UNIT. UPON THE SUCCESSFUL COMPLETION OF THE
2211 * REQUEST, THE NEW MEMORY IS LINKED ON THE FREE LIST BY
2212 * CALLING RELC.
2213 *
2214 * CALL BY
2215 * TRA MORE (ONLY *GETC* SHOULD CALL THIS ROUTINE)
2216 * CALL WITH
2217 * C(T) = TRAP-BLOCK
2218 * CALLS
2219 * $CHSEG
2220 * R$RELC
2221 * EXIT TO R$GETC3
2222 * DESTROYS <ALL REGISTERS>
2223 * USES NO LOCAL TEMPORARIES
2224 *
001077 2225 MORE BSS 0
001077 003323 2220 00 2226 LDX X,$M$TOP GET CURRENT TOP OF MEMORY
001100 001000 0220 03 2227 ADLX X,$M$QUAN,DU ADD ONE CORE UNIT
001101 003323 7420 00 2228 STX X,$M$TOP SAVE NEW EXPECTED TOP
001102 001301 7070 00 2229 TSX L,$M$REG REQUEST MEMORY
001103 2230 MORE1 BSS 0
END OF BINARY CARD LPCP0023
001103 003323 2350 00 2231 LDA $M$TOP GET BACK OLD TOP OF MEMORY
001104 001000 1350 03 2232 SBLA $M$QUAN,DU IN AU
001105 001000 2750 07 2233 ORA $M$QUAN,DL AND LENGTH IN AL
001106 001110 7070 00 2234 RELC A NOW DO A RELEASE TO LINK ON FREE LIST
001107 001024 7100 00 2235 TSX L,$R$RELC
TRA GETC3 TRY AGAIN
    
```

R

CORE MANAGEMENT -- DE-ALLOCATION

001110

2237 USE CODE
2238 HEAD R

2239 *
2240 *
2241 *
2242 *
2243 *
2244 *
2245 *
2246 *
2247 *
2248 *
2249 *
2250 *
2251 *
2252 *
2253 *
2254 *
2255 *
2256 *
2257 *
2258 *
2259 *
2260 *
2261 *

DE-ALLOCATION

THIS ROUTINE LINKS THE 'RELEASED' BLOCK OF MEMORY INTO THE FREE MEMORY LIST ACCORDING TO THE BLOCK'S ADDRESS. SINCE THE FREE LIST IS ORDERED BY BLOCK ADDRESSES FROM LOWEST TO HIGHEST, IN ORDER TO MAKE INSERTIONS AND DELETIONS EASIEST THERE IS ASSOCIATED WITH EACH BLOCK A FORWARD AND BACKWARD POINTER AS WELL AS A COUNT OF THE TOTAL NUMBER OF WORDS IN THE BLOCK.

CALL BY
TSX L,R\$RELC
CALL WITH
C(AU) = BLOCK-ADDRESS
C(AL) = LENGTH
CALLS
R\$MEMCK (CONDITIONALLY)
EXIT TO O,L
PRESERVES ALL REGISTERS EXCEPT C(A)
USES LOCAL TEMPORARIES MREG THRU MREG+7

001110
001110

001110 003400 7530 00
001111 777777 3750 03
001112 003406 7550 00
001113 003322 2350 00
001114 003323 2360 00
001115 003406 1110 00
001116 777777 6010 00
001117 003406 2220 00
001120 003404 7200 00
001121 777777 6000 00
001122 003406 0400 00
001123 000000 4400 12
001124 003406 1110 00
001125 777777 6010 00
001126 003353 2220 00
001127 001131 7100 00

2262 USE CODE
2263 RELC BSS 0
2264 SREG MREG
2265 ANA -1,DU
2266 STA TEMP
2267 LDA \$MTOPO
2268 LDQ \$MTOP
2269 CWL TEMP
2270 TNZ \$ERROR
2271 LDX X,TEMP
2272 LXL O,MREG+4
2273 TZE \$ERROR
2274 ASX O,TEMP
2275 SXL O,LEN,X
2276 CWL TEMP
2277 TNZ \$ERROR
2278 LDX X,FIRST
2279 TRA **2

RELEASE A BLOCK OF MEMORY
SAVE REGISTERS
ISOLATE RELEASE ADDRESS
SAVE FOR TEST
TEST TO SEE IF ADDRESS
OF BLOCK TO BE RELEASED
IS IN BUFFER AREA
BLEWIT
GET INSERT ADDRESS
GET LENGTH
***BLEWIT
ADD TO STARTING ADDRESS
SAVE IN BLOCK
TEST IF ALL IS IN RANGE
***BLEWIT
GET POINTER TO FIRST FREE BLOCK OF FREE LIST
ENTER SEARCH LOOP

2280 *
2281 * LOCATE WHERE TO INSERT BLOCK IN FREE LIST ACCORDING TO ADDRESS
2282 *

END OF BINARY CARD LPC0024

001130 000000 2220 12
001131 001144 6000 00
001132 003404 1020 00

2283 LDX X,LINKF,X
2284 TZE RELC2
2285 CMPX X,INSRT

GET PTR TO NEXT BLOCK ON FREE LIST
DID WE JUST FALL OFF THE END OF THE LIST?
NO,ARE WE POINTING PAST THE HOLE?

R CORE MANAGEMENT -- DE-ALLOCATION

```

001133 001130 6020 00      2286      TNC      *-3      NO. LOOK AGAIN
                          2287 *
                          2288 *
                          2289 *
                          2290 *      LOCATED POSITION OF BLOCK WITH XR=X POINTING TO SUCCESSOR
                          2291 *
                                001134
001134 003404 2240 00      2292 RELC1 BSS      0
001135 000001 2230 12      2293      LDX      Z,INSRT      NOW Z POINTS TO INSERT
                          2294      LDX      Y,LINKR,X      *** Y POINTS TO PREDECESSOR
                          2295                          *** X POINTS TO SUCCESSOR
001136 000001 4500 14      2296      STZ      LINKR,Z      ***DBG
001137 000001 7430 14      2297      STX      Y,LINKR,Z      SET BACKWARD LINK OF INSERT
001140 000000 7420 14      2298      STX      X,LINKF,Z      " FORWARD " " "
001141 000001 7440 12      2299      STX      Z,LINKR,X      RESET SUCCESSOR'S BACKWARD LINK
001142 000000 7440 13      2300      STX      Z,LINKF,Y      AND FINISH LINKING BY RESETTING PREDECESSOR
                          2301                          ***FORWARD LINK
001143 001146 7100 00      2302      TRA      RELC3      NOW DO SOME RE-COMBINING
                          2303 *
                          2304 *      RELEASED BLOCK FITS BETWEEN THE LAST FREE BLOCK AND *LAST*
                          2305 *
                                001144
001144 003355 6220 00      2306 RELC2 BSS      0
001145 001134 7100 00      2307      EAX      X,LAST      SO MAKE X POINT TO SUCCESSOR
                          2308      TRA      RELC1      AND TREAT NORMALLY
                          2309 *
                          2310 *
                          2311 *      NOW THAT THE BLOCK HAS BEEN CORRECTLY LINKED ON THE FREE
                          2312 *      LIST TRY TO RECOMBINE WITH ITS BUDDIES.
                          2313 *      THERE ARE FOUR (4) CASES TO CONSIDER:
                          2314 *
                          2315 *      CASE I: THERE EXISTS A BUDDY ABOVE IN MEMORY
                          2316 *      CASE II: THERE EXISTS A BUDDY BELOW IN MEMORY
                          2317 *      CASE III: ALL OF THE ABOVE
                          2318 *      CASE IV: NONE OF THE ABOVE
                          2319 *
                                001146
001146 001206 7070 00      2320 RELC3 BSS      0
001147 000000 6220 14      2321      TSX      L,RELC4      CASE I: TRY REJOINING WITH BUDDY ABOVE
001150 000000 6240 13      2322      EAX      X,0,Z      LET X POINT TO INSERT
001151 003404 7440 00      2323      EAX      Z,0,Y      LET Z POINT TO ITS PREDECESSOR
001152 001206 7070 00      2324      STX      Z,INSRT      NOW CALL THE PREDECESSOR INSERT
                          2325      TSX      L,RELC4      FUDGE CASE II TO CASE I
    
```

R CORE MANAGEMENT -- DE-ALLOCATION

```

2327 *
2328 * WE'RE DONE. BLOCK IS CORRECTLY LINKED IN LIST.
2329 * GRAB CALLER'S REGISTERS AND RETURN.
2330 *
001153 003404 7220 00 2331 LXL X,%REG+4 GET BACK AMOUNT RELEASED
001154 003320 0420 00 2332 ASX X,%$AVAIL STATISTIC: FREE CORE
001155 003323 2220 00 2333 LDX X,%$MTOF GET TOP OF MEMORY
END OF BINARY CARD LPCP0025
001156 003322 1220 00 2334 SBLX X,%$MTOF0 COMPUTE BUFFER SIZE
001157 001000 1220 03 2335 SBLX X,%$MQUAN,DU MINUS A CORE QUANTUM
001160 001202 6020 00 2336 INC RELCX OK, FORGET ABOUT IT
001161 003321 1020 00 2337 CMPX X,%$MEMR0 COMPARE IT TO MEMORY REQUIREMENTS
001162 001202 6020 00 2338 INC RELCX CAN WE AFFORD TO RELEASE SOME MEMORY?
001163 003306 2340 00 2339 SIN OS$BUSY+OS$CORE BUT FIRST IS MEMORY BUSY?
001164 001202 6010 00 2340 INC RELCX DON'T UNDER ANY CIRCUMSTANCES TRY TO CALL MEMCK.
001165 003360 2210 03 2341 LDX T,%TRAP,DU GET DUMMY FOR
001166 003360 7260 07 2342 LXL J,%TRAP,DL END DUMMY JOB
001167 001167 001167 2343 BRANCH NOPASS, MEMCK OK TO SET UP TASK TO RELEASE MEMORY
001167 001325 7000 00
001170 000000 6220 11 EAX X,0,T
001171 000005 2210 12 LDX T,%$LINK,X
001172 000000 6210 12 EAX T,0,X
001173 001223 6200 00 EAX OS$MEMCK
001174 000004 7400 11 STX OUT$TRA,T
001175 000004 6200 11 EAX OS$OFFST,T
001176 003262 7170 00 XED OS$ADD+OS$TASK
001177 000005 2210 12 LDX T,%$LINK,X
001200 001200 525211 2200 11 BUGXR (0,X)
001201 525211 2220 03 BUGBUG SET BUGBUG+1
001202 003400 0730 00 2344 RELCX BSS 0 EXIT
001203 001203 525212 2350 03 2345 LREG MREG RESTORE REGISTERS
001204 525212 2750 07 2346 BUGA BUGA REMIND HIM THAT A IS INVALID
END OF BINARY CARD LPCP0026
001205 000000 7100 17 2347 TRA BUGBUG,DL
001205 000000 7100 17 2347 TRA O,L RETURN TO CALLER
    
```

R

CORE MANAGEMENT -- DE-ALLOCATION

```

2349 *
2350 * THIS SUBROUTINE TRIES TO RECOMBINE A BLOCK OF MEMORY
2351 * AND ITS BUDDY ABOVE (IF IT IS ALSO IN THE FREE LIST)
2352 * INTO A SINGLE BLOCK.
2353 *
2354 * CALL BY
2355 *     TSX L,RELC4
2356 * CALL WITH
2357 *     C(Z) = INSERT-BLOCK-ADDRESS
2358 *     C(X) = INSERT'S SUCCESSOR
2359 * EXIT TO 0,L
2360 * DESTROYS 0,X,Z
2361 *
001206 000000 7200 14 001206 2362 RELC4 BSS 0
001207 003404 0200 00 2363 LXL 0,LEN,Z COMPUTE ADDRESS OF BUDDY IN
001210 000000 1000 14 2364 ADLX 0,INSRT ...UPPER MEMORY
001211 000000 6010 17 2365 CMPX 0,LINKF,Z IS IT PART OF THE FREE LIST?
2366 TNZ 0,L NO, SO RETURN TO CALLER
2367 *
2368 * FOUND BUDDY
2369 *
001212 000000 2200 12 2370 LDX 0,LINKF,X RESET INSERT'S FORWARD LINK TO
001213 000000 7400 14 2371 STX 0,LINKF,Z THAT OF BUDDY'S.
001214 000001 7440 10 2372 STX Z,LINKB,0 RESET PREDECESSOR'S BACKWARD POINTER
001215 000000 7200 12 2373 LXL 0,LEN,X GET THE LENGTH OF BUDDY
001216 003406 7400 00 2374 STX 0,TEMP SAVE IT
001217 000000 7200 14 2375 LXL 0,LEN,Z GET LENGTH OF INSERT
001220 003406 0200 00 2376 ADLX 0,TEMP TO GET TOTAL LENGTH
001221 000000 4400 14 2377 SXL 0,LEN,Z AND RESET LENGTH OF BLOCK
001222 000000 7100 17 2378 TRA 0,L RETURN TO CALLER
2379 *
2380 *
003357 2381 USE STORE
003360 2382 EIGHT
003360 2383 TRAP BSS T$LEN DUMMY TCB WHEN CREATING MEMCK
003400 2384 MREG EQU TRAP+T$LEN-8 TEMP REGISTER STORAGE
003404 2385 INSRT EQU MREG+4 POINTER TO INSERT BLOCK
003406 2386 TEMP EQU MREG+6 FOR SCRATCH WORK
001223 2387 USE PREVIOUS

```

R CORE MANAGEMENT -- MEMORY RELEASE

```

001223 2389 USE CODE
2390 HEAD R
2391 *
2392 *
2393 *
2394 *
2395 *
2396 *
2397 *
2398 *
2399 *
2400 *
2401 *
2402 *
2403 *
2404 *
2405 *
2406 *
2407 *
2408 *
2409 *
2410 *
2411 *
2412 *
2413 *
2414 *
001223 2415 MEMCK BSS 0
001223 2416 ENQ CORE GET ON CORE QUEUE
001223 003302 7170 00 XED Q$CORE+Q$XENQ
001224 003323 2220 00 2417 LDX X,$MTOPT GET TOP OF BUFFER AREA
001225 003322 1220 00 2418 SBLX X,$MTOPO MINUS BOTTOM = TOTAL
001226 000027 7420 11 2419 STX X,$STEMP1,T C($STEMP1,T) = TOTAL
END OF BINARY CARD LPCP0027
001227 003320 1220 00 2420 SBLX X,$AVAIL MINUS AVAIL = USED
001230 777777 6040 00 2421 TMI $ERROR ***dLEWIT
001231 000026 7420 11 2422 STX X,$STEMP2,T C($STEMP2,T) = USED
001232 003321 2350 00 2423 LDA $MEMRQ GET AMOUNT OF MEMORY REQUIRED
001233 000026 1350 11 2424 SBLA $STEMP2,T MINUS USED = AMOUNT NEEDED STILL
001234 000025 7550 11 2425 STA $STEMP3,T SAVE IT
001235 777777 6040 00 2426 TMI $ERROR ***dLWEIT
001236 001276 6000 00 2427 TZE MEMX EXIT IF CONDITIONS CHANGED ON US
001237 000022 7710 00 2428 ARL 36-18 MOVE TO AL FOR RPL
2429 *
2430 * SEARCH FOR BLOCK OF *NEEDED* SIZE
2431 *
2432 *
2433 *
2434 *
2435 *
2436 *
001240 003353 2240 00 2432 LDX Z,FIRST GET POINTER TO FIRST FREE BLOCK
001241 000302 5002 00 2433 MEM1 RPL $TNC,TZE RUN DOWN LINKED LIST
001242 000000 1150 14 2434 CMPA 0,Z FOR A BLOCK BIG ENOUGH
001243 001250 6000 00 2435 TZE MEM2 FOUND A BLOCK THAT IS *JUST RIGHT*
001244 001250 6020 00 2436 TNC MEM2 MORE THAN BIG ENOUGH

```

```

R
CORE MANAGEMENT -- MEMORY RELEASE

001245 000000 2240 14 2437 LDX Z,0,Z GET BACK PTR OF WHERE WE LEFT OFF
001246 001241 6010 00 2438 TNZ MEM1 IF NOT AT END OF LIST, KEEP SEARCHING
001247 001276 7100 00 2439 TRA MEMX OTHERWISE EXIT
2440 *
2441 * Z POINTS TO BLOCK NEEDED EVENTUALLY
2442 *
001250 000000 7230 14 2443 MEM2 LXL Y,LEN,Z GET THE LENGTH OF THIS BLOCK
001251 003355 6220 00 2444 EAX X,LAST GET PTR TO LAST BLOCK
001252 000001 1040 12 2445 CMPX Z,LINK,X DOES Z POINT TO LAST BLOCK?
001253 001256 6010 00 2446 TNZ *+J NO, NOT LAST
001254 000025 1230 11 2447 SBLX Y,T$TEMP3,T YES, SO SUBTRACT OFF NEEDED
END OF BINARY CARD LPCP002A
001255 001260 7100 00 2448 TRA MEM3 CONTINUE
001256 000001 2240 12 2449 LDX Z,LINK,X GET PTR TO LAST FREE BLOCK
001257 000000 7230 14 2450 LXL Y,LEN,Z GET ITS LENGTH
001260 001000 1030 03 2451 MEM3 CMPX Y,$MQUAN,DU COMPARE TO CORE QUANTUM
001261 001276 6020 00 2452 TNC MEMX IF SMALLER, EXIT
001262 777000 3630 03 2453 ANX Y,-$MQUAN,DU OTHERWISE ROUND DOWN TO MULTIPLE OF MQUAN
001263 000024 7430 11 2454 STX Y,T$TEMP4,T SAVE AMOUNT FOR RELEASE
001264 000000 7200 14 2455 LXL 0,LEN,Z GET BACK ITS LENGTH
001265 000024 1200 11 2456 SBLX 0,T$TEMP4,T MINUS AMOUNT TO RELEASE
001266 000000 4400 14 2457 SXL 0,LEN,Z RESTORE NEW LENGTH
001267 001272 6010 00 2458 TNZ *+J IS THE BLOCK NULL
001270 000001 2200 14 2459 LDX 0,LINK,Z YES, RESET LAST
001271 000001 7400 12 2460 STX 0,LINK,X TO POINT TO NEW LAST BLOCK
001272 000024 3220 11 2461 LCX X,T$TEMP4,T GET BACK AMOUNT TO BE RELEASED
001273 003323 0420 00 2462 ASX X,$MTOPT SUBTRACT FROM TOP OF MEMORY PTR
001274 003320 0420 00 2463 ASX X,$AVAIL SUBTRACT FROM AVAILABLE
001275 001301 7070 00 2464 TSX L,MREQ RELEASE MEMORY TO SYSTEM
001276 001276 2465 MEMX BSS 0 DONE
001276 003304 7170 00 2466 DEQ CORE RELEASE CORE QUEUE, AWAKEN NEXT TASK
001277 001335 7000 00 2467 XED Q$CORE+Q$XDEQ
001300 001300 2467 TSX 0,T$RELT AND RELEASE TCB
001300 001547 7100 00 2468 EXIT EVAPORATE
TRA $EXIT

```

F

CORE MANAGEMENT -- MEMORY REQUESTS

```

001301      2470      USE      CODE
              2471      HEAD      R
              2472 *
              2473 *
              2474 *
              2475 *
              2476 *
              2477 *
              2478 *
              2479 *
              2480 *
              2481 *
              2482 *
              2483 *
              2484 *
              2485 *
              2486 *
              2487 *
              2488 *
              2489 *
              2490 *
              2491 *
              2492 *
              2493 *
              2494 *
001301 000017 4470 11      MREQ  SXL      L,T$TEM9,T      SAVE RETURN ADDRESS
END OF BINARY CARD LPCP0029
001302 003323 2200 00      2495      LDX      0,$M$TOP      GET PASSED SETTING
001303 000777 0200 03      2496      ADLX     0,$M$QUAN-1,DU  ROUND UP TO
001304 777000 3600 03      2497      ANX      0,$M$QUAN,DU    NEXT CORE MULTIPLE
001305 003323 7400 00      2498      STX      0,$M$TOP      AND SAVE IT
              001306      2499 MREQ1  CHSEG    $BUFSEG,$M$TOP  MEMORY REQUEST
001306 000605 7000 00      TSX      0,$CHSEG
001307 000000 0000 00      ARG      $BUFSEG
001310 003323 0000 00      ARG      $M$TOP
              001311      2500      CHECK   MREQ2,$R$RZ,MREQ1,$B$IO,MREQ1
001311 000000 7200 11      LXL      0,T$SRW1,T
001312 000077 3600 03      ANX      0,$B$STMK,DU
001313 001321 6000 00      TZE      MREQ2
001314 000003 1000 03      CMPX     0,$B$RZ,DU
001315 001306 6000 00      TZE      MREQ1
001316 000004 1000 03      CMPX     0,$B$IO,DU
001317 001306 6000 00      TZE      MREQ1
001320 777777 7100 00      TRA      $ERROR
              001321      2501 MREQ2  BSS      0      SUCCESSFUL REQUEST
001321 000017 7270 11      2502      LXL      L,T$TEM9,T      RETRIEVE RETURN ADDRESS
              001322      2503      BUGL    (T$TEM9,T)      BUG IT
              525213      BUGBUG  SET      BUGBUG+1
001322 525213 2200 03      LDX      0,BUGBUG,DU
001323 000017 4400 11      SXL      0,T$TEM9,T
001324 000000 7100 17      2504      TRA      0,L      RETURN TO CALLER
    
```

R

TRAP MANAGEMENT -- DESCRIPTION

```

001325 2506      USE      CODE
        2507      HEAD    T
        2508 *
        2509 *
        2510 *      THESE MACROS GET AND RELEASE TRAP BLOCKS.
        2511 *      CLOBFRS C(O), C(X), C(T).
        2512 *
        2513 *
        2514 *
        2515 *
        2516 GETT  MACRO  <NO-ARGUMENTS>
        2517      TSX    O,TSGETT      CALL SUBROUTINE
        2518      ENDM  GETT
        2519 *
        2520 *
        2521 *
        2522 *
        2523 RELT  MACRO  <NO-ARGUMENTS>
        2524      TSX    O,TSRELT      CALL SUBROUTINE
        2525      ENDM  RELT
    
```

T

TRAP MANAGEMENT -- GETT

		001325	2527	USE	CODE	
			2528	HEAD	T	
			2529 *			
			2530 *			
			2531 *			GETT
			2532 *			
			2533 *	THIS SUBROUTINE GETS A TRAP BLOCK. C(XT) POINTS TO THE		
			2534 *	CURRENT TBLOCK WHILE C(T\$LINK,T) POINTS TO THE OLD ONE		
			2535 *	ENTERED WITH		
			2536 *	C(XT) = TCB		
			2537 *	C(XJ) = JCB		
			2538 *	ENTERED BY		
			2539 *	TSX D,R\$GETT		
			2540 *	CALLS		
			2541 *	R\$GETC		
			2542 *	RETURNS 0,0		
			2543 *	RETURNS WITH		
			2544 *	C(XT) = NEW TCB		
			2545 *	C(XJ) = JCB		
			2546 *			
			2547 *			
		001325	2548	GETT	BSS	0
		001325	2549		GETC	(LEN,DL)
					LDA	LEN,DL
					TSX	L,R\$GETC
			2550	STX	T,LINK,AU	POINT TO PREVIOUS TBLOCK
			2551	EAX	T,0,AU	MAKE C(XT) POINT TO NEW BLOCK
			2552	TZE	\$ERROR	***DBG
			2553	TMI	\$ERROR	***DBG
			2554	SXL	J,T\$JCB,T	SET JCB POINTER
			2555	TRA	0,0	RETURN TO CALLER
001325	000030	2350	07			
001326	001011	7070	00			
END	OF BINARY CARD	LPC0030				
001327	000005	7410	01			
001330	000000	6210	01			
001331	777777	6000	00			
001332	777777	6040	00			
001333	000006	4460	11			
001334	000000	7100	10			

T TRAP MANAGEMENT -- RELT

001335	2557	USE	CODE	
	2558	HEAD	T	
	2559 *			
	2560 *			
	2561 *			RELT
	2562 *			
	2563 *	THIS SUBROUTINE RELEASES THE CURRENT TRAP BLOCK.		
	2564 *			
	2565 *	ENTERED WITH		
	2566 *		C(XT) = TCR	
	2567 *		C(XJ) = JCB	
	2568 *	ENTERED BY		
	2569 *		TSX 0,R\$RELT	
	2570 *	CALLS		
	2571 *		R\$RELC	
	2572 *	RETURNS TO 0,C		
	2573 *	RETURNS WITH		
	2574 *		C(XJ) = JCB	
	2575 *			
	2576 *			
	001335	2577	RELT BSS	0
001335	000000	6210	11	
001336	777777	6000	00	2578 EAX T,0,T CHECK FOR LEGAL RELEASE
001337	000000	6350	11	2579 TZE \$ERROR NG
001340	000030	2750	07	2580 EAA 0,T TRAP ADDRESS TO AU
		001341		2581 ORA LEN,DL TRAP LEN TO AL
001341	001110	7070	00	2582 RELC A RELEASE IT
		001342		2583 TSX L,R\$RELC
		525214		BUGXR T SPPML
001342	525214	2210	03	BUGBUG SET BUGBUG+1
001343	000000	7100	10	LDX T,BUGBUG,DU
		2584	TRA	0,0 RETURN TO CALLER

T JOB CONTROL BLOCK MANAGEMENT -- DESCRIPTION

```

001344 2586 USE CODE
2587 HEAD J
2588 *
2589 *
2590 * THESE MACROS GET AND RELEASE JOB'S (JOB CONTROL BLOCKS)
2591 * CLOBBERS C(A). RETURNS IMMEDIATELY TO CALLER.
2592 *
2593 *
2594 * GETJ MACRO
2595 *
2596 GETJ MACRO <NO-ARGUMENT>
2597 TSX L,J$GETJ CALL SUBROUTINE
2598 ENDM GETJ RETURN C(XJ) = JCB-ADDRESS
2599 *
2600 *
2601 * RELJ MACRO
2602 *
2603 RELJ MACRO C(XJ) = JCB TO BE RELEASED
2604 TSX L,J$RELJ CALL SUBROUTINE
2605 ENDM RELJ CLOBBERS C(XJ)
    
```

J JOB CONTROL BLOCK MANAGEMENT -- GETJ

		001344	2607	USE	CODE		
			2608	HEAD	J		
			2609 *				
			2610 *				
			2611 *			GETJ	
			2612 *				
			2613 *	THIS SUBROUTINE GETS THE FIRST AVAILABLE JCB.			
			2614 *	IT RETURNS A POINTER TO IT IN C(XJ).			
			2615 *				
			2616 *	ENTERED WITH			
			2617 *	C(XT) = TCB			
			2618 *	ENTERED BY			
			2619 *	TSX L,JSGETJ			
			2620 *	CALLS			
			2621 *	NONE			
			2622 *	RETURNS 0,L			
			2623 *	RETURNS WITH			
			2624 *	C(XT) = TCB			
			2625 *	C(XJ) = NEW JCB			
			2626 *				
			2627 *				
		001344	2628	GETJ	BSS	0	ENTRY POINT
			2629	EAX	J,JCB0		POINT TO FIRST JCB
001344	003644	6260	00	2630	LDA	0,DL	SET A FOR MATCH
001345	000000	2350	07	2631	RPT	JCBN,LEN,TZE	SEARCH
001346	006300	5202	24	2632	CMPA	0,J	TEST
001347	000000	1150	16	2633	TNZ	\$ERROR	OOFS
001350	777777	6010	00	2634	EAX	J,-LEN+1,J	POINT TO FREE ONE
001351	777755	6260	16	2635	STX	T,ALLC,J	MARK IT BUSY
001352	777777	7410	16				
END OF BINARY CARD	LPCP0031			2636	RPT	LEN-1,1,TZE	CLEAN OUT BLOCK
001353	046300	5202	01	2637	STZ	0,J	CLEAR IT
001354	000000	4500	16	2638	EAX	J,-LEN+1,J	RESET J
001355	777755	6260	16	2639	TRA	0,L	RETURN TO CALLER
001356	000000	7100	17				

J

JOB CONTROL BLOCK MANAGEMENT -- RELJ

```

001357 2641 USE CODE
2642 HEAD J
2643 *
2644 *
2645 * RELJ
2646 *
2647 * THIS SUBROUTINE RELEASES THE JCB POINTED TO BY C(XJ).
2648 *
2649 * ENTERED WITH
2650 * C(XT) = TCB
2651 * C(XJ) = JCB
2652 * ENTERED BY
2653 * TSX L,J$RELJ
2654 * CALLS
2655 * NONE
2656 * RETURNS O,L
2657 * RETURNS WITH
2658 * C(XT) = TCB
2659 *
2660 *
001357 2661 RELJ BSS 0 ENTRY POINT
001357 777777 2340 16 2662 SZN ALLC,J IS IT BUSY
001360 777777 6000 00 2663 TZE $ERROR IS SHOULD BE!
001361 777777 4500 16 2664 STZ ALLC,J MAKR IT FREE
001362 001362 2665 BUGXR J SPPML
001362 525215 2260 03 BUGBUG SET BUGBUG+1
001363 000000 7100 17 2666 LDX J,BUGBUG,DU
2666 TRA O,L RETURN TO CALLER
    
```

J

PERIPHERAL MANAGEMENT -- DESCRIPTION

```

001364 2668      USE      CODE
        2669      HEAD    R
        2670 *
        2671 *
        2672 *
        2673 *
        2674 *      THESE MACROS GET A RELEASE A SINGLE PERIPHERAL
        2675 *
        2676 *
        2677 *
        2678 *
        2679 *      GET A PERIPHERAL
        2680 *
        2681 GETP  MACRO   TYPE/ PA
        2682      INE     #1,,PA
        2683      LDA     #1      GET TYPE IN AU
        2684      TSX     L,R$GETP  CALL SUBROUTINE
        2685      ENDM    GETP
        2686 *
        2687 *
        2688 *
        2689 *
        2690 *      RELEASE A PERIPHERAL
        2691 *
        2692 RELP  MACRO   DEVICE NUMBER/ PA
        2693      INE     #1,,PA
        2694      LDA     #1      GET DEVICE NUMBER IN AL
        2695      TSX     L,R$RELP  CALL SUBROUTINE
        2696      ENDM    RELP
    
```

F PERIPHERAL MANAGEMENT -- GETP

		001364	2698	USE	CODE			
			2699	HEAD	R			
			2700 *					
			2701 *					
			2702 *					
			2703 *			GETP		
			2704 *			GETP GETS A SINGLE PERIPHERAL UNIT OF A SPECIFIED TYPE.		
			2705 *			IF THERE ARE MORE UNITS OF THE SAME TYPE STILL AVAILABLE		
			2706 *			AFTER THE GET, THEN THE NEXT JOB WAITING FOR THE SAME		
			2707 *			PERIPHERAL TYPE IS AWAKENED.		
			2708 *					
			2709 *	CALL BY				
			2710 *		TSX L,RSGETP			
			2711 *	CALL WITH				
			2712 *		C(J) = JOB NUMBER			
			2713 *		C(T) = TBLOCK ADDRESS			
			2714 *		C(L) = RETURN ADDRESS			
			2715 *		C(AU) = PERIPHERAL TYPE			
			2716 *	CALLS				
			2717 *		NONE			
			2718 *	RETURNS WITH				
			2719 *		C(J) = JOB NUMBER			
			2720 *		C(T) = TBLOCK ADDRESS			
			2721 *		C(AU) = PERIPHERAL TYPE			
			2722 *		C(AL) = DEVICE NUMBER			
			2723 *		C(QU) = DEVICE FRN			
			2724 *		C(QL) = DEVICE NUMBER			
			2725 *	EXIT TO	0,L			
			2726 *	USES				
			2727 *		NO LOCAL TEMPORARIES			
			2728 *		T\$TEMP1,T\$TEMP2			
			2729 *					
		001364	2730	GETP	BSS	0	ENTRY POINT	
001364	000004	4470	11	2731	SXL	L,T\$TRA,T	SAVE RETURN ADDRESS	
001365	000027	7550	11	2732	STA	T\$TEMP1,T	SAVE TYPE	
001366	000027	2240	11	2733	LDX	Z,T\$TEMP1,T	GET PERIPHERAL TYPE	
001367	000003	3640	03	2734	ANX	Z,3,DU	MASK TO TYPE ONLY	
001370	003074	2240	14	2735	LDX	Z,TABLE,Z	POINT TO PERIPHERAL TYPE TABLE	
001371	777777	6000	00	2736	TZE	\$ERROR	NO SUCH PERIPHERAL	
001372	000026	7440	11	2737	STX	Z,T\$TEMP2,T	SAVE TYPE POINTER	
				2738 *				
				2739 *			LOOP TO LOOK FOR FREE DEVICE	
				2740 *				
001373	000001	3360	14	2741	LCQ	MAX,Z	GET NUMBER TO CHECK COMPLEMENTED	
001374	777777	6000	00	2742	TZE	\$ERROR	***BLEWIT	
001375	000000	2200	14	2743	LDX	0,PTR,Z	GET POINTER TO UNITS	
001376	700000	2350	03	2744	LDA	BUSY+CLOSE+RSVE,DU	GET BITS TO CHECK	
END OF BINARY CARD	LPCP0032							
001377	000002	3150	10	2745	GETP1	CANA	FLAG,0	IS THIS UNIT FREE?
001400	001405	6000	00	2746	TZE	GETP2	YES, TAKE IT	

R PERIPHERAL MANAGEMENT -- GETP

001401	000003	0200	03	2747	ADLX	0,RSDEVLN,DU	STEP TO NEXT DEVICE
001402	000001	0760	07	2748	ADQ	1,DL	TEST FOR DONE
001403	001377	6040	00	2749	TM1	GETP1	NO, SO CONTINUE SEARCH
001404	777777	7100	00	2750	TRA	\$ERROR	***SOMEBODY FORGOT TO RESERVE *EM,
				2751	*		
				2752	*		
				2753	*	FOUND THE REQUESTED UNIT	
				2754	*		
		001405		2755	GETP2	BSS 0	
001405	000027	4400	11	2756	SXL	0,T\$TEMP1,T	SAVE POINTER TO DEVICE
001406	000002	4460	10	2757	SXL	J,ALLC,0	MARK IT ALLOCATED TO US
001407	400000	2220	03	2758	LDX	X,BUSY,DU	SET THE BUSY BIT ON
001410	000002	2420	10	2759	ORSX	X,FLAG,0	
001411	000027	2350	11	2760	LDA	T\$TEMP1,T	GET RETURN WORDS FOR USER
001412	000001	2360	10	2761	LDQ	FRN,0	
		001413		2762	BUGXR	(0,X,Y,Z,0)	
		525216			BUGBUG	SET	BUGBUG+1
001413	525216	2200	03		LDX	0,BUGBUG,DU	
001414	525216	2220	03		LDX	X,BUGBUG,DU	
001415	525216	2230	03		LDX	Y,BUGBUG,DU	
001416	525216	2240	03		LDX	Z,BUGBUG,DU	
001417	525216	2250	03		LDX	Q,BUGBUG,DU	
001420	000004	7270	11	2763	LXL	L,T\$TRA,T	RETRIEVE RETURN
		001421		2764	BUGL	(T\$TRA,T)	BUG IT
		525217			BUGBUG	SET	BUGBUG+1
001421	525217	2200	03		LDX	0,BUGBUG,DU	
001422	000004	4400	11		SXL	0,T\$TRA,T	
001423	000027	2350	11	2765	LDA	T\$TEMP1,T	GET RETURN WORD FOR CALLER
		001424		2766	BUG	(T\$TEMP1,T)	BUG IT
		525220			BUGBUG	SET	BUGBUG+1
001424	525220	2200	03		LDX	0,BUGBUG,DU	
END OF BINARY CARD	LPCP0033						
001425	000027	7400	11		STX	0,T\$TEMP1,T	
001426	000027	4400	11		SXL	0,T\$TEMP1,T	
		001427		2767	BUG	(T\$TEMP2,T)	
		525221			BUGBUG	SET	BUGBUG+1
001427	525221	2200	03		LDX	0,BUGBUG,DU	
001430	000026	7400	11		STX	0,T\$TEMP2,T	
001431	000026	4400	11		SXL	0,T\$TEMP2,T	
001432	000000	7100	17	2768	TRA	0,L	RETURN TO CALLER

R PERIPHERAL MANAGEMENT -- RELP

			001433	2770	USE	CODE	
				2771	HEAD	R	
				2772 *			
				2773 *			
				2774 *			REL P
				2775 *			
				2776 *	REL P	RELEASES	A PERIPHERAL UNIT OF A SPECIFIED TYPE.
				2777 *			
				2778 *	CALL	WITH	
				2779 *		TSX L,R\$REL P	
				2780 *	CALL	WITH	
				2781 *		C(J) =	JOB NUMBER
				2782 *		C(T) =	TBLOCK ADDRESS
				2783 *		C(L) =	RETURN ADDRESS
				2784 *		C(A) =	PERIPHERAL TYPE/ DEVICE ADDRESS
				2785 *	CALLS		
				2786 *		C\$MESSX	(CONDITIONALLY)
				2787 *	RETURNS	WITH	
				2788 *		C(J) =	JOB NUMBER
				2789 *		C(T) =	TBLOCK ADDRESS
				2790 *	EXITS	TO	0,L
				2791 *	USES		
				2792 *		NO	LOCALS
				2793 *		T\$TEMP1,	T\$TEMP2, T\$TEMP3
				2794 *			
				2795 *	EXITS	WITH	PERIPHERAL DEALLOCATED.
				2796 *			
			001433	2797	REL P	BSS	0
001433	000004	4470	11	2798	SXL	L,T\$TRA,T	SAVE RETURN ADDRESS
				2799 *			
				2800 *	PERFORM	CONSISTANCY	CHECKS
				2801 *			
001434	000027	7550	11	2802	STA	T\$TEMP1,T	SAVE INFORMATION
001435	000027	7220	11	2803	LXL	X,T\$TEMP1,T	GET DEVICE ADDRESS
001436	000002	2340	12	2804	SZN	FLAG,X	SHOULD BE BUSY
001437	777777	6000	00	2805	TZE	\$ERROR	IT ISN'T I
001440	777777	2360	03	2806	LDQ	-1,DU	SET FOR LOWER HALF COMPARE
001441	000002	2350	12	2807	LDA	ALLC,X	GET ALLOCATED JOB NUMBER
001442	000006	2110	11	2808	CMK	T\$JCB,T	CHECK FOR CORRECT JCB
001443	777777	6010	00	2809	TNZ	\$ERROR	SHOULD BE THE SAME
			001444	2810	BUGL	(ALLC,X)	OK, DESTROY IT
			525222		BUGBUG	SET	BUGBUG+1
001444	525222	2200	03		LDX	0,BUGBUG,DU	
001445	000002	4400	12		SXL	0,ALLC,X	
				2811 *			
				2812 *	CHECK	IF	CLOSE REQUESTED
				2813 *			
001446	000002	2350	12	2814	LDA	FLAG,X	GET PERIPHERAL FLAG
001447	100000	3150	03	2815	CANA	RSVE,DU	IS IT RESERVED?
001450	001517	6000	00	2816	TZE	REL P5	NO, SO CONTINUE

R PERIPHERAL MANAGEMENT -- RELP

```

2817 *
2818 *      CLOSE REQUESTED -- CLOSE AND LOG
2819 *
001451 300000 6750 03
END OF BINARY CARD LPCP0034
001452 000002 7550 12
001453 000001 2230 12
001454 000025 7430 11
                001455
                525223
001455 525223 2200 03
001456 000001 7400 12

                001457
001457 000616 7000 00
001460 000025 0000 11
                001461
001461 000000 7200 11
001462 000077 3600 03
001463 001467 6000 00
001464 000003 1000 03
001465 001457 6000 00
001466 777777 7100 00
                001467

2820 ERA      CLOSE+RSVE,DU  MARK IT CLOSED AND NOT RESERVED

2821 STA      FLAG,X          RESTORE FLAG WORD
2822 LDX      Y,FRN,X          GET FRN OF DEVICE
2823 STX      Y,T$TEMP3,T     SAVE FOR CLOSE
2824 BUGU     (FRN,X)         BUG FRN
      BUGBUG SET      BUGBUG+1
      LDX     0,BUGBUG,DU
      STX     0,FRN,X

2825 *
2826 *      CLOSE PERIPHERAL
2827 *
2828 RELP1  CLOSE  (T$TEMP3,T)  CLOSE THE DEVICE
      TSX     0,$CLOSE
      ARG     T$TEMP3,T

2829 CHECK  RELP2,B$BZ,RELP1
      LXL     0,T$SRW1,T
      ANX     0,B$STMK,DU
      TZE     RELP2
      CMPX    0,B$BZ,DU
      TZE     RELP1
      TRA     $ERROR
2830 RELP2  BSS     0

2831 *
2832 *      INFORM MINITOR
2833 *
2834 LDX      Z,T$NCB,T        GET POINTER TO NCB
2835 LDA      C$STATE,Z        GET STATE
2836 STA      T$TEMP2,T        SAVE AS STATE FOR CAUSE
2837 LXL      X,T$TEMP1,T      GET BACK POINTER TO DEVICE
2838 LDA      NAME,X           GET NAME
2839 ANA      7,DL             ISOLATE UNIT NUMBER
2840 ALS      18              MOVE TO MESSAGE FIELD
2841 ORA      B$REL,DU         MARK AS RELEASED
2842 STA      T$TEMP3,T        SAVE AS MESSAGE
2843 BRANCH   NOPASS,C$MESSX,(T$TEMP2,T),(T$TEMP3,T)

      TSX     0,T$GETT
001500 001325 7000 00
END OF BINARY CARD LPCP0035
001501 000000 6220 11
001502 000005 2210 12
001503 000026 2360 11
001504 000027 7560 12
001505 000025 2360 11
001506 000026 7560 12
001507 000000 6210 12
001510 001613 6200 00
001511 000004 7400 11

      EAX     X,0,T
      LDX     T,T$LINK,X
      LDR     T$TEMP2,T
      STQ     T$TEMP1,X
      LDR     T$TEMP3,T
      STQ     T$TEMP2,X
      EAX     T,0,X
      EAX     0,C$MESSX
      STX     0,T$TRA,T

```

R

PERIPHERAL MANAGEMENT -- RELP

001512	000004	6200	11		EAX	0,Q\$OFFST,T	
001513	003262	7170	00		XED	Q\$XADD+Q\$TASK	
001514	000005	2210	12		LDX	T,T\$LINK,X	
				001515	BUGXR	(0,X)	
				525224	BUGRUG	SET	BUGRUG+1
001515	525224	2200	03		LDX	0,BUGRUG,DU	
001516	525224	2220	03		LDX	X,BUGRUG,DU	
				2844	*		
				2845	*	PERIPHERAL RELEASED	
				2846	*		
				001517	REL P5	BSS	0
001517	000027	7220	11	2848	LXL	X,T\$TEMP1,T	GET BACK POINTER TO DEVICE
001520	400000	2350	03	2849	LDA	BUSY,DU	GET THE BUSY BIT
001521	000002	6550	12	2850	ERSA	FLAG,X	UNSET IT
				001522	REL P6	BSS	0
				001522	2852	BUG	(T\$TEMP1,T)
				525225	BUGRUG	SET	BUGRUG+1
001522	525225	2200	03		LDX	0,BUGRUG,DU	
001523	000027	7400	11		STX	0,T\$TEMP1,T	
001524	000027	4400	11		SXL	0,T\$TEMP1,T	
				001525	2853	BUG	(T\$TEMP2,T)
				525226	BUGRUG	SET	BUGRUG+1
001525	525226	2200	03		LDX	0,BUGRUG,DU	
END OF BINARY CARD	LPC0036						
001526	000026	7400	11		STX	0,T\$TEMP2,T	
001527	000026	4400	11		SXL	0,T\$TEMP2,T	
				001530	2854	BUG	(T\$TEMP3,T)
				525227	BUGRUG	SET	BUGRUG+1
001530	525227	2200	03		LDX	0,BUGRUG,DU	
001531	000025	7400	11		STX	0,T\$TEMP3,T	
001532	000025	4400	11		SXL	0,T\$TEMP3,T	
				001533	2855	BUGXR	(X,Y,Z,Q)
				525230	BUGRUG	SET	BUGRUG+1
001533	525230	2220	03		LDX	X,BUGRUG,DU	
001534	525230	2230	03		LDX	Y,BUGRUG,DU	
001535	525230	2240	03		LDX	Z,BUGRUG,DU	
001536	525230	2250	03		LDX	Q,BUGRUG,DU	
				001537	2856	BUGA	
				525231	BUGRUG	SET	BUGRUG+1
001537	525231	2350	03		LDA	BUGRUG,DU	
001540	525231	2750	07		ORA	BUGRUG,DL	
				001541	2857	BUGQ	
				525232	BUGRUG	SET	BUGRUG+1
001541	525232	2360	03		LDQ	BUGRUG,DU	
001542	525232	2760	07		ORQ	BUGRUG,DL	
001543	000004	7270	11	2858	LXL	L,T\$TRA,T	
				001544	2859	BUGL	(T\$TRA,T)
				525233	BUGRUG	SET	BUGRUG+1
001544	525233	2200	03		LDX	0,BUGRUG,DU	
001545	000004	4400	11		SXL	0,T\$TRA,T	RETRIEVE RETURN

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LINE PRINTER/ CARD PUNCH MODULE

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R

PERIPHERAL MANAGEMENT -- RELP

001546 000000 7100 17

2860

TRA

0,L

RETURN TO CALLER

R

RESOURCE ALLOCATION -- PERIPHERAL TYPE TABLE

		001547	2862	USE	CODE	
			2863	HEAD	R	
			2864 *			
			2865 *			
			2866 *			
			2867 *			
			2868 *			
			2869 *			
			2870 *			
			2871 *			
			2872 *			
			2873 *			
			2874 *			
			2875 *			
			2876 *			
			2877 *			
			2878 *			
		003074	2879	USE	CONST	
		003074	2880	TABLE	BSS	0
003074	000000	0000 00	2881	ARG	0	0 = INVALID
003075	003410	0000 00	2882	TABCP	ARG	CPTAB
003076	003413	0000 00	2883	TABL	ARG	LPTAB
003077	000000	0000 00	2884	ARG	0	2 = LINE PRINTER
		001547	2885	USE	PREVIOUS	3 = INVALID
			2886 *			
			2887 *			
		000001	2888	TYPCP	EQU	TABCP-TABLE
		000002	2889	TYPLP	EQU	TABL-TABLE
						TYPE: CARD PUNCH
						TYPE: LINE PRINTER

PERIPHERAL TYPE TABLE

THE PERIPHERAL TYPE TABLE HAS AN ENTRY FOR EACH TYPE OF RESOURCE. THUS THE ENTRY POINTER POINTS TO THE RESOURCE DEVICE HEADER. THE HEADER CONTAINS SUCH INFORMATION AS THE TOTAL NUMBER OF DEVICES OF THIS TYPE, THE NUMBER CURRENTLY AVAILABLE, A POINTER TO THE FIRST DEVICE, ETC. THE PERIPHERAL DEVICE ITSELF CONTAINS SUFFICIENT INFORMATION FOR ANY OPERATION ON THE PERIPHERAL.

TABLE OF PERIPHERAL TYPES

- 0 = INVALID
- 1 = CARD PUNCH
- 2 = LINE PRINTER
- 3 = INVALID

R

RESOURCE ALLOCATION -- PERIPHERAL HEADER TABLE

001547	2891	USE	CODE	
	2892	HEAD	R	
	2893 *			
	2894 *			
	2895 *			
	2896 *			
	2897 *			
	2898 *			
	2899 *			
	2900 *			
	2901 *			
	2902 *			
	2903 *			
	2904 *			
	2905 *			
	2906 *			
	2907 PTR	EQU	0	POINTER TO DEVICE TABLE
000000	2908 MAX	EQU	PTR+1	MAX NUMBER IN EXISTANCE
000001	2909 SPARE	EQU	MAX+1	SPARE
000002	2910 *			
	2911 *			
	2912 *			
	2913 *			
	2914 DEVHDR	MACRO	NAME,MAX	
	2915 #1TAB	ARG	#1	
	2916	VFD	36/#2	
	2917	DEC	0	SPARE
	2918	ENDM	DEVHDR	

PERIPHERAL HEADER TABLE

A DEVICE HEADER IS THE ITEM THAT AN ENTRY IN THE TABLE OF PERIPHERAL TYPES POINTS TO. THE HEADER CONTAINS THE POINTER TO THE DEVICES OF A CERTAIN TYPE (I.E. LINE PRINTERS). IT ALSO CONTAINS SUCH INFORMATION AS THE CONFIGURATION OF THE SYSTEM (MAXIMUM NUMBER OF DEVICES OF A CERTAIN TYPE). LASTLY, IT CONTAINS A POINTER TO THE CORRESPONDING QUEUE.

FORMAT OF DEVICE HEADER

DEVICE HEADER GENERATING MACRO

R

RESOURCE ALLOCATION -- PERIPHERAL HEADER TABLE

```

2920 *
2921 *
2922 *      HERE IS THE LIST OF DEVICE HEADERS.
2923 *
2924 *
2925         003410      USE      STORE
003410      003410      2926 DEVHR BSS      0      START OF DEVICE HEADER LIST
2927 *
2928 *      CARD PUNCHES (CP)
2929 *
2930         003410      DEVHDR CP,CPMAX
END OF BINARY CARD LPCP0037
003410      003416 0000 00      CPTAB ARG      CP
003411      0000000000001      VFD      36/CPMAX
003412      0000000000000      DEC      0

2931 *
2932 *      LINE PRINTERS (LP)
2933 *
2934         003413      DEVHDR LP,LPMAX
003413      003421 0000 00      LPTAB ARG      LP
003414      0000000000002      VFD      36/LPMAX
003415      0000000000000      DEC      0
001547      2935      USE      PREVIOUS
    
```

R

RESOURCE ALLOCATION -- PERIPHERAL DEVICE TABLE

```

001547 2937 USE CODE
        2938 HEAD R
        2939 *
        2940 *
        2941 *
        2942 *
        2943 *
        2944 *
        2945 *
        2946 *
        2947 *
        2948 *
        2949 *
        2950 *
        2951 *
000000 2952 NAME EQU 0 FOUR CHAR ASCII ABBREVIATION
000001 2953 FRN EQU NAME+1 (UPPER) FRN OF PERIPHERAL WHEN OPEN
000002 2954 FLAG EQU FRN+1 (UPPER) FLAG BITS FOR THE PERIPHERAL
000002 2955 ALLC EQU FLAG (LOWER) JOB NUMBER USING IT WHEN BUSY
000003 2956 DEVLN EQU ALLC+1-NAME DEVICE ENTRY LENGTH
        2957 *
        2958 *
        2959 *
        2960 *
        2961 BUSY EQU BSSIGN ON IF NOT ALLOCATABLE
        2962 CLOSE EQU BUSY/2 ON IF CLOSED
        2963 RSVE EQU CLOSE/2 ON IF OPERATOR REQUESTED A CLOSE
        2964 *
        2965 *
        2966 *
        2967 *
        2968 DEVICE MACRO NAME
        2969 UASCII 1,#1 NAME
        2970 BUGBUG SET BUGBUG+1
        2971 ZERO BUGBUG,TT FRN/ UNIT NUMBER
        2972 ZERO BUSY+CLOSE,0
        2973 ENDM DEVICE
        2974 *
        2975 *
        2976 DEVT MACRO NAME,(LIST OF DEVICE NUMBERS)
        2977 #1 BSS 0
        2978 TT SET 0 INITIALIZE COUNTER
        2979 IDRП #2
        2980 SET SET #2
        2981 DEVICE #1#2
        2982 TT SET TT+1
        2983 IDRП
        2984 #1MAX SET SET+1
        2985 ENDM DEVT
    
```

R

RESOURCE ALLOCATION -- PERIPHERAL DEVICE TABLE

```

2987 *
2988 *
2989 *      HERE IS THE TABLE OF DEVICES
2990 *
2991 *
2992 *      USE      STORE
003416      003416      2993 DEVTB  BSS      0      START OF DEVICE TABLE
003416
2994 *
2995 *      CARD PUNCHES (CP)
2996 *
003416      003416      2997 *      DEVT      CP,(00)
003416      CP      BSS      0
000000      TT      SET      0
000000      SET      SET      00
003416      003416      DEVT      DEVICE CP00
003416      103120060060      UASCI      1,CP00      NAME
525234      BUGBUG SET      BUGBUG+1
003417 525234 000000      ZERO      BUGBUG,TT
003420 600000 000000      ZERO      BUSY+CLOSE,0
000001      TT      SET      TT+1
000001      CPMAX SET      SET+1
2998 *
2999 *      LINE PRINTERS (LP)
3000 *
003421      003421      3001 *      DEVT      LP,(00,01)
003421      LP      BSS      0
000000      TT      SET      0
000000      SET      SET      00
003421      003421      DEVT      DEVICE LP00
003421      114120060060      UASCI      1,LP00      NAME
525235      BUGBUG SET      BUGBUG+1
003422 525235 000000      ZERO      BUGBUG,TT
003423 600000 000000      ZERO      BUSY+CLOSE,0
000001      TT      SET      TT+1
000001      SET      SET      01
003424      003424      DEVT      DEVICE LP01
003424      114120060061      UASCI      1,LP01      NAME
525236      BUGBUG SET      BUGBUG+1
003425 525236 000001      ZERO      BUGBUG,TT
003426 600000 000000      ZERO      BUSY+CLOSE,0
000002      TT      SET      TT+1
000002      LPMAX SET      SET+1
001547      3002 *      USE      PREVIOUS
    
```


R

JOB TABLE

3004 HEAD J

3005 *

3006 *

3007 *

3008 *

3009 *

3010 *

3011 *

3012 *

3013 *

3014 *

003427

000020

3015

3016 MAXJB

3017 *

3018 *

003430

003430

003430

003430 000000 000000
END OF BINARY CARD LPCP0038

001547

3019

3020 JTAB

3021

3022

3023

USE STORE

EQU 16

EIGHT

BSS 0

DUP 1, MAXJB

ZERO 0,0

USE PREVIOUS

JOB TABLE

THE JOB TABLE IS A TABLE OF ONE WORD ENTRIES, INDEXED BY THE JOB NUMBER. INFORMATION IN THE JOB TABLE IS THAT WHICH MUST BE LOCATED OR MATCHED FOR CONSISTENCY CHECKS AND FOR DEBUGGING PURPOSES. THE TABLE CONTAINS MAXJB ENTRIES.

MAXIMUM NUMBER OF JOBS IN THE SYSTEM

TO MAKE DEBUGGING EASIER
JOB TABLE

(UPPER) PTR TO JCB/ (LOWER) PTR TO DEVICE
INITIALLY OFF

```

      J
      EXIT
      001547 3025 USE CODE
      3026 HEAD
      3027 *
      3028 *
      3029 * EXIT
      3030 *
      3031 * COMPLETE ONE TASK AND BEGIN ANOTHER FROM Q$TASK QUEUE
      3032 *
      3033 * ENTER BY
      3034 * TRA $EXIT
      3035 * RETURN TO NEXT TASK
      3036 * RETURNS WITH
      3037 * C(J) = JOB NUMBER
      3038 * C(T) = TRAP BLOCK
      3039 * C(L) = TASK-START-ADDRESS
      3040 *
      001547 3041 EXIT BSS 0 ENTRY POINT
      001547 3042 CKPT 0 DEBUGGING
      001547 000474 7170 00 XED X$CKPT
      001550 001550 3043 INHIB SAVE,ON LOCK OUT UNWANTED INTERRUPTS
      3044 EXIT1 BSS 0 TIME TO FOOL WITH THE Q$TASK QUEUE
      001550 003261 6202 00 3045 EAX 0,Q$FIRST+1+Q$TASK ADDRESS OF FIRST ELEMENT
      001551 003261 1002 00 3046 CMPX 0,Q$LAST+Q$TASK *DOES LAST POINT TO IT?
      001552 001610 6002 00 3047 TZE WAIT
      001553 003260 2212 00 3048 LDX T,Q$FIRST+Q$TASK *OFFSET POINTER TO BLOCK
      001554 777777 6002 00 3049 TZE $ERROR ***PROBLEM
      001555 003261 1012 00 3050 CMPX T,Q$LAST+Q$TASK *IS THIS LAST?
      001556 001561 6012 00 3051 TNZ **3 NO
      END OF BINARY CARD LPCP0039
      001557 003261 6202 00 3052 EAX 0,Q$FIRST+1+Q$TASK YES, SET THIS QUEUE
      001560 003261 7402 00 3053 STX 0,Q$LAST+Q$TASK *TO EMPTY STATUS
      001561 000004 1212 03 3054 SBLX T,Q$OFFST,DJ RELATE THE BEGINNING OF BLOCK
      001562 777777 6002 00 3055 TZE $ERROR ***DBG
      001563 777777 6042 00 3056 TMI $ERROR ***DBG
      001564 000003 2222 11 3057 LDX X,Q$LINK,T GET OFFSET POINTER TO NEXT BLOCK
      001565 003260 7422 00 3058 STX X,Q$FIRST+Q$TASK *AND MAKE IT NOW FIRST
      3059 INHIB RESTORE RESUME NORMAL TELECAST
      001566 000006 7260 11 3060 LXL J,T$JCB,T RESTORE JCB POINTER
      001567 777777 6000 00 3061 TZE $ERROR ***DBG
      001570 777777 6040 00 3062 TMI $ERROR ***DBG
      001571 000004 2270 11 3063 LDX L,T$TRA,T AND TRANSFER ADDRESS
      001572 777777 6000 00 3064 TZE $ERROR ***DBG
      001573 777777 6040 00 3065 TMI $ERROR ***DBG
      001574 001574 3066 BUGU (T$TRA,T) BUG RETURN
      525237 BUGBUG SET BUGBUG+1
      001574 525237 2200 03 LDX 0,BUGBUG,DU
      001575 000004 7400 11 STX 0,T$TRA,T
      001576 001576 3067 BUGXR (0,X,Y,Z,Q) BUG THE REGISTERS
      525240 BUGBUG SET BUGBUG+1
      001576 525240 2200 03 LDX 0,BUGBUG,DU
  
```

EXIT

001577	525240	2220	03		LDX	X,BUGBUG,DU	
001600	525240	2230	03		LDX	Y,BUGBUG,DU	
001601	525240	2240	03		LDX	Z,BUGBUG,DU	
001602	525240	2250	03		LDX	Q,BUGBUG,DU	
		001603		3068	BUGA		
		525241			BUGBUG SET	BUGBUG+1	
001603	525241	2350	03		LDA	BUGBUG,DU	
001604	525241	2750	07		ORA	BUGBUG,DL	
		001605		3069	BUGQ		
		525242			BUGBUG SET	BUGBUG+1	
END OF BINARY CARD	LPCP0040						
001605	525242	2360	03		LDA	BUGBUG,DU	
001606	525242	2760	07		ORA	BUGBUG,DL	
001607	000000	7100	17	3070	TRA	0,L	AND AWAY WE GO!
				3071 *			
				3072 *			
				3073 *		WAIT FOR SOMETHING TO HAPPEN	
				3074 *			
				3075 *			
				3076	INHIB	SAVE,ON	
001610	000017	2202	03	3077	WAIT LDX	0,%.PAUSE,DU	PAUSE AND START AT
001611	000000	0012	00	3078	MME		ANY INTERRUPT
				3079	INHIB	RESTORE	
001612	001550	7100	00	3080	TRA	EXIT1	SKIP CHECKPOINT

COMMUNICATIONS -- DESCRIPTION

```

001613 3082 USE CODE
3083 HEAD C
3084 *
3085 *
3086 * COMMUNICATIONS NETWORK STRUCTURE
3087 *
3088 * THERE EXISTS A PRIVATE COMMUNICATIONS NETWORK AMONG THE
3089 * THE MONITOR AND ALL OF ITS SUB-MODULES (I.E. PERIPHERAL DRIVERS).
3090 * AT STARTUP TIME FOR THE MONITOR, IT CREATES THE NETWORK BY
3091 * OPENING THREE SCRATCH EVENTS AND PASSING A FRN OF EACH TO EACH
3092 * SUB-MODULES SPAWNED. FOR THE SUB-MODULES, THESE EVENTS ARE
3093 * REFERENCED BY CANONICAL NUMBERS:
3094 *
3095 * $FRNO --
3096 * THIS IS THE COMMAND EVENT FOR THE DRIVERS. EACH DRIVER IS
3097 * ALLOWED NOTIFY ACCESS ONLY. COMMANDS ARE CHANNLED TO THE
3098 * SPECIFIED SUB-MODULE BY THE STATE WHEN CAUSED.
3099 *
3100 * $FRN1 --
3101 * THIS IS THE COMMAND REPLY EVENT FOR THE DRIVERS. EACH DRIVER
3102 * IS ALLOWED CAUSE ACCESS ONLY. IN ORDER TO INFORM THE
3103 * MONITOR THE PERIPHERAL DRIVER CAUSES THIS EVENT WITH ITS STATE.
3104 *
3105 * $FRN2 --
3106 * AS IMPLIED ABOVE, $FRNO AND $FRN1 ARE AN INPUT/OUTPUT PAIR.
3107 * $FRN2 HOWEVER IS NOT PAIRED AT ALL. THE MONITOR USES THIS
3108 * EVENT AS A PASS EVENT SENDING FILES TO BE PROCESSED AND DEVICES
3109 * DOWN TO ITS SONS. THE SONS NEVER EVER PASS ANYTHING BACK TO
3110 * THE FATHER. THEY SIMPLY CLOSE FILES.
3111 *
3112 *
3113 * MESSAGE FORMATS: RETURNED IN T$SRW2.T (UPPER)
3114 *
3115 * FOR $FRN2 --
3116 * BITS 0 - 3 = JOB NUMBER (WITH 0 ILLEGAL)
3117 * BITS 4 = BANNER (ON MEANS SUPPLY BANNER)
3118 * BITS 5 = OUTPUT MODE: 512/ 320 (ON MEANS 320)
3119 * BITS 6 -17 = START ADDRESS (IN ELEMENTS)
3120 *
3121 * FOR $FRNO --
3122 * BITS 0 - 3 = JOB NUMBER (MUST BE ZERO)
3123 * BITS 4 -14 = <NOT USED>
3124 * BITS 15-17 = DEVICE UNIT NUMBER (0 THRU 7)

```

C COMMUNICATIONS -- SEND MESSAGE

```

001613 3126 USE CODE
3127 HEAD C
3128 *
3129 *
3130 *
3131 *
3132 *
3133 *
3134 *
3135 *
3136 *
3137 *
3138 *
3139 *
3140 *
3141 *
3142 *
3143 *
3144 *
001613 3145 MESSX BSS 0 ENTRY POINT
001613 3146 CAUSE ($FRN1,DU),(1,DU),(T$TEMP1,T),(T$TEMP2,T),(0,DU),(0,DU)
001613 000657 7000 00 TSX 0,$CAUSE
001614 000001 0000 03 ARG $FRN1,DU
001615 000001 0000 03 ARG 1,DU
001616 000027 0000 11 ARG T$TEMP1,T
001617 000026 0000 11 ARG T$TEMP2,T
001620 000000 0000 03 ARG 0,DU
001621 000000 0000 03 ARG 0,DU
001622 000000 7200 11 001622 3147 CHECK MESS1,B$BZ,MESSX
001623 000077 3600 03 LXL 0,T$SRW1,T
001624 001630 6000 00 ANX 0,B$STMK,DU
001625 000003 1000 03 TZE MESS1
001626 001613 6000 00 CMPX 0,B$BZ,DU
001627 777777 7100 00 TZE MESSX
001630 000000 2220 11 3148 MESS1 LDX X,T$SRW1,T GET NUMBER OF PEOPLE NOTIFIED
001631 001613 6000 00 3149 TZE MESSX NONE, RE-SEND
3150 *
3151 * MESSAGE SENT
3152 *
3153 RELT RELEASE TRAP BLOCK
001632 001335 7000 00 001632 END OF BINARY CARD LPCP0041
001633 001547 7100 00 001633 3154 EXIT 0,T$RELT EVAPORATE
TRA $EXIT

```

C

COMMUNICATIONS -- CTRAP SERVICE

```

001634 3156 USE CODE
3157 HEAD C
3158 *
3159 * CTRAP SERVICE
3160 *
3161 * THIS SUBROUTINE IS ENTERED WHENEVER AN OUTSTANDING
3162 * NOTIFY IS CAUSED. THE ROUTINE ASCERTAINS THE REASON
3163 * FOR THE CAUSE AND TRANSFERS CONTROL TO THE AP-
3164 * PROPRIATE SUBROUTINE.
3165 *
3166 * CALL WITH
3167 * C(XT) = CTRAP ADDRESS
3168 * CALLS
3169 * CSNSRVX (TO RE-ISSUE NOTIFY)
3170 * OR APPROPRIATE SUBROUTINE
3171 *
001634 3172 NSRV BSS 0
001634 3173 CHECK NSRV1,B$TLE,NSRVX
001634 3174 NSRV1 BSS 0 IT WAS REALLY CAUSED
001634 3175 LXL L,CSRLINK,T WELL, WHO GETS IT?
001634 3176 TRA 0,L LET HIM HANDLE IT
001634 000000 7200 11
001635 000077 3600 03
001636 001642 6000 00
001637 000011 1000 03
001640 001644 6000 00
001641 777777 7100 00
001642 000005 7270 11
001643 000000 7100 17
    
```

C COMMUNICATIONS -- RE-ISSUE NOTIFY

```

                                001644
                                3178      USE      CODE
                                3179      HEAD     C
                                3180 *
                                3181 *
                                3182 *
                                3183 *
                                3184 *
                                3185 *
                                3186 *
                                3187 *
                                3188 *
                                3189 *
                                3190 *
                                001644
                                001644
                                001644 001325 7000 00
                                001645 000000 6220 11
                                001646 000005 2210 11
                                001647 001634 6200 00
                                001650
                                001650 000510 7170 00
                                001651 000000 6210 12
                                001652 000005 2230 11
                                001653
                                001653 000646 7000 00
                                001654 000030 0000 13
                                001655 000031 0000 13
                                001656
                                001656 3200
                                001656 000000 7200 11
                                001657 000077 3600 03
                                001660 001664 6000 00
                                001661 000003 1000 03
                                001662 001652 6000 00
                                001663 777777 7100 00
                                001664
                                001664 3201 NSX2 BSS      0
                                001664 3202      RELT
                                001664 001335 7000 00
                                001665 001665 3203      TSX      0,TSRELT
                                001665 001547 7100 00      EXIT
                                TRA      $EXIT
                                3191 NSRVX BSS      0
                                3192      GETT      GET A TRAP BLOCK
                                3193      TSX      0,TSGETT
                                3194      EAX      X,0,T      SAVE A PTR TO IT
                                3195      LDX      T,TSLINK,T    LET T POINT TO ORIGINAL TCB
                                3196      EAX      0,NSRV      RESTART ADDRESS
                                3197      SETUP
                                3198 NSX1  XED      $$SETUP      RESTART CTRAP
                                3199      EAX      T,0,X      GET BACK PTR TO NEW TCB
                                NOTIF (ERN,3),(STATE,3) MAKE X3 POINT TO NCB
                                TSX      0,$NOTIF
                                ARG      ERN,3
                                ARG      STATE,3
                                CHECK NSX2,B$BZ,NSX1
                                LXL      0,TS$RW1,T
                                ANX      0,B$STMK,DU
                                TZE      NSX2
                                CMPX    0,B$BZ,DU
                                TZE      NSX1
                                TRA      $ERROR
                                3201 NSX2 BSS      0
                                3202      RELT
                                3203      TSX      0,TSRELT
                                EXIT
                                TRA      $EXIT
                                SUCCESSFULLY RE-ISSUED NOTIFY
                                RELEASE TBLOCK
                                EXIT
    
```

C

COMMUNICATIONS -- NOTIFY CONTROL BLOCKS

NOTIFY CONTROL BLOCKS

```

3205 *
3206 *
3207 *
3208 *
3209 * LINE PRINTER COMMAND EVENT
3210 *
3211 NCB LPNCB0,C$COMD,$FRN0,$LPST,0,R$TYPLP,B$SLP
      USE STORE
      EIGHT
      LPNCB0 BSS 0
      ZERO 0,B$STRO
      ZERO 0,0
      ZERO 0,0
      ZERO 0,0
      ZERO **,0
      ZERO 0,C$COMD
      ZERO *-C$NCB, *-C$JCB
      DEC 0
      DUP 1,16
      DEC 0
      ZERO $FRN0,
      ZERO $LPST,0
      VFD 36/0
      ZERO R$TYPLP,B$SLP
      USE PREVIOUS

3212 *
3213 * LINE PRINTER PASS FILE EVENT
3214 *
3215 NCB LPNCB2,$INIT,$FRN2,$LPST,0,R$TYPLP,B$SLP
      USE STORE
      EIGHT
      LPNCB2 BSS 0
      ZERO 0,B$STRO
      ZERO 0,0
      ZERO 0,0
      ZERO 0,0
      ZERO **,0
      ZERO 0,$INIT
      ZERO *-C$NCB, *-C$JCB
      DEC 0
      DUP 1,16
      DEC 0
      ZERO $FRN2,
      ZERO $LPST,0
      VFD 36/0
      ZERO R$TYPLP,B$SLP
      USE PREVIOUS
  
```

001666
 003450
 003450
 003450
 003450
 003450 000000 000035
 003451 000000 000000
 003452 000000 000000
 003453 000000 000000
 003454 000000 000000
 003455 000000 001666
 003456 003450 003450
 003457 000000000000
 003460
 003460 000000000000
 END OF BINARY CARD LPCP0043
 003500 000000 000000
 003501 003000 000000
 003502 000000000000
 003503 000002 000400
 001666
 001666
 003504
 003510
 003510
 003510 000000 000035
 003511 000000 000000
 003512 000000 000000
 003513 000000 000000
 END OF BINARY CARD LPCP0044
 003514 000000 000000
 003515 000000 002023
 003516 003510 003510
 003517 000000000000
 003520
 003520 000000000000
 003540 000002 000000
 003541 003000 000000
 END OF BINARY CARD LPCP0045
 003542 000000000000
 003543 000002 000400
 001666

C

COMMUNICATIONS -- NOTIFY CONTROL BLOCKS

			3217 *	
			3218 *	
			3219 *	CARD PUNCH COMMAND EVENT
			3220 *	
			3221	NCB CPNCB0,C\$COMD,\$FRNC,\$CPST,0,R\$TYPCP,B\$CP
		001666	USE	STORE
		003544		
		003550	EIGHT	
		003550	CPNCB0 BSS	0
003550	000000	000035	ZERO	0,B\$TRO
003551	000000	000000	ZERO	0,0
003552	000000	000000	ZERO	0,0
003553	000000	000000	ZERO	0,0
003554	000000	000000	ZERO	**0
003555	000000	001666	ZERO	0,C\$COMD
003556	003550	003550	ZERO	**C\$NCB,+-C\$JCR
003557	000000000000		DEC	0
		003560	DUP	1,16
003560	000000000000		DEC	0
END OF BINARY CARD	LPCP0046			
003600	000000	000000	ZERO	\$FRNC,
003601	001000	000000	ZERO	\$CPST,0
003602	000000000000		VFD	36/0
003603	000001	000200	ZERO	R\$TYPCP,B\$CP
		001666	USE	PREVIOUS
			3222 *	
			3223 *	
			3224 *	CARD PUNCH PASS FILE EVENT
			3225 *	
			3226	NCB CPNCB2,\$INIT,\$FRN2,\$CPST,0,R\$TYPCP,B\$CP
		001666	USE	STORE
		003604		
		003610	EIGHT	
		003610	CPNCB2 BSS	0
003610	000000	000035	ZERO	0,B\$TRO
003611	000000	000000	ZERO	0,0
003612	000000	000000	ZERO	0,0
003613	000000	000000	ZERO	0,0
003614	000000	000000	ZERO	**0
003615	000000	002023	ZERO	0,\$INIT
003616	003610	003610	ZERO	**C\$NCB,+-C\$JCR
003617	000000000000		DEC	0
		003620	DUP	1,16
END OF BINARY CARD	LPCP0047			
003620	000000000000		DEC	0
003640	000002	000000	ZERO	\$FRN2,
003641	001000	000000	ZERO	\$CPST,0
003642	000000000000		VFD	36/0
003643	000001	000200	ZERO	R\$TYPCP,B\$CP
		001666	USE	PREVIOUS

C

COMMUNICATIONS -- COMMANDS

		001666	3228	USE	CODE	
			3229	HEAD	C	
			3230	*		
			3231	*		
			3232	*		COMMANDS
			3233	*		
			3234	*		THIS ROUTINE DECODES A COMMAND SENT TO IT FROM THE MONITOR.
			3235	*		IF A LEGAL COMMAND, THE APPROPRIATE ACTION IS TAKEN. IF
			3236	*		ILLEGAL, WELL IT IS INGORED (MORE OFTEN THAN NOT)
			3237	*		
			3238	*		
		001666	3239	COMD	BSS	0
			3240	LDQ	TSSRW2,T	ENTER HERE FROM CSNSRV GET COMMAND
001666	000001	2360 11				
END OF BINARY CARD	LPCP0048					
001667	000012	7720 00	3241	QRL	18-4-4	RIGHT JUSTIFY IN QU
001670	000017	3760 03	3242	ANQ	=017,DU	MASK TO COMMAND
001671	000006	1160 03	3243	CMPQ	CMAX,DU	TEST VALIDITY OF COMMAND
001672	001702	6030 00	3244	TRC	COMDX	NOPE; EXIT
001673	001674	7100 22	3245	TRA	**1,QU*	BRANCH TO SUBROUTINE
001674	777777	0000 00	3246	CMDTB	ARG	\$ERROR
001675	001703	0000 00	3247	ARG	GET	0 = ILLEGAL
001676	001714	0000 00	3248	ARG	KILL	1 = GET PERIPHERAL
001677	001724	0000 00	3249	ARG	REL	2 = KILL PERIPHERAL NOW
001700	777777	0000 00	3250	ARG	\$ERROR	3 = RELEASE PERIPHERAL WHEN NOT BUSY
001701	001747	0000 00	3251	ARG	RSTR	4 = SPARE
	000006		3252	CMAX	EQU	*-CMDTB
			3253	*		5 = RESTART A PERIPHERAL
			3254	*		NUMBER OF COMMANDS
			3255	*		
			3256	*		COMDX
			3257	*		
			3258	*		RE-ISSUE NOTIFY ON THIS NCB
			3259	*		
		001702	3260	COMDX	BSS	0
001702	001644	7100 00	3261	TRA	NSRVX	RE-ISSUE THE NOTIFY

C

COMMUNICATIONS -- COMMAND GET

		001703	3263	USE	CODE	
			3264	HEAD	C	
			3265 *			
			3266 *			
			3267 *		COMMAND GET	
			3268 *			
			3269 *		THIS SUBROUTINE 'GETS' THE SPECIFIED PERIPHERAL.	
			3270 *			
			3271 *	ENTER WITH		
			3272 *		C(XT) = NCB-ADDRESS	
			3273 *			
		001703	3274	GET	BSS	0
001703	002005	7070 00	3275	TSX	L,PERI	GET PERIPHERAL INFORMATION
			3276 *			
			3277 *		CHECK IF CURRENTLY CLOSED	
			3278 *			
001704	000002	2350 14	3279	LDA	R\$FLAG,Z	GET PERIPHERAL FLAGS
001705	200000	3150 03	3280	CANA	R\$CLOSE,DU	CHECK IF CLOSED
001706	001702	6000 00	3281	TZE	COMDX	IGNORE COMMAND IF NOT
001707	000000	2220 11	3282	LDX	X,T\$SRW1,T	GET PASSED FRN
001710	000001	7420 14	3283	STX	X,R\$FRN,Z	SAVE IN TABLE
001711	077777	2220 03	3284	LDX	X,-1-R\$BUSY-R\$CLOSE-R\$RSVE,DU	SET OFF A FEW BITS
001712	000002	3420 14	3285	ANSX	X,R\$FLAG,Z	IN THE PERIPHERAL TABLE
001713	001702	7100 00	3286	TRA	COMDX	AND EXIT

C

COMMUNICATIONS -- COMMAND KILL

			001714	3268	USE	CODE	
				3269	HEAD	C	
				3290	*		
				3291	*		
				3292	*		COMMAND KILL
				3293	*		
				3294	*		THIS ROUTINE STOPS THE SPECIFIED PERIPHERAL IMMEDIATELY.
				3295	*		
				3296	*		ENTER WITH
				3297	*		C(XT) = NCB-ADDRESS
				3298	*		
			001714	3299	KILL	BSS	0
END OF BINARY CARD	LPCP0049						
001714	002005	7070	00	3300	TSX	L,PERI	GET PERIPHERAL INFORMATION
				3301	*		
				3302	*		CHECK IF PERIPHERAL IS IN USE
				3303	*		
001715	000002	2350	14	3304	LDA	R\$FLAG,Z	GET PERIPHERAL FLAG
001716	400000	3150	03	3305	CANA	R\$BUSY,DU	CHECK IF BUSY
001717	001702	6000	00	3306	TZE	COMDX	EXIT IF NOT
001720	000002	7260	14	3307	LXL	J,R\$ALLC,Z	GET POINTER TO JCB OF DEVICE OWNER
001721	400000	2350	07	3308	LDA	B\$KILL,DL	TURN ON THE KILL BIT
001722	000000	2550	16	3309	ORSA	J\$FLAGS,J	IN THE JOB FLAG WORD
001723	001702	7100	00	3310	TRA	COMDX	AND EXIT

C COMMUNICATIONS -- COMMAND RELEASE

```

001724 3312 USE CODE
3313 HEAD C
3314 *
3315 *
3316 * COMMAND RELEASE
3317 *
3318 * THIS ROUTINE RELEASES A PERIPHERAL AS SOON AS IT IS FREE
3319 *
3320 * ENTER WITH
3321 * C(XT) = NCB-ADDRESS
3322 *
001724 001724
001724 002005 7070 00 3323 REL BSS 0
3324 TSX L,PERI GET PERIPHERAL INFORMATION
3325 *
3326 * CHECK FOR ALREADY CLOSED
3327 *
001725 000002 2350 14 3328 LDA R$FLAG,Z GET PERIPHERAL FLAGS
001726 200000 3150 03 3329 CANA R$CLOSE,DU CHECK IF CLOSED
001727 001702 6010 00 3330 TNZ COMDX EXIT IF SO -- MONITOR SHOULD KNOW IT IS CLOSED
001730 100000 2750 03 3331 ORA R$RSVE,DU ASK TO HAVE IT CLOSED
001731 000002 7550 14 3332 STA R$FLAG,Z RESTORE FLAGS
001732 400000 3150 03 3333 CANA R$BUSY,DU CHECK IF CURRENTLY BUSY
001733 001702 6010 00 3334 TNZ COMDX BUSY, IT WILL CLOSE EVENTUALLY
3335 *
3336 * DEVICE IDLE -- SEIZE IT
3337 *
001734 001734
001734 400000 2750 03 3338 REL2 BSS 0
001735 000002 7550 14 3339 ORA R$BUSY,DU MARK IT BUSY
001736 000002 4460 14 3340 STA R$FLAG,Z IN THE PERIPHERAL FLAG WORD
001737 000006 4460 11 3341 SXL J,R$ALLC,Z AND ALLOCATED TO US
001740 000031 2350 11 3342 SXL J,T$JCB,T FUDGE
END OF BINARY CARD LPCP0050 3343 LDA C$STATE,T GET STATE
001741 000011 7710 00 3344 ARL 9 MAP INTO TYPE
001742 000027 7550 11 3345 STA T$TEMP1,T SAVE FOR RELEASE
001743 000027 4440 11 3346 SXL Z,T$TEMP1,T AND SAVE DEVICE NUMBER
001744 000027 2350 11 3347 RELP (T$TEMP1,T) RELEASE IT
001745 001433 7070 00 LDA T$TEMP1,T
001746 001702 7100 00 3348 TSX L,R$RELP
TRA COMDX EXIT

```

C COMMUNICATIONS -- COMMAND RESTART

		001747	3350	USE	CODE	
			3351	HEAD	C	
			3352	*		
			3353	*		
			3354	*		
			3355	*		COMMAND RESTART
			3356	*		THIS COMMAND RESTARTS THE JOB ON THE SPECIFIED PER-
			3357	*		IPHERAL TO THE SPECIFIED ELEMENT NUMBER.
			3358	*		
		001747	3359	RSTR	BSS	0
001747	002005	7070 00	3360	TSX	L,PERI	GET PERIPHERAL INFORMATION
001750	000027	7440 11	3361	STX	Z,T\$TEMP1,T	SAVE PTR TO DEVICE
			3362	*		
			3363	*		CHECK IF PERIPHERAL IS IN USE
			3364	*		
001751	000002	2350 14	3365	LDA	R\$FLAG,Z	GET PERIPHERAL FLAG
001752	400000	3150 03	3366	CANA	R\$BUSY,DU	CHECK IF BUSY
001753	001702	6000 00	3367	TZE	COMDX	EXIT IF NOT
			3368	*		
			3369	*		HALT READING IN ORDER TO DO SET POINTER
			3370	*		
001754	000002	7260 14	3371	LXL	J,R\$ALLC,Z	GET PTR TO JCB OF DEVICE OWNER
001755	000006	4460 11	3372	SXL	J,T\$JCB,T	SAVE JCB POINTER
001756	010000	2350 07	3373	LDA	B\$HALT,DL	GET THE HALT BIT
001757	000000	2550 16	3374	ORSA	J\$FLAGS,J	MARK READING HALTED
			3375	*		
			3376	*		SET POINTER TO RESTART ADDRESS REQUEST (I.E. 0)
			3377	*		
001760	000006	4500 16	3378	STZ	J\$RSTR,J	***RESET TO BEGINNING FOR NOW
001761	000026	4500 11	3379	STZ	T\$TEMP2,T	FIRST FIND CURRENT SETTING
		001762	3380	RST1	SPTR	(J\$IFRN,J),(T\$TEMP2,T)
001762	000564	7000 00		TSX	0,\$SPTR	
001763	000002	0000 16		ARG	J\$IFRN,J	
001764	000026	0000 11		ARG	T\$TEMP2,T	
		001765	3381	CHECK	RST2,B\$BZ,RST1	
001765	000000	7200 11		LXL	0,T\$SRW1,T	
END OF BINARY CARD	LPCP0051					
001766	000077	3600 03		ANX	0,B\$STMK,DU	
001767	001773	6000 00		TZE	RST2	
001770	000003	1000 03		CMPX	0,B\$BZ,DU	
001771	001762	6000 00		TZE	RST1	
001772	777777	7100 00		TRA	\$ERROR	
		001773	3382	RST2	BSS	0
001773	000001	2350 11	3383	LDA	T\$SRW2,T	GET CURRENT SETTING
001774	777777	3750 07	3384	ANA	-1,DL	ONLY
001775	000006	0750 16	3385	ADA	J\$RSTR,J	PLUS THE AMOUNT IT HAS MOVED ON US (ASYNC)
001776	000000	5310 00	3386	NEG		COMPUTE REQUESTED MINUS CURRENT FOR CORRECT SIGN
001777	000000	6350 05	3387	EAA	0,AL	MOVE TO AL
002000	000026	7550 11	3388	STA	T\$TEMP2,T	SAVE FOR SET POINTER
002001	001762	6010 00	3389	TNZ	RST1	SET PROPERLY

C

COMMUNICATIONS -- COMMAND RESTART

			3390 *			
			3391 *		RESET FLAG BITS	
			3392 *			
002002	010000	2350	07	3393	LDA B\$HALT,DL	UNSET HALT BIT
002003	000000	6550	16	3394	ERSA JS\$FLAGS,J	
002004	001702	7100	00	3395	TRA COMDX	AND EXIT THRU COMMON ROUTINE

C

COMMUNICATIONS -- PERIPHERAL INFORMATION (PERI)

		002005	3397	USE	CODE				
			3398	HEAD	C				
			3399	*					
			3400	*					
			3401	*		PERI			
			3402	*					
			3403	*		THIS ROUTINE CHECKS TO SEE IF THE PERIPHERAL NAME EXISTS			
			3404	*		AND IF SO RETURNS A POINTER TO THE DEVICE ENTRY BLOCK,			
			3405	*		OTHERWISE IT EXISTS			
			3406	*					
			3407	*		ENTER WITH			
			3408	*		C(XT) = NCB-ADDRESS			
			3409	*		RETURN WITH			
			3410	*		C(XZ) = DEVICE ENTRY BLOCK			
			3411	*					
		002005	3412	PERI	BSS	0			
	002005	000033	2220	11	3413	LDX	X,C\$RES,T	GET PERIPHERAL TYPE	
	002006	003074	2200	12	3414	LDX	0,R\$TABLE,X	GET POINTER TO TYPE TABLE	
	002007	000000	2240	10	3415	LDX	Z,R\$PTR,0	GET PTR TO FIRST DEVICE OF THIS TYPE	
	002010	000001	2350	11	3416	LDA	T\$SRW2,T	GET BACK UNIT NUMBER	
	002011	000022	7710	00	3417	ARL	36-18	RIGHT JUSTIFY IN AL	
	002012	000007	3750	07	3418	ANA	7,DL	MASK TO UNIT NUMBER	
END OF BINARY CARD	LPCP0052								
	002013	000000	2750	14	3419	ORA	R\$NAME,Z	GET NAME	
	002014	000001	3360	10	3420	LCQ	R\$MAX,0	GET THE NUMBER OF DEVICES TO CHECK	
	002015	000000	1150	14	3421	PERI1	CMPA	R\$NAME,Z	TEST FOR MATCH
	002016	000000	6000	17	3422		TZE	0,L	RETURN WITH Z POINTING TO DEVICE
	002017	000003	0240	03	3423		ADLX	Z,R\$DEVLN,DU	NO, SKIP TO NEXT DEVICE
	002020	000001	0760	07	3424		ADQ	1,DL	TEST FOR DONE
	002021	002015	6040	00	3425		TMI	PERI1	NO, LOOP
	002022	001702	7100	00	3426		TRA	COMDX	INGORE MONITOR

JOB INITIALIZATION ROUTINE

002052	000001	3750	07	3476	ANA	B\$B0TMK,DL	ISOLATE IT
002053	003100	2350	05	3477	LDA	B\$Z,AL	GET PROPER BUFFER SIZE
002054	000005	7550	16	3478	STA	J\$BUFSZ,J	SAVE FOR R/W TASKS
		003100		3479	USE	CONST	
003100	044000	001000		3480	B\$Z	ZERO	BITS/ELEMENT // BUFFER SIZE
003101	026400	000500		3481	ZERO	36*320,320	
		002055		3482	USE	PREVIOUS	
002055	000014	7370	00	3483	LLS	12	GET RESTART ADDRESS
002056	007777	3750	07	3484	ANA	B\$B\$TMK,DL	MASK TO ADDRESS IN ELEMENTS
002057	000006	7550	16	3485	STA	J\$RSTRT,J	SAVE
002060	000000	2220	11	3486	LDX	X,T\$SRW1,T	GET PASSED FRN
002061	000002	7420	16	3487	STX	X,J\$IFRN,J	SAVE AS INPUT FRN
END OF BINARY CARD LPCP0054							
002062	000002	2360	07	3488	LDQ	J\$N,DL	GET NUMBER OF WORKING BUFFERS
002063	000011	7560	16	3489	STQ	J\$EMPTY,J	SET EMPTY AND
002064	000012	4500	16	3490	STZ	J\$FULL,J	FULL COUNTS
002065	000020	6350	16	3491	EAA	J\$RESET,J	POINT TO FIRST WORD OF BUF Q -1
002066	000013	7550	16	3492	STA	J\$RPTR,J	SET READ POINTER
002067	000014	7550	16	3493	STA	J\$WPTR,J	AND WRITE POINTER
002070	000001	0350	03	3494	ADLA	1,DU	NOW POINT TO FIRST WORD OF BUF Q
002071	000000	6220	01	3495	EAX	X,0,AU	MOVE TO X
002072	000020	4420	16	3496	SXL	X,J\$RESET,J	WRAP POINTER
		002073		3497	DUP	2,J\$N	RESET BUFFER QUEUE
002073	000000	4500	12	3498	STZ	0,X	RESET IT
002074	000001	0220	03	3499	ADLX	X,1,DU	STEP TO NEXT
002077	000020	7420	16	3500	STX	X,J\$RESET,J	END POINTER
		002100		3501	GETP	(C\$RES,T)	GET REQUESTED RESOURCE
002100	000033	2350	11		LDA	C\$RES,T	
002101	001364	7070	00		TSX	L,R\$GETP	
002102	000016	7550	16	3502	STA	J\$RES,J	SAVE RESOURCE POINTERS IN JCB
002103	000003	7560	16	3503	STQ	J\$OFRN,J	SAVE FRN/ UNIT NUMBER IN JCB
		002104		3504	INIT2	BSS	0
				3505	*		
				3506	*	CHECK ELEMENT SIZE	
				3507	*		
		002104		3508	RQST	(J\$IFRN,J)	REQUEST STATUS
002104	000575	7000	00		TSX	0,\$RQST	
002105	000002	0000	16		ARG	J\$IFRN,J	
		002106		3509	CHECK	INIT3,B\$BZ,INIT2	
002106	000000	7200	11		LXL	0,T\$SRW1,T	
002107	000077	3600	03		ANX	0,B\$B\$TMK,DU	
END OF BINARY CARD LPCP0055							
002110	002114	6000	00		TZE	INIT3	
002111	000003	1000	03		CMPX	0,B\$BZ,DU	
002112	002104	6000	00		TZE	INIT2	
002113	777777	7100	00		TRA	\$ERROR	
002114	000000	2220	11	3510	INIT3	LDX	X,T\$SRW1,T
002115	000005	1020	16	3511	CMPX	X,J\$BUFSZ,J	GET NUMBER OF BITS/ELEMENT
002116	777777	6010	00	3512	TNZ	\$ERROR	IS IT WHAT WE EXPECTED?
				3513	*		NOPE

JOB INITIALIZATION ROUTINE

			3514 *	SET READ POINTER	
			3515 *		
002117	000027	4500 11	3516	STZ	T\$TEMP1,T FIRST FIND CURRENT SETTING
		002120	3517	INIT4 BSS	0
		002120	3518	SPTR	(J\$IFRN,J),(T\$TEMP1,T)
002120	000564	7000 00		TSX	0,\$SPTR
002121	000002	0000 16		ARG	J\$IFRN,J
002122	000027	0000 11		ARG	T\$TEMP1,T
		002123	3519	CHECK	INIT5,B\$BZ,INIT4
002123	000000	7200 11		LXL	0,T\$SRW1,T
002124	000077	3600 03		ANX	0,B\$STMK,DU
002125	002131	6000 00		TZE	INIT5
002126	000003	1000 03		CMPX	0,B\$BZ,DU
002127	002120	6000 00		TZE	INIT4
002130	777777	7100 00		TRA	\$ERROR
		002131	3520	INIT5 BSS	0
002131	000001	2350 11	3521	LDA	T\$SRW2,T GET CURRENT SETTING
002132	777777	3750 07	3522	ANA	-1,DL ONLY
002133	000006	1750 16	3523	SBA	J\$RSTRT,J MINUS REQUESTED
002134	000000	5310 00	3524	NEG	COMPUTE REQUESTED MINUS CURRENT FOR CORRECT SIGN
002135	000000	6350 05	3525	EAA	0,AL MOVE TO AU
END OF BINARY CARD	LPCP0056				
002136	000027	7550 11	3526	STA	T\$TEMP1,T SAVE FOR SET POINTER
002137	002120	6010 00	3527	TNZ	INIT4 SET PROPERLY
			3528 *		
			3529 *	CREATE READ TASK	
			3530 *		
		002140	3531	GETT	GET A TASK BLOCK
002140	001325	7000 00		TSX	0,T\$GETT
002141	000015	7410 16	3532	STX	T,J\$RTASK,J SAVE PTR TO READ TASK
002142	002304	2220 03	3533	LDX	X,RTASK,DU GET RESTART ADDRESS
002143	000004	7420 11	3534	STX	X,T\$TRA,T SAVE IN TCB
002144	000004	6200 11	3535	EAX	0,Q\$OFFST,T PREPARE TO START READ TASK
002145	000005	2220 11	3536	LDX	X,T\$LINK,T GET BACK PTR TO OLD TCB (IE NCB)
002146	000006	7420 11	3537	STX	X,T\$NCB,T SAVE PTR TO NCB
002147	000000	6210 12	3538	EAX	T,0,X RESTORE T
002150	003262	7170 00	3539	XED	Q\$XADD+Q\$TASK PLACE ON THE MASTER TASK QUEUE
			3540 *		
			3541 *	CREATE WRITE TASK	
			3542 *		
		002151	3543	GETT	GET A TCB
002151	001325	7000 00		TSX	0,T\$GETT
002152	000015	4410 16	3544	SXL	T,J\$WTASK,J SAVE PTR TO WRITE TASK
002153	002661	2220 03	3545	LDX	X,WTASK,DU GET RESTART ADDRESS
002154	000004	7420 11	3546	STX	X,T\$TRA,T SAVE IN TCB
002155	000004	6200 11	3547	EAX	0,Q\$OFFST,T PREPARE TO START WRITE TASK
002156	000005	2220 11	3548	LDX	X,T\$LINK,T GET BACK PTR TO OLD TCB (IE NCB)
002157	000006	7420 11	3549	STX	X,T\$NCB,T SAVE PTR TO NCB
002160	000000	6210 12	3550	EAX	T,0,X RESTORE T
002161	003262	7170 00	3551	XED	Q\$XADD+Q\$TASK PLACE ON THE MASTER TASK QUEUE

JOB INITIALIZATION ROUTINE

			3552 *			
			3553 *	SET NEW MEMORY REQUIREMENTS		
			3554 *			
	002162	000005	2360	16		
	002163	777777	3760	07		
END OF BINARY CARD	LPCP0057					
	002164	000002	4020	07	3555	LDQ JSBUFSZ,J GET BUFFER SIZE
	002165	000060	0760	07	3556	ANQ -1,DL ONLY
	002166	000000	6360	06	3557	MPY JSN,DL TIMES NUMBER OF BUFFERS
	002167	003321	0560	00	3558	ADQ 2*T\$LEN,DL PLUS TASK CONTROL BLOCKS
					3559	CAC C,DL MOVE TO CU
					3560	ASQ \$MEMRQ UPDATE MEMORY REQUIREMENT WORD
					3561 *	
002170	001644	7100	00		3562	TRA C\$NSRVX RE-ISSUE NOTIFY

JOB PROCESSING -- DIJKSTRA IMPLEMENTATION

002171

3564 USE CODE
3565 HEAD

3566 *
3567 *
3568 *
3569 *
3570 *
3571 *
3572 *
3573 *
3574 *
3575 *
3576 *
3577 *
3578 *
3579 *
3580 *
3581 *
3582 *
3583 *
3584 *
3585 *

DIJKSTRA IMPLEMENTATION

JOB PROCESSING IS BASED ON THE DIJKSTRA DESIGN MENTIONED PREVIOUSLY. IT ENTAILS THE MANAGEMENT OF COOPERATING SEQUENTIAL PROCESSES. (SEE P- AND V- MACROS.) EACH JOB TO BE PROCESSED IS REPRESENTED BY TWO ASYNCHRONOUS TASKS: THE 'READ TASK' (HEREAFTER CALLED RTASK) AND THE 'WRITE TASK' (HEREAFTER CALLED WTASK). THE TWO RUN IN PARALLEL. THE FLOWCHARTS ARE THE FOLLOWING:

RTASK	WTASK
-----	-----
P(EMPTY)	P(FULL)
FILL	EMPTY
V(FULL)	V(EMPTY)
LOOP RTASK	LOOP WTASK
- - - - -	- - - - -
EXIT	EXIT

READ TASK -- FILL

```

002171 3587 USE CODE
3588 HEAD
3589 *
3590 *
3591 * FILL
3592 *
3593 * FILL FILLS AN INPUT BUFFER.
3594 *
3595 FILL MACRO <NO-ARGUMENTS>
3596 TSX L,$FILL CALL SUBROUTINE
3597 ENDM FILL
3598 *
3599 *
3600 * FILL -- SUBROUTINE
3601 *
3602 * THIS SUBROUTINE DOES THE ACTUAL READING OF THE INPUT FILE.
3603 * IT IS CHARGED WITH ALLOCATING THE CORRECT SIZE BUFFER AND
3604 * WITH READING THE RIGHT NUMBER OF ELEMENTS. IT HANDLES ALL
3605 * ERRORS AND RETURNS THE STATUS IN JSFLAG,J FOR THE CALLER.
3606 *
3607 * ENTER WITH
3608 * C(XJ) = JCB-ADDRESS
3609 * C(XT) = TBLOCK-ADDRESS
3610 * ENTER BY
3611 * TSX L,FILL
3612 * CALLS
3613 * $READ
3614 * EXIT TO 0,L
3615 * USES
3616 * NO LOCALS
3617 * T$TEMP1
3618 *
002171 3619 FILL BSS 0
002171 000004 4470 11 3620 SXL L,T$TRA,T SAVE RETURN ADDRESS
002172 000005 2350 16 002172 3621 GETC (J$BUFSZ,J) ALLOCATE AN INPUT BUFFER
002173 001011 7070 00 LDA J$BUFSZ,J
002174 000013 2220 16 TSX L,$GETC
002175 000001 0220 03 3622 LDX X,$RPTR,J GET OLD BUFFER POINTER
002176 000020 1020 16 3623 ADLX X,1,DU BUMP TO NEW
002177 002201 6020 00 3624 CMPX X,$RESET,J TEST FOR WRAP
002200 000020 7220 16 3625 TNC **2 OK
002201 000013 7420 16 3626 LXL X,$RESET,J WRAP
002202 000000 7550 12 3627 STX X,$RPTR,J SAVE NEW POINTER
002203 000027 7550 11 3628 STA 0,X SAVE BUFFER ADDRESS
002204 000000 2350 16 3629 STA T$TEMP1,T AND HERE FOR EASY ACCESS
002205 010000 3150 07 3630 FILLO LDA J$FLAGS,J GET STATUS BITS
002206 002277 6010 00 3631 CANA $HALT,DL ARE WE TO HALT READING?
002207 400000 3150 07 3632 TNZ FILL6 YES, HALT FOR AWHILE
002210 002230 6010 00 3633 CANA B$KILL,DL ARE WE TO KILL THIS JOB?
3634 TNZ FILL2 YES, SO DO IT

```

READ TASK -- FILL

END OF BINARY CARD LPCP0058

002211 200000 3150 07
002212 002256 6010 00

3635 CANA B\$RHDR,DL ARE WE SUPPOSED TO OUTPUT A HEADER?
3636 TN7 FILL4 YES- SO DO IT

3637 *
3638 *
3639 * INITIATE A READ
3640 *

002213 000536 7000 00
002214 000002 0000 16
002215 000027 0000 11
002216 000001 0000 03
002217 000000 0000 03

3641 FILL1 READ (J\$IFRN,J),(T\$TEMP1,T),(1,DU),(0,DU)
TSX 0,\$READ
ARG J\$IFRN,J
ARG T\$TEMP1,T
ARG 1,DU
ARG 0,DU

002220 000000 7200 11
002221 000077 3600 03
002222 002236 6000 00
002223 000003 1000 03
002224 002204 6000 00
002225 000016 1000 03
002226 002230 6000 00
002227 777777 7100 00

3642 CHECK FILL3,B\$RZ,FILLO,B\$EOF,FILL2
LXL 0,T\$SRW1,T
ANX 0,B\$STMK,DU
TZE FILL3
CMPX 0,B\$BZ,DU
TZE FILLO
CMPX 0,B\$EOF,DU
TZE FILL2
TRA \$ERROR

3643 *
3644 * INFORMATION READ
3645 *

002230 000027 2350 11
002231 001110 7070 00
002232 000013 2220 16
002233 000000 4500 12
002234 040000 2350 07
002235 000000 2550 16

3646 FILL2 BSS 0 EOF REACHED
REL (T\$TEMP1,T) RELEASE THE BUFFER
LDA T\$TEMP1,T

002236 000004 7270 11
002237 525243 2350 03
002240 525243 2750 07

3648 LDX X,J\$RPTR,J GET POINTER TO INPUT BUFFER
3649 STZ 0,X MARK AS SUCH
3650 LDA B\$ENDR,DL GET EOF FLAG FOR READ TASK
3651 ORSA J\$FLAGS,J MARK AS SUCH
3652 FILL3 BSS 0 SUCCESSFUL READ
3653 LXL L,T\$TRA,T RETRIEVE RETURN ADDRESS
3654 BUGA BUGA BUG ALL REGISTERS EXCEPT T AND J
BUGBUG SET BUGBUG+1

END OF BINARY CARD LPCP0059

002237 525243 2350 03
002240 525243 2750 07

3655 LDA BUGBUG,DU
ORA BUGBUG,DL
BUGG

002241 525244 2360 03
002242 525244 2760 07

3656 BUGXR (0,X,Y,Z,0)
BUGBUG SET BUGBUG+1
LDQ BUGBUG,DU
ORQ BUGBUG,DL

002243 525245 2200 03
002244 525245 2220 03
002245 525245 2230 03
002246 525245 2240 03

LDX 0,BUGBUG,DU
LDX X,BUGBUG,DU
LDX Y,BUGBUG,DU
LDX Z,BUGBUG,DU

READ TASK -- FILL

002247	525245	2250 03		LDX	0,BUGBUG,DU	
		002250	3657	BUG	(T\$TEMP1,T)	
		525246		BUGBUG SET	BUGBUG+1	
002250	525246	2200 03		LDX	0,BUGBUG,DU	
002251	000027	7400 11		STX	0,T\$TEMP1,T	
002252	000027	4400 11		SXL	0,T\$TEMP1,T	
		002253	3658	BUGL	(T\$TRA,T)	
		525247		BUGBUG SET	BUGBUG+1	
002253	525247	2200 03		LDX	0,BUGBUG,DU	
002254	000004	4400 11		SXL	0,T\$TRA,T	
002255	000000	7100 17	3659	TRA	0,L	RETURN TO CALLER
			3660 *			
			3661 *			
			3662 *	SEND OUT HEADER		
			3663 *			
		002256	3664	FILL4 BSS	0	
002256	200000	6750 07	3665	ERA	B\$RHDR,DL	TURN OFF READ HEADER BIT
002257	000000	7550 16	3666	STA	J\$FLAGS,J	AND SAVE IT
			3667 *			
			3668 *	DETERMINE WHERE HEADER IS TO BE SENT		
			3669 *			
002260	000400	3150 07	3670	CANA	B\$LP,DL	IS IT A LINE PRINTER HEADER?
002261	002271	6000 00	3671	TZE	FILL5	NO, MUST BE CARD PUNCH
002262	003102	6220 00	3672	EAX	X,LPHDR	C(X) = FROM POINTER
002263	000013	2230 36	3673	LDX	Y,\$SRPTR,J*	C(Y) = TO POINTER
END OF BINARY CARD	LPCP0060					
002265	161700	5602 01	3674	RPD	LPHLN,1,TZE	MOVE IN HEADER DATA
002266	000000	2360 12	3675	LDQ	0,X	
002267	000000	7560 13	3676	STQ	0,Y	
002270	002236	7100 00	3677	TRA	FILL3	FUDGE TO LOOK LIKE A READ
		002271	3678	FILL5 BSS	0	OUTPUT HEADER FOR CARD PUNCH
002271	003172	6220 00	3679	EAX	X,CPHDR	C(X) = FROM POINTER
002272	000013	2230 36	3680	LDX	Y,\$SRPTR,J*	C(Y) = TO POINTER
002273	067700	5602 01	3681	RPD	CPHLN,1,TZE	MOVE IN HEADER DATA
002274	000000	2350 12	3682	LDA	0,X	
002275	000000	7550 13	3683	STA	0,Y	
002276	002236	7100 00	3684	TRA	FILL3	FUDGE TO LOOK LIKE A READ
		002277	3685	FILL6 BSS	0	
002277	002204	6200 00	3686	EAX	0,FILL0	FUDGE RESTART ADDRESS
002300	000004	7400 11	3687	STX	0,T\$TRA,T	
002301	000004	6200 11	3688	EAX	0,Q\$OFFST,T	GET OFFSET PTR TO TCB
002302	003262	7170 00	3689	XED	Q\$XADD+Q\$TASK	PLACE SELF ON Q\$TASK
		002303	3690	EXIT		AND WAIT FOR AWHILE--TO DO SPTR.
002303	001547	7100 00		TRA	\$EXIT	

READ TASK -- BANNERS

	002304	3692	USE	CODE	
		3693	HEAD		
		3694 *			
		3695 *			
		3696 *		BANNERS	
		3697 *			
		3698 *		THESE ARE THE OUTPUT BANNERS TO THE LINE PRINTERS AND	
		3699 *		CARD PUNCHES, RESPECTIVELY.	
		3700 *			
		3701 *			
		3702 *		THIS IS THE LINE PRINTER BANNER. IS SAYS FILE	
		3703 *			
	003102	3704	USE	CONST	
	003102	3705	LPHDR	0	LINE PRINTER BANNER
	003102	3706	OCT	772017171717	
	003103	3707	BCI	5,XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX	
END OF BINARY CARD	LPCP0061				
	003110	3708	OCT	770217171717	
	003111	3709	BCI	5,XXXXXXX XXX X	XXXXXXX
	003116	3710	OCT	770117171717	
	003117	3711	BCI	4,X X X	X
	003123	3712	OCT	770117171717	
	003124	3713	BCI	4,X X X	X
	003130	3714	OCT	770117171717	
	003131	3715	BCI	5,XXXXX X X	XXXXX
END OF BINARY CARD	LPCP0062				
	003136	3716	OCT	770117171717	
	003137	3717	BCI	4,X X X	X
	003143	3718	OCT	770117171717	
	003144	3719	BCI	4,X X X	X
	003150	3720	OCT	770117171717	
	003151	3721	BCI	4,X X X	X
	003155	3722	OCT	770117171717	
	003156	3723	BCI	5,X XXX XXXXXX XXXXXX	
	003163	3724	OCT	770217171717	
END OF BINARY CARD	LPCP0063				
	003164	3725	BCI	5,XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX	
	003171	3726	OCT	772017171717	
	000070	3727	LPHLN	EQU	*-LPHDR HEADER LENGTH IN WORDS

READ TASK -- BANNERS

		3729 *		
		3730 *		
		3731 *		
		3732 *	THIS IS THE CARD PUNCH BANNER. IT SAYS FILE	
		3733 *		
		3734 *		
	003172	3735 CPHDR	BSS 0	CARD PUNCH BANNER
003172	777740014001	3736	OCT 777740014001	
		3737	DUP 1.5	
	003173	3738	OCT 400140014001	
003173	400140014001	3739	OCT 477545014501	
003200	477545014501	3740	OCT 450145014501	
003201	450145014501	3741	OCT 440144014401	
003202	440144014401	3742	OCT 400140014001	
003203	400140014001	3743	OCT 400147754001	
003204	400147754001	3744	OCT 400140014001	
003205	400140014001	3745	OCT 477540054005	
003206	477540054005	3746	OCT 400540054005	
003207	400540054005	3747	OCT 400540054005	
003210	400540054005	3748	OCT 400140014001	
003211	400140014001			
END OF BINARY CARD	LPCP0064			
003212	477544454445	3749	OCT 477544454445	
003213	444544454445	3750	OCT 444544454445	
003214	440544054005	3751	OCT 440544054005	
	003215	3752	DUP 1.7	
003215	400140014001	3753	OCT 400140014001	
003224	400177770000	3754	OCT 400177770000	
	000033	3755 CPHLN	EGU *-CPHDR	HEADER LENGTH IN WORDS
	002304	3756	USE PREVIOUS	

READ TASK -- MAIN

		002304	3758	USE	CODE	
			3759	HEAD		
			3760 *			
			3761 *			
			3762 *			
			3763 *			
			3764 *			
			3765 *			
			3766 *			
			3767 *			
			3768 *			
			3769 *			
			3770 *			
			3771 *			
			3772 *			
			3773 *			
			3774 *			
			3775 *			
			3776 *			
			3777 *			
			3778 *			
			3779 *			
			3780 *			
			3781 *			
			3782 *			
			3783 *			
			3784 *			
			3785 *			
			3786 *			
			3787	RTASK	BSS	0
		002304	3788		P	(J\$EMPTY,J)
002304	000011	6250 16			EAX	Q,J\$EMPTY,J
002305	000751	7070 00			TSX	L,Q\$P
		002306	3789		FILL	
002306	002171	7070 00			TSX	L,\$FILL
		002307	3790		V	(J\$FULL,J)
002307	000012	6250 16			EAX	Q,J\$FULL,J
002310	000772	7070 00			TSX	L,Q\$V
002311	000000	2350 16	3791		LDA	J\$FLAGS,J
002312	040000	3150 07	3792		CANA	B\$ENDR,DL
002313	002304	6000 00	3793		TZE	RTASK
			3794 *			
			3795 *			
			3796 *			
			3797	RTASK1	BSS	0
		002314	3798		REL	
002314	001335	7000 00			TSX	Q,TSRELT
		002315	3799		EXIT	
002315	001547	7100 00			TRA	\$EXIT

READ TASK

THIS SECTION OF CODE IS EXECUTED BY THE READ TASK TO READ AN INPUT FILE INTO CORE. IT WILL READ IN BLOCKS OF THE UNIT SIZED SPECIFIED WHEN OPENED BY THE MASTER. IT WILL FILL AS MANY BUFFERS AS TOLD AND WILL KEEP RE-FILLING THEM UNTIL THE END OF FILE IS REACHED. IT WILL THEN TERMINATE ITSELF.

THE NUMBER OF BUFFERS TO FILL IS PRE-DEFINED BY AN ASSEMBLY TIME CONSTANT TO BE 2. THIS IMPLIES THAT ALL INPUT (AND OBVIOUSLY ALL OUTPUT) IS DOUBLE-BUFFERED. TO BECOME MULTI-BUFFERED, SIMPLY CHANGE THE 2 TO N WHERE N IS THE NUMBER OF BUFFERS TO WORK WITH.

ENTER WITH

C(XT) = TBLOCK-ADDRESS

C(XJ) = JOB-CONTROL-BLOCK-ADDRESS

CALLS

Q\$P

FILL

Q\$V

TSRELT

USES NO TEMPORARIES ITSELF

FILL THIS BUFFER

DO AN UP ON NUMBER OF FULL BUFFERS

GET JOB STATUS FLAGS

TEST FOR DONE

YES, BY HOOK OR CROOK

DONE, EVAPORATE PROCESSOR

RELEASE TBLOCK

POOF.

WRITE TASK -- WRITE

```

002316 3801 USE CODE
3802 HEAD
3803 *
3804 *
3805 * WRITE
3806 *
3807 *
3808 WRITE MACRO <NO-ARGUMENTS>
3809 TSX L,$WRITE CALL SUBROUTINE
3810 ENDM WRITE
3811 *
3812 *
3813 * WRITE -- SUBROUTINE
3814 *
3815 *
3816 * THIS SUBROUTINE DOES THE ISSUING OF THE APPEND PRIMITIVE
3817 * TO TRANSFER THE DATA TO THE OUTPUT DEVICE. IT DOES ALL OF
3818 * ITS OWN ERROR RECOVERY.
3819 *
3820 *
3821 * ENTER WITH
3822 * C(XJ) = JBC-ADDRESS
3823 * C(XT) = TBLOCK-ADDRESS
3824 * C(T$TEMP1,T) = STARTING ADDRESS
3825 * C(T$TEMP2,T) = NUMBER TO SEND
3826 * C(T$TEMP3,T) = MODE
3827 * ENTER BY
3828 * TSX L,$WRITE
3829 * CALLS
3830 * $APEND
3831 * C$MESSX (CONDITIONALLY)
3832 * RETURN TO
3833 * TRA 0,L
3834 * RETURN WITH
3835 * C(XJ) = JBLOCK-ADDRESS
3836 * C(XT) = TBLOCK-ADDRESS
3837 * C(XL) = RETURN-ADDRESS
3838 * USES
3839 * NO LOCALS
3840 * TEMP1, TEMP2, TEMP3 ARE PASSED TO IT
3841 * TEMP4, TEMPS (CONDITIONALLY)
3842 *
3843 *
3844 *

```

002316
 END OF BINARY CARD LPCP0065

```

002316 000022 7470 11 3846 STX L,$T$TEMP6,T SAVE RETURN ADDRESS
002317 000017 4500 16 3847 STZ J$RTRY,J RESET ERROR COUNTER
002320 000000 2350 16 3848 LDA J$FLAGS,J GET FLAG BITS
002321 000200 3150 07 3849 CANA B$CP,DL IS IT A CARD PUNCH JOB?

```

WRITE TASK -- WRITE

002322	002327	6000 00	3850	TZE	WT1	NO. GO WRITE IT OUT
002323	000004	2340 16	3851	SZN	JSCARD1,J	YES. WELL IS IT THE FIRST?
002324	002327	6010 00	3852	TNZ	WT1	NO. GO WRITE IT OUT
002325	000004	5540 16	3853	STC1	JSCARD1,J	YES. MARK IT AS SCUH
002326	002357	7100 00	3854	TRA	WT2.1	TELL OPER TO REMOVE CARDS AND READY PUNCH
	002327		3855	WT1	APEND	(J\$OFRN,J),(T\$TEMP1,T),(T\$TEMP2,T),(T\$TEMP3,T)
002327	000551	7000 00		TSX	0,\$APEND	
002330	000003	0000 16		ARG	J\$OFRN,J	
002331	000027	0000 11		ARG	T\$TEMP1,T	
002332	000026	0000 11		ARG	T\$TEMP2,T	
002333	000025	0000 11		ARG	T\$TEMP3,T	
	002334		3856	CHECK	WT4,B\$BZ,WT1,B\$HDWE,WT2	
002334	000000	7200 11		LXL	0,T\$SRW1,T	
002335	000077	3600 03		ANX	0,B\$STMK,DU	
002336	002430	6000 00		TZE	WT4	
002337	000003	1000 03		CMPX	0,B\$BZ,DU	
002340	002327	6000 00		TZE	WT1	
002341	000012	1000 03		CMPX	0,B\$HDWE,DU	
002342	002344	6000 00		TZE	WT2	
002343	777777	7100 00		TRA	\$ERROR	
	002344		3857	WT2	BSS	0
			3858	*		HARDWARE ERROR
			3859	*		HARDWARE ERROR
			3860	*		
END OF BINARY CARD	LPCP0066					
002344	000001	7220 11	3861	LXL	X,T\$SRW2,T	GET PHYSICAL STATUS
002345	000000	2350 16	3862	LDA	J\$FLAGS,J	IT IS A PRINTER
002346	000400	3150 07	3863	CANA	B\$LP,DL	OR PUNCH ERROR
002347	002357	6000 00	3864	TZE	WT2.1	PUNCH ERROR -- ALWAYS DO ACCEPT ATTENTION
002350	000020	3020 03	3865	CANX2	=020,DU	***GDP TEST***
002351	002357	6010 00	3866	TNZ	WT2.1	YES. DO AN ACCEPT ATTENTION
002352	000017	0540 16	3867	AOS	J\$RTRY,J	BUMP RETRY COUNT
002353	000017	2350 16	3868	LDA	J\$RTRY,J	GET CURRENT COUNT
002354	000005	1150 07	3869	CMPA	\$RTMAX,DL	COMPARE TO MAX
002355	002327	6020 00	3870	TNC	WT1	OK -- RETRY AGAIN
002356	002430	7100 00	3871	TRA	WT4	NOPE -- SKIP ENTIRE BLOCK
	002357		3872	WT2.1	BSS	0
002357	000006	2220 11	3873	LDX	X,T\$NCB,T	DO ACCEPT ATTENTION
002360	000031	2350 12	3874	LDA	C\$STATE,X	GET PTR TO NCB
002361	000024	7550 11	3875	STA	T\$TEMP4,T	GET STATE FOR CAUSE
002362	000016	7220 16	3876	LXL	X,J\$RES,J	SAVE FOR STATE
002363	000000	2350 12	3877	LDA	R\$NAME,X	GET DEVICE PTR
002364	000007	3750 07	3878	ANA	7,DL	GET NAME
002365	000022	7350 00	3879	ALS	18	ISOLATE UNIT NUMBER
002366	016000	2750 03	3880	ORA	B\$RDY,DU	MOVE TO MESSAGE FIELD
002367	000023	7550 11	3881	STA	T\$TEMP5,T	MARK IT TO BE READIED
	002370		3882	BRANCH	NOPASS,C\$MESSX,(T\$TEMP4,T),(T\$TEMP5,T)	SAVE FOR MESSAGE
002370	001325	7000 00		TSX	0,T\$GETT	
002371	000000	6220 11		EAX	X,0,T	
END OF BINARY CARD	LPCP0067					

WRITE TASK -- WRITE

002372	000005	2210	12	LDX	T,T\$LINK,X	
002373	000024	2360	11	LDQ	T\$TEMP4,T	
002374	000027	7560	12	STQ	T\$TEMP1,X	
002375	000023	2360	11	LDQ	T\$TEMP5,T	
002376	000026	7560	12	STQ	T\$TEMP2,X	
002377	000000	6210	12	EAX	T,0,X	
002400	001613	6200	00	EAX	0,C\$MESSX	
002401	000004	7400	11	STX	0,T\$TRA,T	
002402	000004	6200	11	EAX	0,Q\$OFFST,T	
002403	003262	7170	00	XED	Q\$XADD+Q\$TASK	
002404	000005	2210	12	LDX	T,T\$LINK,X	
		002405		BUGXR	(0,X)	
		525250		BUGBUG	SET	BUGBUG+1
002405	525250	2200	03	LDX	0,BUGBUG,DU	
002406	525250	2220	03	LDX	X,BUGBUG,DU	
		002407		3883	BUG	(T\$TEMP4,T)
		525251		BUGBUG	SET	BUGBUG+1
002407	525251	2200	03	LDX	0,BUGBUG,DU	
002410	000024	7400	11	STX	0,T\$TEMP4,T	
002411	000024	4400	11	SXL	0,T\$TEMP4,T	
		002412		3884	BUG	(T\$TEMP5,T)
		525252		BUGBUG	SET	BUGBUG+1
002412	525252	2200	03	LDX	0,BUGBUG,DU	
002413	000023	7400	11	STX	0,T\$TEMP5,T	
002414	000023	4400	11	SXL	0,T\$TEMP5,T	
		002415		3885 *		
				3886 *	NOW PUT	OUT AN ACCEPT ATTENTION ON DEVICE
				3887 *		
				3888	WT3	APEND (J\$OFRN,J),(0,DU),(0,DU),(1,DU)
002415	000551	7000	00	TSX	0,\$APEND	
002416	000003	0000	16	ARG	J\$OFRN,J	
002417	000000	0000	03	ARG	0,DU	
END	OF BINARY CARD	LPCP0068				
002420	000000	0000	03	ARG	0,DU	
002421	000001	0000	03	ARG	1,DU	
		002422		3889	CHECK	WT1,B\$BZ,WT3
002422	000000	7200	11	LXL	0,T\$SRW1,T	
002423	000077	3600	03	ANX	0,B\$STMK,DU	
002424	002327	6000	00	TZE	WT1	
002425	000003	1000	03	CMPX	0,B\$BZ,DU	
002426	002415	6000	00	TZE	WT3	
002427	777777	7100	00	TRA	\$ERROR	
		002430		3890 *		
				3891 *	SUCCESSFUL TRANSFER,	CLEAN UP
				3892 *		
002430	000022	2270	11	3893	WT4	BSS 0
		002431		3894		LDX L,T\$TEMP6,T
		525253		3895		BUG (T\$TEMP1,T)
002431	525253	2200	03	BUGBUG	SET	BUGBUG+1
					LDX	0,BUGBUG,DU
						RETRIEVE RETURN ADDRESS
						BUG EVERYTHING

WRITE TASK -- WRITE

002432	000027	7400	11		STX	0,T\$TEMP1,T
002433	000027	4400	11		SXL	0,T\$TEMP1,T
		002434		3896	BUG	(T\$TEMP2,T)
		525254			BUGBUG SET	BUGBUG+1
002434	525254	2200	03		LDX	0,BUGBUG,DU
002435	000026	7400	11		STX	0,T\$TEMP2,T
002436	000026	4400	11		SXL	0,T\$TEMP2,T
		002437		3897	BUG	(T\$TEMP3,T)
		525255			BUGBUG SET	BUGBUG+1
002437	525255	2200	03		LDX	0,BUGBUG,DU
002440	000025	7400	11		STX	0,T\$TEMP3,T
002441	000025	4400	11		SXL	0,T\$TEMP3,T
		002442		3898	BUGXR	(X,Y,Z,Q)
		525256			BUGBUG SET	BUGBUG+1
002442	525256	2220	03		LDX	X,BUGBUG,DU
002443	525256	2230	03		LDX	Y,BUGBUG,DU
002444	525256	2240	03		LDX	Z,BUGBUG,DU
002445	525256	2250	03		LDX	Q,BUGBUG,DU
		002446		3899	BUGU	(T\$TEMP6,T)
		525257			BUGBUG SET	BUGBUG+1
END OF BINARY CARD	LPCP0069					
002446	525257	2200	03		LDX	0,BUGBUG,DU
002447	000022	7400	11		STX	0,T\$TEMP6,T
		002450		3900	BUGA	
		525260			BUGBUG SET	BUGBUG+1
002450	525260	2350	03		LDA	BUGBUG,DU
002451	525260	2750	07		ORA	BUGBUG,DL
		002452		3901	BUGQ	
		525261			BUGBUG SET	BUGBUG+1
002452	525261	2360	03		LDQ	BUGBUG,DU
002453	525261	2760	07		ORQ	BUGBUG,DL
002454	000000	7100	17	3902	TRA	0,L

RETURN TO CALLER

WRITE TASK -- 320 OUTPUT MODE

```

002455 3904      USE      CODE
        3905      HEAD
        3906 *
        3907 *
        3908 *
        3909 *
        3910 *
        3911 *
        3912 *
        3913 *
        3914 *
        3915 *
        3916 *
        3917 *
        3918 *
        3919 *
        3920 *
        3921 *
        3922 *
        3923 *
        3924 *
        3925 *
        3926 *
        3927 *
        3928 *
        3929 *
        3930 *
        3931 *
        3932 *
        3933 *
        3934 *
        3935 *
        3936 *
        3937 *
        3938 *
        3939 *
        3940 *
        3941 *
        3942 *
        3943 *
        3944 *
        3945 *
        3946 *
        3947 *
        3948 *
    
```

320 OUTPUT MODE
GECOS FORMAT

THIS SUBROUTINE OUTPUTS THE BUFFER IN 320 LINE EDITED
FORMAT. IT ASSUMES STANDARD GERC SYSTEM FORMAT:

BLOCK SIZE -- DATA BLOCKS ARE VARIABLE IN LENGTH UP TO A
UP TO A MAXIMUM BLOCK SIZE OF 320 WORDS (DECIMAL).
BLOCK SERIAL NUMBER-- A BLOCK SERIAL NUMBER WILL EXIST AS
THE FIRST WORD OF EACH DATA BLOCK AND WILL CONTAIN TWO
BINARY VALUES AS FOLLOWS:
BITS 0 - 17 BLOCK SERIAL NUMBER -- THE SPEQUENTIAL NUMBER
BITS 18 -35 BLOCK SIZE -- SIZE OF BLOCK IN WORDS, NOT INCLUDING
ITSELF

RECORD FORMAT -- RECORDS WITHIN A BLOCK ARE VARIABLE LENGTH
BITS 0 - 17 RECORD SIZE IN WORDS, NOT INCLUDING ITSELF
BITS 18- 23 NOT USED UNLES 0 - 17 ARE ZERO, THEN FILE MARK CHAR
BITS 24 -27 ZEROS
BITS 28-29 LOGICAL RECORD CODE
0 = NOT A MEDIA CONVERSION RECORD
1 = BINARY CARD IMAGE
2 = HOLLERITH CARD IMAGE
3 = PRINT-LINE IMAGE
BITS 30- 35 REPORT CODE

ENTER WITH
C(XJ) = JCB-ADDRESS
C(XT) = TBLOCK-ADDRESS

ENTER BY
TRA TABLE,AL* (TYPE)
WRITE

RETURN WITH
C(XT) = TBLOCK-ADDRESS
C(XJ) = JCB-ADDRESS

RETURNS TO EMPT1
USES
NO LOCALS
TEMP1 IS PASSED
TEMP2, TEMP3, TEMP6

WRITE TASK -- 320 OUTPUT MODE

			3950 *			
			3951 *			
			3952 *			
		002455	3953 .320	BSS	0	320 WORD BLOCK PROCESSOR
		002455	3954 GECOS	BSS	0	PRINT-LINE/ GECOS FORMAT
002455	000014	2220 36	3955	LDX	X,J\$WPTR,J*	GET OUTPUT BUFFER ADDRESS
002456	000010	7420 16	3956	STX	X,J\$XDATA,J	SAVE AS END OF BUFFER
002457	000000	7240 12	3957	LXL	Z,0,X	GET BLOCK SIZE
002460	000500	1040 03	3958	CMPX	Z,320,DU	IS IT VALID?
002461	002520	6030 00	3959	TRC	GCOS4	NO, TOO BIG
002462	000001	0240 03	3960	ADLX	Z,1,DU	INCLUDING ITSELF
002463	000010	0440 16	3961	ASX	Z,J\$XDATA,J	UPDATE END OF BUFFER POINTER
002464	002470	7100 00	3962	TRA	GCOS5	ENTER LOOP
			3963 *			
			3964 *		OUTPUT NEXT LINE	
			3965 *			
		002465	3966 GCOS1	BSS	0	
002465	000007	2240 16	3967	LDX	Z,J\$RCW,J	GET ADDRESS OF OLD RCW
002466	000000	2220 14	3968	LDX	X,0,Z	GET LENGTH OF RCW
002467	000007	0220 16	3969	ADLX	X,J\$RCW,J	TO COMPUTE ADDRESS OF NEXT RCW
		002470	3970 GCOS5	BSS	0	
002470	000001	0220 03	3971	ADLX	X,1,DU	INCLUDE RCW ITSELF
002471	000007	7420 16	3972	STX	X,J\$RCW,J	AND SAVE IT
002472	000001	0220 03	3973	ADLX	X,1,DU	ONE AGAIN TO POINT TO STARTING ADDRESS OF DATA
END OF BINARY CARD	LPCP0070					
002473	000027	7420 11	3974	STX	X,T\$TEMP1,T	SAVE FOR WRITE SUBROUTINE
002474	000007	2220 36	3975	LDX	X,J\$RCW,J*	GET NUMBER OF WORDS IN THIS RCW
002475	002520	6000 00	3976	TZE	GCOS4	ZERO, DONE WITH THIS BLOCK
002476	000027	0220 11	3977	ADLX	X,T\$TEMP1,T	TEST FOR VALID LENGTH
002477	000010	1020 16	3978	CMPX	X,J\$XDATA,J	
002500	002502	6000 00	3979	TZE	*+2	
002501	002520	6030 00	3980	TRC	GCOS4	NG, DONE
002502	000007	2360 36	3981	LDQ	J\$RCW,J*	GET BACK LENGTH
002503	000022	7720 00	3982	QRL	18	IN QL
002504	000007	2350 36	3983	LDA	J\$RCW,J*	GET LOGICAL RECORD MODE
002505	001700	3750 07	3984	ANA	B\$MODMK,DL	MASK TO MEDIA ONLY
002506	000006	7710 00	3985	ARL	6	AND RIGHT JUSTIFIED
002507	000000	7220 16	3986	LXL	X,J\$FLAGS,J	GET JOB STATUS BITS
002510	000400	3020 03	3987	CANX	X,B\$LP,DU	IS IT A PRINTER JOB?
002511	003232	6000 25	3988	TZE	CPTAB,AL*	NO, BRANCH ON CP MODE
002512	003225	7100 25	3989	TRA	LPTAR,AL*	BRANCH ON LP MODE

WRITE TASK -- J20 OUTPUT MODE

002513	000000	6360	06	002513	3991 *			
002514	000026	7560	11	002514	3992 *			
002515	000025	7420	11	002515	3993 *			
				002516	3994 GCOS7	BSS	0	JOINED HERE FROM ABOVE TRANSFERS
002516	002316	7070	00	002516	3995	EAQ	0,QL	MOVE CHARACTER COUNT TO QU
				002517	3996	STQ	T\$TEMP2,T	SAVE FOR WRITE
002517	002465	7100	00	002517	3997	STX	X,T\$TEMP3,T	SAVE MODE
					3998 GCOS2	WRITE		NOW WRITE IT OUT
						TSX	L,SWRITE	
					3999 GCOS3	BSS	0	
					4000	TRA	GCOS1	LOOP
					4001 *			
					4002 *	INFORMATION WRITTEN		
					4003 *			
002520	000022	7270	11	002520	4004 GCOS4	LXL	L,T\$TEMP6,T	RETRIEVE RETURN
				002521	4005	BUGXR	(0,X,Y,Z,Q)	
				525262	BUGBUG	SET	BUGBUG+1	
END OF BINARY CARD	LPCP0071							
002521	525262	2200	03			LDX	0,BUGBUG,DU	
002522	525262	2220	03			LDX	X,BUGBUG,DU	
002523	525262	2230	03			LDX	Y,BUGBUG,DU	
002524	525262	2240	03			LDX	Z,BUGBUG,DU	
002525	525262	2250	03			LDX	Q,BUGBUG,DU	
				002526	4006	BUGA		
				525263	BUGBUG	SET	BUGBUG+1	
002526	525263	2350	03			LDA	BUGBUG,DU	
002527	525263	2750	07			ORA	BUGBUG,DL	
				002530	4007	BUGQ		
				525264	BUGBUG	SET	BUGBUG+1	
002530	525264	2360	03			LDR	BUGBUG,DU	
002531	525264	2760	07			ORQ	BUGBUG,DL	
002532	002622	7100	00	002532	4008	TRA	EMPT1	RETURN FROM WHENCE CALLED

WRITE TASK -- 320 FORMAT CONVERSIONS

		002533	4010	USE	CODE	
			4011	HEAD		
			4012 *			
			4013 *			
			4014 *			
			4015 *			GECOS FORMAT CONVERSION
			4016 *			ONE BRANCHES THOUGH THIS TABLE TO PICK UP THE CORRECT
			4017 *			CHARACTER COUNT AND MODE FOR PRINTING A 320 BLOCK.
			4018 *			
		003225	4019	USE	CONST	
		003225	4020	LPTAB	BSS	0
003225	002534	0000 00	4021	ARG	LP.0	NOT A MEDIA CONVERSION RECORD
003226	002564	0000 00	4022	ARG	IGNOR	BINARY CAR IMAGE
003227	002534	0000 00	4023	ARG	LP.2	HOLLERITH CARD IMAGE
003230	002534	0000 00	4024	ARG	LP.3	PRINT LINE
003231	002564	0000 20	4025	ARG	IGNOR.*	
		002533	4026	USE	PREVIOUS	
002533	002564	0000 00	4027	ARG	IGNOR	NOT IMPLEMENTED
			4028 *			
			4029 *			
			4030 *			
			4031 *			ONE BRANCHES THOUGH THIS TABLE TO PICK UP THE CORRECT
			4032 *			CHARACTER COUNT AND MODE FOR PUNCHING A 320 BLOCK.
			4033 *			
		003232	4034	USE	CONST	
		003232	4035	CPTAB	BSS	0
003232	002542	0000 00	4036	ARG	CP.0	NOT A MEDIA CONVERSION RECORD
003233	002550	0000 00	4037	ARG	CP.1	BINARY CARD IMAGE
003234	002556	0000 00	4038	ARG	CP.2	HOLLERITH CARD IMAGE
END OF BINARY CARD	LPCP0072					
003235	002556	0000 00	4039	ARG	CP.3	PRINT LINE
		003236	4040	DUP	1,16-4	
003236	002564	0000 20	4041	ARG	IGNOR.*	
		002534	4042	USE	PREVIOUS	

WRITE TASK -- 320 OUTPUT PARAMETER ROUTINES

		002534	4044	USE	CODE	
			4045	HEAD		
			4046 *			
			4047 *			
			4048 *			
			4049 *			PARAMETER ROUTINES
			4050 *			THESE ROUTINES BASED ON THE LOGICAL RECORD MODE PICK
			4051 *			RETURN THE NUMBER OF CHARACTERS TO SEND AND THE MODE.
			4052 *			
			4053 *	ENTER WITH		
			4054 *	C(QL) = NUMBER OF WORDS		
			4055 *	RETURN TO		
			4056 *	GCOS7 EXCEPT IGNOR RETURNS TO GCOS4		
			4057 *	RETURN WITH		
			4058 *	C(QL) = NUMBER OF CHARACTERS TO PRINT		
			4059 *	C(XX) = MODE		
			4060 *			
		002534	4061 LP.0	BSS	0	NOT A MEDIA CONVERSION RECORD
		002534	4062 LP.2	BSS	0	HOLLERITH CARD IMAGE
		002534	4063 LP.3	BSS	0	PRINT LINE IMAGE
002534	000006	4020 07	4064	MPY	6,DL	CONVERT TO CHARACTER COUNT
002535	010000	1160 07	4065	CMPQ	4096,DL	TEST AGAINST MAX
002536	002540	6020 00	4066	TNC	**2	OK
002537	010000	2360 07	4067	LDQ	4096,DL	GET MAX INSTEAD
002540	000003	2220 03	4068	LDX	X,B\$LP3,DU	GET MODE
002541	002513	7100 00	4069	TRA	GCOS7	RETURN
			4070 *			
			4071 *			
			4072 *			
		002542	4073 CP.0	BSS	0	NOT A MEDIA CONVERSION RECORD
002542	002564	7100 00	4074	TRA	IGNOR	***FOR NOW
002543	000016	1160 07	4075	CMPQ	14,DL	TEST FOR BCD
END OF BINARY CARD	LPCP0073					
002544	002556	6000 00	4076	TZE	CP.2	YES
002545	000033	1160 07	4077	CMPQ	27,DL	TEST FOR BINARY
002546	002550	6000 00	4078	TZE	CP.1	YES
002547	002564	7100 20	4079	TRA	IGNOR,*	
			4080 *			
		002550	4081 CP.1	BSS	0	BINARY CARD IMAGE
002550	000006	4020 07	4082	MPY	6,DL	CONVERT TO CHARACTER COUNT
002551	000240	1160 07	4083	CMPQ	160,DL	TEST AGAINST MAX
002552	002554	6020 00	4084	TNC	**2	OK
002553	000240	2360 07	4085	LDQ	160,DL	GET MAX INSTEAD
002554	000002	2220 03	4086	LDX	X,B\$CP2,DU	GET BINARY MODE
002555	002513	7100 00	4087	TRA	GCOS7	RETURN
			4088 *			
		002556	4089 CP.2	BSS	0	HOLLERITH IMAGE
		002556	4090 CP.3	BSS	0	
002556	000006	4020 07	4091	MPY	6,DL	CONVERT TO CHARACTER COUNT
002557	000120	1160 07	4092	CMPQ	80,DL	TEST AGAINST MAX

WRITE TASK -- 320 OUTPUT PARAMETER ROUTINES

002560	002562	6020	00	4093	TNC	**2	OK	
002561	000120	2360	07	4094	LDQ	80,DL	GET MAX INSTEAD	
002562	000003	2220	03	4095	LDX	X,B%CP3,DU	GET HOLLERITH MODE	
002563	002513	7100	00	4096	TRA	GCOS7	RETURN	
				4097	*			
				4098	*			
		002564		4099	IGNOR	BSS	0	IGNORE ENTIRE BLOCK
002564	002520	7100	00	4100	TRA	GCOS4	RETURN AS IF DONE	

WRITE TASK -- 512 OUTPUT MODE

002565	4102	USE	CODE	
	4103	HEAD		
	4104 *			
	4105 *			
	4106 *			LP512 OUTPUT MODE
	4107 *			
	4108 *	THIS SUBROUTINE OUTPUTS THE BUFFER IN 512. WORDS EDITED		
	4109 *	CONTINUOUS MODE. IT SETS UP ALL THE PARAMETERS		
	4110 *			
	4111 *	ENTER WITH		
	4112 *	C(XJ) = JCB-ADDRESS		
	4113 *	C(XT) = TCB-ADDRESS		
	4114 *			
	4115 *	ENTER BY		
	4116 *	TRA TABLE,AL (TYPE) *		
	4117 *	RETURNS TO EMPT1		
	4118 *			
	4119	.512	BSS	0
				512 WORD BLOCK PROCESSOR
	002565	002565	4120	LP512
			BSS	0
002565	000000	2350	16	4121
				LDA
002566	000400	3150	07	4122
				CANA
002567	777777	6000	00	4123
				TZE
002570	006000	2220	03	4124
				LDX
				X,512+6,DU
				SET UP CHARACTER COUNT
				0
	002571			4125
END OF BINARY CARD	LPCP0074			LP1
				BSS
002571	000026	7420	11	4126
				STX
002572	000002	2230	03	4127
				LDX
002573	000025	7430	11	4128
				STX
				Y,T\$TEMP3,T
				PASS ON TO WRITE ROUTINE
				DO THE WRTIE
				0
	002574			4129
002574	002316	7070	00	WRITE
				TSX
002575	002622	7100	00	4130
				TRA
				L,\$WRITE
				EMPT1
				RETURN TO EMPTY ROUTINE

WRITE TASK -- EMPTY

```

002576      4132      USE      CODE
            4133      HEAD
            4134 *
            4135 *
            4136 *
            4137 *
            4138 *      EMPTY
            4139 *
            4140 *      EMPTY FORCES THE OUTPUT OF A CORE BUFFER
            4141 *
            4142 *      MACRO <NO-ARGUMENTS>
            4143 *      TSX L,EMPTY      CALL SUBROUTINE
            4144 *      ENDM      EMPTY
            4145 *
            4146 *      EMPTY -- SUBROUTINE
            4147 *
            4148 *      THIS SUBROUTINE DOES THE ACTUAL WRITING OF THE OUTPUT. IT
            4149 *      IS CHARGED WITH THE WRITING THE CORRECT NUMBER OF UNITS
            4150 *      IN THE PROPER FORMAT TO THE OUTPUT FILE (DEVICE). IT THEN
            4151 *      DE-ALLOCATES THE BUFFER. IT HANDLES ALL ERRORS AND RETURNS
            4152 *      A JOB STATUS TO THE CALLER.
            4153 *
            4154 *      ENTER WITH
            4155 *      C(XJ) = JCB-ADDRESS
            4156 *      C(XT) = TBLOCK-ADDRESS
            4157 *
            4158 *      ENTER BY
            4159 *      TSX L,EMPTY
            4160 *
            4161 *      CALLS
            4162 *      .512
            4163 *      .320
            4164 *      WRITE
            4165 *
            4166 *      RETURN WITH
            4167 *      C(XJ) = JCB-ADDRESS
            4168 *      C(XT) = TBLOCK-ADDRESS
            4169 *
            4170 *      EXIT TO 0,L
            4171 *      USES
            4172 *      NO LOCALS
            4173 *      TEMP1, TEMP2, TEMP3
            4174 *
002576      002576      4171 *      Empty BSS      0
002576      000004 4470 11      4172 *      SXL      L,TSTRAT      SAVE RETURN ADDRESS
002577      000014 2220 16      4173 *      LDX      X,J$WPTR,J      GET OLD BUFFER POINTER
002600      000001 0220 03      4174 *      ADLX     X,1,DU      BUMP TO NEW
002601      000020 1020 16      4175 *      CMPX     X,J$RESET,J      TEST FOR WRAP
002602      002604 6020 00      4176 *      TNC      **2      OK
002603      000020 7220 16      4177 *      LXL      X,J$RESET,J      WRAP
002604      000014 7420 16      4178 *      STX      X,J$WPTR,J      SAVE NEW POINTER
002605      000000 2350 12      4179 *      LDA      0,X      GET ADDRESS OF NEXT BUFFER
002606      002656 6000 00      4180 *      TZE     EMPTX      DONE
002607      000027 7550 11      4181 *      STA      T$TEMP1,T      SAVE FOR EASY ACCESS

```

WRITE TASK -- EMPTY

002610	000000	2350 16	4182	LDA	J\$FLAGS,J	GET FLAGS	
002611	400000	3150 07	4183	CANA	B\$KILL,DL	TEST FOR ABORT	
002612	002622	6010 00	4184	TNZ	EMPT1	YES, JUST RELEASE BUFFERS TILL EOF HIT	
002613	100000	3150 07	4185	CANA	B\$WHDR,DL	DO WE NEED A HEADER?	
002614	002640	6010 00	4186	TNZ	EMPT3	YES	
002615	010000	3750 03	4187	ANA	B\$OUTMK,DU	ISOLATE OUTPUT FORMAT	
END OF BINARY CARD	LPCP0075						
002616	000036	7710 00	4188	ARL	36-4-1-1	RIGHT JUSTIFIED	
002617	002620	7100 25	4189	TRA	*+1,AL*	TRANSFER TO OUTPUT FORMAT ROUTINE	
002620	002565	0000 00	4190	ARG	.512	512 WORD BLOCK	
002621	002455	0000 00	4191	ARG	.320	320 WORD BLOCK	
			4192 *				
			4193 *				
			4194 *				
		002622	4195	EMPT1	BSS	0	JOINED HERE BY ALL OUTPUTER
002622	000006	0540 16	4196	AOS	J\$RSTR,J	BUMP RESTART ADDRESS IN BLOCK	
		002623	4197	RELC	(J\$WPTR,J*)	RELEASE BUFFER, FORCE HOLES TO TOP OF MEM	
002623	000014	2350 36		LDA	J\$WPTR,J*		
002624	001110	7070 00		TSX	L,R\$RELC		
		002625	4198	EMPT2	BSS	0	GET READY TO RETURN TO CALLER
002625	000004	7270 11	4199	LXL	L,T\$TRA,T	RETRIEVE RETURN ADDRESS	
		002626	4200	BUGA		BUG ALL REGISTERS EXCEPT T AND J	
		525265		BUGBUG	SET	BUGBUG+1	
002626	525265	2350 03		LDA	BUGBUG,DU		
002627	525265	2750 07		ORA	BUGBUG,DL		
		002630	4201	BUGQ			
		525266		BUGBUG	SET	BUGBUG+1	
002630	525266	2360 03		LDR	BUGBUG,DU		
002631	525266	2760 07		ORQ	BUGBUG,DL		
		002632	4202	BUGXR	(0,X,Y,Z,Q)		
		525267		BUGBUG	SET	BUGBUG+1	
002632	525267	2200 03		LDR	0,BUGBUG,DU		
002633	525267	2220 03		LDR	X,BUGBUG,DU		
002634	525267	2230 03		LDR	Y,BUGBUG,DU		
002635	525267	2240 03		LDR	Z,BUGBUG,DU		
002636	525267	2250 03		LDR	Q,BUGBUG,DU		
002637	000000	7100 17	4203	TRA	0,L	RETURN TO CALLER	
			4204 *				
			4205 *				
			4206 *				
		002640	4207	EMPT3	BSS	0	LP BANNER
		002640	4208	DECRM	(J\$RSTR,J)	BACK UP START ADDRESS FOR HEADER	
002640	000001	3360 07		LCQ	1,DL		
002641	000006	0560 16		ASQ	J\$RSTR,J		
002642	100000	6750 07	4209	ERA	B\$WHDR,DL	TURN OFF WRITE HEADER BIT	
002643	000000	7550 16	4210	STA	J\$FLAGS,J	SAVE IT	
END OF BINARY CARD	LPCP0076						
002644	000400	3150 07	4211	CANA	B\$LP,DL	SEE HERE HEADER IS TO GO	
002645	002650	6000 00	4212	TZE	EMPT4	CARD PUNCH	
002646	000520	2220 03	4213	LDR	X,LPHLN*6,DU	LINE PRINT; GET NUBER OF CHARACTERS	

WRITE TASK -- EMPTY

002647	002571	7100	00	4214	TRA	LP1	USE LINE PRINTER ROUTINE	
			002650	4215	EMPT4	BSS	0	OUTPUT HEADER ON CARD PUNCH
002650	000240	2220	03	4216	LDX	X,CPHLN*6-2,DU	CFT NUMBER OF CHARACTERS	
002651	000026	7420	11	4217	STX	X,T\$TEMP2,T	SAVE	
002652	000002	2230	03	4218	LDX	Y,2,DU	MAKE MODE BINARY	
002653	000025	7430	11	4219	STX	Y,T\$TEMP3,T	SAVE IT	
			002654	4220	WRITE		WRITE IT OUT	
002654	002316	7070	00		TSX	L,\$WRITE		
002655	002622	7100	00	4221	TRA	EMPT1	DONE	
				4222	*			
				4223	*	EOF REACHED		
				4224	*			
			002656	4225	EMPTX	BSS	0	
002656	020000	2350	07	4226	LDA	B\$ENDW,DL	GIVE EOF STATUS	
002657	000000	2550	16	4227	ORSA	J\$FLAGS,J	GIVE IT TO CALLER	
002660	002625	7100	00	4228	TRA	EMPT2	ACT NORMAL	

WRITE TASK -- MAIN

```

002661 4230 USE CODE
4231 HEAD
4232 *
4233 *
4234 *
4235 *
4236 *
4237 *
4238 *
4239 *
4240 *
4241 *
4242 *
4243 *
4244 *
4245 *
4246 *
4247 *
4248 *
4249 *
4250 *
4251 *
4252 *
4253 *
4254 *
4255 *
4256 *
4257 *
002661 4258 WTask BSS 0
002661 4259 P (J$FULL,J) DO A DOWN ON NUMBER OF FULL BUFFERS
002661 000012 6250 16 EAX Q,J$FULL,J
002662 000751 7070 00 TSX L,Q$P
002663 002576 7070 00 4260 EMPTY EMPTY THIS BUFFER
002664 000011 6250 16 4261 TSX L,$EMPTY
002665 000772 7070 00 V (J$EMPTY,J) DO AN UP ON THE NUMBER OF EMPTY RUFFERS
002666 000000 2350 16 EAX Q,J$EMPTY,J
002667 020000 3150 07 4262 TSX L,Q$V GET JOB STATUS FLAGS
002670 002661 6000 00 4263 LDA J$FLAGS,J TEST FOR DONE
4264 CANA B$ENDW,DL
4265 WTSK1 BSS 0 NO, LOOP
4266 *
4267 * DONE, CLOSE INPUT FILE
4268 *
002671 4269 CLOSE (J$IFRN,J) CLOSE INPUT FILE, WHICH ALSO UNLOCKS IT
END OF BINARY CARD LPCP0077
002671 000616 7000 00 TSX O,$CLOSE
002672 000002 0000 16 ARG J$IFRN,J
002673 000000 7200 11 4270 CHECK WTSK2,$$BZ,WTSK1
LXL O,$$SRW1,T
    
```

WRITE TASK

THIS SECTION OF CODE IS EXECUTED BY THE WRITE TASK TO WRITE AN OUTPUT BUFFER TO PRE-OPENED FILE (DEVICE) IN A SPECIFIED FORMAT. IT WILL WRITE OUT AS MANY BUFFERS AS TOLD, AND WILL KEEP OUTPUTTING UNTIL AN END OF FILE STATUS IS REACHED. IT WILL THEN SEND A MESSAGE TO THE MONITOR NOTIFYING HIM OF A SUCCESSFUL TERMINATION AND THEN TERMINATE ITSELF.

ENTER WITH

C(XT) = TBLOCK-ADDRESS

C(XJ) = JOB-CONTROL-BLOCK-ADDRESS

CALLS

Q\$P

EMPTY

Q\$V

\$CLOSE

C\$MESSC

R\$RELF

J\$RELJ

T\$RELT

WRITE TASK -- MAIN

002674	000077	3600	03		ANX	0,B\$STMK,DU	
002675	002701	6000	00		TZE	WTSK2	
002676	000003	1000	03		CMPX	0,B\$RZ,DU	
002677	002671	6000	00		TZE	WTSK1	
002700	777777	7100	00		TRA	\$ERROR	
	002701			4271	WTSK2	BSS	0
				4272	*		
				4273	*	INFORM MONITOR	
				4274	*		
002701	014000	2350	03	4275	LDA	B\$DONE,DU	ASSUME SUCCESSFUL
002702	000000	2360	16	4276	LDQ	J\$FLAGS,J	GET JOB NUMBER
002703	400000	3160	07	4277	CANG	B\$KILL,DL	TEST FOR KILLED
002704	002707	6000	00	4278	TZE	*+3	NOPE
002705	000006	2350	16	4279	LDA	J\$RSTR,J	GET RESTART ADDRESS
002706	004000	2750	03	4280	ORA	B\$ABORT,DU	AND ABORTED
002707	000026	7550	11	4281	STA	T\$TEMP2,T	SAVE AS MESSAGE
002710	000004	7370	00	4282	LLS	4	WHICH WILL BE STATE FOR CAUSE
002711	000017	3750	07	4283	ANA	B\$BJBMK,DL	MASK TO JOB NUMBER
002712	000027	7550	11	4284	STA	T\$TEMP1,T	SAVE AS STATE
002713	003430	4500	05	4285	STZ	J\$JTAB,AL	RESET JOB TABLE
				4286	*		
				4287	BRANCH	NOPASS,C\$MESSX,(T\$TEMP1,T),(T\$TEMP2,T)	
002714	001325	7000	00		TSX	0,T\$GETT	
002715	000000	6220	11		EAX	X,0,T	
002716	000005	2210	12		LDX	T,T\$LINK,X	
END OF BINARY CARD	LPCP0078						
002717	000027	2360	11		LDQ	T\$TEMP1,T	
002720	000027	7560	12		STQ	T\$TEMP1,X	
002721	000026	2360	11		LDQ	T\$TEMP2,T	
002722	000026	7560	12		STQ	T\$TEMP2,X	
002723	000000	6210	12		EAX	T,0,X	
002724	001613	6200	00		EAX	0,C\$MESSX	
002725	000004	7400	11		STX	0,T\$TRA,T	
002726	000004	6200	11		EAX	0,Q\$OFFST,T	
002727	003262	7170	00		XED	Q\$XADD+Q\$TASK	
002730	000005	2210	12		LDX	T,T\$LINK,X	
	002731				BUGXR	(0,X)	
	525270			BUGRUG	SET	BUGRUG+1	
002731	525270	2200	03		LDX	0,BUGRUG,DU	
002732	525270	2220	03		LDX	X,BUGRUG,DU	
				4288	*		
				4289	*	RELEASE RESOURCES	
				4290	*		
				4291	RELp	(J\$RES,J)	RELEASE PERIPHERAL
002733	000016	2350	16		LDA	J\$RES,J	
002734	001433	7070	00		TSX	L,R\$RELp	
002735	000005	2360	16	4292	LDQ	J\$BUFSZ,J	GET BUFFER SIZE
002736	777777	3760	07	4293	ANG	-1,DL	ONLY
002737	000002	4020	07	4294	MPY	J\$N,DL	TIMES NUMBER OF BUFFERS
002740	000060	0760	07	4295	ADQ	2*T\$LEN,DL	PLUS TCB'S

WRITE TASK -- MAIN

002741	000000	5330 00	4296	NEGL		GET 2'S COMPLEMENT
002742	000000	6360 06	4297	EAG	0,0L	MOVE TO QU
002743	003321	0560 00	4298	ASQ	MEMRQ	REDUCE MEMORY REQUIREMENTS
002744	777777	6040 00	4299	TMI	\$ERROR	OOPS..
		002745	4300	RELJ		RELEASE JCB
END OF BINARY CARD	LPCP0079					
002745	001357	7070 00		TSX	L,JSRELJ	
		002746	4301	RELT		RELEASE TCR
002746	001335	7000 00		TSX	C,TSRELT	
		002747	4302	EXIT		
002747	001547	7100 00		TRA	\$EXIT	

INITIALIZATION

002750		4304	USE	CODE	
		4305	HEAD		
		4306 *			
		4307 *			
		4308 *			
		4309 *			
		4310 *			
		4311 *			
		4312 *			
		4313 *			
		4314 *			
		4315 *			
		4316 *			
		4317 *			
		4318 *			
		4319 *			
		4320 *			
		4321 *			
	002750	4322 ANIT	BSS	0	INITIALIZATION ENTRY
	002750	4323 UP	EQU	ANIT	
		4324 *			
		4325 *			
		4326 *			
002750	000000 4500 00	4327	STZ	0	BURN OUR BRIDGES BEHIND US
	002751	4328	CKPT		SAVE REGISTERS
002751	000474 7170 00		XED	X\$CKPT	
002752	003740 2210 03	4329	LDX	T,SPTCB,DU	INITIALIZE T TO SPECIAL TCB
002753	003740 2260 03	4330	LDX	J,SPTCB,DU	AND MAKE J POINT TO SAME
002754	000006 4460 11	4331	SXL	J,\$JCB,T	SAVE IT FOR \$EXIT
002755	004400 6340 07	4332	LDI	B\$OVM+B\$PAM,DL	MASK OFF OVERFLOW AND PARITY ERRORS
002756	000010 2350 03	4333	LDA	ZZ1,DU	INITIALIZE TO AVAILABLE MEMORY
002757	003320 7550 00	4334	STA	\$AVAIL	
002760	003430 5540 00	4335	STC1	JSJTAB	MAKE JOB NUMBER 0 ILLEGAL
		4336 *			
		4337 *			
		4338 *			
		4339 ANIT1	SETFV	(X\$FV,DU)	SET FAULT VECTOR
002761	000526 7000 00		TSX	0,\$SETFV	
002762	000000 0000 03		ARG	X\$FV,DU	
	002763	4340	CHECK	ANIT2,B\$BZ,ANIT1	
002763	000000 7200 11		LXL	0,T\$SRW1,T	
002764	000077 3600 03		ANX	0,B\$STMK,DU	
002765	002772 6000 00		TZE	ANIT2	
002766	000003 1000 03		CMPX	0,B\$BZ,DU	
002767	002761 6000 00		TZE	ANIT1	
002770	777777 7100 00		TRA	\$ERROR	
	002771	4341	RELT		RELEASE TCB
002771	001335 7000 00		TSX	0,T\$RELT	

INITIALIZATION

THIS ROUTINE INITIALIZES THE PERIPHERAL SCHEDULER MONITOR BY PERFORMING THE FOLLOWING FUNCTIONS.

FUNCTIONS

INITIALIZE REGISTERS AND LOCATION ZERO

SET FAULT VECTOR

SET UP COMMUNICATIONS NETWORK

IT IS A NON-REENTRANT ROUTINE WHICH DOES NOT OVERLAP ANY OPERATION. IT CAN BE OVERLAYED BY BUFFER STORAGE AFTER INITIALIZATION IF DESIRED (NOT IMPLEMENTED YET)

INITIALIZATION

```

4343 *
4344 *
4345 *      SET UP COMMUNICATIONS NETWORK
4346 *
4347 *      FRN 0 = INPUT EVENT
4348 *      FRN 1 = OUTPUT EVENT
4349 *      FRN 2 = PASS EVENT
4350 *      STATE DIFFERENTIATES BETWEEN LP AND CP MODULES
4351 *
4352 *      SET UP LP COMMUNICATIONS
4353 *
4354 ANIT2 BSS 0
002772
END OF BINARY CARD LPCP0080
002772 003450 2210 03      4355      LDX      T,LPNCB0,DU      SET UP LP INPUT LINE
002773 000006 7260 11      4356      LXL      J,T$JCB,T      GET JCB POINTER
002774 001325 7000 00      4357      BRANCH  PASS,C$NSRVX
002775 000000 6220 11      TSX      0,T$GETT
002776 000005 2210 12      EAX      X,0,T
002777 001644 6200 00      LDX      T,T$LINK,X
003000 000004 7400 11      EAX      0,C$NSRVX
003001 000004 6200 11      STX      0,T$TRA,T
003002 003262 7170 00      EAX      0,Q$OFFST,T
003003 000000 6210 12      XED      Q$XADD+Q$TASK
003004 003004      EAX      T,0,X
003004 525271 2200 03      BUGXR   (0,X)
003005 525271 2220 03      BUGBUG SET  BUGBUG+1
003006 001335 7000 00      4358      RELT
003006 001335 7000 00      TSX      0,T$RELT      RELEASE TCB
4359 *
4360 *
003007 003510 2210 03      4361      LDX      T,LPNCB2,DU      SET UP LP PASS LINE
003010 000006 7260 11      4362      LXL      J,T$JCB,T      GET JCB POINTER
003011 001325 7000 00      4363      BRANCH  PASS,C$NSRVX
003012 000000 6220 11      TSX      0,T$GETT
003013 000005 2210 12      EAX      X,0,T
003014 001644 6200 00      LDX      T,T$LINK,X
003015 000004 7400 11      EAX      0,C$NSRVX
003016 000004 6200 11      STX      0,T$TRA,T
003017 003262 7170 00      EAX      0,Q$OFFST,T
003017 003262 7170 00      XED      Q$XADD+Q$TASK
END OF BINARY CARD LPCP0081
003020 000000 6210 12      EAX      T,0,X
003021 003021      BUGXR   (0,X)
003021 525272 2200 03      BUGBUG SET  BUGBUG+1
003022 525272 2220 03      4364      LDX      0,BUGBUG,DU
003022 525272 2220 03      LDX      X,BUGBUG,DU
003023 001335 7000 00      RELT
003023 001335 7000 00      TSX      0,T$RELT      RELEASE TCB

```

INITIALIZATION

```

4366 *
4367 *
4368 *          SET UP CP COMMUNICATIONS
4369 *
003024 003550 2210 03      4370      LDX      T,CPNCB0,DU      SET UP CP INPUT LINE
003025 000006 7260 11      4371      LXL      J,TSJCB,T        GET JCB POINTER
                                003026      4372      BRANCH  PASS,C$NSRVX
003026 001325 7000 00      TSX      0,TSGETT
003027 000000 6220 11      EAX      X,0,T
003030 000005 2210 12      LDX      T,TSLINK,X
003031 001644 6200 00      EAX      0,C$NSRVX
003032 000004 7400 11      STX      0,T$TRA,T
003033 000004 6200 11      EAX      0,Q$OFFST,T
003034 003262 7170 00      XED      Q$XADD,Q$TASK
003035 000000 6210 12      EAX      T,0,X
                                003036      BUGXR   (0,X)
                                525273      BUGBUG SET   BUGBUG+1
003036 525273 2200 03      LDX      0,BUGBUG,DU
003037 525273 2220 03      LDX      X,BUGBUG,DU
                                003040      4373      RELT
003040 001335 7000 00      TSX      0,T$RELT

4374 *
4375 *
003041 003610 2210 03      4376      LDX      T,CPNCB2,DU      SET UP CP PASS LINE
003042 000006 7260 11      4377      LXL      J,TSJCB,T        GET JCB POINTER
                                003043      4378      BRANCH  PASS,C$NSRVX
003043 001325 7000 00      TSX      0,TSGETT
003044 000000 6220 11      EAX      X,0,T
003045 000005 2210 12      LDX      T,TSLINK,X
END OF BINARY CARD LPCP0082
003046 001644 6200 00      EAX      0,C$NSRVX
003047 000004 7400 11      STX      0,T$TRA,T
003050 000004 6200 11      EAX      0,Q$OFFST,T
003051 003262 7170 00      XED      Q$XADD,Q$TASK
003052 000000 6210 12      EAX      T,0,X
                                003053      BUGXR   (0,X)
                                525274      BUGBUG SET   BUGBUG+1
003053 525274 2200 03      LDX      0,BUGBUG,DU
003054 525274 2220 03      LDX      X,BUGBUG,DU
                                003055      4379      RELT      RELEASE TCB
003055 001335 7000 00      TSX      0,T$RELT
                                003056      4380      EXIT      DONE
003056 001547 7100 00      TRA      $EXIT

```

ASSEMBLY CONTROL CARDS

	4382	HEAD		
	4383 *			
	4384 *			
	4385 *		HOUSEKEEPING CARDS	
	4386 *			
	4387 *	HERE WE CLEAN ANY AND ALL ASSEMBLER BUGS.		
	4388 *	THE JCB'S ARE PRE-ALLOCATED, THE LAST USED LOCATION OF		
	4389 *	CORE IS CALCULATED AND WHAT REMAINS UP TO THE END OF THE		
	4390 *	BAR IF ANY IS LINKED ON THE FREE MEMORY LIST.		
	4391 *			
003057	4392	USE	CODE	
	4393	HEAD		
003060	4394	EIGHT		
003060	4395	ZCODEL EQU	*-ZCODE	CODE UNDER CODE
	4396 *			
	4397 *			
003252	4398	USE	CONST	
	4399	LIT		FORCE LITERAL POOL HERE
003260	4400	EIGHT		
000200	4401	ZCONSL EQU	*-ZCONS	CODE UNDERCONST
	4402 *			
	4403 *			
003314	4404	USE	QSTOR	
003320	4405	EIGHT		
000040	4406	ZQSTRL EQU	*-ZQSTR	CODE UNDER QSTOR
	4407 *			
	4408 *			
003644	4409	USE	STORE	
	4410	HEAD	J	
003644	4411	JCB0 BSS	0	PRE-ALLOCATED JCB'S
000003	4412	JCBN EQU	R\$LPMAX+R\$CPMAX	*NUMRER TO PRE-ALLOCATE
003644	4413	DUP	1,LEN*JCBN	GENERATE
003644 000000000000	4414	DEC	0	
END OF BINARY CARD LPCP0085				
003740	4415	JCBX EQU	*	END OF JCB'S
	4416	HEAD		
	4417 *			
000037	4418	TLEN EQU	T\$LEN+7	ROUND T\$LEN TO
000030	4419	TLENR EQU	TLEN/8*8	MULTIPLE OF EIGHT
003740	4420	EIGHT		START OF DYNAMIC BUFFER AREA
003740	4421	SPTCB BSS	TLENR-1	FIRST TCB (START OF DYNAMIC BUFFERS
003767 000000000000	4422	DEC	0	FOR THE CRUMMY LOADER
003770	4423	EIGHT		
000450	4424	ZSTORL EQU	*-ZSTOR	CODE UNDER STORE
	4425 *			
	4426 *			
003770	4427	LASTC EQU	ZCODEL+ZCONSL+ZQSTRL+ZSTORL	LAST CARD

ASSEMBLY CONTROL CARDS

		4429 *				
		4430 *				
	003740	4431 ZTOP0	EQU	SPTCB	START OF DYNAMIC BUFFER AREA	
	004767	4432 ZZ	EQU	LASTC+MQUAN-1	ROUND UP TO NEXT MULTIPLE	
	004000	4433 ZTOP	EQU	ZZ/MQUAN*MQUAN	OF CORE PAGE SIZE	
	000010	4434 ZZ1	EQU	ZTOP-LASTC	LENGTH OF LEFT OVER CORE	
		4435 *				
	003770	4440 NEXTF	EQU	LASTC	STICK EXTRA CORE ON FREE LIST	
	003770	4441 NEXTB	EQU	NEXTF		
003770	003355	000010	4442	ZERO	R\$LAST,ZZ1	INITIALIZE HEADER DATA FOR LINKED LIST
003771	003353	000000	4443	ZERO	R\$FIRST,0	
		4444 *				
		4445 *				
END OF BINARY CARD	LPCP0086					
	002750	4446	TCD	\$UP	MARK END OF BINARY DECK	
END OF BINARY CARD	LPCP0087					
		4447 *				
		4448	DCARD	2,\$		
		4449 \$	DKEND			
		4450 \$EOD				
		4451 *				
		4452 *				
		4453 *				
	002750	4454 THIS	END	UP		
END OF BINARY CARD	LPCP0090					
3772 IS THE NEXT AVAILABLE LOCATION. GMAP VERSION JMPA/062770 JMPB/062770 JMPC/062770						
THERE WERE NO WARNING FLAGS IN THE ABOVE ASSEMBLY						

OCTAL	SYMBOL	REFERENCES BY ALTER NO.							
15	GERETS								
2	GEROAD								
31	GEROLL								
30	GEROUT								
24	GERSTR								
23	GESAVE								
14	GESETS								
5	GESNAP								
44	GESNUM								
20	GESPEC								
26	GESYOT								
21	GETIME								
32	GEUSER								
34	GEWAKE								
2564	IGNOR	4099	4022	4025	4027	4041	4074	4079	4099
2104	INIT2	3504	3504	3509					
2114	INIT3	3510	3509	3510					
2120	INIT4	3517	3517	3519	3527				
2131	INIT5	3520	3519	3520					
2023	INIT	3449	3215	3226	3449				
2455	.320	3953	3953	4191					
2565	.512	4119	4119	4190					
5	.APEND	141	141	1243					
32	.CATDR	162	162						
26	.CATLG	158	158						
42	.CATLK	170	170						
47	.CAUSE	175	175	1612					
22	.CHSEG	154	154	1378					
25	.CLOSE	157	157	1421					
21	.CLSEG	153	153						
57	.CRSG	181	181						
50	.DELET	176	176						
27	.DESTR	159	159						
36	.EMM								
23	.EXSEG	155	155						
44	.LOCK	172	172	1464					
55	.MSTA	179	179						
46	.NOTIF	174	174	1554					
24	.OPEN	156	156	772					
30	.OPENS	160	160						
36	.OPENW	166	166						
52	.OPSCE	178	178						
20	.OPSEG	152	152						
17	.PAUSE	151	151	783	805	830	3077		
0	.PRIV	136	136						
34	.RDAACL	164	164						
37	.RDBRN	167	167						
35	.RDDIR	165	165						
40	.RDLK	168	168						
56	.RDME	180	180						

OCTAL	SYMBOL	REFERENCES BY ALTER NO.																
4	.READ	140	140	1189														
13	.RQDT	147	147	765														
14	.RQERT	148	148															
12	.RQST	146	146	1332														
63	.RQWD	184	184															
6	.RRF	142	142															
10	.SCR	144	144	797														
1	.SETFV	137	137	1136														
2	.SETSQ	138	138															
15	.SPAWN	149	149															
11	.SPTR	145	145	1289														
3	.SQUEZ	139	139															
16	.TERM	150	150	839														
51	.UNCAU	177	177															
45	.UNLCK	173	173	1507														
31	.UPDAT	161	161															
61	.WAMI	183	183															
43	.WBRAN	171	171															
33	.WRACL	163	163															
7	.WRF	143	143	818														
41	.WSINF	169	169															
60	.WTME	182	182															
6	J J	2629	2634	2638	2665													
7	J L																	
2	J N	455	455	456	458	3488	3497	3557	4294									
5	J Q																	
1	J T	2635																
2	J X																	
3	J Y																	
4	J Z																	
24	J LEN	456	456	2631	2634	2636	2638	4413										
7	J RCW	436	436	3967	3969	3972	3975	3981	3983									
16	J RES	449	449	450	3502	3876	4291											
777777	J ALLC	427	427	456	2635	2662	2664											
1	J FLAG	429	429	3460														
12	J FULL	441	441	3490	3790	4259												
1344	J GETJ	2628	2628	3454														
2	J IFRN	430	430	3380	3487	3508	3518	3641	4269									
3644	J JCBO	4411	2629	4411														
3	J JCBN	4412	2631	4412	4413													
3740	J JCBX	4415	4415															
3430	J JTAB	3020	3020	3467	3469	4285	4335											
3	J OFRN	431	431	3503	3855	3888												
1357	J RELJ	2661	2661	4300														
13	J RPTR	443	443	3492	3622	3627	3648	3673	3680									
17	J RTRY	450	450	3847	3867	3868												
14	J WPTR	445	445	3493	3955	4173	4178	4197										
6	J	607	607	2632	2634	2635	2637	2638	2662	2664	3060	3309	3374	3378	3380	3385	3394	
				3455	3460	3462	3464	3469	3474	3478	3485	3487	3489	3490	3491	3492	3493	3496
				3500	3502	3503	3508	3511	3518	3523	3532	3544	3555	3621	3622	3624	3626	3627

OCTAL	SYMBOL	REFERENCES BY ALTER NO.
5	Q Q	1750 1935 2006 2017
1	Q T	1746 1938
772	Q V	1978 1978 3790 4261
2	Q X	1750 1800 1802 1803 1935 1979 1980 1981 1990
3	Q Y	1750 1935
4	Q Z	1750 1929 1930 1931 1935
767	Q P1	1937 1932 1937
724	Q DEQ	1789 1789 2006 2017
674	Q ENQ	1738 1738 2006 2017
16	Q LEN	554 554
11	Q MAX	549 549 550 1741 1743
15	Q ABBR	553 553 554
10	Q BUSY	547 547 549 1744 1746 1791 1793 1801 2339
3276	Q CORE	2017 2017 2136 2176 2339 2416 2466
716	Q ENQ1	1752 1745 1752
1	Q LAST	543 543 544 1053 1055 1795 1805 1808 2006 2017 3046 3050 3053
3	Q LINK	558 558 1802 3057
3260	Q TASK	2006 1053 1055 1804 1988 2006 2343 2843 3045 3046 3048 3050 3052 3053 3058 3539
2	Q XADD	544 544 545 1756 1804 1988 2343 2843 3539 3551 3689 3882 4287 4357 4363 4372
6	Q XDEQ	546 546 547 2176 2466
4	Q XENG	545 545 546 2136 2416
5	Q	606 606 1739 1740 1741 1743 1744 1746 1756 1790 1791 1793 1794 1795 1797 1801
		1803 1805 1807 1808 1929 1931 1938 1979 1981 1984 1989 3067 3656 3788 3790
		3898 4005 4202 4259 4261
12	QAVAIL	550 550 551 1739 1740 1790
0	QFIRST	542 542 543 1794 1797 1803 1807 2006 2017 3045 3048 3052 3058
4	QOFFST	557 557 558 1753 1800 1985 2343 2843 3054 3535 3547 3688 3882 4287 4357 4363
		4372 4378
13	QSPAR1	551 551 552
14	QSPAR2	552 552 553
6	R J	2342 2757
7	R L	2229 2234 2321 2325 2464 2494 2502 2731 2763 2798 2858
5	R Q	2762 2855
1	R T	2341 2343 2843
2	R X	2163 2170 2172 2173 2186 2197 2198 2226 2227 2228 2271 2278 2283 2285 2298
		2307 2322 2331 2332 2333 2334 2335 2337 2343 2417 2418 2419 2420 2422 2444
		2461 2462 2463 2758 2759 2762 2803 2837 2843 2848 2855
3	R Y	2162 2175 2187 2294 2297 2443 2447 2450 2451 2453 2454 2762 2822 2823 2855
4	R Z	2149 2155 2158 2168 2171 2182 2191 2293 2299 2300 2323 2324 2372 2432 2437
		2445 2449 2733 2734 2735 2737 2762 2834 2855
3416	R CP	2997 2930 2997
3421	R LP	3001 2934 3001
2	R TT	3001 2997 3001
1	R FRN	2953 2761 2822 2824 2953 2954 3283
0	R LEN	570 570 2164 2169 2275 2363 2373 2375 2377 2443 2450 2455 2457
1	R MAX	2908 2741 2908 2909 3420
0	R PTR	2907 2743 2907 2908 3415
1	R SET	3001 2997 3001

OCTAL	SYMBOL	REFERENCES BY ALTER NO.															
1144	RRELC2	2306	2284	2306													
1146	RRELC3	2320	2302	2320													
1206	RRELC4	2362	2321	2325	2362												
1202	RRELCX	2344	2336	2338	2340	2344											
1457	RRELP1	2828	2828	2829													
1467	RRELP2	2830	2829	2830													
1517	RRELP5	2847	2816	2847													
1522	RRELP6	2851	2851														
2	RSPARE	2909	2909														
3075	RTABCP	2882	2882	2888													
3074	RTABLE	2880	2735	2880	2888	2889	3414										
3076	RTABLP	2883	2883	2889													
2304	RTASK	3787	3533	3787	3793												
5	RTMAX	622	622	3869													
2314	RTSK1	3797	3797														
1	RTYPCP	2888	2888	3221	3226												
2	RTYPLP	2889	2889	3211	3215												
3340	SETFT	1142	1132	1135	1142												
526	SETFV	1132	1132	4339													
510	SETUP	1038	1038	1134	1184	1238	1286	1330	1375	1419	1462	1505	1551	1605	3196		
3740	SPTCB	4421	4329	4330	4421	4431											
564	SPTR	1284	1284	3380	3518												
3343	SPTRT	1295	1284	1287	1288	1295											
6	T J		2554														
7	T L		2549	2582													
5	T Q																
1	T T		2550	2551	2578	2583											
2	T X																
3	T Y																
4	T Z																
6	T JCB	372	372	482	2554	2808	3060	3342	3372	3455	4331	4356	4362	4371	4377		
30	T LEN	374	374	377	500	2383	2384	2549	2581	3558	4295	4418					
6	T NCB	371	371	372	481	2834	3537	3549	3873								
2	T RET	361	361	476													
4	T TRA	366	366	478	1039	1752	1937	2343	2731	2763	2764	2798	2843	2858	2859	3063	3066
			3534	3546	3620	3653	3658	3687	3882	4172	4199	4287	4357	4363	4372	4378	
3	T XED	362	362	477	1044												
1325	T GETT	2548	2343	2548	2843	3192	3531	3543	3882	4287	4357	4363	4372	4378			
5	T LINK	370	370	479	2343	2550	2843	3194	3198	3536	3548	3882	4287	4357	4363	4372	4378
1335	T RELT	2577	2467	2577	3153	3202	3798	4301	4341	4358	4364	4373	4379				
0	T SRW1	359	359	474	1042	2500	2829	3147	3148	3173	3200	3282	3381	3486	3509	3510	3519
			3642	3856	3889	4270	4340										
1	T SRW2	360	360	475	3240	3383	3416	3450	3459	3521	3861						
17	T TEM9	388	388	389	492	2494	2502	2503									
1	T	602	602	1039	1042	1044	1752	1753	1937	2135	2138	2144	2146	2148	2158	2165	2168
			2180	2186	2197	2200	2343	2419	2422	2424	2425	2447	2454	2456	2461	2494	2500
			2502	2503	2554	2578	2580	2731	2732	2733	2737	2756	2760	2763	2764	2765	2766
			2767	2798	2802	2803	2808	2823	2828	2829	2834	2836	2837	2842	2843	2848	2852
			2853	2854	2858	2859	3048	3050	3054	3057	3060	3063	3066	3146	3147	3148	3173
			3175	3193	3194	3198	3200	3240	3282	3342	3343	3345	3346	3347	3361	3372	3379

OCTAL	SYMBOL	REFERENCES	BY	ALTER NO.													
4	Z	605	605	2152	2155	2162	2163	2164	2169	2170	2172	2296	2297	2298	2322	2363	2365
			2371	2375	2377	2434	2437	2443	2450	2455	2457	2459	2735	2741	2743	2835	3067
			3279	3283	3285	3304	3307	3328	3332	3340	3341	3365	3371	3419	3421	3656	3898
			3957	3958	3960	3961	3967	3968	4005	4202							
0	ZCODE	107	107	4395													
3060	ZCODEL	4395	4395	4427													
3060	ZCONS	111	111	4401													
200	ZCONSL	4401	4401	4427													
3260	ZQSTR	115	115	4406													
40	ZQSTRL	4406	4406	4427													
3320	ZSTOR	119	119	4424													
450	ZSTORL	4424	4424	4427													
3740	ZTOP0	4431	663	4431													
4000	ZTOP	4433	664	4433	4434												
10	ZZ1	4434	661	4333	4434	4436	4442										
4767	ZZ	4432	4432	4433													

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13. ABSTRACT The problem is to design and implement an Input/Output Scheduler for the General Electric Corporate Research and Development Center. Given the Center's current time-sharing environment of master and slave modes, a slave mode scheduling is proposed. This system is composed of two distinct levels: a <u>monitor</u> , which handles all external input/output and scheduling, comprises the upper level; the lower level contains all the <u>peripheral driver modules</u> , which, while also operating in slave mode, transfer the data to/from such peripheral devices as line printers and card punches. Just such a system has been successfully written and is operating on the Center's computer system.			

