M7018-00001   CODE: D   CS: B   ETCH: C

OCT-72 - PROBLEM 1: Etch missing on "B " revision boards from E0403 to E0404.

CORRECTION 1: Add missing etch to PC boards; rework all "B " boards by adding a wire from E0403 to E0404.

PROBLEM 2: Capacitors C52 and C43, C44 and C53, are too close together. They cannot be inserted without bending one of the capacitors.

CORRECTION 2: Move capacitors apart to correct spacing.

In-plant effectivity -Rework all revision "B " boards immediately.

M7018-00002   CODE: P   CS: C

MAR-73 - CORRECTION: Corrects print errors.

In-plant effectivity -Documentation change only

M7018-C0003   CODE: F   CS: D

JAN-74 - PROBLEM 1: When using LPSAD-12 with an lpsam-sg or BA408, the PST and MC delays must be increased.

CORRECTION 1: Change capacitor C43 from 820 pfd to 4700 pfd; this increases the PST delay to 15 usec. Change capacitor C53 from 680 pfd to 3300 pfd; this increases the MC delay to 11 usec.

PROBLEM 2: Dual sample and hold logic must be changed to work with lpsam-sg.

CORRECTION 2: Rework the M7018 as follows: Cut etch between E04 pin 10 and E05 pin 12. Cut etch between E16 pin 10 and resistor R18. Add wires from: E24 pin 06 to E24 pin 09, E24 pin 08 to E05 pin 12, E24 pin 10 to R18 to CF1, E24 pin 04 to E24 pin 05 to E04 pin 10, and E25 pin 10 to E16 pin 10.

In-plant effectivity -A complete phase-in must be implemented by March 1, 1974.

Field effectivity -Rework M7018's when installing an lpsam-sg or BA408.

(Time To Install And Test 2.0 Hours.) (Kit Contents -PF1153 - FCO/Prints And Parts)