

PROCESSOR TYPE PDP-11 Family

M7223-C0001 CODE: F CS: C ETCH: D
SEP-71 - PROBLEM: Cannot read, to bus, sync extension bits SYNC 8 thru SYNC 12, as they are improperly decoded to 174XX1; should be 174XX7.
CORRECTION: Correct select decoding gates as follows: Remove etch from E4 pin 3; connect E4 pin 3 to etch from AH1, SELECT 6.
In-plant effectivity -Rework immediately
Field effectivity -Rework all M7223's if DP11-CA is installed.
(Time To Install And Test .5 Hour.) (Kit Contents --FCO Only)

M7223-C0002 CODE: DF CS: D ETCH: E
DEC-71 - PROBLEM: MOV B is not decoded for loading the transmitter.
CORRECTION: Change IC E19 from a 7400 to a 7410; Connect E12 pin 4 to E19 pin 11, E12 pin 2 to E19 pin 10, E12 pin 1 to E19 pin 9, and BK2 to E19 pin 8.

NOTE: This FCO should be installed in conjunction with FCO DP11D-C0005.
In-plant effectivity -Rework all M7223's as of 1/15/72. Production is to install immediately for all DP11-CA options.
Field effectivity -Rework all M7223's as required for DP11-D operation.
(Time To Install And Test 1.0 Hour.) (Kit Contents --FCO/Prints And Parts)

M7223-C0003 CODE: F CS: E ETCH: F
AUG-72 - PROBLEM 1: OVERRUN clears itself two character times after being set. This shows up in overloaded systems, large systems, and with ctp/gtp.
CORRECTION 1: Change logic to latch the O'RUN and RX DONE flip-flops against further clocking inputs.
CORRECTION 2: Correct print errors.
In-plant effectivity -02 -Phase-in
Field effectivity -Rework all M7223's
(Time To Install And Test 1.5 Hours.) (Kit Contents -FCO/Prints)

M7223-00004 CODE: D CS: F
JUN-73 - PROBLEM: Possible part availability problems with the DEC 380 bus receiver chips.
CORRECTION: Make the 11380 IC #19-11113 an allowable substitute for the DEC 380 #19-09485 bus receivers. The combination of DEC 380's and 11380's as bus receivers is not allowable in the same option.
In-plant effectivity -06 * -Documentation/design change. Immediately add Allowable Substitution List to the documentation.