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SIZE CODE D TC MCA25-0-1
REV. A

DOCUMENT NUMBER	REV	DESCRIPTION
D-TC-MCA25-0-1	A	MCA25 FIELD MAINT. PRINT SET (TC)
D-MJ-KL10-PW-CPU	A	MODULE UTILIZATION
D-SP-KL10-PW-2	A	OPTION/SERIAL NUMBER CHART

OPTION MODULES (C.S.)

DOCUMENT NUMBER	REV	DESCRIPTION
D-DD-M852-0	A	CACHE DIRECTORY
E-UA-M852-0-0	A	CACHE DIRECTORY
D-CS-M852-0-CHA1	A	CACHE DIRECTORY
D-CS-M852-0-CHA2	A	CACHE DIRECTORY
D-CS-M852-0-CHA3	A	CACHE DIRECTORY
D-CS-M852-0-RES	A	CACHE DIRECTORY
D-DD-M853-0	A	CACHE EXTENSION
D-UA-M853-0-0	A	CACHE EXTENSION
D-CS-M853-CHX1	A	CACHE EXTENSION
D-CS-M853-CHX2	A	CACHE EXTENSION
D-CS-M853-CHX3	A	CACHE EXTENSION
D-CS-M853-CHX4	A	CACHE EXTENSION
D-CS-M853-CHX5	A	CACHE EXTENSION
D-CS-M853-RES	A	CACHE EXTENSION
D-DD-M854-0	A	PHYSICAL MEM ADR
D-UA-M854-0-0	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA1	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA2	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA3	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA4	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA5	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA6	A	PHYSICAL MEM ADR
D-CS-M854-0-RES	A	PHYSICAL MEM ADR

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REV. A
NUMBER MCA25-0-1
SIZE CODE D TC

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE 26-JUL-84	ENG. C. A. JENS	DATE 26-JUL-84	TITLE: MCA25
	CHK'D. G. ALSTON	DATE 26-JUL-84	BOARD LOCATION: N/A	CACHE PAGER
DSK:MCATC.T2P(4,21)		26-JUL-84 15:41	NEXT HIGHER ASSEMBLY:	SIZE CODE D TC
FIRST USED ON OPTION/MODEL: MCA25		KL10-PW		NUMBER MCA25-0-1
				REV. A

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 SIZE CODE NUMBER
 D TC MCA25-0-1
 REV. A

OPTION MODULES (C.S) CONTINUED

DOCUMENT NUMBER	REV	DESCRIPTION
D-DD-M857-0	A	MBC MBOX CONTROL
D-LIA-M857-0-0	A	MBC MBOX CONTROL
D-CS-M857-0-MBC1	A	MBC MBOX CONTROL
D-CS-M857-0-MBC2	A	MBC MBOX CONTROL
D-CS-M857-0-MBC3	A	MBC MBOX CONTROL
D-CS-M857-0-MBC4	A	MBC MBOX CONTROL
D-CS-M857-0-MBC5	A	MBC MBOX CONTROL
D-CS-M857-0-MBC6	A	MBC MBOX CONTROL
D-CS-M857-0-RES	A	MBC MBOX CONTROL

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	26-JUL-84	C. A. JENS	26-JUL-84	MCA25 CACHE PAGER
CHK'D.	DATE	BOARD LOCATION:		
G. ALSTON	26-JUL-84	N/A		
DSK: MCA25.TP(4,21)		26-JUL-84 15:42	NEXT HIGHER ASSEMBLY:	SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL: MCA25		KL10-PW	D TC MCA25-0-1	A

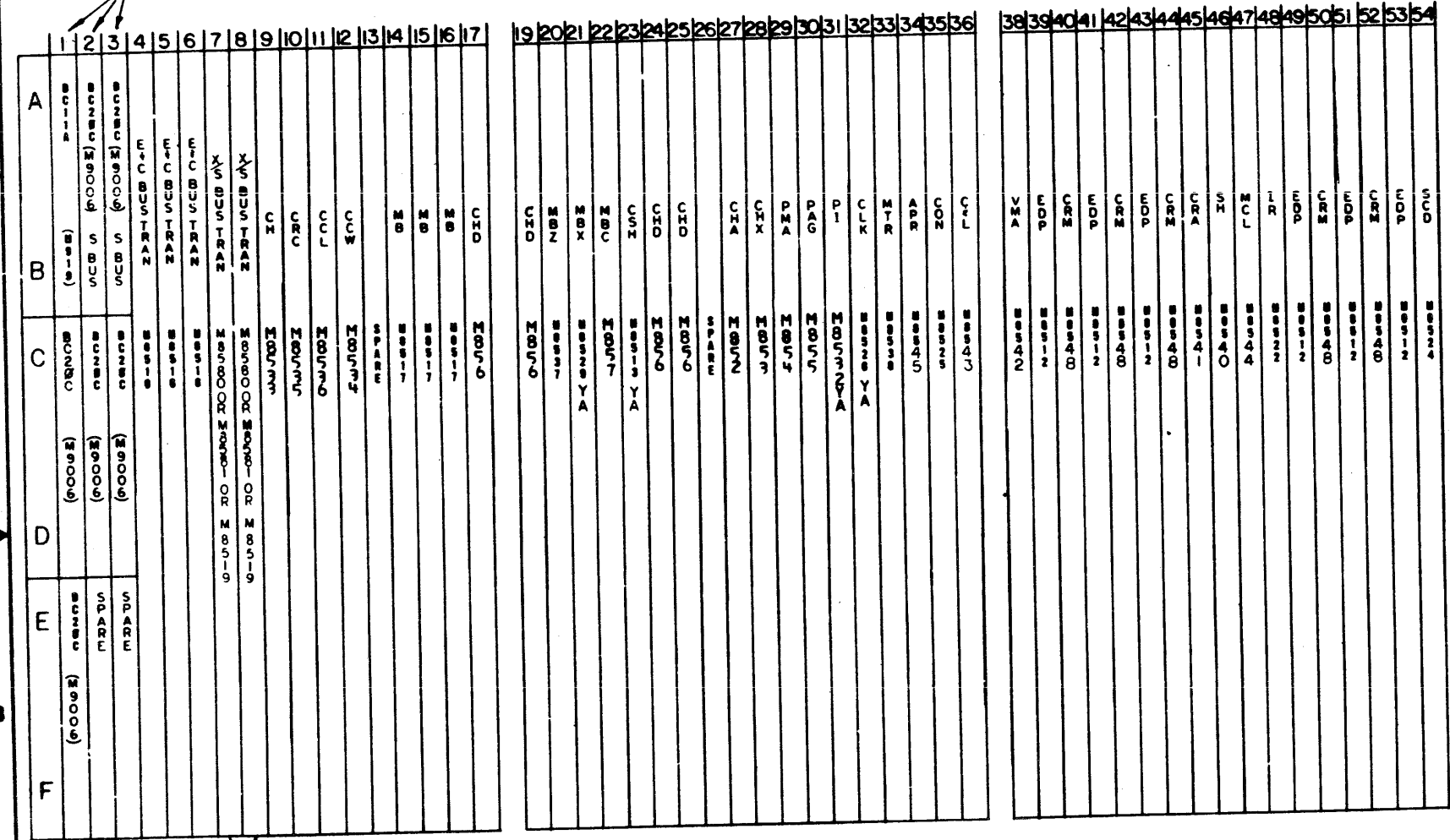
8 7 6 5 4 3 2 1
 SIZE CODE NUMBER
 D TC MCA25-0-1
 REV. A

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KL10-PW-CPU 2

SLOT	KL10PV W/MCA20	KL10PW W/MCA25
17	M8521	M856
19	M8521	M856
22	M8531-YA	M857
24	M8521	M856
25	M8521	M856
27	M8514	M852
28	M8515	M853
29	M8518 YA	M854
30	M8520 YA	M855
	(KL10-PV REF)	(KL10-PW REF)

SEE NOTES 1 2 4 3 6 6 7 9



SEE NOTE 4 5 8

REF. PIN SIDE
*REFERENCE LOCATION CHART FOR CACHE OPTIONS. MCA25 OPTION SHOWN ABOVE IN KL10-PW

NOTES:

- SLOT 2 & 3 (8000) DC20C CABLES FROM INTERNAL MEMORY OR DMA20 OR EMPTY
 - SLOT 1 (E AND C BUS) DC11A & DC20C CABLES FROM BTE
 - X BUS CABLE BOARD (ITEM #54 OF MF20 UNIT ASSY) IS PLUGGED INTO SLOT 2 OF KL10-PW LOGIC ASSY.
 - INSTALL 2 M8500 MODULES (ITEM #41 OF MF20 UNIT ASSY) INTO SLOTS 7 AND 8 OF KL10-PW LOGIC ASSY. IF SLOTS ARE PRESENTLY USED BY M8519 MODULES, REMOVE THEM AND INSTALL M8500 MODULE (ITEM #41 OF MF20 UNIT ASSY) FOR MF20-LA, LB, LP/LR, LS/LT ONLY.
 - INSTALL 2 M8501 MODULES (ITEM #2 OF MF20 UNIT ASSY) INTO SLOTS 7 AND 8 OF KL10-PW LOGIC ASSY. IF SLOTS ARE PRESENTLY USED BY M8500 MODULES, REMOVE THEM AND INSTALL 2 M8501 MODULES (ITEM #2 OF MF20 UNIT ASSY) FOR MF20-LH, LL, LM, LN, LU/LV ONLY.
 - X BUS CABLE BOARD (ITEM #63 OF MF20 UNIT ASSY) IS PLUGGED INTO SLOT 3 OF KL10-PW LOGIC ASSY. FOR MF20-LH, LL, LM, LN ONLY OR SLOT 2 FOR MF20-LP/LR, LS/LT.
 - IF AN MF20-LP/LR, LS/LT ALREADY EXISTS IN THE SYSTEM, THE X BUS CABLE BOARD (ITEM #63) MUST BE MOVED FROM SLOT 2 TO SLOT 3 SO AN MF20-LU/LV CAN BE INSTALLED.
- FOR MG20-LA/LB INSTALLATIONS:
 8. INSTALL (2) M8500 IN PLACE OF (2) M8519 MODULES IN SLOTS 7 AND 8.
 9. INSTALL X BUS CABLE BOARD (M8572) INTO SLOT 2.

RELEASED A

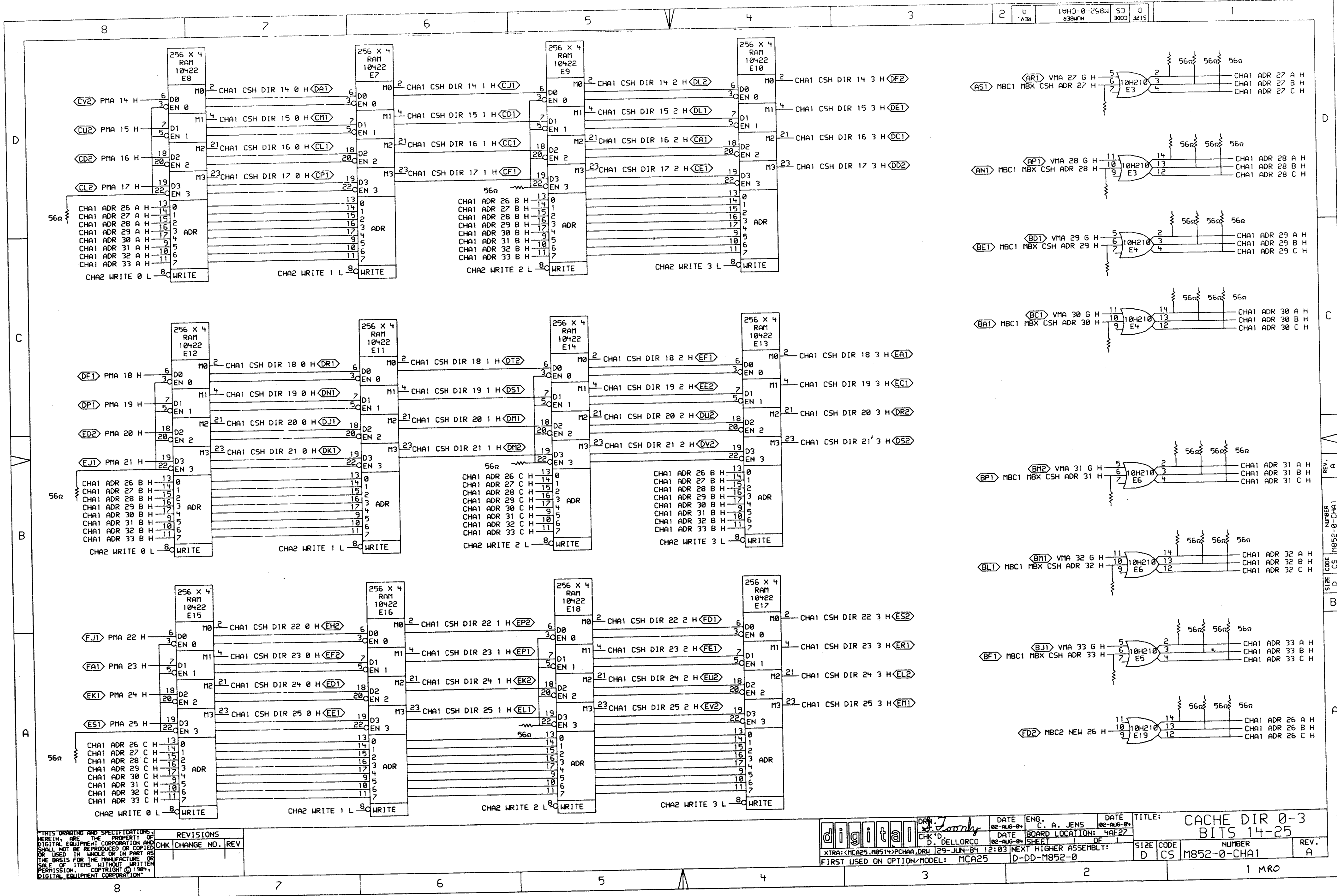
QUANTITY & VARIATION	DESCRIPTION	EQUIV. PART NO.	ITEM NO.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES									
				CLASS OF ACCURACY		TOLERANCE		FINISH		MATERIAL		TREATMENT	
ASSEMBLY PRESENTATION	QUANTITY	CLASS OF ACCURACY	ITEM NO.	1	2	3	4	5	6	7	8	9	10
ASSEMBLY PRESENTATION	1	CLASS OF ACCURACY	ITEM NO.										
QUANTITY & VARIATION	1	DESCRIPTION	ITEM NO.										
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ASSEMBLY PRESENTATION	1	DESCRIPTION	ITEM NO.										
QUANTITY & VARIATION	1	DESCRIPTION	ITEM NO.										

DRAWING NUMBER	NO. OF SHT	PART NUMBER	DESCRIPTION	REVISION
		M852-00	MODULE REVISION	A1
E-UA-M852-0-0	1		CHA	A
K-PL-M852-0-DBP	1		PART LIST, M852	A
D-CS-M852-0-CHA1	1		CACHE DIR 0-3 BITS 14-25	A
D-CS-M852-0-CHA2	1		CACHE DIR PARITY & WR BITS	A
D-CS-M852-0-CHA3	1		CACHE DIR POWER, GND, CAP	A
D-CS-M852-0-RES	1		CACHE DIRECTORY TERMINATORS	A
K-PC-M852-0-DBM	-		MULTIWIRE DESIGN DATA BASE TAPE	A
D-DD-5016369-01-0	1		DRAWING DIRECTORY, 5016369-01	REF

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN	DATE	ENG.	DATE	TITLE:
	CHK'D	27-JUN-84	C. JENS	27-JUN-84	M852
	D. DELLORCO	27-JUN-84			DRAWING DIRECTORY
DSK:M852A.T2P[4,21]		127-JUN-84 13:25	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: KL10-PW		MCA25		D	DD
				NUMBER	REV.
				M852-0	A



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REV.	DATE	ENG.	CHK'D.	DATE	BOARD LOCATION	SHEET	OF	SIZE	CODE	NUMBER	REV.
1	02-AUG-84	C. A. JENS	D. DELLORCO	02-AUG-84	4AF27	1	1	D	CS	M852-0-CHA1	A

REV.	DATE	ENG.	CHK'D.	DATE	BOARD LOCATION	SHEET	OF	SIZE	CODE	NUMBER	REV.
1	02-AUG-84	C. A. JENS	D. DELLORCO	02-AUG-84	4AF27	1	1	D	CS	M852-0-CHA1	A

REV.	DATE	ENG.	CHK'D.	DATE	BOARD LOCATION	SHEET	OF	SIZE	CODE	NUMBER	REV.
1	02-AUG-84	C. A. JENS	D. DELLORCO	02-AUG-84	4AF27	1	1	D	CS	M852-0-CHA1	A

REV.	DATE	ENG.	CHK'D.	DATE	BOARD LOCATION	SHEET	OF	SIZE	CODE	NUMBER	REV.
1	02-AUG-84	C. A. JENS	D. DELLORCO	02-AUG-84	4AF27	1	1	D	CS	M852-0-CHA1	A

REV.	DATE	ENG.	CHK'D.	DATE	BOARD LOCATION	SHEET	OF	SIZE	CODE	NUMBER	REV.
1	02-AUG-84	C. A. JENS	D. DELLORCO	02-AUG-84	4AF27	1	1	D	CS	M852-0-CHA1	A

REV.	DATE	ENG.	CHK'D.	DATE	BOARD LOCATION	SHEET	OF	SIZE	CODE	NUMBER	REV.
1	02-AUG-84	C. A. JENS	D. DELLORCO	02-AUG-84	4AF27	1	1	D	CS	M852-0-CHA1	A

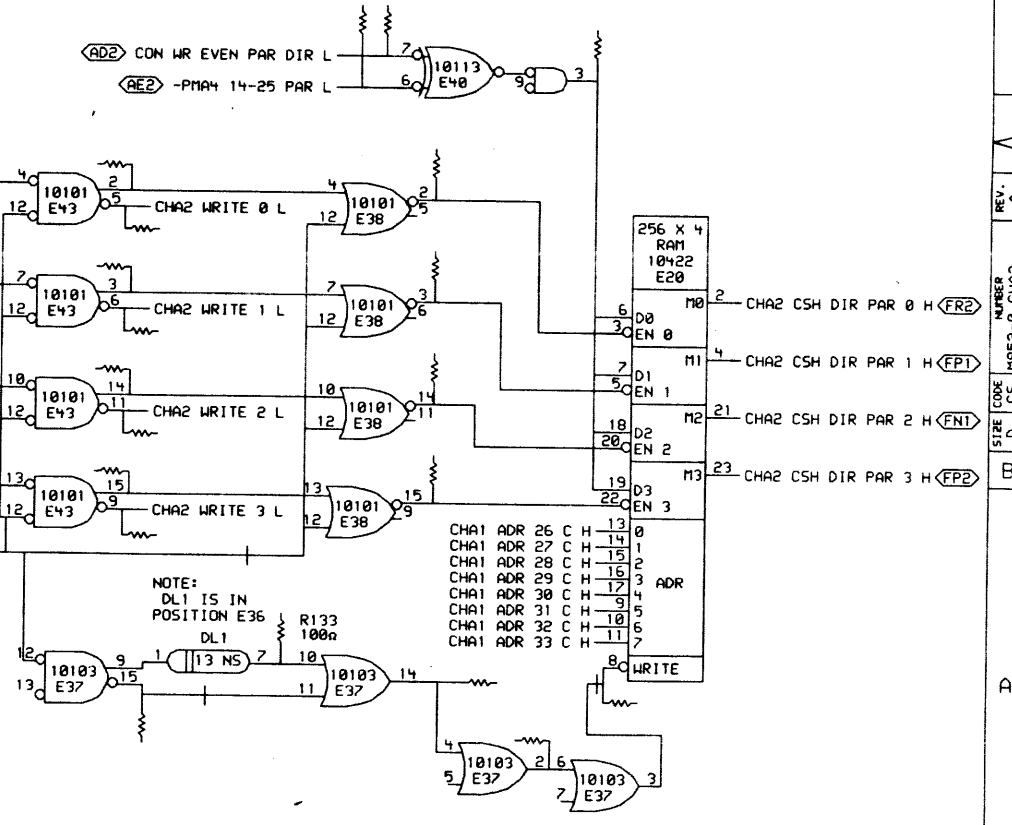
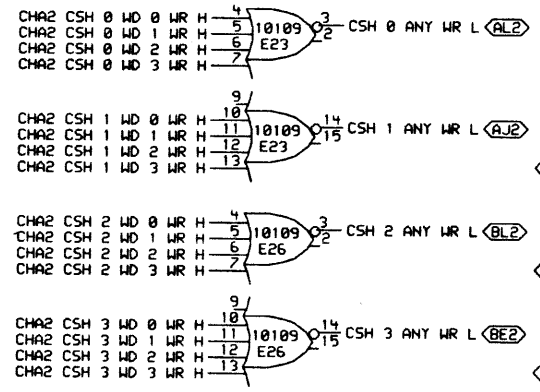
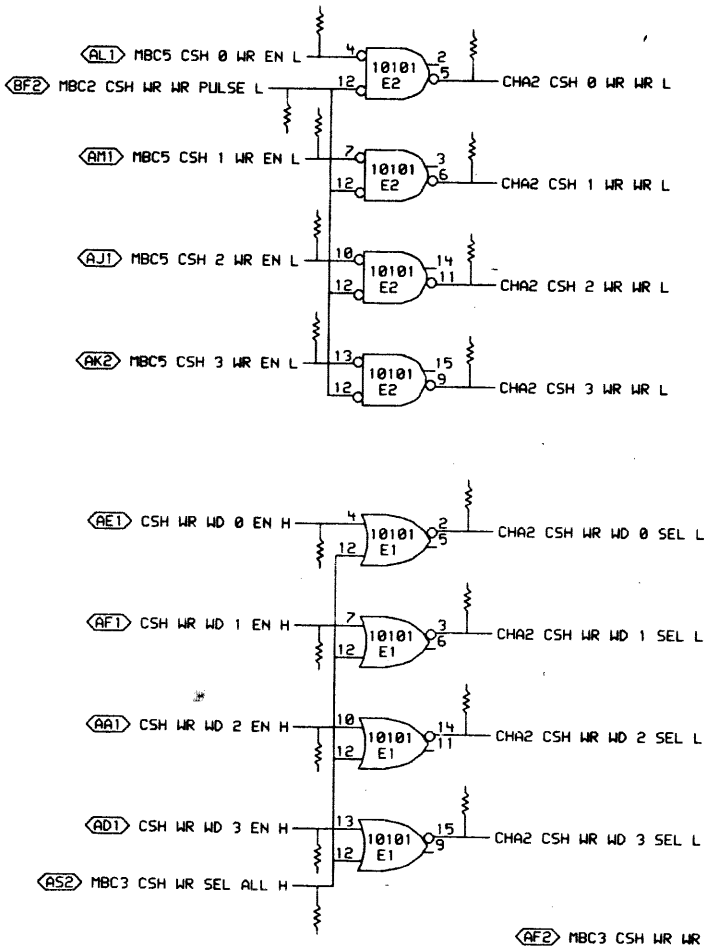
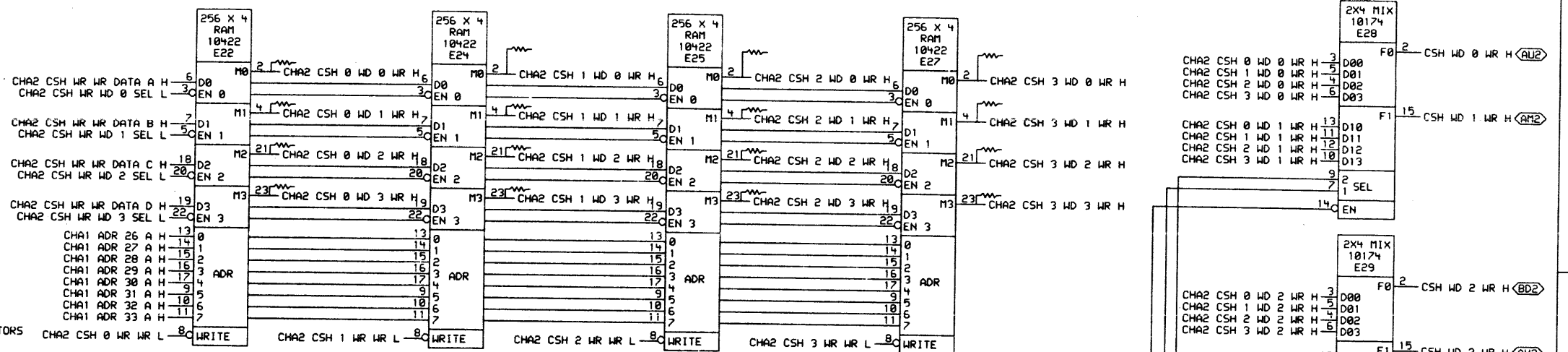
REV.	DATE	ENG.	CHK'D.	DATE	BOARD LOCATION	SHEET	OF	SIZE	CODE	NUMBER	REV.
1	02-AUG-84	C. A. JENS	D. DELLORCO	02-AUG-84	4AF27	1	1	D	CS	M852-0-CHA1	A

REV.	DATE	ENG.	CHK'D.	DATE	BOARD LOCATION	SHEET	OF	SIZE	CODE	NUMBER	REV.
1	02-AUG-84	C. A. JENS	D. DELLORCO	02-AUG-84	4AF27	1	1	D	CS	M852-0-CHA1	A

digital
 DATE: 02-AUG-84
 ENG: C. A. JENS
 DATE: 02-AUG-84
 CHK'D: D. DELLORCO
 BOARD LOCATION: 4AF27
 SHEET: 1 OF 1
 TITLE: CACHE DIR 0-3 BITS 14-25
 FIRST USED ON OPTION/MODEL: MCA25
 D-DD-M852-0
 SIZE: D CS
 NUMBER: M852-0-CHA1
 REV: A

- (FS2) PAG1 PT WRITEABLE H
- (FS1) CHA TERM 01\#400
- (FR1) -PAG3 PT MATCH H
- (FF1) CHA TERM 03\#400
- (BR2) CHA TERM 04\#400
- (BP2) CHA TERM 05\#400
- (FE2) CHA TERM 06\#400
- (FJ2) CHA TERM 07\#400
- (BS2) CHA TERM 08\#400
- (BL2) CHA TERM 09\#400
- (BV2) CHA TERM 10\#400
- (BT2) CHA TERM 11\#400
- (FV2) PAG1 PT SOFTWARE H
- (FF2) PAG1 PT ACCESS H

NOTE: CHA TERMS ARE SPARE TERMINATORS



NOTE: DL1 IS IN POSITION E36
DL1 13 NS

REV.	CHG.	NO.	REV.

digital	DATE: 02-AUG-84	ENG.: C. A. JENS	DATE: 02-AUG-84	TITLE: CACHE DIR PARITY & WR BITS
CHK'D: D. DELLORCO	DATE: 02-AUG-84	BOARD LOCATION: 4AF27	SHEET: 1	
FIRST USED ON OPTION/MODEL: MCA25 D-DD-M852-0			SIZE CODE: D CS	NUMBER: M852-0-CHA2
				REV.: A

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RESISTOR LOC(PIN)	DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	DRW#	ON REF	VALUE	TERMINATES SIGNAL
R133(1)	CHA2	A3	100n	%DL1(7)	R21(1)	CHA1	C1	56n	CHA1 ADR 29 B H	R84(1)	CHA2	A5	68n	CHA2 CSH WR WR DATA C H	R125(1)	CHA3	C1	56n	NC
R11(1)	CHA1	B8	56n	%E11(3)	R22(1)	CHA1	C1	56n	CHA1 ADR 29 C H	R86(1)	CHA2	A5	68n	CHA2 CSH WR WR DATA D H	R126(1)	CHA3	C1	56n	NC
R107(1)	CHA1	B5	56n	%E13(3)	R23(1)	CHA1	C1	56n	CHA1 ADR 30 A H	R56(1)	CHA2	B3	68n	-CHA2 WRITE 0 H	R127(1)	CHA3	C1	56n	NC
R13(1)	CHA1	A8	56n	%E15(3)	R24(1)	CHA1	C1	56n	CHA1 ADR 30 B H	R58(1)	CHA2	B3	68n	-CHA2 WRITE 1 H	R128(1)	CHA3	C1	56n	NC
R109(1)	CHA1	A5	56n	%E17(3)	R25(1)	CHA1	C1	56n	CHA1 ADR 30 C H	R61(1)	CHA2	B3	68n	-CHA2 WRITE 2 H	R129(1)	CHA3	C1	56n	NC
R112(1)	CHA2	A2	68n	%E37(14)	R26(1)	CHA1	B1	56n	CHA1 ADR 31 A H	R66(1)	CHA2	A3	68n	-CHA2 WRITE 3 H	R130(1)	CHA3	C1	56n	NC
R36(1)	CHA2	A3	68n	%E37(15)	R27(1)	CHA1	B1	56n	CHA1 ADR 31 B H	R91(1)	CHA2	C2	68n	-CON WR EVEN PAR DIR H	R131(1)	CHA3	C1	56n	NC
R113(1)	CHA2	A2	68n	%E37(2)	R28(1)	CHA1	B1	56n	CHA1 ADR 31 C H	R76(1)	CHA2	C1	68n	-CSH WR OUT EN H	R132(1)	CHA3	C1	56n	NC
R54(1)	CHA2	A2	68n	%E37(3)	R9(1)	CHA1	B1	56n	CHA1 ADR 32 A H	R39(1)	CHA2	B7	68n	CSH WR WD 0 EN H	R135(1)	CHA3	C1	56n	NC
R65(1)	CHA2	B2	68n	%E38(14)	R8(1)	CHA1	B1	56n	CHA1 ADR 32 B H	R4(1)	CHA2	A7	68n	CSH WR WD 1 EN H	R136(1)	CHA3	C1	56n	NC
R72(1)	CHA2	A2	68n	%E38(15)	R7(1)	CHA1	B1	56n	CHA1 ADR 32 C H	R43(1)	CHA2	A7	68n	CSH WR WD 2 EN H	R137(1)	CHA3	C1	56n	NC
R57(1)	CHA2	B2	68n	%E38(2)	R6(1)	CHA1	A1	56n	CHA1 ADR 33 A H	R44(1)	CHA2	A7	68n	CSH WR WD 3 EN H	R138(1)	CHA3	C1	56n	NC
R60(1)	CHA2	B2	68n	%E38(3)	R5(1)	CHA1	A1	56n	CHA1 ADR 33 B H	R30(1)	CHA1	D2	68n	MBC1 MBX CSH ADR 27 H	R139(1)	CHA3	C1	56n	NC
R88(1)	CHA2	C2	68n	%E40(3)	R4(1)	CHA1	A1	56n	CHA1 ADR 33 C H	R29(1)	CHA1	D2	68n	MBC1 MBX CSH ADR 28 H	R106(1)	CHA2	C7	68n	PAG1 PT ACCESS H
R64(1)	CHA2	B3	68n	%E43(14)	R63(1)	CHA2	D6	68n	CHA2 CSH 0 WD 0 WR H	R31(1)	CHA1	C2	68n	MBC1 MBX CSH ADR 29 H	R105(1)	CHA2	C7	68n	PAG1 PT SOFTWARE H
R69(1)	CHA2	B3	68n	%E43(15)	R62(1)	CHA2	D6	68n	CHA2 CSH 0 WD 1 WR H	R32(1)	CHA1	C2	68n	MBC1 MBX CSH ADR 30 H	R93(1)	CHA2	D7	68n	PAG1 PT WRITEABLE H
R55(1)	CHA2	B3	68n	%E43(2)	R73(1)	CHA2	D6	68n	CHA2 CSH 0 WD 2 WR H	R34(1)	CHA1	B2	68n	MBC1 MBX CSH ADR 31 H	R95(1)	CHA2	D7	68n	-PAG3 PT MATCH H
R59(1)	CHA2	B3	68n	%E43(3)	R71(1)	CHA2	D6	68n	CHA2 CSH 0 WD 3 WR H	R33(1)	CHA1	B2	68n	MBC1 MBX CSH ADR 32 H	R89(1)	CHA2	C2	68n	PH4 14-25 PAR H
R10(1)	CHA1	D8	56n	%E7(3)	R90(1)	CHA2	C7	68n	-CHA2 CSH 0 WR WR H	R35(1)	CHA1	A2	68n	MBC1 MBX CSH ADR 33 H					
R108(1)	CHA1	D5	56n	%E9(3)	R74(1)	CHA2	D5	68n	CHA2 CSH 1 WD 0 WR H	R134(1)	CHA2	A4	68n	-MBC2 CSH ADR WR PULSE H					
R94(1)	CHA2	D7	68n	CHA TERM 01\#400\	R70(1)	CHA2	D5	68n	CHA2 CSH 1 WD 1 WR H	R46(1)	CHA2	C8	68n	-MBC2 CSH WR WR PULSE H					
R96(1)	CHA2	D7	68n	CHA TERM 03\#400\	R68(1)	CHA2	D5	68n	CHA2 CSH 1 WD 2 WR H	R37(1)	CHA2	A7	68n	MBC3 CSH WR SEL ALL H					
R97(1)	CHA2	D7	68n	CHA TERM 04\#400\	R67(1)	CHA2	D5	68n	CHA2 CSH 1 WD 3 WR H	R79(1)	CHA2	A6	68n	MBC3 CSH WR WR DATA H					
R98(1)	CHA2	D7	68n	CHA TERM 05\#400\	R51(1)	CHA2	C7	68n	-CHA2 CSH 1 WR WR H	R92(1)	CHA2	C7	68n	-MBC5 CSH 0 WR EN H					
R99(1)	CHA2	D7	68n	CHA TERM 06\#400\	R81(1)	CHA2	D4	68n	CHA2 CSH 2 WD 0 WR H	R52(1)	CHA2	C7	68n	-MBC5 CSH 1 WR EN H					
R100(1)	CHA2	D7	68n	CHA TERM 07\#400\	R75(1)	CHA2	D4	68n	CHA2 CSH 2 WD 1 WR H	R50(1)	CHA2	B7	68n	-MBC5 CSH 2 WR EN H					
R101(1)	CHA2	D7	68n	CHA TERM 08\#400\	R77(1)	CHA2	D4	68n	CHA2 CSH 2 WD 2 WR H	R48(1)	CHA2	B7	68n	-MBC5 CSH 3 WR EN H					
R102(1)	CHA2	D7	68n	CHA TERM 09\#400\	R78(1)	CHA2	D4	68n	CHA2 CSH 2 WD 3 WR H	R110(1)	CHA3	C1	56n	NC					
R103(1)	CHA2	D7	68n	CHA TERM 10\#400\	R49(1)	CHA2	B7	68n	-CHA2 CSH 2 WR WR H	R111(1)	CHA3	C1	56n	NC					
R104(1)	CHA2	D7	68n	CHA TERM 11\#400\	R53(1)	CHA2	D3	68n	CHA2 CSH 3 WD 0 WR H	R114(1)	CHA3	C1	56n	NC					
R3(1)	CHA1	A1	56n	CHA1 ADR 26 A H	R83(1)	CHA2	D3	68n	CHA2 CSH 3 WD 1 WR H	R115(1)	CHA3	C1	56n	NC					
R2(1)	CHA1	A1	56n	CHA1 ADR 26 B H	R85(1)	CHA2	D3	68n	CHA2 CSH 3 WD 2 WR H	R116(1)	CHA3	C1	56n	NC					
R1(1)	CHA1	A1	56n	CHA1 ADR 26 C H	R87(1)	CHA2	D3	68n	CHA2 CSH 3 WD 3 WR H	R117(1)	CHA3	C1	56n	NC					
R14(1)	CHA1	D1	56n	CHA1 ADR 27 A H	R47(1)	CHA2	B7	68n	-CHA2 CSH 3 WR WR H	R118(1)	CHA3	C1	56n	NC					
R15(1)	CHA1	D1	56n	CHA1 ADR 27 B H	R38(1)	CHA2	B7	68n	-CHA2 CSH WR WD 0 SEL H	R119(1)	CHA3	C1	56n	NC					
R16(1)	CHA1	D1	56n	CHA1 ADR 27 C H	R40(1)	CHA2	A7	68n	-CHA2 CSH WR WD 1 SEL H	R120(1)	CHA3	C1	56n	NC					
R17(1)	CHA1	D1	56n	CHA1 ADR 28 A H	R42(1)	CHA2	A7	68n	-CHA2 CSH WR WD 2 SEL H	R121(1)	CHA3	C1	56n	NC					
R18(1)	CHA1	D1	56n	CHA1 ADR 28 B H	R45(1)	CHA2	A7	68n	-CHA2 CSH WR WD 3 SEL H	R122(1)	CHA3	C1	56n	NC					
R19(1)	CHA1	D1	56n	CHA1 ADR 28 C H	R80(1)	CHA2	A5	68n	CHA2 CSH WR WR DATA A H	R123(1)	CHA3	C1	56n	NC					
R20(1)	CHA1	C1	56n	CHA1 ADR 29 A H	R82(1)	CHA2	A5	68n	CHA2 CSH WR WR DATA B H	R124(1)	CHA3	C1	56n	NC					

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND <> INDICATES PIN NUMBER

REVISIONS	
CHK	CHANGE NO. REV

DATE	ENG.	DATE	TITLE:
02-AUG-84	C. A. JENS	02-AUG-84	CACHE DIRECTORY TERMINATORS
DATE	BOARD LOCATION:	DATE	SHEET
02-AUG-84	DE 1	02-AUG-84	1
FIRST USED ON OPTION/MODEL: MCA25		NEXT HIGHER ASSEMBLY: D-DD-M852-0	

SIZE	CODE	NUMBER	REV.
D	CS	M852-0-RES	A

REV. A
 NUMBER M852-0-RES
 SIZE CODE D CS
 B

8 7 6 5 4 3 2 1

SIZE CODE DD 00 00
 00 3000 3215
 0-ES84
 M853-0

DRAWING NUMBER	NO. OF SHT	PART NUMBER	DESCRIPTION	REVISION
		M853-00	MODULE REVISION	A1
D-LA-M853-0-0	1		CACHE EXTENSION	A
K-PL-M853-0-DBP	2		PART LIST, M853	A
D-CS-M853-0-CHX1	1		CSH ADR COMPR & VAL BIT MIXER	A
D-CS-M853-0-CHX2	1		CSH VALID BITS AND ADR MIXERS	A
D-CS-M853-0-CHX3	1		CACHE USE BITS ADR BUF & LRU BITS	A
D-CS-M853-0-CHX4	1		CSH DIR PAR NET AND DIAG MIXERS	A
D-CS-M853-0-CHX5	1		CHX POWER, GND, CAP	A
D-CS-M853-0-RES	2		CACHE EXTENSION TERMINATORS	A
K-PC-M853-0-DBI	-		P.C. DESIGN DATA BASE TAPE	A
D-DD-5017667-0	1		DRAWING DIRECTORY, 5017667	REF

D
C
V
B
A

D
C
REV. A
NUMBER M853-0
CODE DD
SIZE D
B
A

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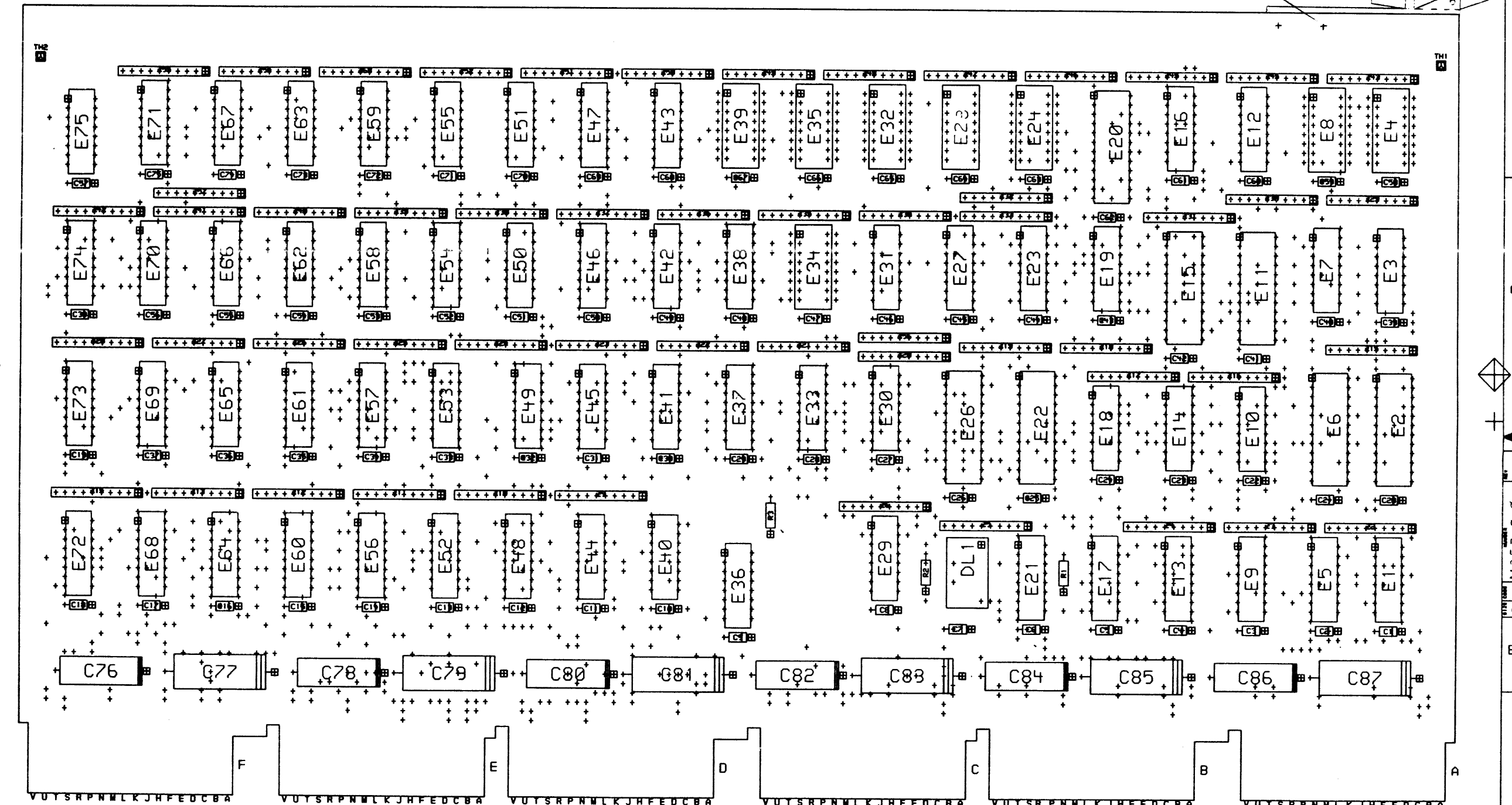
REVISIONS	
CHK	CHANGE NO. REV

digital	DATE 29-JUN-84	ENG. C. JENS	DATE 29-JUN-84	TITLE: M853
	CHK'D. D. CALINTER	DATE 29-JUN-84	BOARD LOCATION: N/A	DRAWING DIRECTORY
DSK: M853A.T2P(4,2) 29-JUN-84 15:54			NEXT HIGHER ASSEMBLY:	SIZE CODE DD M853-0
FIRST USED ON OPTION/MODEL: KL10-PW			MCA25	NUMBER 1
				REV. A

8 7 6 5 4 3 2 1 MRO

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 1984

20 (QTY 12) COMPONENT SIDE VIEW



NOTES:
 SPARE COMPONENTS: E4, E8, E24, E28, E32, E34, E35, E39, Z34, Z43, Z44, Z47 THRU Z49

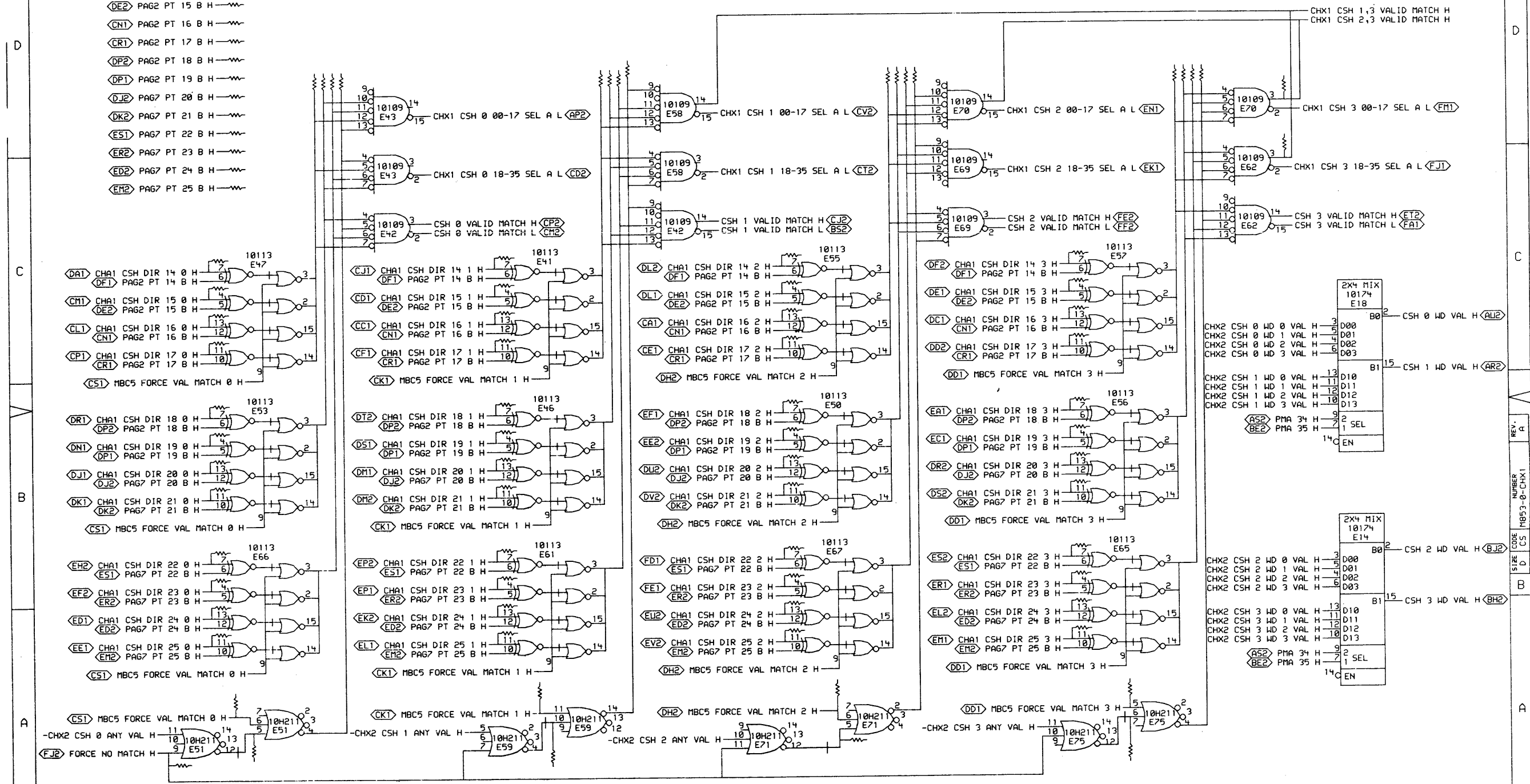
STEP	E	+ Y AXIS	0	STEP	0	TIMES
REPEAT		+ X AXIS	0	STEP	0	TIMES

CHG	NO	REV

ETCH REV.	AI
-----------	----

SIGNATURES		DATE	digital
DRN.		4/27/84	
CHK'D.	R. W. Carter		TITLE CACHE EXTENSION
MECH. ENG.			
PROJ. ENG.			
PROD.	David Brown	8-2-84	SIZE CODE NUMBER DUA M853-0-0
SCALE	2/1		
SHT.	1	OF	1
TCP DOC. NO: D-00-M853-0			

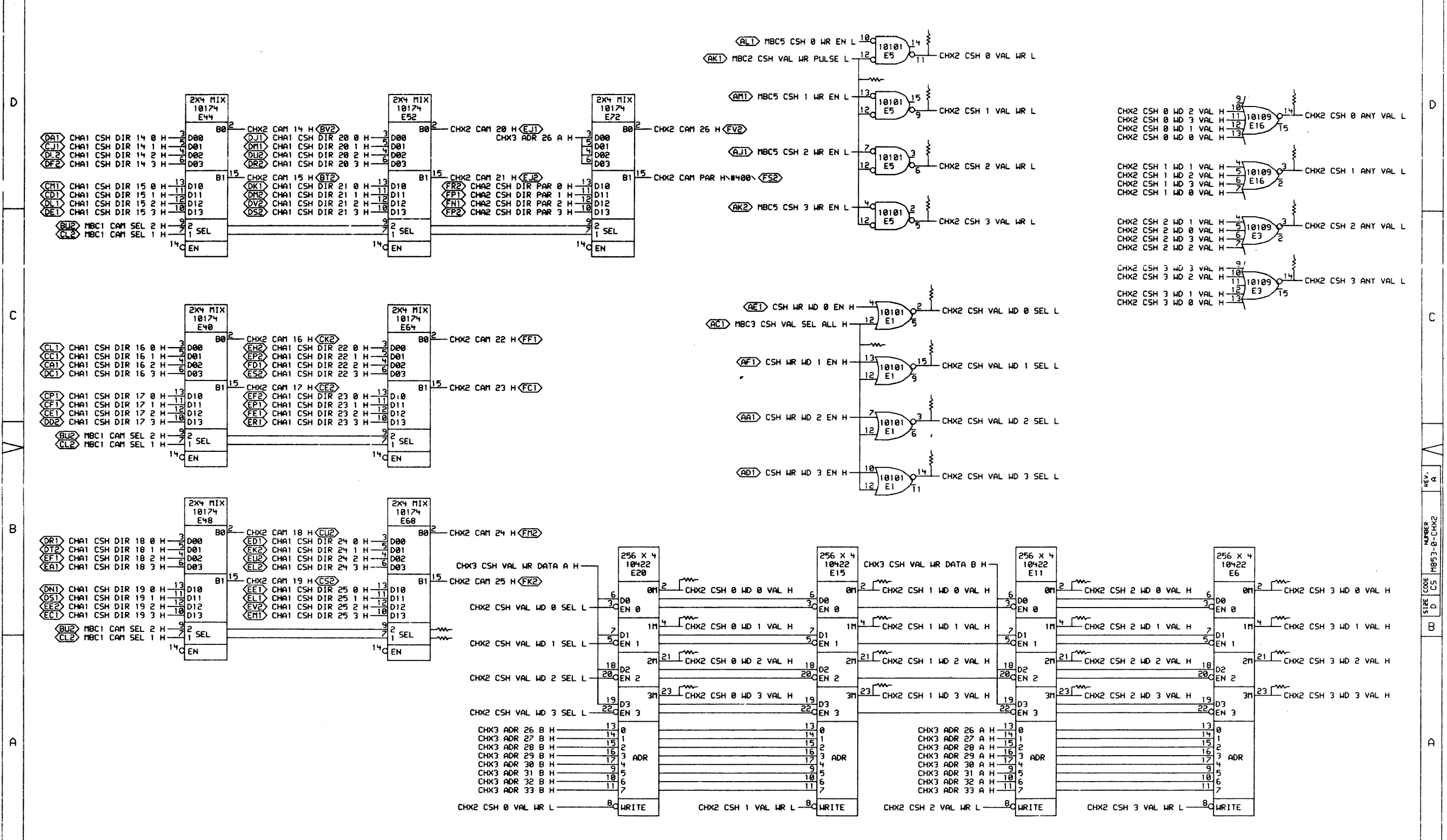
- DF1 PAG2 PT 14 B H
- DE2 PAG2 PT 15 B H
- CN1 PAG2 PT 16 B H
- CR1 PAG2 PT 17 B H
- DP2 PAG2 PT 18 B H
- DP1 PAG2 PT 19 B H
- DJ2 PAG7 PT 20 B H
- DK2 PAG7 PT 21 B H
- ES1 PAG7 PT 22 B H
- ER2 PAG7 PT 23 B H
- ED2 PAG7 PT 24 B H
- EM2 PAG7 PT 25 B H



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REVISIONS		
CHK	CHANGE NO.	REV

	DRN. J. J. J. J. CHK. D. DELLORCO	DATE 06-AUG-84 DATE 06-AUG-84	ENG. C. A. JENS BOARD LOCATION: 4AF28	DATE 06-AUG-84 SHEET 1 OF 1	TITLE: CSH ADR COMP & VAL BIT MIXER
	XTRA: MCA25, M8515, PCHXA, DRW 23-JUL-84 12:24 NEXT HIGHER ASSEMBLY: D-DD-M853-0	SIZE D CS	CODE M853-0-CHX1	NUMBER 1	REV. A



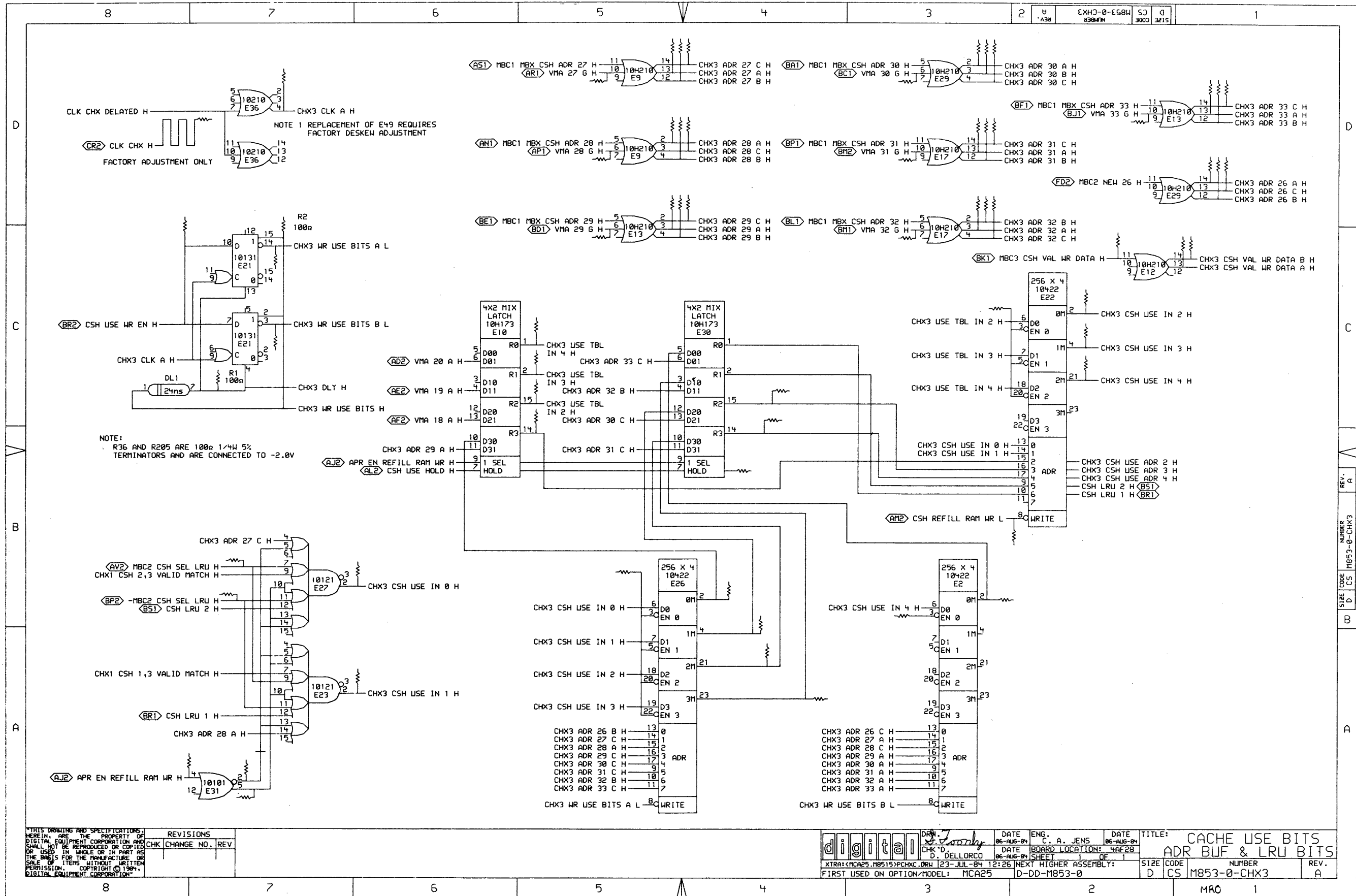
REV.	DATE	ENG.	DATE	TITLE:
1	06-AUG-84	C. A. JENS	06-AUG-84	CSH VALID BITS AND ADR MIXERS

REV.	DATE	ENG.	DATE	TITLE:
1	06-AUG-84	C. A. JENS	06-AUG-84	CSH VALID BITS AND ADR MIXERS

REV.	DATE	ENG.	DATE	TITLE:
1	06-AUG-84	C. A. JENS	06-AUG-84	CSH VALID BITS AND ADR MIXERS

REV.	DATE	ENG.	DATE	TITLE:
1	06-AUG-84	C. A. JENS	06-AUG-84	CSH VALID BITS AND ADR MIXERS

REV.	DATE	ENG.	DATE	TITLE:
1	06-AUG-84	C. A. JENS	06-AUG-84	CSH VALID BITS AND ADR MIXERS



NOTE 1 REPLACEMENT OF E49 REQUIRES FACTORY DESKTOP ADJUSTMENT

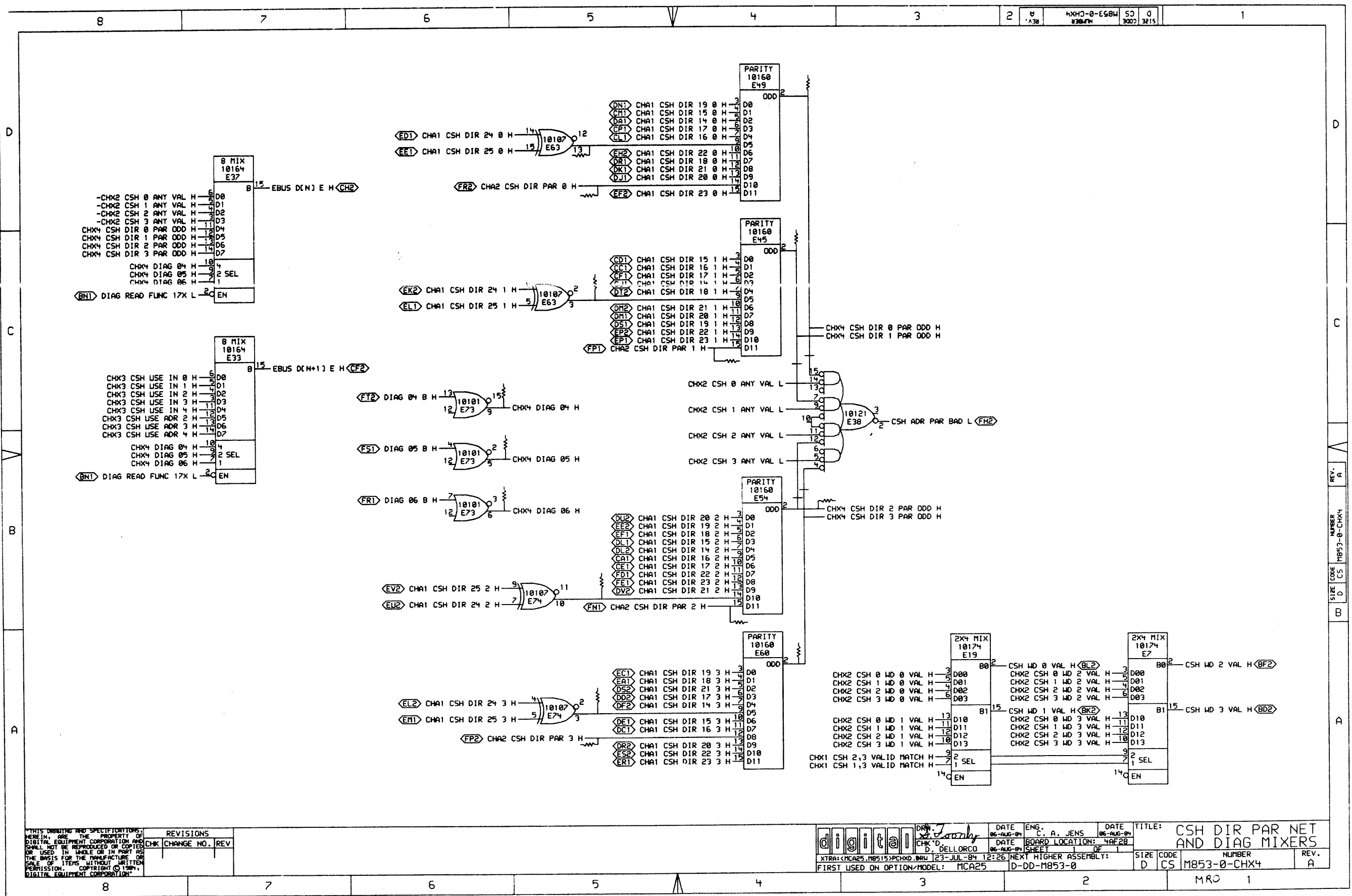
NOTE: R36 AND R205 ARE 100Ω 1/4W 5% TERMINATORS AND ARE CONNECTED TO -2.0V

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REVISIONS	CHK	CHANGE NO.	REV

digital	D. DELLORCO	DATE	ENG.	DATE	TITLE:
		06-AUG-84	C. A. JENS	06-AUG-84	CACHE USE BITS
XTRAI\MCA25.M8515\PCMC.DRW 123-JUL-84 12:26		DATE	BOARD LOCATION:		
FIRST USED ON OPTION/MODEL: MCA25		06-AUG-84	4AF28		
NEXT HIGHER ASSEMBLY: D-DD-M853-0		SHEET	OF	SIZE	CODE
		1	1	D	CS
		NUMBER		REV.	
		M853-0-CHX3		A	

REV. A
SIZE CODE NUMBER
D CS M853-0-CHX3
A



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REVISIONS	CHK	CHANGE NO.	REV.

digit@l

DATE: 06-AUG-84
 ENG: C. A. JENS
 DATE: 06-AUG-84
 BOARD LOCATION: 4AF28
 DATE: 06-AUG-84
 SHEET: 1 OF 1

TITLE: CSH DIR PAR NET AND DIAG MIXERS

SIZE CODE: D CS M853-0-CHX4
 NUMBER: M853-0-CHX4
 REV: A

REV. A
 NUMBER M853-0-CHX4
 SIZE CODE CS
 D

RESISTOR LOC (PIN)	SHOWN ON DRAW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC (PIN)	SHOWN ON DRAW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC (PIN)	SHOWN ON DRAW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC (PIN)	SHOWN ON DRAW#	ON REF	VALUE	TERMINATES SIGNAL
220(8)	CHX3	B3	68n	%E2(2)	29(1)	CHX1	C6	68n	CHA1 CSH DIR 15 1 H	254(5)	CHX1	A6	68n	CHA1 CSH DIR 25 1 H	216(1)	CHX3	D5	68n	CHX3 ADR 27 A H
215(5)	CHX3	B3	68n	%E2(3)	252(10)	CHX1	C4	68n	CHA1 CSH DIR 15 2 H	257(1)	CHX1	A4	68n	CHA1 CSH DIR 25 2 H	245(10)	CHX3	D4	68n	CHX3 ADR 27 B H
219(3)	CHX3	C3	68n	%E22(3)	225(10)	CHX1	C3	68n	CHA1 CSH DIR 15 3 H	220(10)	CHX1	A3	68n	CHA1 CSH DIR 25 3 H	256(3)	CHX3	D4	68n	CHX3 ADR 27 C H
23(6)	CHX3	B4	68n	%E26(2)	251(1)	CHX1	C7	68n	CHA1 CSH DIR 16 0 H	224(1)	CHX4	D5	68n	CHA2 CSH DIR PAR 0 H	233(5)	CHX3	D5	68n	CHX3 ADR 28 A H
26(6)	CHX3	A4	68n	%E26(21)	29(6)	CHX1	C6	68n	CHA1 CSH DIR 16 1 H	223(3)	CHX4	C4	68n	CHA2 CSH DIR PAR 1 H	245(6)	CHX3	D4	68n	CHX3 ADR 28 B H
220(10)	CHX3	A4	68n	%E26(23)	252(5)	CHX1	C4	68n	CHA1 CSH DIR 16 2 H	224(10)	CHX4	B4	68n	CHA2 CSH DIR PAR 2 H	22(1)	CHX3	D4	68n	CHX3 ADR 28 C H
220(1)	CHX3	B5	68n	%E26(3)	225(6)	CHX1	C3	68n	CHA1 CSH DIR 16 3 H	212(5)	CHX4	A5	68n	CHA2 CSH DIR PAR 3 H	230(5)	CHX3	D5	68n	CHX3 ADR 29 A H
220(5)	CHX3	A4	68n	%E26(4)	250(10)	CHX1	C7	68n	CHA1 CSH DIR 17 0 H	230(3)	CHX1	C2	68n	CHA1 CSH 1,3 VALID MATCH H	246(5)	CHX3	D4	68n	CHX3 ADR 29 B H
233(6)	CHX3	A7	68n	%E31(2)	29(8)	CHX1	C6	68n	CHA1 CSH DIR 17 1 H	217(5)	CHX1	D2	68n	CHX1 CSH 2,3 VALID MATCH H	219(10)	CHX3	D4	68n	CHX3 ADR 29 C H
232(3)	CHX3	A7	68n	%E31(5)	252(6)	CHX1	C4	68n	CHA1 CSH DIR 17 2 H	235(10)	CHX2	D1	68n	-CHX2 CSH 0 ANY VAL H	215(1)	CHX3	D3	68n	CHX3 ADR 30 A H
239(10)	CHX1	D5	68n	%E41(2)	212(10)	CHX1	C3	68n	CHA1 CSH DIR 17 3 H	246(10)	CHX2	D3	68n	-CHX2 CSH 0 VAL WR H	246(3)	CHX3	D3	68n	CHX3 ADR 30 B H
240(1)	CHX1	D5	68n	%E46(2)	211(3)	CHX1	B7	68n	CHA1 CSH DIR 18 0 H	217(8)	CHX2	B5	68n	CHX2 CSH 0 WD 0 VAL H	256(6)	CHX3	D3	68n	CHX3 ADR 30 C H
222(10)	CHX1	D7	68n	%E47(2)	223(10)	CHX1	B6	68n	CHA1 CSH DIR 18 1 H	210(6)	CHX2	B5	68n	CHX2 CSH 0 WD 1 VAL H	216(5)	CHX3	D3	68n	CHX3 ADR 31 A H
213(8)	CHX1	D4	68n	%E50(2)	239(3)	CHX1	B4	68n	CHA1 CSH DIR 18 2 H	217(6)	CHX2	A5	68n	CHX2 CSH 0 WD 2 VAL H	218(1)	CHX3	D3	68n	CHX3 ADR 31 B H
252(1)	CHX1	A7	68n	%E51(12)	210(3)	CHX1	B3	68n	CHA1 CSH DIR 18 3 H	210(5)	CHX2	A5	68n	CHX2 CSH 0 WD 3 VAL H	256(5)	CHX3	D3	68n	CHX3 ADR 31 C H
237(1)	CHX1	D7	68n	%E51(4)	225(3)	CHX1	B7	68n	CHA1 CSH DIR 19 0 H	235(8)	CHX2	D1	68n	-CHX2 CSH 1 ANY VAL H	216(3)	CHX3	D3	68n	CHX3 ADR 32 A H
250(6)	CHX1	D7	68n	%E53(2)	223(8)	CHX1	B6	68n	CHA1 CSH DIR 19 1 H	210(3)	CHX2	D3	68n	-CHX2 CSH 1 VAL WR H	221(1)	CHX3	D3	68n	CHX3 ADR 32 B H
213(10)	CHX1	D4	68n	%E55(2)	239(1)	CHX1	B4	68n	CHA1 CSH DIR 19 2 H	245(3)	CHX2	A4	68n	CHX2 CSH 1 WD 2 VAL H	27(10)	CHX3	D3	68n	CHX3 ADR 32 C H
241(10)	CHX1	D2	68n	%E56(2)	210(5)	CHX1	B3	68n	CHA1 CSH DIR 19 3 H	245(5)	CHX2	B4	68n	CHX2 CSH 1 WD 1 VAL H	216(8)	CHX3	D1	68n	CHX3 ADR 33 A H
220(3)	CHX1	D2	68n	%E57(2)	211(1)	CHX1	B7	68n	CHA1 CSH DIR 20 0 H	231(3)	CHX2	A4	68n	CHX2 CSH 1 WD 3 VAL H	217(3)	CHX3	D1	68n	CHX3 ADR 33 B H
222(8)	CHX1	D5	68n	%E59(14)	237(5)	CHX1	B6	68n	CHA1 CSH DIR 20 1 H	222(5)	CHX2	C1	68n	-CHX2 CSH 2 ANY VAL H	256(10)	CHX3	D1	68n	CHX3 ADR 33 C H
268(3)	CHX1	A6	68n	%E59(4)	224(6)	CHX1	B4	68n	CHA1 CSH DIR 20 2 H	216(6)	CHX2	D3	68n	-CHX2 CSH 2 VAL WR H	25(3)	CHX3	D7	68n	CHX3 CLK A H
240(3)	CHX1	D5	68n	%E61(2)	212(3)	CHX1	B3	68n	CHA1 CSH DIR 20 3 H	233(1)	CHX2	B2	68n	CHX2 CSH 2 WD 0 VAL H	25(1)	CHX3	B5	68n	CHX3 CSH USE ADR 2 H
210(1)	CHX4	D5	68n	%E63(13)	210(10)	CHX1	B7	68n	CHA1 CSH DIR 21 0 H	217(1)	CHX2	B2	68n	CHX2 CSH 2 WD 1 VAL H	218(8)	CHX3	C4	68n	CHX3 CSH USE ADR 3 H
29(5)	CHX4	C5	68n	%E63(3)	223(5)	CHX1	B6	68n	CHA1 CSH DIR 21 1 H	230(1)	CHX2	A2	68n	CHX2 CSH 2 WD 2 VAL H	217(10)	CHX3	C4	68n	CHX3 CSH USE ADR 4 H
242(3)	CHX1	D2	68n	%E65(2)	224(3)	CHX1	B4	68n	CHA1 CSH DIR 21 2 H	215(8)	CHX2	A2	68n	CHX2 CSH 2 WD 3 VAL H	220(3)	CHX3	B6	68n	CHX3 CSH USE IN 0 H
250(8)	CHX1	D7	68n	%E66(2)	212(8)	CHX1	B3	68n	CHA1 CSH DIR 21 3 H	222(3)	CHX2	C1	68n	-CHX2 CSH 3 ANY VAL H	26(1)	CHX3	A6	68n	CHX3 CSH USE IN 1 H
220(1)	CHX1	D4	68n	%E67(2)	213(3)	CHX1	B7	68n	CHA1 CSH DIR 22 0 H	23(3)	CHX2	C3	68n	-CHX2 CSH 3 VAL WR H	221(8)	CHX3	C2	68n	CHX3 CSH USE IN 2 H
255(8)	CHX1	A4	68n	%E71(12)	213(1)	CHX1	B6	68n	CHA1 CSH DIR 22 1 H	233(3)	CHX2	B1	68n	CHX2 CSH 3 WD 0 VAL H	221(6)	CHX3	C2	68n	CHX3 CSH USE IN 3 H
214(1)	CHX1	D4	68n	%E71(2)	257(5)	CHX1	B4	68n	CHA1 CSH DIR 22 2 H	231(8)	CHX2	B1	68n	CHX2 CSH 3 WD 1 VAL H	215(3)	CHX3	C2	68n	CHX3 CSH USE IN 4 H
230(8)	CHX4	B5	68n	%E74(10)	213(5)	CHX1	B3	68n	CHA1 CSH DIR 22 3 H	229(3)	CHX2	A1	68n	CHX2 CSH 3 WD 2 VAL H	246(8)	CHX3	C1	68n	CHX3 CSH VAL WR DATA A H
212(1)	CHX4	A5	68n	%E74(3)	241(5)	CHX1	B7	68n	CHA1 CSH DIR 23 0 H	229(3)	CHX2	A1	68n	CHX2 CSH 3 WD 3 VAL H	23(1)	CHX3	C1	68n	CHX3 CSH VAL WR DATA B H
242(8)	CHX1	A3	68n	%E75(12)	29(3)	CHX1	B6	68n	CHA1 CSH DIR 23 1 H	215(10)	CHX2	A1	68n	CHX2 CSH 3 WD 3 VAL H	R1(1)	CHX3	C7	100n	CHX3 DLY H
240(5)	CHX1	D2	68n	%E75(4)	255(1)	CHX1	B4	68n	CHA1 CSH DIR 23 2 H	245(1)	CHX2	C3	68n	-CHX2 CSH VAL WD 0 SEL H	219(6)	CHX3	C5	68n	CHX3 USE TBL IN 2 H
26(5)	CHX3	A7	68n	APR EN REFILL RAM WR H	227(5)	CHX1	B3	68n	CHA1 CSH DIR 23 3 H	246(6)	CHX2	C3	68n	-CHX2 CSH VAL WD 1 SEL H	219(5)	CHX3	C5	68n	CHX3 USE TBL IN 3 H
230(1)	CHX1	C7	68n	CHA1 CSH DIR 14 0 H	254(1)	CHX1	A7	68n	CHA1 CSH DIR 24 0 H	245(8)	CHX2	C3	68n	-CHX2 CSH VAL WD 2 SEL H	218(10)	CHX3	C5	68n	CHX3 USE TBL IN 4 H
29(10)	CHX1	C6	68n	CHA1 CSH DIR 14 1 H	254(3)	CHX1	A6	68n	CHA1 CSH DIR 24 1 H	246(1)	CHX2	B3	68n	-CHX2 CSH VAL WD 3 SEL H	R2(1)	CHX3	C7	100n	CHX3 WR USE BITS H
252(8)	CHX1	C4	68n	CHA1 CSH DIR 14 2 H	254(8)	CHX1	A4	68n	CHA1 CSH DIR 24 2 H	22(8)	CHX3	D1	68n	CHX3 ADR 26 A H	25(10)	CHX3	C7	68n	-CHX3 WR USE BITS A H
212(6)	CHX1	C3	68n	CHA1 CSH DIR 14 3 H	220(8)	CHX1	A3	68n	CHA1 CSH DIR 24 3 H	231(10)	CHX3	D1	68n	CHX3 ADR 26 B H	22(5)	CHX3	C7	68n	-CHX3 WR USE BITS B H
251(5)	CHX1	C7	68n	CHA1 CSH DIR 15 0 H	268(10)	CHX1	A7	68n	CHA1 CSH DIR 25 0 H	22(6)	CHX3	D1	68n	CHX3 ADR 26 C H	236(1)	CHX4	D4	68n	CHX4 CSH DIR 0 PAR ODD H

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

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	CHK	CHANGE NO. REV.				
			24-JUL-84 11:28	C. A. JENS	06-AUG-84	CACHE EXTENSION TERMINATORS
			06-AUG-84	D. DELLORCO	06-AUG-84	4A28
			FIRST USED ON OPTION/MODEL: MCA25		NEXT HIGHER ASSEMBLY: D-DD-M853-0	
			SIZE CODE		NUMBER	
			D CS		M853-0-RES	
			REV.		A	

D
C
B
A

D
C
B
A

RESISTOR LOC(PIN)	SHOWN ON DRW#	VALUE REF	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	VALUE REF	TERMINATES SIGNAL
Z36(5)	CHX4	C4 68Ω	CHX4 CSH DIR 1 PAR ODD H	Z3(5)	CHX3	D5 68Ω	VMA 27 G H
Z22(1)	CHX4	B4 68Ω	CHX4 CSH DIR 2 PAR ODD H	Z3(8)	CHX3	D5 68Ω	VMA 28 G H
Z36(3)	CHX4	A4 68Ω	CHX4 CSH DIR 3 PAR ODD H	Z4(6)	CHX3	C5 68Ω	VMA 29 G H
Z21(3)	CHX4	C6 68Ω	CHX4 DIAG 04 H	Z6(8)	CHX3	D3 68Ω	VMA 30 G H
Z21(5)	CHX4	B6 68Ω	CHX4 DIAG 05 H	Z4(8)	CHX3	D3 68Ω	VMA 31 G H
Z21(10)	CHX4	B6 68Ω	CHX4 DIAG 06 H	Z4(5)	CHX3	C3 68Ω	VMA 32 G H
R3(1)	CHX3	D7 68Ω	CLK CHX H	Z4(3)	CHX3	D2 68Ω	VMA 33 G H
Z19(8)	CHX3	B2 68Ω	-CSH REFILL RAM WR H				
Z6(10)	CHX3	B4 68Ω	CSH USE HOLD H				
Z5(5)	CHX3	C7 68Ω	CSH USE WR EN H				
Z42(6)	CHX1	A8 68Ω	FORCE NO MATCH H				
Z14(5)	CHX2	A6 68Ω	MBC1 CAM SEL 1 H				
Z14(3)	CHX2	B6 68Ω	MBC1 CAM SEL 2 H				
Z56(1)	CHX3	B7 68Ω	MBC2 CSH SEL LRU H				
Z32(1)	CHX3	B7 68Ω	-MBC2 CSH SEL LRU H				
Z2(10)	CHX2	D4 68Ω	-MBC2 CSH VAL WR PULSE H				
Z2(3)	CHX2	C4 68Ω	MBC3 CSH VAL SEL ALL H				
Z30(6)	CHX3	C2 68Ω	MBC3 CSH VAL WR DATA H				
Z37(3)	CHX1	A7 68Ω	MBC5 FORCE VAL MATCH 0 H				
Z26(3)	CHX1	A6 68Ω	MBC5 FORCE VAL MATCH 1 H				
Z42(1)	CHX1	A4 68Ω	MBC5 FORCE VAL MATCH 2 H				
Z42(10)	CHX1	A3 68Ω	MBC5 FORCE VAL MATCH 3 H				
Z34(1)	CHX5	B4 68Ω	NC				
Z43(1)	CHX5	B4 68Ω	NC				
Z44(1)	CHX5	B4 68Ω	NC				
Z47(1)	CHX5	B4 68Ω	NC				
Z48(1)	CHX5	B4 68Ω	NC				
Z49(1)	CHX5	B4 68Ω	NC				
Z51(8)	CHX1	D7 68Ω	PAG2 PT 14 B H				
Z51(6)	CHX1	D7 68Ω	PAG2 PT 15 B H				
Z51(3)	CHX1	D7 68Ω	PAG2 PT 16 B H				
Z37(6)	CHX1	D7 68Ω	PAG2 PT 17 B H				
Z37(8)	CHX1	D7 68Ω	PAG2 PT 18 B H				
Z37(10)	CHX1	D7 68Ω	PAG2 PT 19 B H				
Z23(1)	CHX1	D7 68Ω	PAG7 PT 20 B H				
Z23(6)	CHX1	D7 68Ω	PAG7 PT 21 B H				
Z57(6)	CHX1	D7 68Ω	PAG7 PT 22 B H				
Z55(3)	CHX1	D7 68Ω	PAG7 PT 23 B H				
Z54(10)	CHX1	C7 68Ω	PAG7 PT 24 B H				
Z57(3)	CHX1	C7 68Ω	PAG7 PT 25 B H				

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND (<) INDICATES PIN NUMBER

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REVISIONS		
CHK	CHANGE NO.	REV

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digital	DRW: J. Jansky	DATE: 05-AUG-84	ENG: C. A. JENS	DATE: 06-AUG-84	TITLE: CACHE EXTENSION TERMINATORS
	CHK: D. DELLORCO	DATE: 24-JUL-84 11:32	BOARD LOCATION: 4AF28	SHEET: 2 OF 2	
XTRA: (MCA25.M8515)M8532.DRW					
FIRST USED ON OPTION/MODEL: MCA25					
NEXT HIGHER ASSEMBLY: D-DD-M853-0					
SIZE: D	CODE: CS	NUMBER: M853-0-RES	REV: A	MRO 1	

8 7 6 5 4 3 2 1

DRAWING NUMBER	NO. OF SHT	PART NUMBER	DESCRIPTION	REVISION
		M854-00	MODULE REVISION	A1
D-UA-M854-0-0	1		PHYSICAL MEMORY ADDRESS	A
K-PL-M854-0-DBP	2		PART LIST, M854	A
D-CS-M854-0-PMA1	1		EBR & UBR REG UEBR MIX & VA BUF	A
D-CS-M854-0-PMA2	1		CACHE CLEARER ADR CTR	A
D-CS-M854-0-PMA3	1		PHY ADR MIX PA 14-31	A
D-CS-M854-0-PMA4	1		ERA REG, ADR PAR, PA 32-35 MIX	A
D-CS-M854-0-PMA5	1		PHY MEM ADR CONTROL LOGIC	A
D-CS-M854-0-PMA6	1		PHYSICAL MEM ADR PWR, GND, CAP	A
D-CS-M854-0-RES	2		PHYSICAL MEMORY ADDRESS TERMINATORS	A
K-PC-M854-0-DBI	-		P.C. DESIGN DATA BASE TAPE	A
D-DD-5017660-0	1		DRAWING DIRECTORY, 5017660	REF

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REVISIONS		
CHK	CHANGE NO.	REV

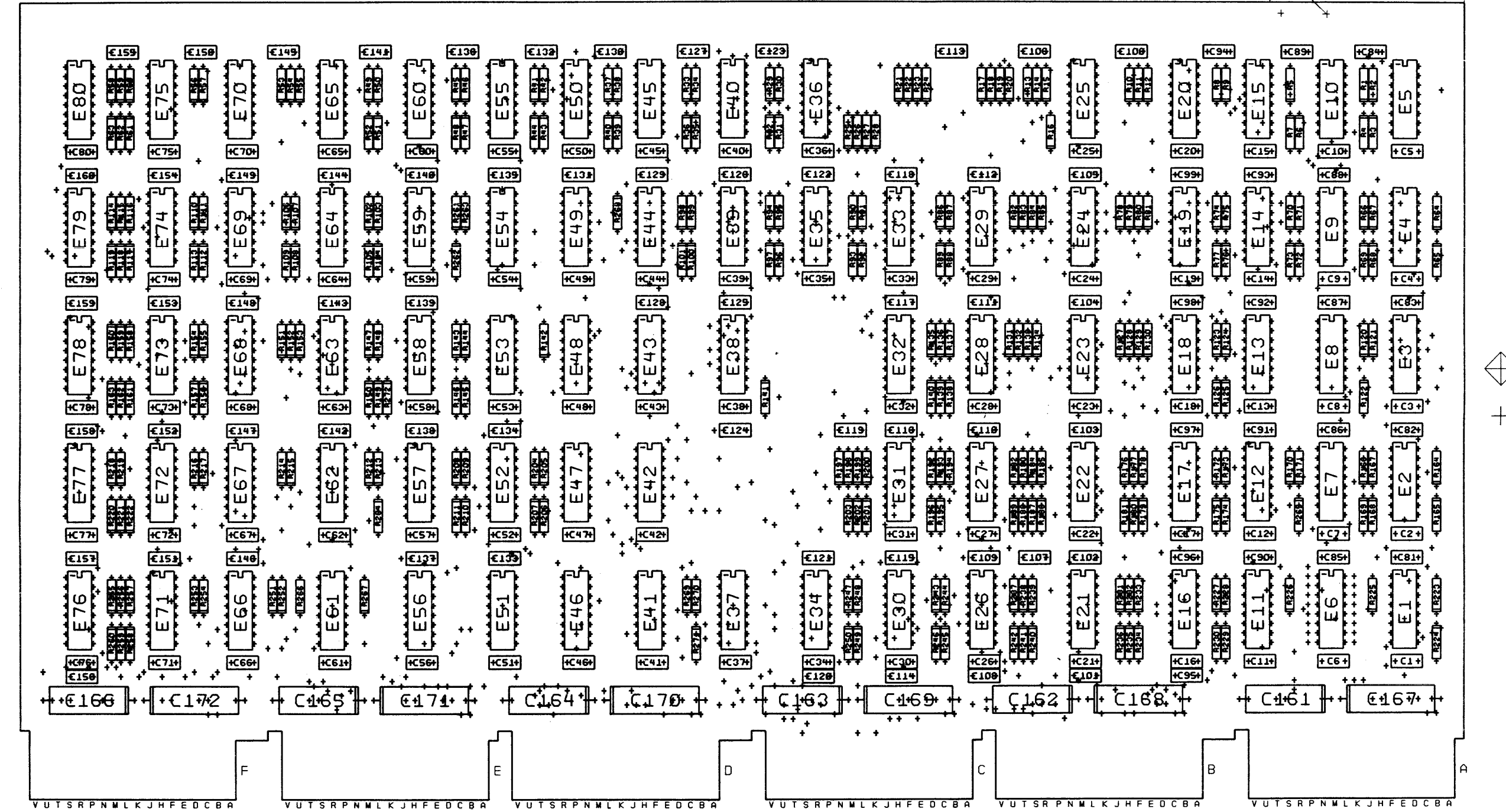
DRN. D. DELORCO	DATE 09-JUL-84	ENG. C. KACZOR	DATE 09-JUL-84	TITLE: M854
CHK'D D. DELORCO	DATE 09-JUL-84	BOARD LOCATION: N/A	SHEET 1 OF 1	DRAWING DIRECTORY
DSK: M854A.T2P(4,21) 109-JUL-84 16:40			NEXT HIGHER ASSEMBLY:	SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL: KL10-PW			MCA25	D DD M854-0 A

8	7	6	5	4	3	2	1
						MRO	1

REV. A
NUMBER M854-0
SIZE CODE DD

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22(QTY 12) COMPONENT SIDE VIEW



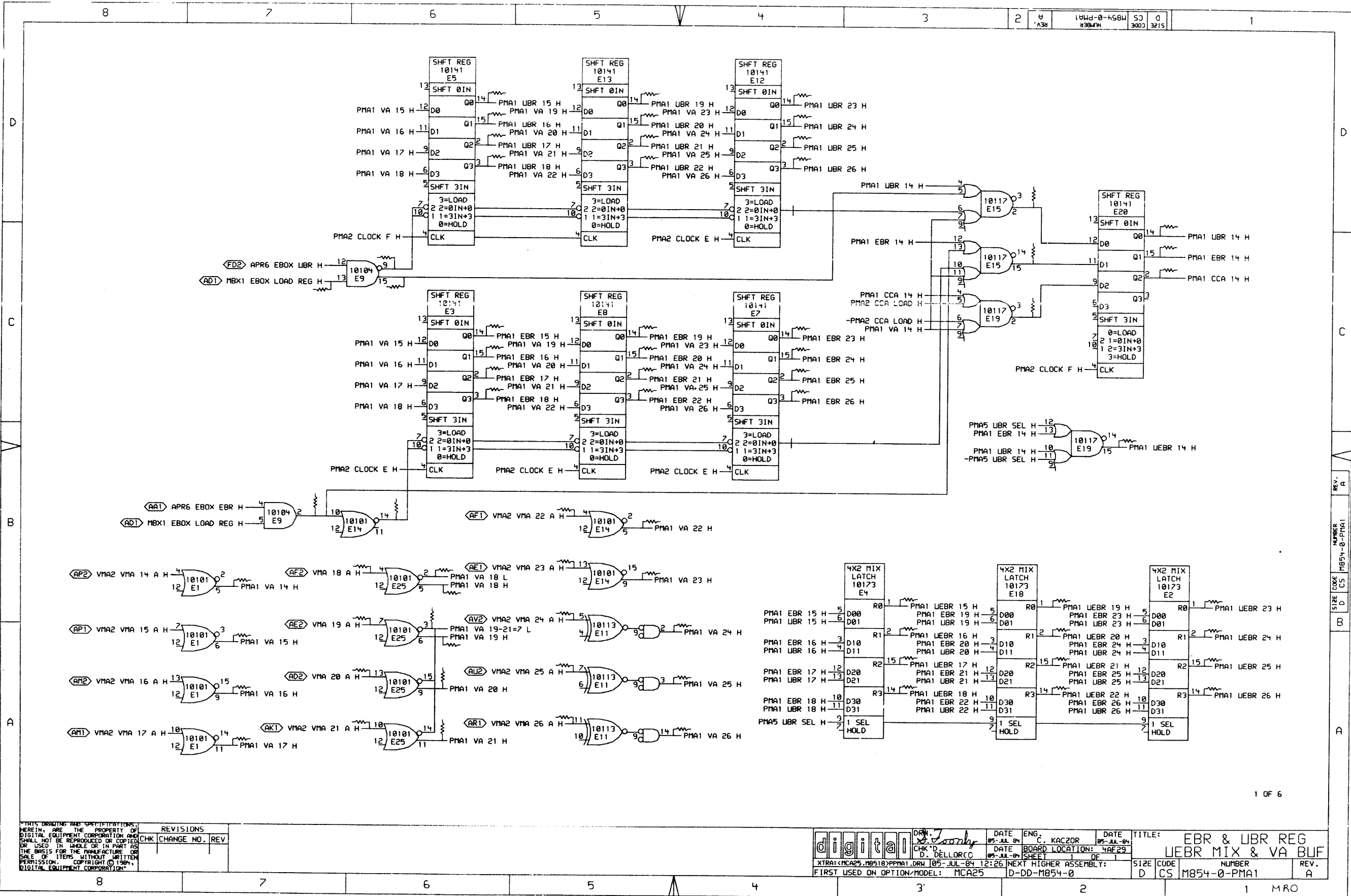
NOTES: SPARE COMPONENT LOCATIONS
 -AE: E6, R49, R72, R88, R101, R154, R195, R203, R217, R254, R197

STEP E	+ Y AXIS	0	STEP 0	TIMES
REPEAT	+ X AXIS	0	STEP 0	TIMES

CHG	NO	REV

SIGNATURES	DATE
DRN. <i>J. Clark</i>	4/10/81
CHK'D <i>D. Lawrence</i>	3/25/81
MECH. ENG. <i>C. Long</i>	5/14/81
PROJ. ENG. <i>C. Long</i>	5/14/81
PROD. <i>Diana P...</i>	5/14/81
SCALE 2/1	
SHT. 1 OF 1	
TOP DOC. NO: D-DD-M854-0	

digital	
TITLE PHYSICAL	
MEMORY ADDRESS	
SIZE CODE	NUMFR
D UA M854-0-0	A
REV	



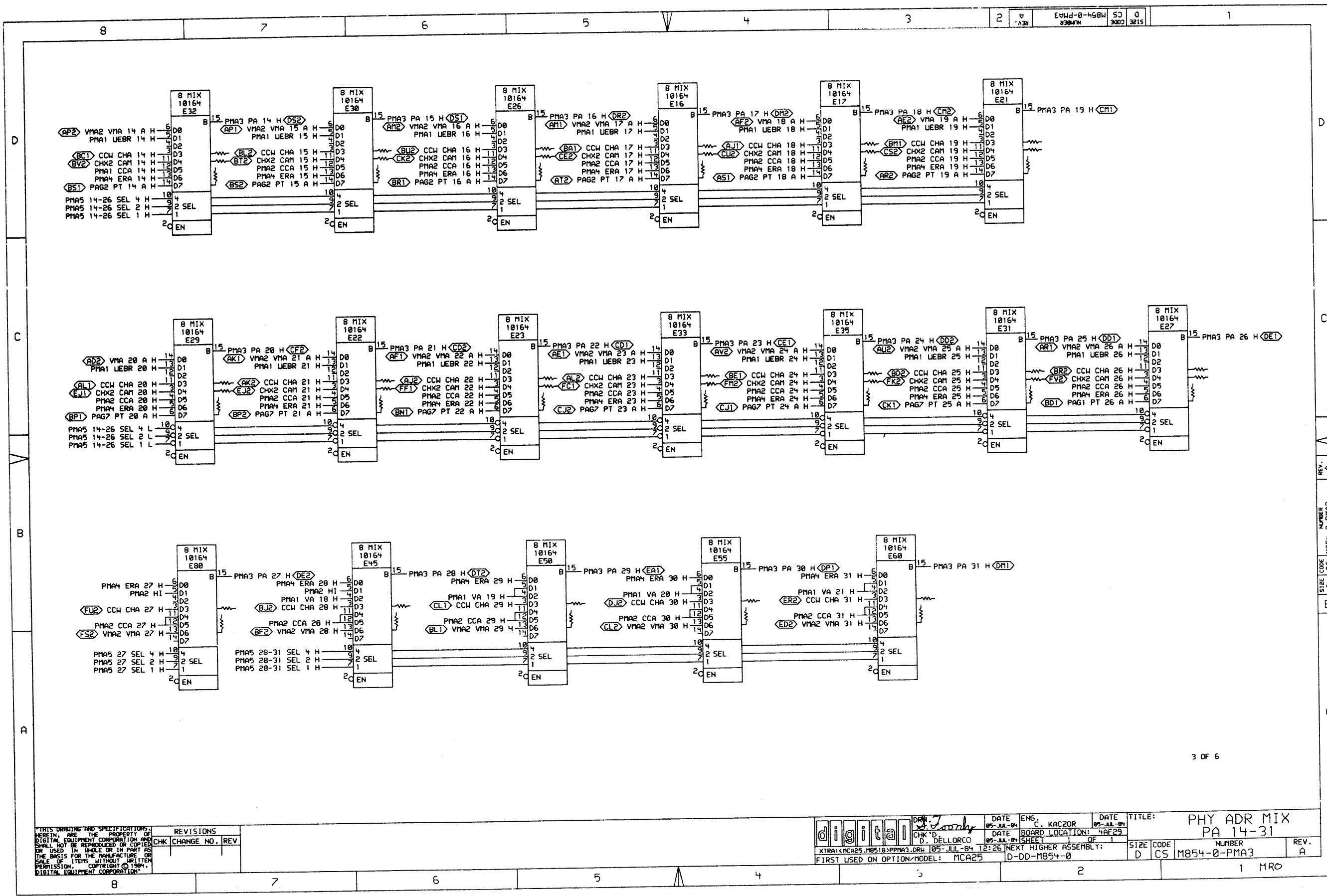
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REVISIONS	
CHK	CHANGE NO. REV

DATE: 05-12-84	ENG: C. KACZOR	DATE: 05-12-84	TITLE: EBR & UBR REG UEBR MIX & VA BUF
CHK'D: D. DELLORCC	BOARD LOCATION: 4AF29	DATE: 05-12-84	SIZE: D CS
	SHEET: 1 OF 1		NUMBER: M854-0-PMA1
			REV: A

XTRA1:MCA25.M854-0-PMA1.DRW 105-JUL-84 12:26 NEXT HIGHER ASSEMBLY: D-DD-M854-0
 FIRST USED ON OPTION/MODEL: MCA25

REV. A
 NUMBER M854-0-PMA1
 SIZE CODE D CS
 BOARD B
 A



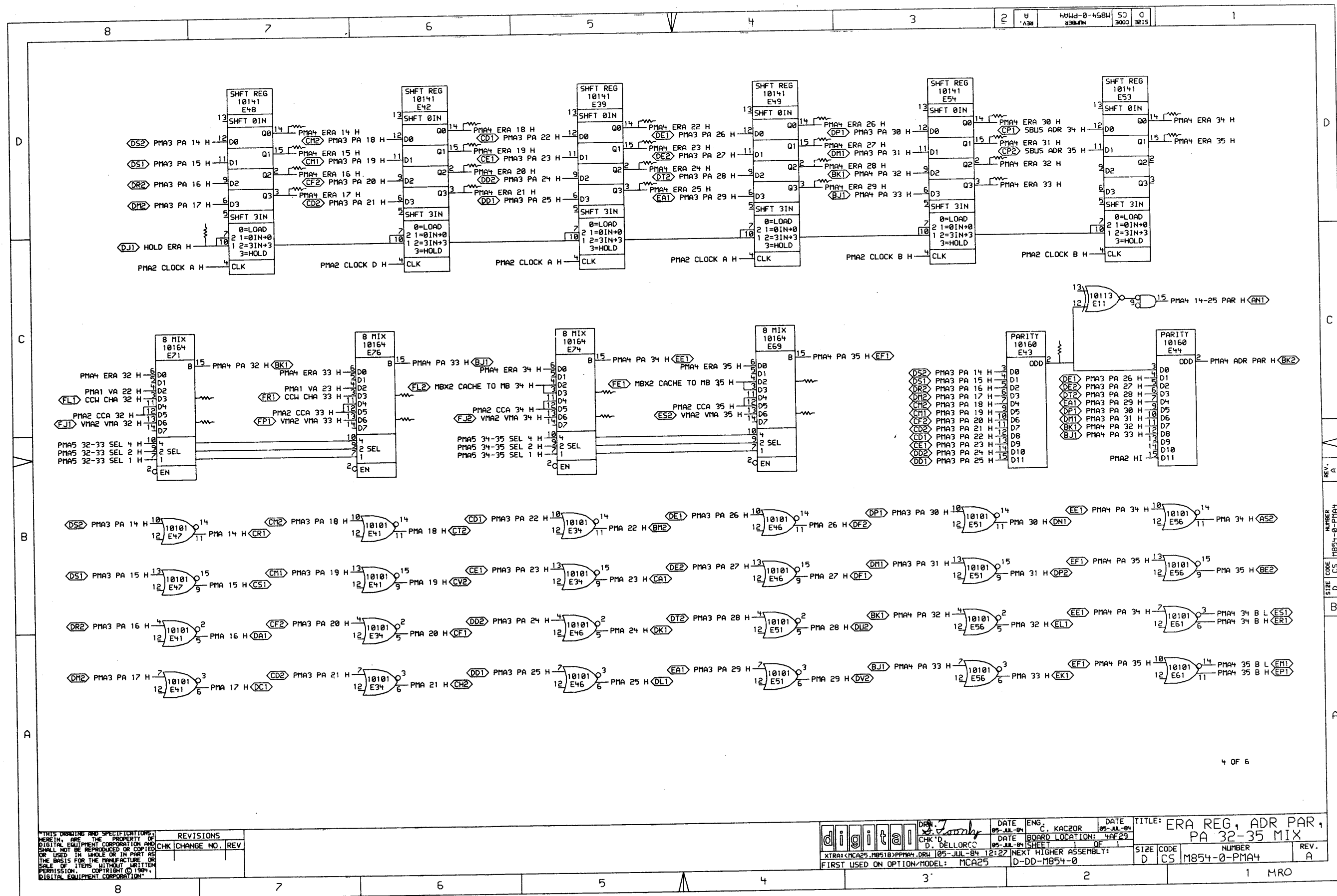
3 OF 6

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REVISIONS
CHK CHANGE NO. REV

	DATE: 05-JUL-84	ENG. C. KACZOR	DATE: 05-JUL-84	TITLE: PHY ADR MIX
	CHK'D: D. DELLORCO	BOARD LOCATION: 4AF29	SHEET: 1 OF 1	PA 14-31
XTRA: MCA25, M8518, PMA3, DRAW 105-JUL-84 12:26		NEXT HIGHER ASSEMBLY: D-DD-M854-0		SIZE CODE: D CS
FIRST USED ON OPTION/MODEL: MCA25		NUMBER: M854-0-PMA3		REV. A

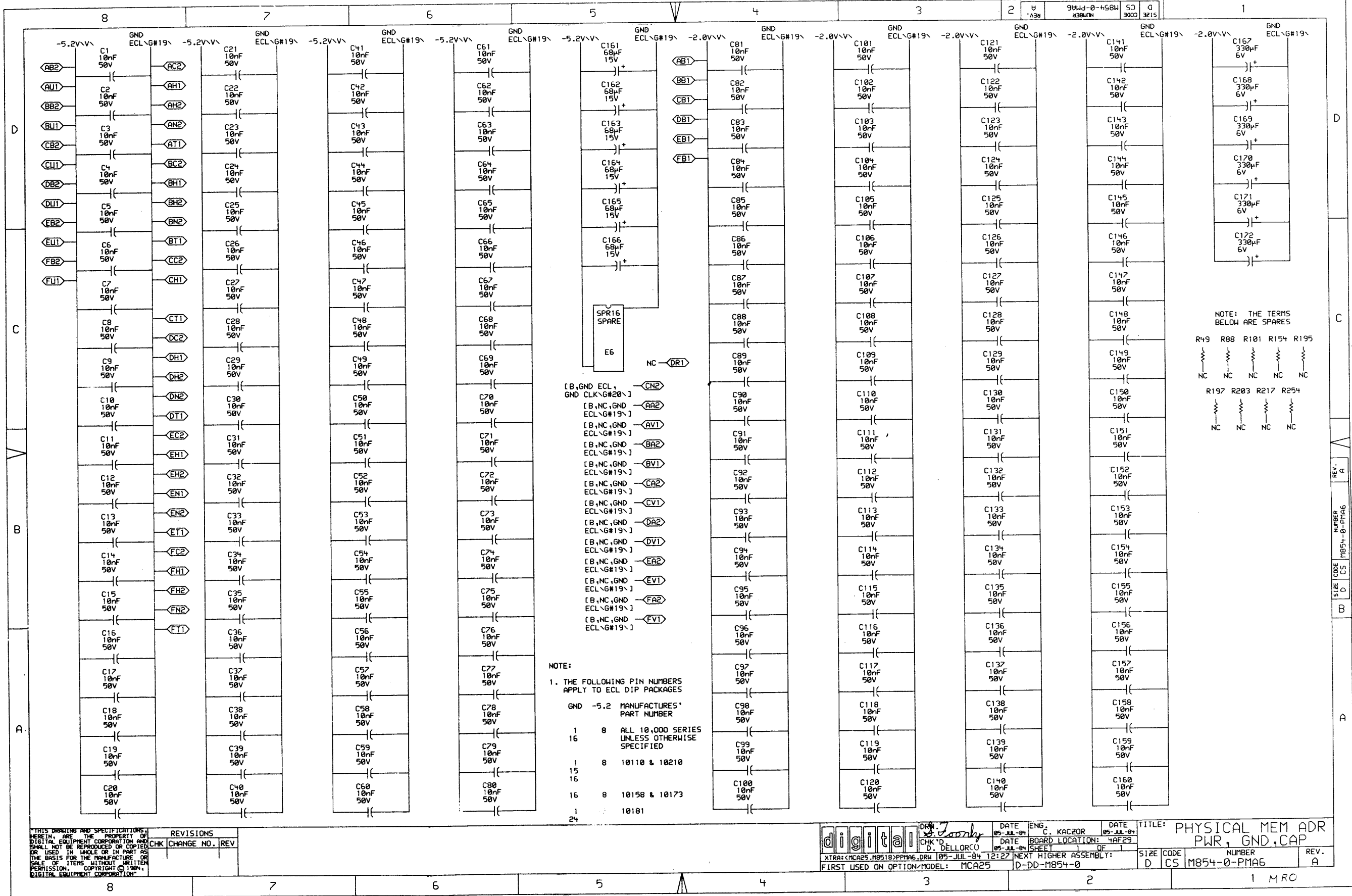
1 MRO



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN: J. J. J.	DATE: 05-JUL-84	ENG: C. KACZOR	DATE: 05-JUL-84	TITLE: ERA REG, ADR PAR, PA 32-35 MIX
	CHK: D. DELLORCO	DATE: 05-JUL-84	BOARD LOCATION: 4AF29	DATE: 05-JUL-84	SHEET: 1 OF 1
XTRAI (MCA25, M8518) PPM4, DRN 105-JUL-84 12:27			NEXT HIGHER ASSEMBLY: D-DD-M854-0		SIZE: D CS M854-0-PM4
FIRST USED ON OPTION/MODEL: MCA25					NUMBER: A



NOTE: THE TERMS BELOW ARE SPARES

R49 R88 R101 R154 R195
 NC NC NC NC NC

R197 R203 R217 R254
 NC NC NC NC

- [B,GND ECL, GND CLK\G#20] → CN2
- [B,NC,GND ECL\G#19] → AA2
- [B,NC,GND ECL\G#19] → AV1
- [B,NC,GND ECL\G#19] → BA2
- [B,NC,GND ECL\G#19] → BV1
- [B,NC,GND ECL\G#19] → CA2
- [B,NC,GND ECL\G#19] → CV1
- [B,NC,GND ECL\G#19] → DA2
- [B,NC,GND ECL\G#19] → DV1
- [B,NC,GND ECL\G#19] → EA2
- [B,NC,GND ECL\G#19] → EV1
- [B,NC,GND ECL\G#19] → FA2
- [B,NC,GND ECL\G#19] → FV1

NOTE:

1. THE FOLLOWING PIN NUMBERS APPLY TO ECL DIP PACKAGES

GND	-5.2	MANUFACTURERS' PART NUMBER
1	8	ALL 10,000 SERIES UNLESS OTHERWISE SPECIFIED
16	8	10110 & 10210
15	8	10158 & 10173
16	8	10158 & 10173
1	24	10181

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REVISIONS	CHK	CHANGE NO.	REV

digital *Loony*

DATE: 05-JUL-84
 ENG: C. KACZOR
 DATE: 05-JUL-84
 BOARD LOCATION: 4AF29
 DATE: 05-JUL-84
 SHEET: 1 OF 1

TITLE: PHYSICAL MEM ADR PWR, GND, CAP

SIZE: D CODE: CS NUMBER: M854-0-PMA6 REV: A

FIRST USED ON OPTION/MODEL: MCA25 D-DD-M854-0

1 MRC

RESISTOR LOC(PIN)	SHOWN ON DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	ON REF	VALUE	TERMINATES SIGNAL
R171(1)	PMA1	B6	68n	%E14(14)	R247(1)	PMA3	D6	68n	CCW CHA 15 H	R263(1)	PMA5	D7	68n	-CSH5 PAGE REFILL T4 H	R120(1)	PMA1	C6	68n	PMA1 EBR 18 H
R11(1)	PMA1	C2	68n	%E15(15)	R243(1)	PMA3	D5	68n	CCW CHA 16 H	R100(1)	PMA4	C7	68n	HOLD ERA H	R130(1)	PMA1	C5	68n	PMA1 EBR 19 H
R12(1)	PMA1	D2	68n	%E15(2)	R232(1)	PMA3	D4	68n	CCW CHA 17 H	R54(1)	PMA2	A6	68n	MBX1 CCA ALL PAGES CYC H	R127(1)	PMA1	C5	68n	PMA1 EBR 20 H
R8(1)	PMA1	C2	68n	%E19(2)	R177(1)	PMA3	D3	68n	CCW CHA 18 H	R32(1)	PMA2	B3	68n	MBX1 CCA SEL 1 H	R123(1)	PMA1	C5	68n	PMA1 EBR 21 H
R30(1)	PMA2	A4	68n	%E36(15)	R238(1)	PMA3	D2	68n	CCW CHA 19 H	R99(1)	PMA2	B3	68n	MBX1 CCA SEL 2 H	R125(1)	PMA1	C5	68n	PMA1 EBR 22 H
R29(1)	PMA2	C4	68n	%E37(13)	R84(1)	PMA3	C7	68n	CCW CHA 20 H	R70(1)	PMA1	C7	68n	MBX1 EBOX LOAD REG H	R169(1)	PMA1	C4	68n	PMA1 EBR 23 H
R226(1)	PMA4	C2	68n	%E43(2)	R187(1)	PMA3	C6	68n	CCW CHA 21 H	R115(1)	PMA4	C5	68n	MBX2 CACHE TO MB 34 H	R166(1)	PMA1	C4	68n	PMA1 EBR 24 H
R219(1)	PMA5	D5	68n	%E52(2)	R80(1)	PMA3	C5	68n	CCW CHA 22 H	R111(1)	PMA4	C4	68n	MBX2 CACHE TO MB 35 H	R165(1)	PMA1	C4	68n	PMA1 EBR 25 H
R163(1)	PMA5	B5	68n	%E57(15)	R202(1)	PMA3	C4	68n	CCW CHA 23 H	R155(1)	PMA5	C7	68n	-MBX4 WRITEBACK T2 H	R225(1)	PMA1	C4	68n	PMA1 EBR 26 H
R160(1)	PMA5	B5	68n	%E57(2)	R93(1)	PMA3	C3	68n	CCW CHA 24 H	R73(1)	PMA5	D8	68n	-MCL2 VMA USER H	R76(1)	PMA1	D2	68n	PMA1 UBR 14 H
R147(1)	PMA5	B5	68n	%E57(3)	R196(1)	PMA3	C2	68n	CCW CHA 25 H	R55(1)	PMA5	B2	68n	-MCL6 EBOX MAY BE PAGED H	R68(1)	PMA1	D6	68n	PMA1 UBR 15 H
R144(1)	PMA5	C2	68n	%E59(2)	R109(1)	PMA3	C1	68n	CCW CHA 26 H	R102(1)	PMA5	D3	68n	MCL6 VMA EPT H	R67(1)	PMA1	D6	68n	PMA1 UBR 16 H
R150(1)	PMA5	A5	68n	%E62(15)	R60(1)	PMA3	B7	68n	CCW CHA 27 H	R252(1)	PMA5	C3	68n	MCL6 VMA LPT H	R64(1)	PMA1	D6	68n	PMA1 UBR 17 H
R153(1)	PMA5	A5	68n	%E62(2)	R34(1)	PMA3	B6	68n	CCW CHA 28 H	R49(1)	PMA6	C1	68n	NC	R122(1)	PMA1	D6	68n	PMA1 UBR 18 H
R152(1)	PMA5	A5	68n	%E62(3)	R44(1)	PMA3	B5	68n	CCW CHA 29 H	R88(1)	PMA6	C1	68n	NC	R129(1)	PMA1	D5	68n	PMA1 UBR 19 H
R145(1)	PMA5	D2	68n	%E64(7)	R48(1)	PMA3	B4	68n	CCW CHA 30 H	R101(1)	PMA6	C1	68n	NC	R120(1)	PMA1	D5	68n	PMA1 UBR 20 H
R28(1)	PMA2	A5	68n	%E65(2)	R52(1)	PMA3	B3	68n	CCW CHA 31 H	R154(1)	PMA6	C1	68n	NC	R124(1)	PMA1	D5	68n	PMA1 UBR 21 H
R204(1)	PMA5	C2	68n	%E66(6)	R255(1)	PMA4	C7	68n	CCW CHA 32 H	R195(1)	PMA6	C1	68n	NC	R126(1)	PMA1	D5	68n	PMA1 UBR 22 H
R146(1)	PMA5	C2	68n	%E66(7)	R218(1)	PMA4	C6	68n	CCW CHA 33 H	R197(1)	PMA6	C1	68n	NC	R168(1)	PMA1	D4	68n	PMA1 UBR 23 H
R251(1)	PMA5	D7	68n	%E67(15)	R89(1)	PMA3	D7	68n	CHX2 CAM 14 H	R203(1)	PMA6	C1	68n	NC	R167(1)	PMA1	D4	68n	PMA1 UBR 24 H
R262(1)	PMA5	D7	68n	%E67(9)	R245(1)	PMA3	D6	68n	CHX2 CAM 15 H	R217(1)	PMA6	C1	68n	NC	R164(1)	PMA1	D4	68n	PMA1 UBR 25 H
R159(1)	PMA5	C5	68n	%E72(15)	R241(1)	PMA3	D5	68n	CHX2 CAM 16 H	R254(1)	PMA6	C1	68n	NC	R224(1)	PMA1	D4	68n	PMA1 UBR 26 H
R114(1)	PMA5	C5	68n	%E72(2)	R230(1)	PMA3	D4	68n	CHX2 CAM 17 H	R140(1)	PMA3	C1	68n	PAG1 PT 26 A H	R138(1)	PMA1	B2	68n	PMA1 UEBR 14 H
R119(1)	PMA5	C5	68n	%E72(3)	R175(1)	PMA3	D3	68n	CHX2 CAM 18 H	R137(1)	PMA3	D7	68n	PAG2 PT 14 A H	R248(1)	PMA1	B3	68n	PMA1 UEBR 15 H
R158(1)	PMA5	D5	68n	%E77(15)	R234(1)	PMA3	D2	68n	CHX2 CAM 19 H	R249(1)	PMA3	D6	68n	PAG2 PT 15 A H	R244(1)	PMA1	A3	68n	PMA1 UEBR 16 H
R162(1)	PMA5	D5	68n	%E77(2)	R19(1)	PMA3	C7	68n	CHX2 CAM 20 H	R237(1)	PMA3	D5	68n	PAG2 PT 16 A H	R233(1)	PMA1	A3	68n	PMA1 UEBR 17 H
R161(1)	PMA5	D5	68n	%E77(3)	R183(1)	PMA3	C6	68n	CHX2 CAM 21 H	R220(1)	PMA3	D4	68n	PAG2 PT 17 A H	R178(1)	PMA1	A3	68n	PMA1 UEBR 18 H
R148(1)	PMA5	D3	68n	%E78(14)	R81(1)	PMA3	C5	68n	CHX2 CAM 22 H	R173(1)	PMA3	D3	68n	PAG2 PT 18 A H	R239(1)	PMA1	B2	68n	PMA1 UEBR 19 H
R151(1)	PMA5	D3	68n	%E78(15)	R23(1)	PMA3	C4	68n	CHX2 CAM 23 H	R231(1)	PMA3	D2	68n	PAG2 PT 19 A H	R85(1)	PMA1	A2	68n	PMA1 UEBR 20 H
R143(1)	PMA5	D3	68n	%E78(2)	R97(1)	PMA3	C3	68n	CHX2 CAM 24 H	R83(1)	PMA3	C7	68n	PAG7 PT 20 A H	R176(1)	PMA1	A2	68n	PMA1 UEBR 21 H
R72(1)	PMA5	A2	68n	%E9(11)	R92(1)	PMA3	C2	68n	CHX2 CAM 25 H	R186(1)	PMA3	C6	68n	PAG7 PT 21 A H	R79(1)	PMA1	A2	68n	PMA1 UEBR 22 H
R9(1)	PMA1	C6	68n	%E9(15)	R198(1)	PMA3	C1	68n	CHX2 CAM 26 H	R133(1)	PMA3	C5	68n	PAG7 PT 22 A H	R18(1)	PMA1	B1	68n	PMA1 UEBR 23 H
R5(1)	PMA1	B7	68n	%E9(2)	R141(1)	PMA2	C2	68n	CLK1 PMA H	R21(1)	PMA3	C4	68n	PAG7 PT 23 A H	R98(1)	PMA1	A1	68n	PMA1 UEBR 24 H
R170(1)	PMA1	C6	68n	%E9(9)	R210(1)	PMA5	B7	68n	CSH1 CCA REQ GRANT H	R96(1)	PMA3	C3	68n	PAG7 PT 24 A H	R193(1)	PMA1	A1	68n	PMA1 UEBR 25 H
R104(1)	PMA5	D3	68n	APR EBOX ERA H	R209(1)	PMA5	B7	68n	CSH1 CHAN REQ GRANT H	R201(1)	PMA3	C2	68n	PAG7 PT 25 A H	R188(1)	PMA1	A1	68n	PMA1 UEBR 26 H
R272(1)	PMA5	D3	68n	APR6 EBOX CCA H	R211(1)	PMA5	C5	68n	CSH1 EBOX CCA GRANT H	R86(1)	PMA1	C2	68n	PMA1 CCA 14 H	R7(1)	PMA1	B7	68n	PMA1 VA 14 H
R103(1)	PMA5	D3	68n	APR6 EBOX EBR H	R213(1)	PMA5	C5	68n	CSH1 EBOX ERA GRANT H	R75(1)	PMA1	C2	68n	PMA1 EBR 14 H	R6(1)	PMA1	A7	68n	PMA1 VA 15 H
R71(1)	PMA5	C3	68n	APR6 EBOX UBR H	R212(1)	PMA5	D5	68n	CSH1 EBOX REQ GRANT H	R69(1)	PMA1	C6	68n	PMA1 EBR 15 H	R1(1)	PMA1	A7	68n	PMA1 VA 16 H
R205(1)	PMA5	D6	68n	CCL CHAN EPT H	R157(1)	PMA5	A7	68n	-CSH1 EBOX REQ GRANT A H	R66(1)	PMA1	C6	68n	PMA1 EBR 16 H	R3(1)	PMA1	A7	68n	PMA1 VA 17 H
R135(1)	PMA3	D7	68n	CCW CHA 14 H	R149(1)	PMA5	C8	68n	-CSH1 READY TO GO H	R65(1)	PMA1	C6	68n	PMA1 EBR 17 H	R105(1)	PMA1	B6	68n	PMA1 VA 18 H

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

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REVISIONS	
CHK	CHANGE NO. REV

digital	DRW#	DATE	ENG.	DATE	TITLE:
	CHK'D	05-JUL-84	C. KACZOR	05-JUL-84	PHYSICAL MEMORY ADDRESS--TERMINATORS
XTRA: MCA25.MB510.MB541.DRW		DATE	BOARD LOCATION:	SHEET	
FIRST USED ON OPTION/MODEL:		05-JUL-84 12:28	D-DD-MB54-0	1 OF 2	
SIZE	CODE	NUMBER	REV.		
D	CS	M854-0-RES	A		

8	7	6	5	4	3	2	1																
<table border="1"> <tr> <td>538-0-456W</td> <td>CS</td> <td>D</td> <td>3003</td> <td>3215</td> </tr> <tr> <td>REV. 1.0</td> <td>REV. 1.0</td> <td>REV. 1.0</td> <td>REV. 1.0</td> <td>REV. 1.0</td> </tr> </table>								538-0-456W	CS	D	3003	3215	REV. 1.0	REV. 1.0	REV. 1.0	REV. 1.0	REV. 1.0						
538-0-456W	CS	D	3003	3215																			
REV. 1.0	REV. 1.0	REV. 1.0	REV. 1.0	REV. 1.0																			
RESISTOR LOC(PIN)	SHOWN ON DRW# REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW# REF	VALUE	TERMINATES SIGNAL																
R215(1)	PMA1 B6	68Ω	-PMA1 VA 18 H	R136(1)	PMA4 D7	68Ω	PMA4 ERA 14 H																
R42(1)	PMA1 A6	68Ω	PMA1 VA 19 H	R250(1)	PMA4 D7	68Ω	PMA4 ERA 15 H																
R107(1)	PMA1 A6	68Ω	-PMA1 VA 19-21=7 H	R242(1)	PMA4 D7	68Ω	PMA4 ERA 16 H																
R46(1)	PMA1 A6	68Ω	PMA1 VA 20 H	R227(1)	PMA4 D7	68Ω	PMA4 ERA 17 H																
R50(1)	PMA1 A6	68Ω	PMA1 VA 21 H	R172(1)	PMA4 D6	68Ω	PMA4 ERA 18 H																
R256(1)	PMA1 B5	68Ω	PMA1 VA 22 H	R235(1)	PMA4 D6	68Ω	PMA4 ERA 19 H																
R222(1)	PMA1 B5	68Ω	PMA1 VA 23 H	R82(1)	PMA4 D6	68Ω	PMA4 ERA 20 H																
R87(1)	PMA1 B4	68Ω	PMA1 VA 24 H	R185(1)	PMA4 D6	68Ω	PMA4 ERA 21 H																
R131(1)	PMA1 A4	68Ω	PMA1 VA 25 H	R134(1)	PMA4 D5	68Ω	PMA4 ERA 22 H																
R265(1)	PMA1 A4	68Ω	PMA1 VA 26 H	R22(1)	PMA4 D5	68Ω	PMA4 ERA 23 H																
R246(1)	PMA2 D6	68Ω	PMA2 CCA 15 H	R95(1)	PMA4 D5	68Ω	PMA4 ERA 24 H																
R240(1)	PMA2 D6	68Ω	PMA2 CCA 16 H	R200(1)	PMA4 D5	68Ω	PMA4 ERA 25 H																
R229(1)	PMA2 D6	68Ω	PMA2 CCA 17 H	R139(1)	PMA4 D4	68Ω	PMA4 ERA 26 H																
R174(1)	PMA2 D6	68Ω	PMA2 CCA 18 H	R61(1)	PMA4 D4	68Ω	PMA4 ERA 27 H																
R236(1)	PMA2 D5	68Ω	PMA2 CCA 19 H	R39(1)	PMA4 D4	68Ω	PMA4 ERA 28 H																
R20(1)	PMA2 D5	68Ω	PMA2 CCA 20 H	R43(1)	PMA4 D4	68Ω	PMA4 ERA 29 H																
R184(1)	PMA2 D5	68Ω	PMA2 CCA 21 H	R47(1)	PMA4 D3	68Ω	PMA4 ERA 30 H																
R132(1)	PMA2 D5	68Ω	PMA2 CCA 22 H	R51(1)	PMA4 D3	68Ω	PMA4 ERA 31 H																
R24(1)	PMA2 D3	68Ω	PMA2 CCA 23 H	R257(1)	PMA4 D3	68Ω	PMA4 ERA 32 H																
R94(1)	PMA2 D3	68Ω	PMA2 CCA 24 H	R221(1)	PMA4 D3	68Ω	PMA4 ERA 33 H																
R199(1)	PMA2 D3	68Ω	PMA2 CCA 25 H	R116(1)	PMA4 D1	68Ω	PMA4 ERA 34 H																
R194(1)	PMA2 B6	68Ω	PMA2 CCA 26 H	R108(1)	PMA4 D1	68Ω	PMA4 ERA 35 H																
R58(1)	PMA2 B6	68Ω	PMA2 CCA 27 H	R181(1)	PMA5 D2	68Ω	PMA5 14-26 SEL 1 H																
R36(1)	PMA2 B6	68Ω	PMA2 CCA 28 H	R271(1)	PMA5 D2	68Ω	-PMA5 14-26 SEL 1 H																
R37(1)	PMA2 B6	68Ω	PMA2 CCA 29 H	R180(1)	PMA5 D2	68Ω	PMA5 14-26 SEL 2 H																
R31(1)	PMA2 B5	68Ω	PMA2 CCA 30 H	R270(1)	PMA5 D2	68Ω	-PMA5 14-26 SEL 2 H																
R25(1)	PMA2 B5	68Ω	PMA2 CCA 31 H	R179(1)	PMA5 D2	68Ω	PMA5 14-26 SEL 4 H																
R26(1)	PMA2 B5	68Ω	PMA2 CCA 32 H	R269(1)	PMA5 D2	68Ω	-PMA5 14-26 SEL 4 H																
R27(1)	PMA2 B5	68Ω	PMA2 CCA 33 H	R62(1)	PMA5 C3	68Ω	PMA5 27 SEL 1 H																
R113(1)	PMA2 B3	68Ω	PMA2 CCA 34 H	R53(1)	PMA5 C3	68Ω	PMA5 27 SEL 2 H																
R109(1)	PMA2 B3	68Ω	PMA2 CCA 35 H	R57(1)	PMA5 D3	68Ω	PMA5 27 SEL 4 H																
R74(1)	PMA2 C7	68Ω	PMA2 CCA LOAD H	R40(1)	PMA5 A3	68Ω	PMA5 28-31 SEL 1 H																
R4(1)	PMA2 C6	68Ω	-PMA2 CCA LOAD H	R98(1)	PMA5 C3	68Ω	PMA5 28-31 SEL 2 H																
R142(1)	PMA2 C2	68Ω	PMA2 CLOCK A H	R35(1)	PMA5 C3	68Ω	PMA5 28-31 SEL 4 H																
R56(1)	PMA2 C2	68Ω	PMA2 CLOCK B H	R220(1)	PMA5 A3	68Ω	PMA5 32-33 SEL 1 H																
R63(1)	PMA2 C2	68Ω	PMA2 CLOCK C H	R258(1)	PMA5 A3	68Ω	PMA5 32-33 SEL 2 H																
R16(1)	PMA2 C2	68Ω	PMA2 CLOCK D H	R259(1)	PMA5 A3	68Ω	PMA5 32-33 SEL 4 H																
R121(1)	PMA2 C2	68Ω	PMA2 CLOCK E H	R117(1)	PMA5 B2	68Ω	PMA5 34-35 SEL 1 H																
R2(1)	PMA2 C2	68Ω	PMA2 CLOCK F H	R118(1)	PMA5 B2	68Ω	PMA5 34-35 SEL 2 H																
R268(1)	PMA2 C6	68Ω	PMA2 HI	R112(1)	PMA5 B2	68Ω	PMA5 34-35 SEL 4 H																
<p>NOTE:</p> <ol style="list-style-type: none"> ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% TOLERANCE UNLESS OTHERWISE SPECIFIED ENTRIES ARE SORTED BY SIGNAL NAME % INDICATES OUTPUT OF DIP LOC AND (<) INDICATES PIN NUMBER 																							
<table border="1"> <tr> <td>DATE: 05-11-84</td> <td>ENG: C. KACZOR</td> <td>DATE: 05-11-84</td> <td>TITLE: PHYSICAL MEMORY ADDRESS--TERMINATORS</td> </tr> <tr> <td>CHK: D</td> <td>D. DELLORCO</td> <td>DATE: 05-11-84</td> <td>BOARD LOCATION: 4AF29</td> </tr> <tr> <td colspan="2">FIRST USED ON OPTION/MODEL: MCA25</td> <td colspan="2">NEXT HIGHER ASSEMBLY: D-DD-M854-0</td> </tr> <tr> <td>SIZE CODE: D CS</td> <td>NUMBER: M854-0-RES</td> <td>REV: A</td> <td>MRO</td> </tr> </table>								DATE: 05-11-84	ENG: C. KACZOR	DATE: 05-11-84	TITLE: PHYSICAL MEMORY ADDRESS--TERMINATORS	CHK: D	D. DELLORCO	DATE: 05-11-84	BOARD LOCATION: 4AF29	FIRST USED ON OPTION/MODEL: MCA25		NEXT HIGHER ASSEMBLY: D-DD-M854-0		SIZE CODE: D CS	NUMBER: M854-0-RES	REV: A	MRO
DATE: 05-11-84	ENG: C. KACZOR	DATE: 05-11-84	TITLE: PHYSICAL MEMORY ADDRESS--TERMINATORS																				
CHK: D	D. DELLORCO	DATE: 05-11-84	BOARD LOCATION: 4AF29																				
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SIZE CODE: D CS	NUMBER: M854-0-RES	REV: A	MRO																				

DRAWING NUMBER	NO. OF SHT	PART NUMBER	DESCRIPTION	REVISION
		M855-00	MODULE REVISION	A1
E-UA-M855-0-0	1		PAG	A1
K-PL-M855-0-DBP	2		PART LIST, M855	A1
D-CS-M855-0-PAG1	1		PAGE TABLE DATA PT ACCESS-PT 17	A1
D-CS-M855-0-PAG2	1		PAGE TABLE DATA PT 16 - PT 26	A1
D-CS-M855-0-PAG3	1		PAGE TABLE DIRECTORY	A1
D-CS-M855-0-PAG4	1		PAGE TABLE CONTROL LOGIC	A1
D-CS-M855-0-PAG5	1		PAGE TABLE PARITY LOGIC	A1
D-CS-M855-0-PAG6	1		PAGING KEEP BIT SELECTION	A1
D-CS-M855-0-PAG7	1		PAGE TABLE DATA PT 20 - 25	A1
D-CS-M855-0-PAG8	1		PAGE TABLE VALID BIT SELECTION	A1
D-CS-M855-0-PAG9	1		PAGE TABLE 1 DIRECTORY	A1
D-CS-M855-0-PAGA	1		PAGING BOARD POWER, GND, CAP	A1
D-CS-M855-0-PAGB	1		PAGING BOARD GND	A1
D-CS-M855-0-RES	3		PAG TERMINATORS	A1
K-PC-M855-0-DBM	-		MULTIWIRE DESIGN DATA BASE TAPE	A1
D-DD-5017661-0	2		DRAWING DIRECTORY, 5017661	REF

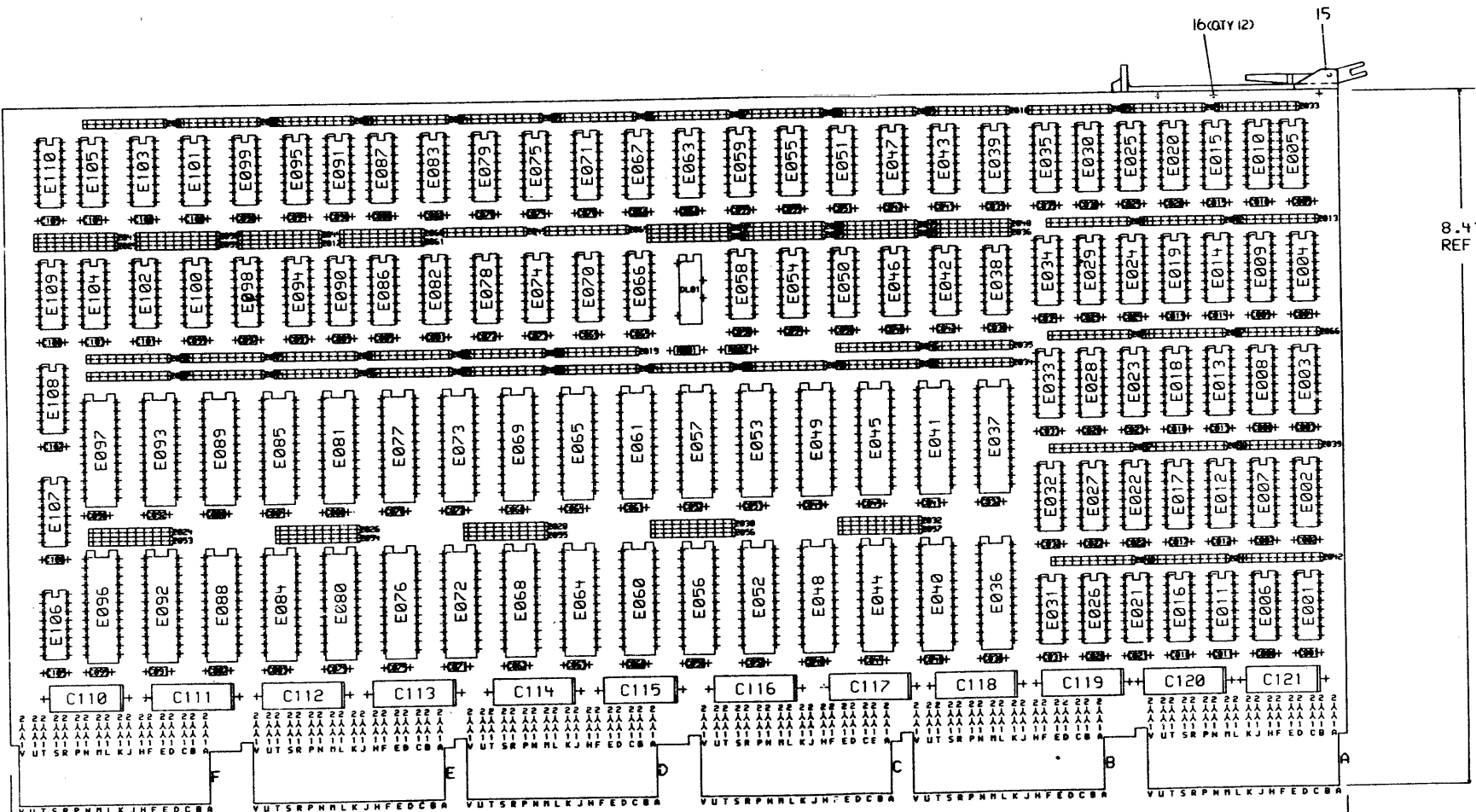
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. D. DELLORCO	DATE 19-JUL-84	ENG. C. KACZOR	DATE 19-JUL-84	TITLE: M855
	CHK'D. D. DELLORCO	DATE 19-JUL-84	BOARD LOCATION: N/A	SHEET 1 OF 2	DRAWING DIRECTORY
DSK: M855A1.T2P[4,21]		19-JUL-84 16:44	NEXT HIGHER ASSEMBLY: KL10-PW	SIZE CODE D DD	NUMBER M855-0
FIRST USED ON OPTION/MODEL: MCA25				REV. A	

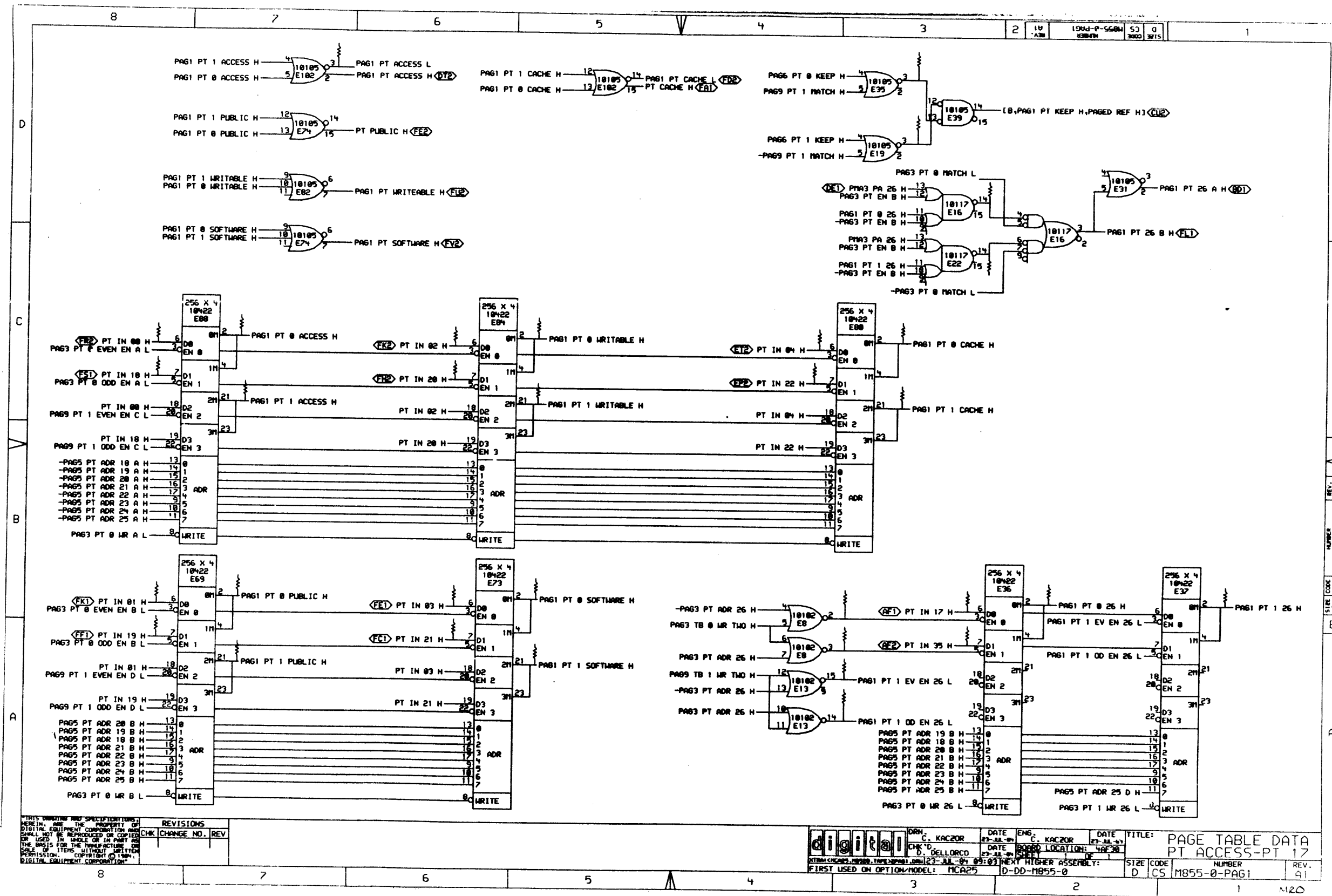
MRO 1

NOTES: 1. THIS BOARD MUST MEET SAFETY REQ. FOR HPWR.
 2. SPARE COMPONENT LOCATIONS ARE: E12, E43, E103, E105.



COMPONENT SIDE
 15.688
 REF

DPN P. LASSEN	DATE 08MAR84	TITLE digital
CHK'D D. LASSEN	DATE 14 Jul 84	PAC
DES. CHG. C. LASSEN	DATE 7-22-84	DOCUMENT NUMBER
RESP. ENG. C. LASSEN	DATE 7-25-84	REV E UA MB55-0 0
DATE 26 July 84	SCALE 2 / 1	SHEET 1 of 1
NEXT HIGHER DOC. 0-00-M855-0		

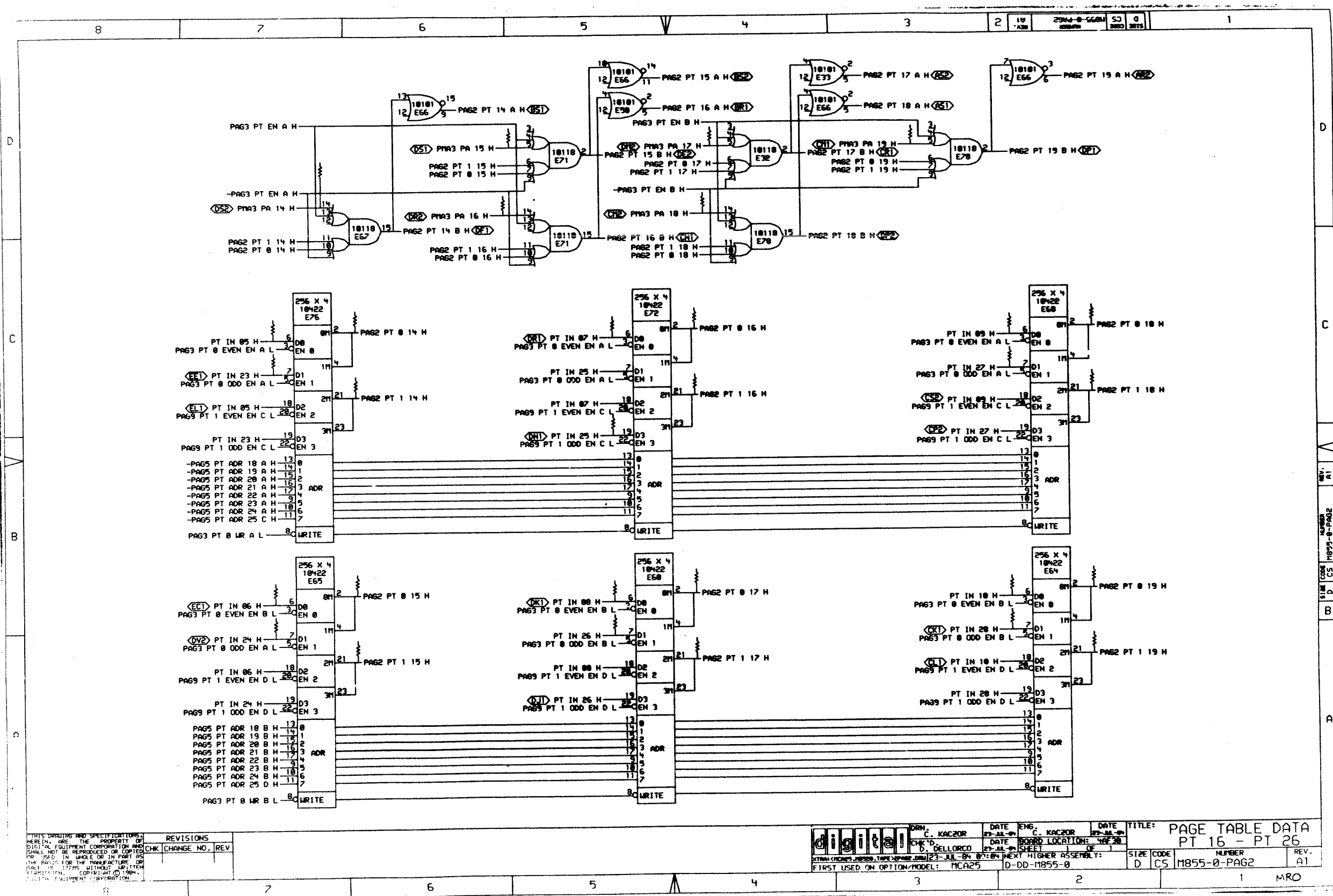


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REVISIONS	
CHK	CHANGE NO. REV

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CHK'D: D. DELLORCO	DATE: 03-28-74	DESIGN LOCATION: 44F30		
FIRST USED ON OPTION MODEL: MCA25				

REV. A1
 NUMBER M855-0-PAG1
 SIZE CODE CS
 D CS
 M20



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REVISIONS	
CHK	CHANGE NO. REV

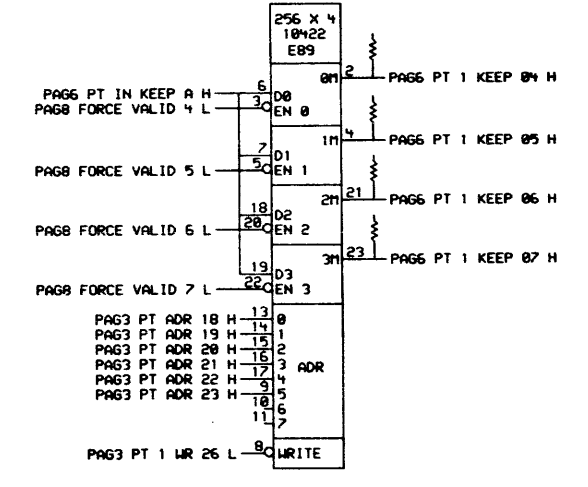
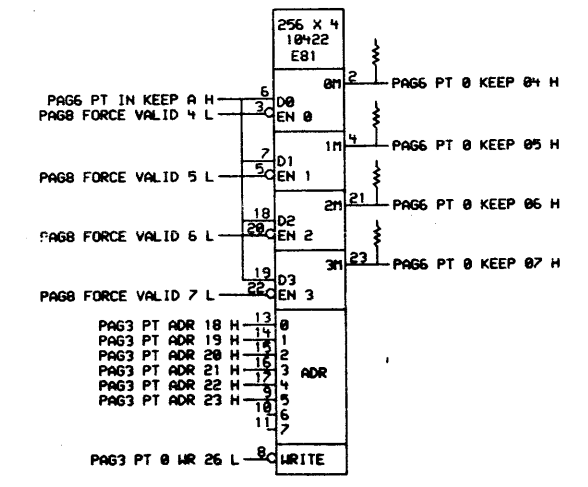
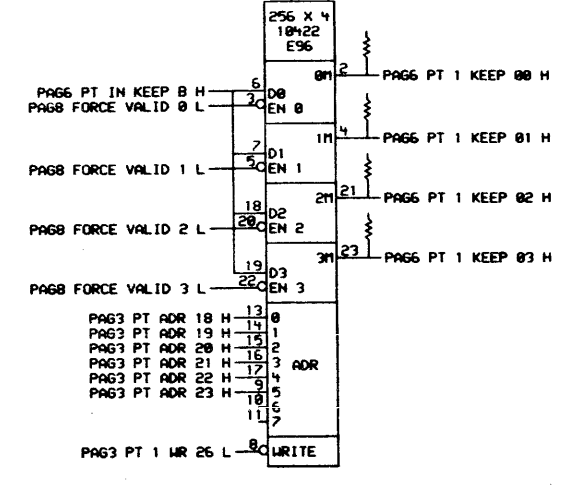
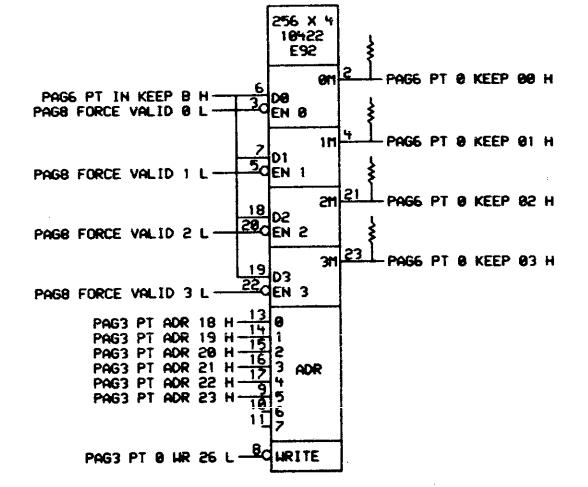
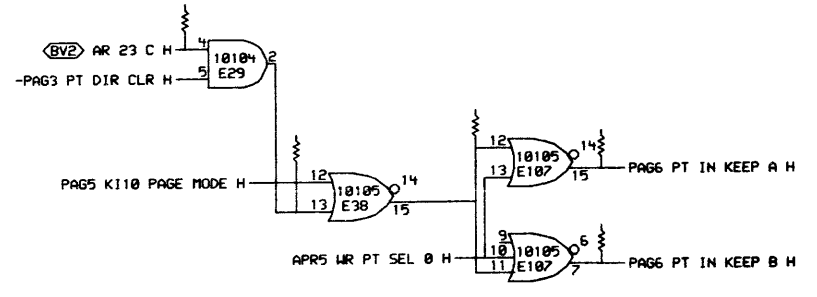
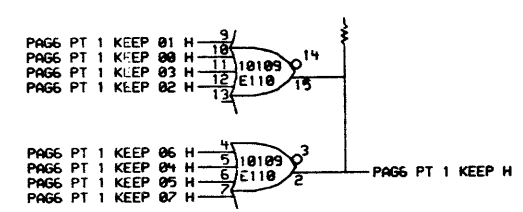
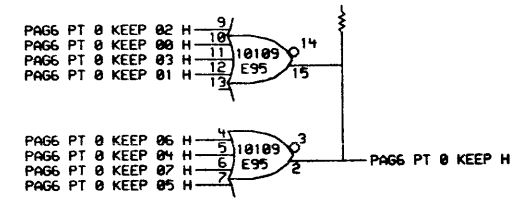
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NEXT HIGHER ASSEMBLY: D-DD-M855-0					SIZE CODE: D CS
FIRST USED ON OPTION MODEL: MCA25					NUMBER: M855-0-PAG2

REV. A1
 NUMBER M855-0-PAG2
 CODE D CS
 SIZE D CS

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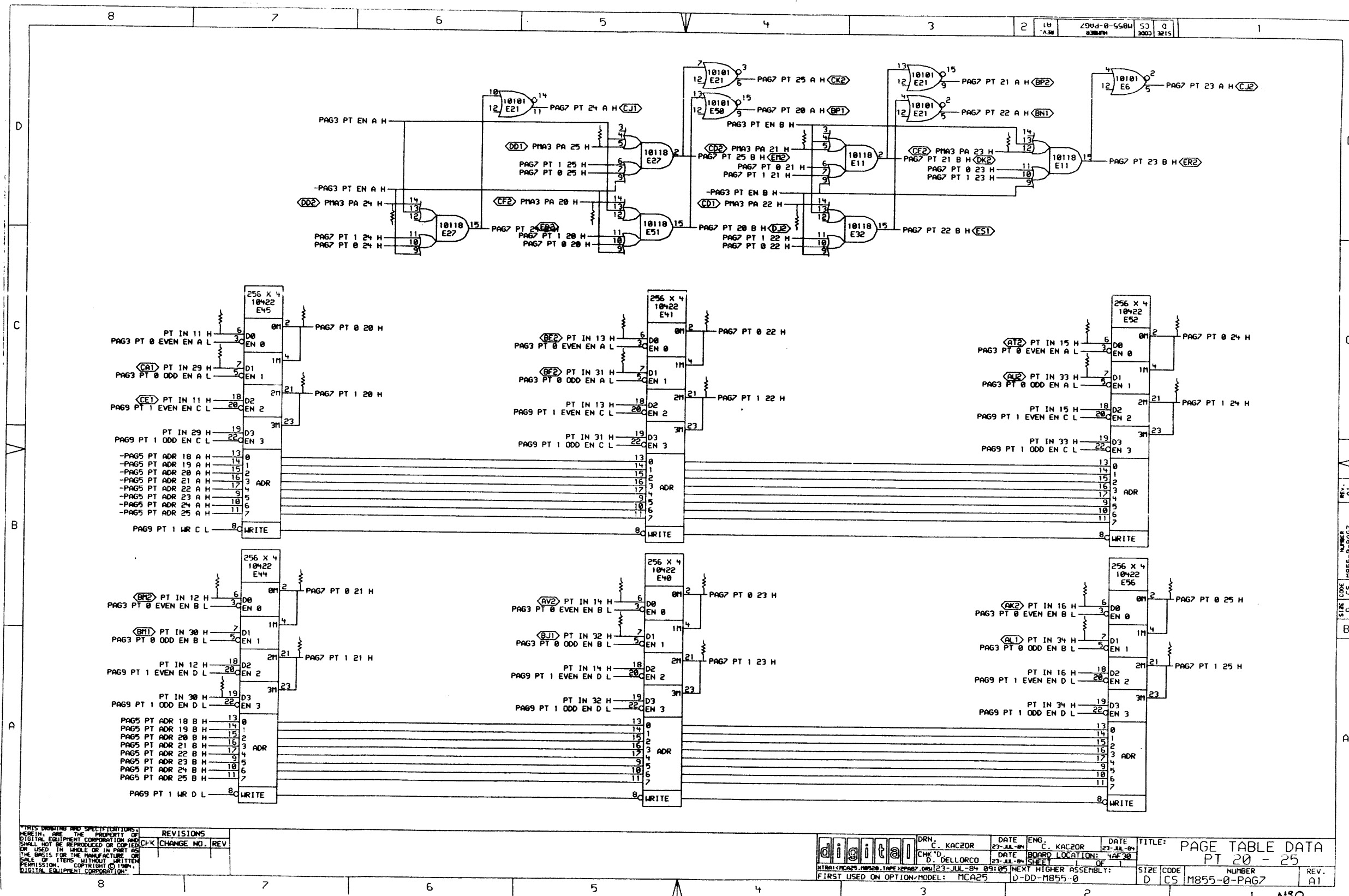
D
 C
 B
 A

8 7 6 5 4 3 2 1 MRO



REVISIONS	CHK	CHANGE NO.	REV

digital	DRN. C. KACZOR	DATE 23-JUL-84	ENG. C. KACZOR	DATE 23-JUL-84	TITLE: PAGING
	CHK'D. D. DELLORCO	DATE 23-JUL-84	BOARD LOCATION: 4A438	SHEET 1 OF 1	KEEP BIT SELECTION
FIRST USED ON OPTION/MODEL: MCA25					SIZE CODE NUMBER REV.
D-DD-M855-0					D CS M855-0-PAG6 A1

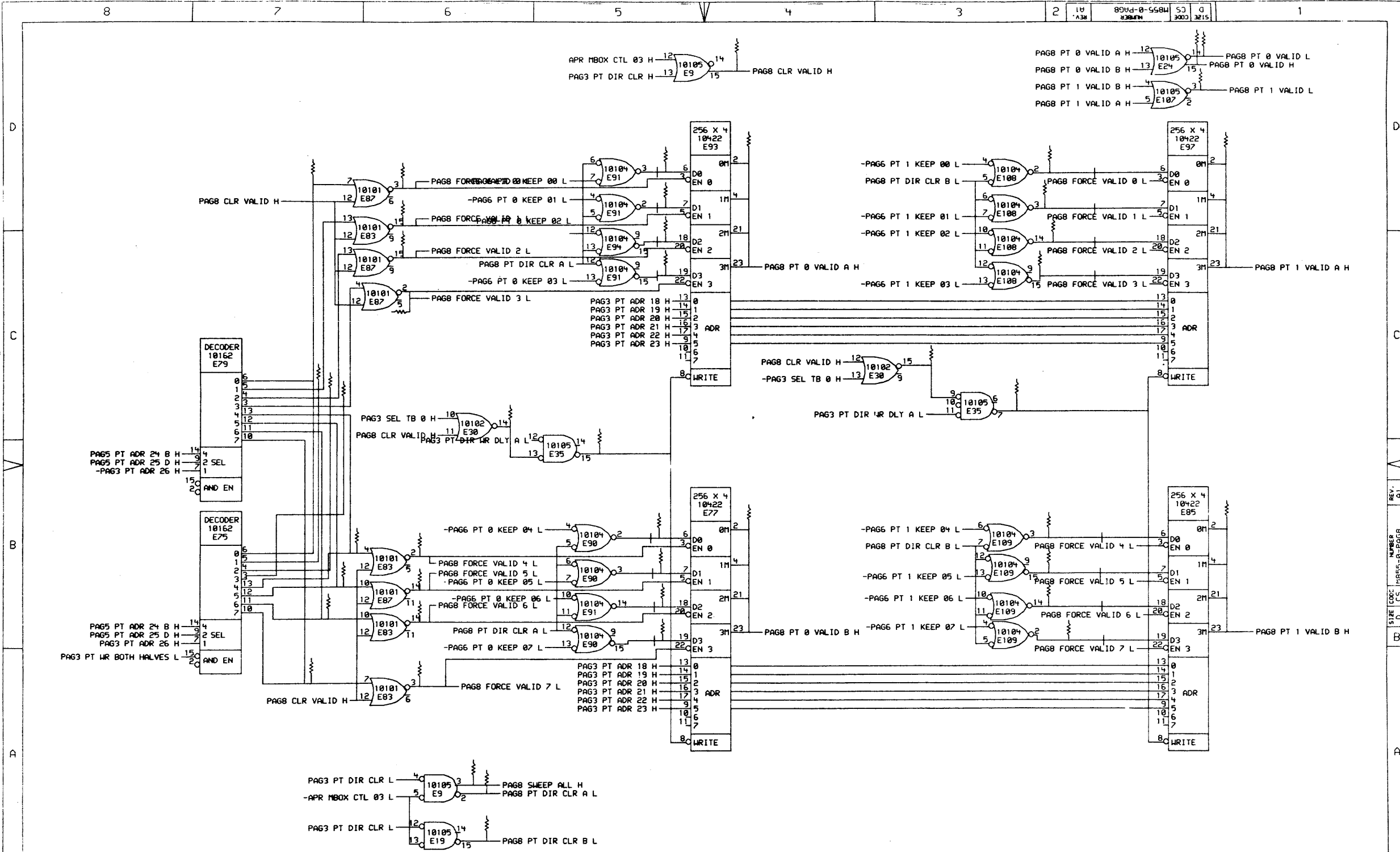


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REV.	CHANGE NO.	REV.
1		

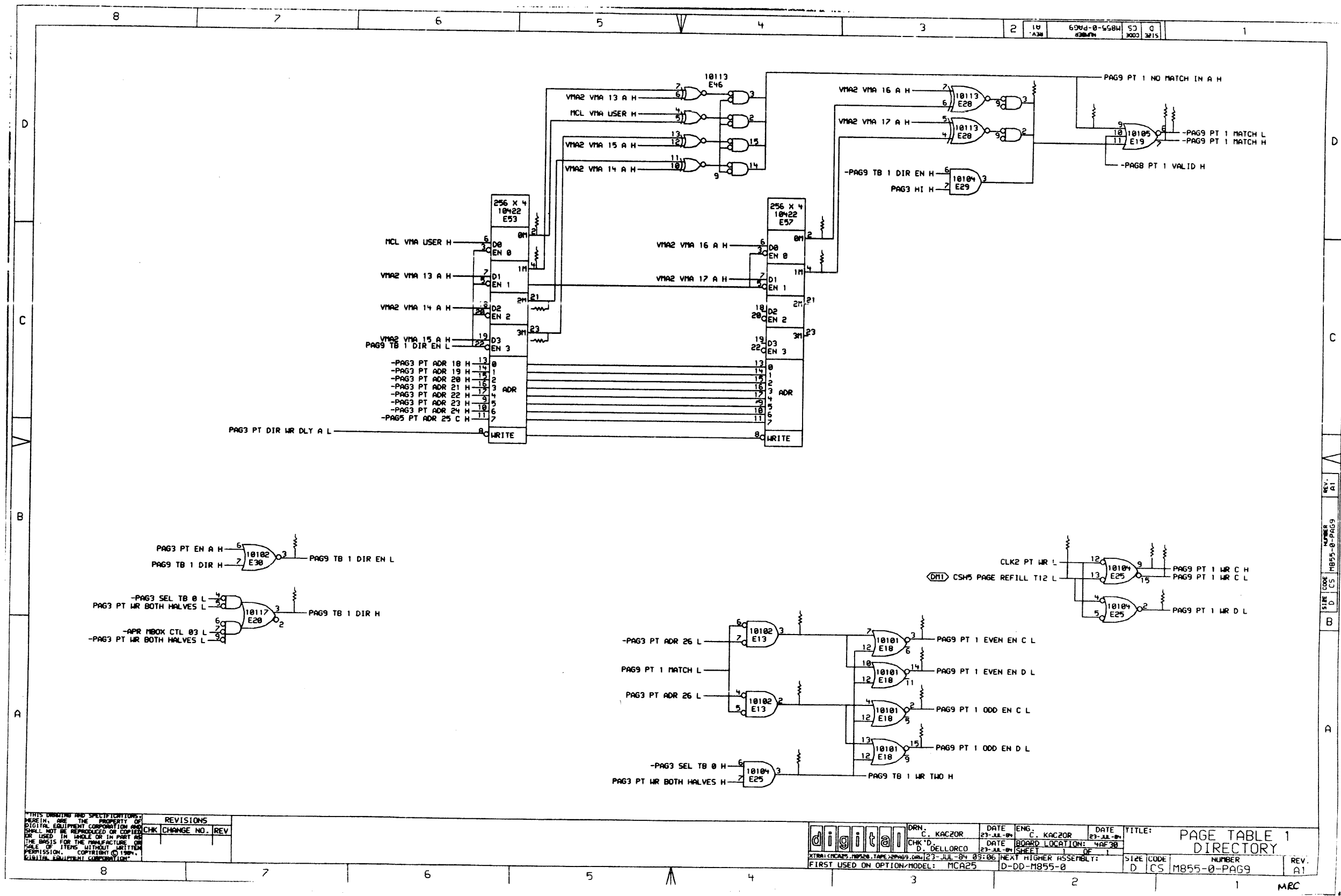
	DRN. C. KACZOR	DATE 27-JUL-84	ENG. C. KACZOR	DATE 27-JUL-84	TITLE: PAGE TABLE DATA
	CHK'D D. DELLORCO	DATE 27-JUL-84	BOARD LOCATION: 4AF20	SHEET 1 OF 1	PT 20 - 25
FIRST USED ON OPTION/MODEL: MCA25					SIZE CODE D CS NUMBER M855-0-PAG7 REV. A1

SIZE CODE D CS
 NUMBER M855-0-PAG7
 REV. A1



REVISIONS	
CHK	CHANGE NO. REV.

	DRN: C. KACZOR	DATE: 22-11-84	ENG: C. KACZOR	DATE: 22-11-84	TITLE: PAGE TABLE
	CHK'D: D. DELLORCO	DATE: 23-JUL-84 09:06	BOARD LOCATION: 4AF38	SHEET: 1 OF 1	VALID BIT SELECTION
FIRST USED ON OPTION/MODEL: MCA25 D-DD-M855-0					SIZE: D CODE: CS NUMBER: M855-0-PAG8 REV: A1

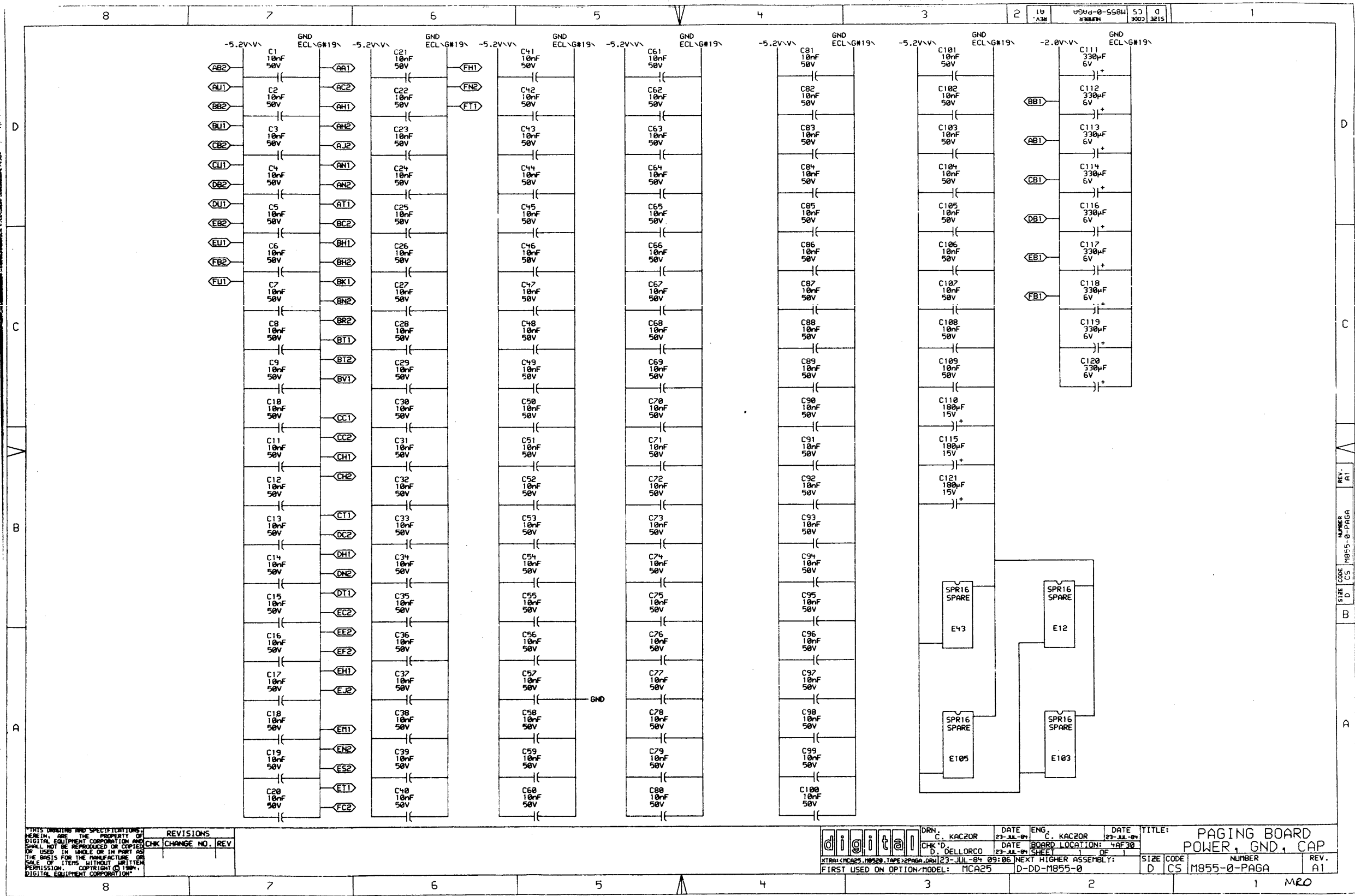


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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN:	C. KACZOR	DATE:	23-JUL-84	ENG:	C. KACZOR	DATE:	23-JUL-84	TITLE:	PAGE TABLE 1			
	CHK'D:	D. DELLORCO	DATE:	23-JUL-84	BOARD LOCATION:	4AF30	SHEET:	1	OF:	1			
FIRST USED ON OPTION/MODEL:		MCA25	NEXT HIGHER ASSEMBLY:		D-DD-M855-0	SIZE:	D	CODE:	CS	NUMBER:	M855-0-PAG9	REV:	A1

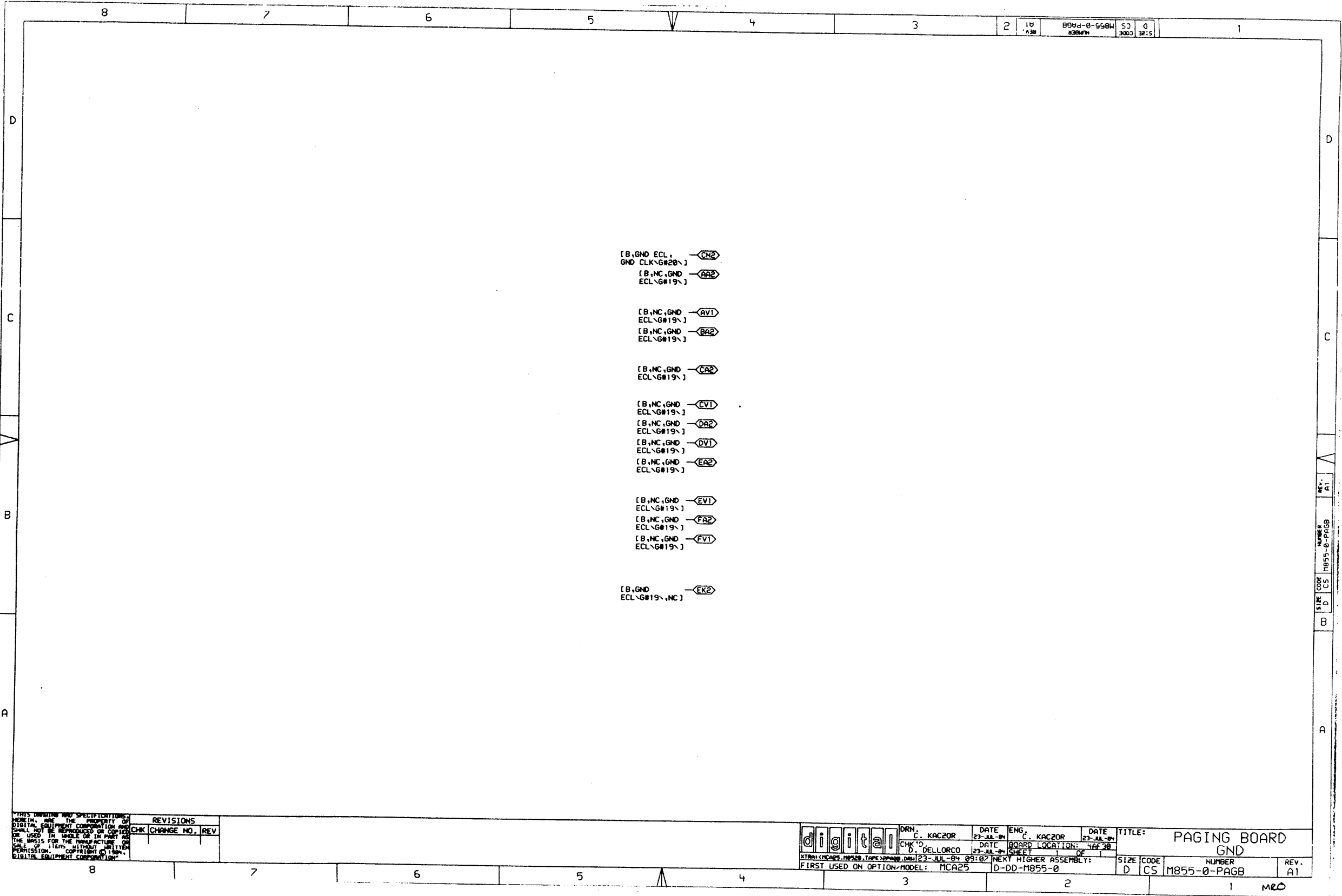
REV. A1
D CS M855-0-PAG9
MRC



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN: C. KACZOR	DATE: 23-JUL-84	ENG: C. KACZOR	DATE: 23-JUL-84	TITLE: PAGING BOARD
	CHK'D: D. DELLORCO	DATE: 23-JUL-84	BOARD LOCATION: 4AF30	SHEET: 1 OF 1	POWER, GND, CAP
FIRST USED ON OPTION/MODEL: MCA25			NEXT HIGHER ASSEMBLY: D-DD-M855-0		SIZE CODE: D CS
NUMBER: M855-0-PAGA				REV: A1	MRO



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. C. KACZOR	DATE 23-JUL-84	ENG. C. KACZOR	DATE 23-JUL-84	TITLE: PAGING BOARD GND
	CHK'D D. DELLORCO	DATE 23-JUL-84	SHEET 1 OF 1	BOARD LOCATION: 4AF30	
FIRST USED ON OPTION/MODEL: MCA25		NEXT HIGHER ASSEMBLY: 0-DD-M855-0		SIZE CODE D CS	NUMBER M855-0-PAGB
					REV. A1

MEO

8				7				6				5				4				3				2				1			
RESISTOR LOC(PIN)	SHOWN DR#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DR#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DR#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DR#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DR#	ON REF	VALUE	TERMINATES SIGNAL							
212(3)	PAG3	C2	68Ω	%E10K(3)	223(5)	PAG9	C5	68Ω	%E53K(2)	238(6)	PAG5	B4	68Ω	-CLK2 PT WR H	247(10)	PAG2	C6	68Ω	PAG2 PT 0 14 H												
253(8)	PAG4	C2	68Ω	%E10K(6)	223(1)	PAG9	C5	68Ω	%E53K(21)	211(3)	PAG3	B5	68Ω	-CON K110 PAGING MODE H	265(8)	PAG2	B6	68Ω	PAG2 PT 0 15 H												
259(3)	PAG4	A3	68Ω	%E10K(15)	223(3)	PAG9	C5	68Ω	%E53K(23)	231(8)	PAG9	B2	68Ω	CSH1 PRGRF CYC A H	265(6)	PAG2	C4	68Ω	PAG2 PT 0 16 H												
252(5)	PAG8	C2	68Ω	%E100K(14)	246(6)	PAG9	C5	68Ω	%E53K(4)	27(3)	PAG3	C7	68Ω	-CSH6 PAGE REFILL T12 H	232(8)	PAG2	B4	68Ω	PAG2 PT 0 17 H												
227(6)	PAG8	C3	68Ω	%E100K(15)	234(3)	PAG9	C5	68Ω	%E57K(2)	237(3)	PAG3	B5	68Ω	-CSH6 MBOX PT DIR WR H	250(5)	PAG2	C2	68Ω	PAG2 PT 0 18 H												
227(10)	PAG8	D2	68Ω	%E100K(2)	235(5)	PAG9	C4	68Ω	%E57K(4)	250(10)	PAG2	B2	68Ω	-CSH6 PAGE FAIL HOLD H	27(8)	PAG2	C6	68Ω	PAG2 PT 0 19 H												
252(8)	PAG8	D2	68Ω	%E100K(3)	249(5)	PAG4	C5	68Ω	%E59K(2)	258(1)	PAG4	C2	68Ω	CSH6 PAGE REFILL ERROR H	265(10)	PAG2	A6	68Ω	PAG2 PT 1 14 H												
216(5)	PAG8	B2	68Ω	%E109K(14)	235(6)	PAG3	D4	68Ω	%E61K(2)	258(3)	PAG4	C4	68Ω	-CSH6 PAGE REFILL ERROR H	26(6)	PAG2	C4	68Ω	PAG2 PT 1 15 H												
216(8)	PAG8	B2	68Ω	%E109K(15)	234(1)	PAG3	C4	68Ω	%E61K(4)	239(1)	PAG5	D8	68Ω	MB 00-05 PAR ODD H	232(1)	PAG2	A4	68Ω	PAG2 PT 1 16 H												
216(6)	PAG8	B2	68Ω	%E109K(2)	27(6)	PAG4	B7	68Ω	%E63K(15)	239(3)	PAG5	D7	68Ω	MB 06-11 PAR ODD H	250(3)	PAG2	C2	68Ω	PAG2 PT 1 17 H												
251(8)	PAG8	B2	68Ω	%E109K(3)	261(3)	PAG4	B6	68Ω	%E63K(2)	239(5)	PAG5	D8	68Ω	MB 12-17 PAR ODD H	250(8)	PAG2	A2	68Ω	PAG2 PT 1 18 H												
267(10)	PAG9	A4	68Ω	%E13K(2)	238(3)	PAG5	C7	68Ω	%E7(10)	238(5)	PAG5	C8	68Ω	MB 18-23 PAR ODD H	271(10)	PAG3	A7	68Ω	PAG3 HI H												
271(5)	PAG9	A4	68Ω	%E13K(3)	221(1)	PAG5	C3	68Ω	%E70K(2)	239(10)	PAG5	C7	68Ω	MB 24-29 PAR ODD H	214(5)	PAG3	B2	68Ω	-PAG3 PT 0 EVEN EN A H												
241(10)	PAG1	D3	68Ω	%E16K(14)	260(3)	PAG8	A7	68Ω	%E75K(10)	242(1)	PAG5	C7	68Ω	MB 30-35 PAR ODD H	263(1)	PAG3	B2	68Ω	-PAG3 PT 0 EVEN EN B H												
230(3)	PAG5	B3	68Ω	%E17K(15)	245(10)	PAG8	B7	68Ω	%E75K(11)	251(5)	PAG5	A5	68Ω	MB SEL 1 H	213(6)	PAG3	C4	68Ω	-PAG3 PT 0 LRU H												
256(1)	PAG5	B3	68Ω	%E17K(2)	260(5)	PAG8	B7	68Ω	%E75K(12)	217(8)	PAG3	A5	68Ω	MB SEL 2 H	244(8)	PAG3	D1	68Ω	PAG3 PT 0 MATCH H												
210(3)	PAG1	D3	68Ω	%E19K(3)	24(5)	PAG8	B6	68Ω	%E75K(13)	271(8)	PAG4	D8	68Ω	MCL VMA USER H	240(1)	PAG3	D1	68Ω	-PAG3 PT 0 MATCH H												
230(1)	PAG5	C7	68Ω	%E2K(10)	24(10)	PAG8	C7	68Ω	%E75K(3)	269(3)	PAG3	B7	68Ω	MCL2 VMA EXEC H	266(10)	PAG3	D2	68Ω	PAG3 PT 0 NO MATCH IN A H												
239(6)	PAG5	D7	68Ω	%E2K(13)	24(8)	PAG8	C7	68Ω	%E75K(4)	27(1)	PAG4	A4	68Ω	-MCL2 VMA WRITE H	215(3)	PAG3	A2	68Ω	-PAG3 PT 0 ODD EN A H												
239(8)	PAG5	D7	68Ω	%E2K(3)	24(1)	PAG8	D7	68Ω	%E75K(5)	224(6)	PAG4	C2	68Ω	MCL3 PAGE ADDRESS COND H	263(3)	PAG3	A2	68Ω	-PAG3 PT 0 ODD EN B H												
240(3)	PAG1	C3	68Ω	%E22K(14)	260(6)	PAG8	D7	68Ω	%E75K(6)	29(5)	PAG4	D7	68Ω	MCL3 PAGE ILL ENTRY H	216(3)	PAG3	D2	68Ω	-PAG3 PT 0 WR 26 H												
266(6)	PAG3	D2	68Ω	%E23K(3)	266(1)	PAG3	B3	68Ω	%E8K(14)	211(6)	PAG4	C4	68Ω	-MCL3 PAGE ILL ENTRY H	214(1)	PAG3	B1	68Ω	-PAG3 PT 0 WR A H												
269(5)	PAG5	C2	68Ω	%E26K(2)	266(5)	PAG3	A3	68Ω	%E8K(15)	236(6)	PAG4	B6	68Ω	MCL3 PAGE TEST PRIVATE H	263(5)	PAG3	B1	68Ω	-PAG3 PT 0 WR B H												
212(5)	PAG9	D2	68Ω	%E20K(2)	257(3)	PAG1	B3	68Ω	%E8K(2)	253(10)	PAG4	C2	68Ω	MCL5 VMA ADR ERR H	253(6)	PAG3	C2	68Ω	-PAG3 PT 1 WR 26 H												
240(5)	PAG5	D3	68Ω	%E29K(14)	257(5)	PAG1	A3	68Ω	%E8K(3)	240(1)	PAG4	D7	68Ω	MCL6 PAGE LIEBR REF H	214(6)	PAG3	D7	68Ω	PAG3 PT ADR 18 H												
236(3)	PAG6	B7	68Ω	%E29K(2)	244(10)	PAG3	A7	68Ω	%E86K(2)	241(8)	PAG1	B2	68Ω	PAG1 PT 0 26 H	230(10)	PAG3	??	68Ω	-PAG3 PT ADR 18 H												
229(8)	PAG8	C6	68Ω	%E30K(14)	217(10)	PAG3	A7	68Ω	%E86K(5)	243(1)	PAG1	C7	68Ω	PAG1 PT 0 ACCESS H	224(3)	PAG3	B7	68Ω	PAG3 PT ADR 19 H												
211(10)	PAG8	C3	68Ω	%E30K(15)	263(6)	PAG8	B5	68Ω	%E90K(15)	259(8)	PAG1	C3	68Ω	PAG1 PT 0 CACHE H	230(8)	PAG3	B7	68Ω	-PAG3 PT ADR 19 H												
22(8)	PAG4	C4	68Ω	%E34K(15)	263(8)	PAG8	B5	68Ω	%E90K(2)	260(3)	PAG1	B7	68Ω	PAG1 PT 0 PUBLIC H	214(8)	PAG3	B7	68Ω	PAG3 PT ADR 20 H												
260(1)	PAG4	C7	68Ω	%E34K(3)	263(10)	PAG8	B5	68Ω	%E90K(3)	219(10)	PAG1	B5	68Ω	PAG1 PT 0 SOFTWARE H	220(6)	PAG3	B7	68Ω	-PAG3 PT ADR 20 H												
227(1)	PAG8	B5	68Ω	%E35K(15)	262(8)	PAG8	B5	68Ω	%E91K(14)	262(5)	PAG1	C5	68Ω	PAG1 PT 0 WRITABLE H	215(8)	PAG3	D7	68Ω	PAG3 PT ADR 21 H												
210(1)	PAG1	D3	68Ω	%E35K(3)	215(10)	PAG8	C5	68Ω	%E91K(15)	237(1)	PAG1	B1	68Ω	PAG1 PT 1 26 H	264(1)	PAG3	D7	68Ω	-PAG3 PT ADR 21 H												
227(8)	PAG8	C3	68Ω	%E35K(7)	227(3)	PAG8	D5	68Ω	%E91K(2)	259(10)	PAG1	C7	68Ω	PAG1 PT 1 ACCESS H	227(5)	PAG3	D7	68Ω	PAG3 PT ADR 22 H												
224(10)	PAG6	B7	68Ω	%E38K(15)	252(3)	PAG8	D5	68Ω	%E91K(3)	259(6)	PAG1	C3	68Ω	PAG1 PT 1 CACHE H	220(8)	PAG3	D7	68Ω	-PAG3 PT ADR 22 H												
230(10)	PAG5	B4	68Ω	%E4K(2)	214(10)	PAG8	C5	68Ω	%E9K(15)	235(10)	PAG1	A3	68Ω	-PAG1 PT 1 EV EN 26 H	253(5)	PAG3	C7	68Ω	PAG3 PT ADR 23 H												
29(6)	PAG4	D4	68Ω	%E42K(2)	211(5)	PAG3	B7	68Ω	APR5 WR PT SEL 0 H	246(1)	PAG1	A3	68Ω	-PAG1 PT 1 00 EN 26 H	264(5)	PAG3	C7	68Ω	-PAG3 PT ADR 23 H												
235(1)	PAG3	D5	68Ω	%E49K(2)	251(1)	PAG3	B7	68Ω	APR5 WR PT SEL 1 H	260(6)	PAG1	A7	68Ω	PAG1 PT 1 PUBLIC H	246(8)	PAG3	A5	68Ω	PAG3 PT ADR 24 H												
235(3)	PAG3	C5	68Ω	%E49K(21)	237(5)	PAG3	B5	68Ω	-APR5 WR PT SEL 1 H	219(8)	PAG1	A5	68Ω	PAG1 PT 1 SOFTWARE H	270(5)	PAG3	A5	68Ω	-PAG3 PT ADR 24 H												
271(1)	PAG3	C5	68Ω	%E49K(23)	236(1)	PAG6	B7	68Ω	AR 23 C H	262(3)	PAG1	C5	68Ω	PAG1 PT 1 WRITABLE H	22(3)	PAG5	B6	68Ω	PAG3 PT ADR 25 A IN H												
271(6)	PAG3	C5	68Ω	%E49K(4)	247(1)	PAG3	C7	68Ω	-CLK2 PT DIR WR H	243(3)	PAG1	D7	68Ω	-PAG1 PT ACCESS H																	

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND (<) INDICATES PIN NUMBER

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	CHK	CHANGE NO. REV

digital	DRN. C.KACZOR	DATE 02-AUG-84	ENG. C.KACZOR	DATE 02-AUG-84	TITLE: MULTIWIRED PAGER TERMINATORS
	CHK'D D. BELLORCO	DATE 02-AUG-84	BOARD LOCATION: 3	SHEET 1 OF 3	SIZE CODE NUMBER REV. D CS M855-0-RES A1
FIRST USED ON OPTION/MODEL: MCA25					D-DD-M855-0

MRO

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
22K5)	PAG3	A5	68Ω	PAG3 PT ADR 25 B IN H	262(10)	PAG4	B3	68Ω	-PAG4 PF CODE 2X H	21K0)	PAG6	B1	68Ω	PAG6 PT 1 KEEP 04 H	267(3)	PAG9	D1	68Ω	-PAG9 PT 1 MATCH H
267(5)	PAG3	A7	68Ω	PAG3 PT ADR 26 H	230(8)	PAG5	A7	68Ω	PAG5 K110 PAGE MODE H	225(10)	PAG6	B1	68Ω	PAG6 PT 1 KEEP 05 H	267(6)	PAG9	D2	68Ω	PAG9 PT 1 NO MATCH IN A H
266(8)	PAG3	A7	68Ω	-PAG3 PT ADR 26 H	240(6)	PAG5	B1	68Ω	PAG5 PT 0 EV PG PAR H	21(10)	PAG6	B1	68Ω	PAG6 PT 1 KEEP 06 H	226(10)	PAG9	A3	68Ω	-PAG9 PT 1 ODD EN C H
260(1)	PAG3	B7	68Ω	PAG3 PT ADR 26 A IN H	257(1)	PAG5	B1	68Ω	PAG5 PT 0 OD PG PAR H	243(10)	PAG6	A1	68Ω	PAG6 PT 1 KEEP 07 H	218(10)	PAG9	A3	68Ω	-PAG9 PT 1 ODD EN D H
213(3)	PAG3	B5	68Ω	PAG3 PT DIR CLR H	240(10)	PAG5	B1	68Ω	PAG5 PT 1 EV PG PAR H	216(1)	PAG6	B6	68Ω	PAG6 PT IN KEEP A H	212(1)	PAG9	B2	68Ω	PAG9 PT 1 WR C H
213(10)	PAG3	B5	68Ω	-PAG3 PT DIR CLR H	240(8)	PAG5	B1	68Ω	PAG5 PT 1 OD PG PAR H	253(1)	PAG6	A6	68Ω	PAG6 PT IN KEEP B H	256(5)	PAG9	B1	68Ω	-PAG9 PT 1 WR C H
R2(1)	PAG3	B7	100Ω	-PAG3 PT DIR WR H	226(3)	PAG5	C7	68Ω	-PAG5 PT ADR 18 A H	249(8)	PAG7	C7	68Ω	PAG7 PT 0 20 H	256(8)	PAG9	B1	68Ω	-PAG9 PT 1 WR D H
R1(1)	PAG3	B7	100Ω	-PAG3 PT DIR WR DLT H	218(3)	PAG5	C7	68Ω	PAG5 PT ADR 18 B H	241(5)	PAG7	B7	68Ω	PAG7 PT 0 21 H	211(8)	PAG9	B7	68Ω	PAG9 TB 1 DIR H
229(6)	PAG3	C7	68Ω	-PAG3 PT DIR WR DLT A H	254(5)	PAG5	B7	68Ω	-PAG5 PT ADR 19 A H	232(5)	PAG7	C4	68Ω	PAG7 PT 0 22 H	220(5)	PAG9	B7	68Ω	PAG9 TB 1 DIR EN H
26(8)	PAG3	A1	68Ω	PAG3 PT EN A H	220(6)	PAG5	B7	68Ω	PAG5 PT ADR 19 B H	241(1)	PAG7	B4	68Ω	PAG7 PT 0 23 H	267(8)	PAG9	A4	68Ω	PAG9 TB 1 WR TWO H
265(5)	PAG3	A1	68Ω	-PAG3 PT EN A H	226(5)	PAG5	B7	68Ω	-PAG5 PT ADR 20 A H	232(6)	PAG7	B2	68Ω	PAG7 PT 0 25 H	27(10)	PAG2	D7	68Ω	PMA3 PA 14 H
245(6)	PAG3	A1	68Ω	PAG3 PT EN B H	220(8)	PAG5	B7	68Ω	PAG5 PT ADR 20 B H	223(8)	PAG7	C7	68Ω	PAG7 PT 1 20 H	26(10)	PAG2	D5	68Ω	PMA3 PA 15 H
250(6)	PAG3	A1	68Ω	-PAG3 PT EN B H	254(6)	PAG5	B7	68Ω	-PAG5 PT ADR 21 A H	241(6)	PAG7	A7	68Ω	PAG7 PT 1 21 H	26(5)	PAG2	D5	68Ω	PMA3 PA 16 H
27(5)	PAG3	D2	68Ω	PAG3 PT MATCH H	218(5)	PAG5	B7	68Ω	PAG5 PT ADR 21 B H	235(8)	PAG7	C4	68Ω	PAG7 PT 1 22 H	234(10)	PAG2	D4	68Ω	PMA3 PA 17 H
231(1)	PAG3	B5	68Ω	PAG3 PT WR BOTH HALVES H	246(5)	PAG5	C5	68Ω	-PAG5 PT ADR 22 A H	242(8)	PAG7	A4	68Ω	PAG7 PT 1 23 H	245(3)	PAG2	D4	68Ω	PMA3 PA 18 H
25(1)	PAG3	B5	68Ω	-PAG3 PT WR BOTH HALVES H	234(8)	PAG5	C5	68Ω	PAG5 PT ADR 22 B H	242(8)	PAG7	A4	68Ω	PAG7 PT 1 23 H	245(8)	PAG2	D3	68Ω	PMA3 PA 19 H
220(10)	PAG3	C1	68Ω	PAG3 SEL TB 0 H	216(10)	PAG5	B5	68Ω	-PAG5 PT ADR 23 A H	237(8)	PAG7	C1	68Ω	PAG7 PT 1 24 H	29(8)	PAG7	D5	68Ω	PMA3 PA 20 H
229(3)	PAG3	C1	68Ω	-PAG3 SEL TB 0 H	262(6)	PAG5	B5	68Ω	PAG5 PT ADR 23 B H	234(6)	PAG7	A2	68Ω	PAG7 PT 1 25 H	241(3)	PAG7	D4	68Ω	PMA3 PA 21 H
229(5)	PAG3	B5	68Ω	PAG3 TB 0 DIR H	226(8)	PAG5	B5	68Ω	-PAG5 PT ADR 24 A H	24(6)	PAG8	D4	68Ω	PAG8 CLR VALID H	234(5)	PAG7	D4	68Ω	PMA3 PA 22 H
264(3)	PAG3	B5	68Ω	-PAG3 TB 0 DIR EN H	25(3)	PAG5	B5	68Ω	PAG5 PT ADR 24 B H	224(5)	PAG8	D6	68Ω	-PAG8 FORCE VALID 0 H	242(10)	PAG7	D3	68Ω	PMA3 PA 23 H
266(3)	PAG3	A2	68Ω	PAG3 TB 0 WR TWO H	222(8)	PAG5	B5	68Ω	-PAG5 PT ADR 25 A H	253(3)	PAG8	D6	68Ω	-PAG8 FORCE VALID 1 H	237(6)	PAG7	D6	68Ω	PMA3 PA 24 H
233(1)	PAG4	D1	68Ω	PAG4 FORCE TB CLK H	257(6)	PAG5	B5	68Ω	PAG5 PT ADR 25 B H	224(1)	PAG8	C6	68Ω	-PAG8 FORCE VALID 2 H	237(10)	PAG7	D5	68Ω	PMA3 PA 25 H
233(8)	PAG4	A6	68Ω	-PAG4 FORCE TO TB 0 H	264(6)	PAG5	A5	68Ω	-PAG5 PT ADR 25 C H	252(6)	PAG8	C6	68Ω	-PAG8 FORCE VALID 3 H	226(6)	PAG1	C8	68Ω	PT IN 00 H
233(3)	PAG4	A6	68Ω	PAG4 FORCE TO TB 1 H	222(3)	PAG5	A5	68Ω	PAG5 PT ADR 25 D H	215(6)	PAG8	B6	68Ω	-PAG8 FORCE VALID 4 H	218(1)	PAG1	B8	68Ω	PT IN 01 H
220(1)	PAG4	D1	68Ω	-PAG4 HBOX CTL 03 H	22(10)	PAG5	C1	68Ω	PAG5 PT PAR ODD H	215(5)	PAG8	B6	68Ω	-PAG8 FORCE VALID 5 H	226(1)	PAG1	C6	68Ω	PT IN 02 H
260(5)	PAG4	D7	68Ω	PAG4 PAGE EXEC PAGED REF H	249(3)	PAG5	C2	68Ω	-PAG5 PT PAR ODD H	215(1)	PAG8	B6	68Ω	-PAG8 FORCE VALID 6 H	218(6)	PAG1	B6	68Ω	PT IN 03 H
225(3)	PAG4	D7	68Ω	-PAG4 PAGE EXEC PAGED REF H	229(10)	PAG6	D6	68Ω	PAG6 PT 0 KEEP H	214(3)	PAG8	A6	68Ω	-PAG8 FORCE VALID 7 H	220(10)	PAG1	C4	68Ω	PT IN 04 H
240(3)	PAG4	D7	68Ω	PAG4 PAGE EXEC REF H	244(1)	PAG6	D4	68Ω	PAG6 PT 0 KEEP 00 H	233(5)	PAG8	D2	68Ω	PAG8 PT 0 VALID H	255(8)	PAG2	C7	68Ω	PT IN 05 H
29(1)	PAG4	D7	68Ω	-PAG4 PAGE EXEC REF H	23(5)	PAG6	D4	68Ω	PAG6 PT 0 KEEP 01 H	271(3)	PAG8	D2	68Ω	-PAG8 PT 0 VALID H	264(10)	PAG2	B7	68Ω	PT IN 06 H
26(1)	PAG4	C3	68Ω	PAG4 PAGE FAIL A H	244(3)	PAG6	D4	68Ω	PAG6 PT 0 KEEP 02 H	211(1)	PAG8	D4	68Ω	PAG8 PT 0 VALID A H	255(6)	PAG2	C5	68Ω	PT IN 07 H
225(6)	PAG4	C3	68Ω	-PAG4 PAGE FAIL A H	23(1)	PAG6	C4	68Ω	PAG6 PT 0 KEEP 03 H	212(10)	PAG8	B4	68Ω	-PAG8 PT 0 VALID B H	220(1)	PAG2	B5	68Ω	PT IN 08 H
265(3)	PAG4	B6	68Ω	PAG4 PAGE TEST PRIVATE H	23(10)	PAG6	B4	68Ω	PAG6 PT 0 KEEP 04 H	233(6)	PAG8	D2	68Ω	PAG8 PT 1 VALID H	255(5)	PAG2	C2	68Ω	PT IN 09 H
225(5)	PAG4	B6	68Ω	-PAG4 PAGE TEST PRIVATE H	244(5)	PAG6	B4	68Ω	PAG6 PT 0 KEEP 05 H	224(8)	PAG8	D1	68Ω	PAG8 PT 1 VALID A H	255(1)	PAG2	B2	68Ω	PT IN 10 H
259(1)	PAG4	C6	68Ω	-PAG4 PAGE TEST WRITE H	23(8)	PAG6	B4	68Ω	PAG6 PT 0 KEEP 06 H	252(10)	PAG8	B1	68Ω	PAG8 PT 1 VALID B H	222(10)	PAG7	C7	68Ω	PT IN 11 H
29(3)	PAG4	C7	68Ω	-PAG4 PAGE UNPAGED REF H	244(6)	PAG6	A4	68Ω	PAG6 PT 0 KEEP 07 H	260(10)	PAG8	A6	68Ω	-PAG8 PT DIR CLR A H	257(10)	PAG7	B7	68Ω	PT IN 12 H
20(3)	PAG4	D7	68Ω	PAG4 PAGE USER PAGED REF H	212(6)	PAG6	C6	68Ω	PAG6 PT 1 KEEP H	225(8)	PAG8	A6	68Ω	-PAG8 PT DIR CLR B H	222(5)	PAG7	C5	68Ω	PT IN 13 H
225(1)	PAG4	D7	68Ω	-PAG4 PAGE USER PAGED REF H	243(8)	PAG6	D1	68Ω	PAG6 PT 1 KEEP 00 H	267(1)	PAG8	A6	68Ω	PAG8 SLEEP ALL H	257(8)	PAG7	B5	68Ω	PT IN 14 H
259(5)	PAG4	D4	68Ω	PAG4 PAGE WRITE OK H	243(6)	PAG6	D1	68Ω	PAG6 PT 1 KEEP 01 H	218(8)	PAG9	A3	68Ω	-PAG9 PT 1 EVEN EN C H	256(3)	PAG7	C2	68Ω	PT IN 15 H
249(1)	PAG4	D4	68Ω	-PAG4 PAGE WRITE OK H	21(6)	PAG6	D1	68Ω	PAG6 PT 1 KEEP 02 H	230(6)	PAG9	B2	68Ω	-PAG9 PT 1 EVEN EN D H	230(6)	PAG7	B2	68Ω	PT IN 16 H
252(1)	PAG4	A2	68Ω	-PAG4 PAGED REF H	243(5)	PAG6	C1	68Ω	PAG6 PT 1 KEEP 03 H	22(1)	PAG9	D1	68Ω	PAG9 PT 1 MATCH H	222(1)	PAG1	B3	68Ω	PT IN 17 H

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND <> INDICATES PIN NUMBER

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REVISIONS	CHK	CHANGE NO.	REV.

	DRN	C. KACZOR	DATE	02-18-84	ENG	C. KACZOR	DATE	02-18-84	TITLE	MULTIWIRE PAGER TERMINATORS
	CHK'D	D. DELLORCO	DATE	02-18-84	BOARD LOCATION					
FIRST USED ON OPTION/MODEL:		MCA25	NEXT HIGHER ASSEMBLY:		D-DD-M855-0	SIZE	CODE	NUMBER	REV.	A1

REV. 1
 SIZE CODE CS M855-0-RES

D
C
B
A

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
254(8)	PAG1	C8	68Ω	PT IN 18 H
250(1)	PAG1	A8	68Ω	PT IN 19 H
254(3)	PAG1	C6	68Ω	PT IN 20 H
262(1)	PAG1	A6	68Ω	PT IN 21 H
254(1)	PAG1	C4	68Ω	PT IN 22 H
255(10)	PAG2	C7	68Ω	PT IN 23 H
264(8)	PAG2	A7	68Ω	PT IN 24 H
228(5)	PAG2	B5	68Ω	PT IN 25 H
256(10)	PAG2	A5	68Ω	PT IN 26 H
255(3)	PAG2	C2	68Ω	PT IN 27 H
228(3)	PAG2	A2	68Ω	PT IN 28 H
270(1)	PAG7	C7	68Ω	PT IN 29 H
230(1)	PAG7	A7	68Ω	PT IN 30 H
222(6)	PAG7	C5	68Ω	PT IN 31 H
232(10)	PAG7	B5	68Ω	PT IN 32 H
230(5)	PAG7	C2	68Ω	PT IN 33 H
256(6)	PAG7	B2	68Ω	PT IN 34 H
246(3)	PAG1	A3	68Ω	PT IN 35 H
213(5)	PAG5	A4	68Ω	SH AR PAR ODD A H
220(3)	PAG3	C6	68Ω	VIA2 VIA 13 A H
270(10)	PAG3	C6	68Ω	VIA2 VIA 14 A H
270(8)	PAG3	C6	68Ω	VIA2 VIA 15 A H
219(3)	PAG3	D4	68Ω	VIA2 VIA 16 A H
270(3)	PAG3	B7	68Ω	VIA2 VIA 17 H
219(5)	PAG3	C4	68Ω	VIA2 VIA 17 A H
242(6)	PAG3	D8	68Ω	VIA2 VIA 18 H
269(1)	PAG3	B7	68Ω	VIA2 VIA 19 H
270(6)	PAG3	B7	68Ω	VIA2 VIA 20 H
242(5)	PAG3	D8	68Ω	VIA2 VIA 21 H
236(8)	PAG3	D8	68Ω	VIA2 VIA 22 H
242(3)	PAG3	C8	68Ω	VIA2 VIA 23 H
251(10)	PAG3	A5	68Ω	VIA2 VIA 24 H
251(6)	PAG3	A7	68Ω	VIA2 VIA 25 H
261(1)	PAG3	A7	68Ω	VIA2 VIA 26 H

- NOTE:
1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND (<) INDICATES PIN NUMBER

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DPN	C. KACZOR	DATE	82-AUG-81	ENG.	C. KACZOR	DATE	82-AUG-81
	CHK'D	D. BELLORCO	DATE	82-AUG-81	BOARD LOCATION:			
FIRST USED ON OPTION/MODEL:		MCA25	NEXT HIGHER ASSEMBLY:					
SIZE	CODE	NUMBER	REV.					
D	CS	M855-0	RES					

MEC

DRAWING NUMBER	NO. OF SHT	PART NUMBER	DESCRIPTION	REVISION
		M855-00	MODULE REVISION	A2
D-UA-M855-0-0	2		2 WAY ASSOCIATIVE PAGER	B
K-PL-M855-0-DBP	2		PART LIST, M855	B
D-CS-M855-0-PAG1	1		PAGE TABLE DATA PT ACCESS-PT 17	B
D-CS-M855-0-PAG2	1		PAGE TABLE DATA PT 16 - PT 26	B
D-CS-M855-0-PAG3	1		PAGE TABLE DIRECTORY	B
D-CS-M855-0-PAG4	1		PAGE TABLE CONTROL LOGIC	B
D-CS-M855-0-PAG5	1		PAGE TABLE PARITY LOGIC	B
D-CS-M855-0-PAG6	1		PAGING KEEP BIT SELECTION	B
D-CS-M855-0-PAG7	1		PAGE TABLE DATA PT 20 - 25	B
D-CS-M855-0-PAG8	1		PAGE TABLE VALID BIT SELECTION	B
D-CS-M855-0-PAG9	1		PAGE TABLE 1 DIRECTORY	B
D-CS-M855-0-PAGA	1		PAGING BOARD POWER, GND, CAP	B
D-CS-M855-0-PAGB	1		PAGING BOARD GND	B
D-CS-M855-0-RES	3		PAG TERMINATORS	B
K-PC-M855-0-DBI	-		P.C. DESIGN DATA BASE TAPE	B
D-DD-5017661-0	2		DRAWING DIRECTORY, 5017661	REF

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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. D. DELLORCO	DATE 20-JUL-84	ENG. C. KACZOR	DATE 20-JUL-84
CHK'D. D. DELLORCO	DATE 20-JUL-84	BOARD LOCATION: N/A	SHEET 2 OF 2
FIRST USED ON OPTION/MODEL: MCA25		NEXT HIGHER ASSEMBLY: KL10-PW	

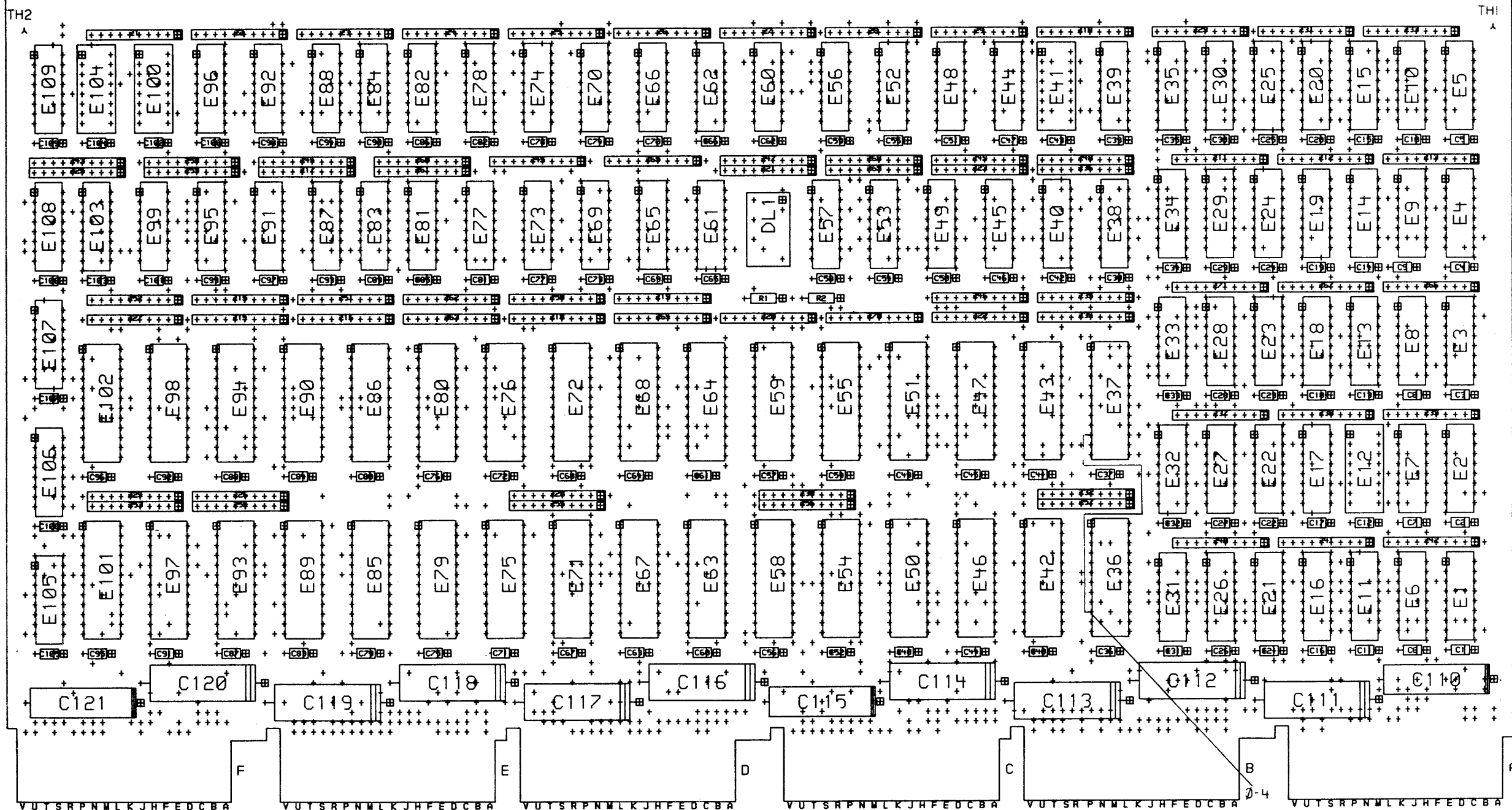
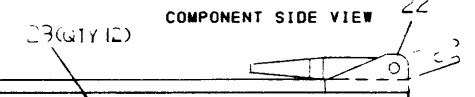
TITLE: M855 DRAWING DIRECTORY			
SIZE	CODE	NUMBER	REV.
D	DD	M855-0	A

MRO 1

8 7 6 5 4 3 2 1

D U A M 8 5 5 - 0 - 0

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NOTES: 1. SPARE COMP. ARE: E12, E41, E100, E104.
2. THIS BOARD MUST MEET SAFETY REQ. FOR HPWR.

STEP E	→ Y AXIS	STEP 0	TIMES
REPEAT	→ X AXIS	STEP 0	TIMES

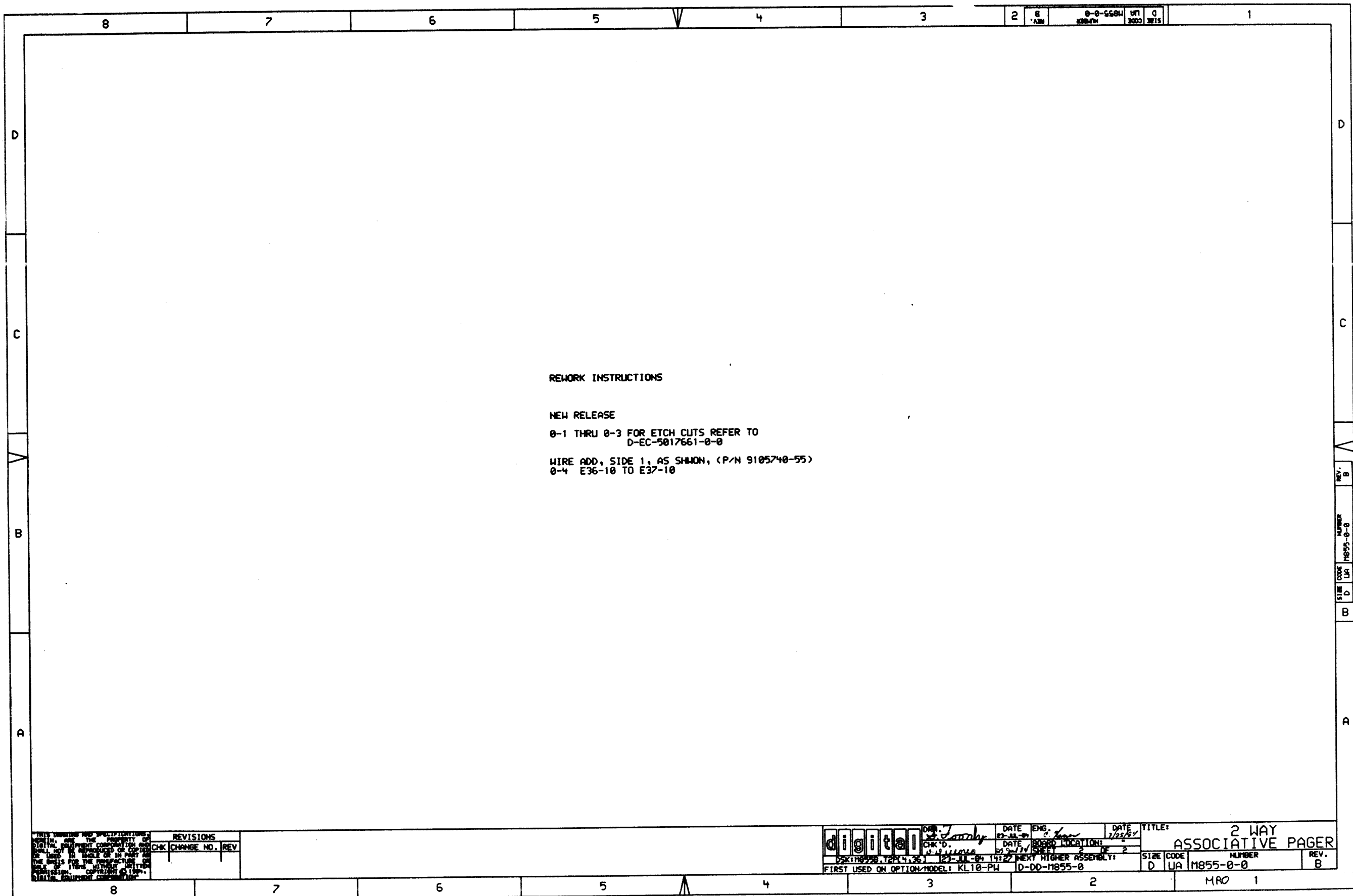
CHG	NO	REV

ETCH REV. B1

SIGNATURES	DATE	digital
DRN. J. MASCN	4-12-84	
CHK'D. M. ...	10-2-84	TITLE 2 WAY
MECH. ENG.		ASSOCIATIVE PAGER
PROD. D. ...	20/1/84	SIZE CODE NUMBER
SCALE 2/1		D U A M 8 5 5 - 0 - 0
SHT. 1 OF 2		REV B
TCP D.C. NC: D-DD-M855-0		

MRO 1 MS# 410642B

8 7 6 5 4 3 2 1



REWORK INSTRUCTIONS

NEW RELEASE

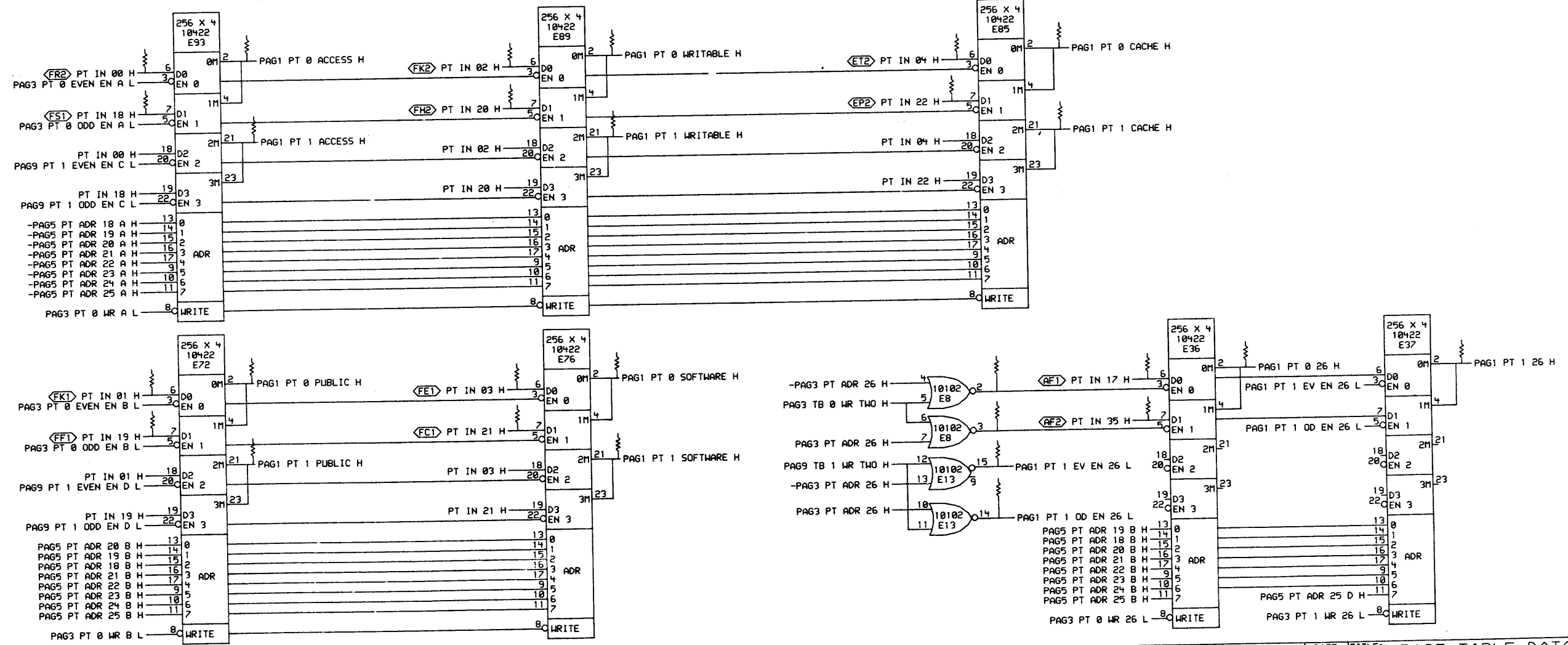
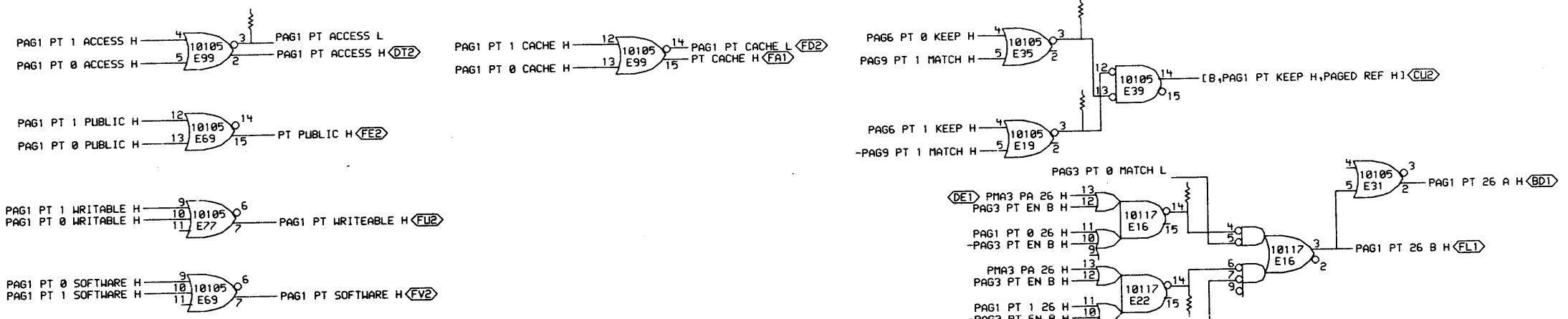
0-1 THRU 0-3 FOR ETCH CUTS REFER TO
D-EC-5017661-0-0

WIRE ADD, SIDE 1, AS SHOWN, (P/N 9105740-55)
0-4 E36-10 TO E37-10

REVISIONS	
CHK	CHANGE NO. REV

digital	DESIGNER: <i>J. J. J.</i>	DATE: 02-22-67	ENG.:	DATE: 2/25/67	TITLE: 2 WAY
	CHK'D: <i>J. J. J.</i>	DATE: 02-22-67	BOARD LOCATION:	DE: 2	ASSOCIATIVE PAGER
FIRST USED ON OPTION/MODEL: KL10-PH		NEXT HIGHER ASSEMBLY: D-DD-11855-0		SIZE CODE: D UA	NUMBER: M855-0-0
					REV. B

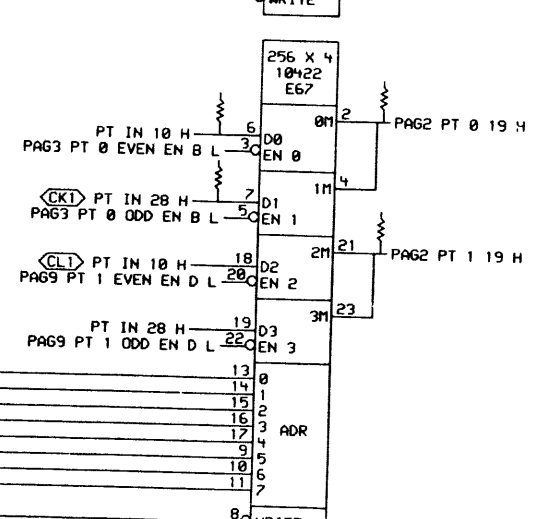
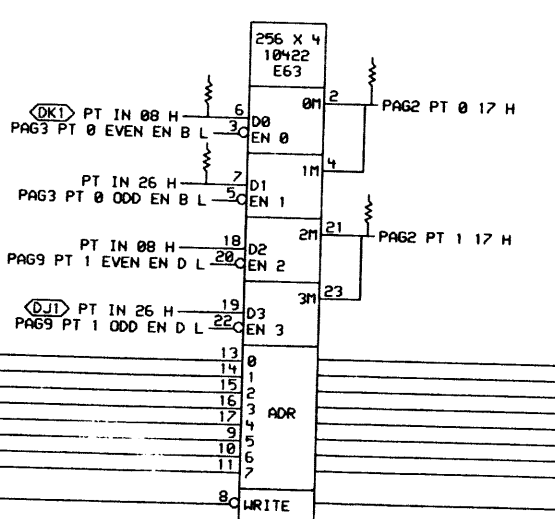
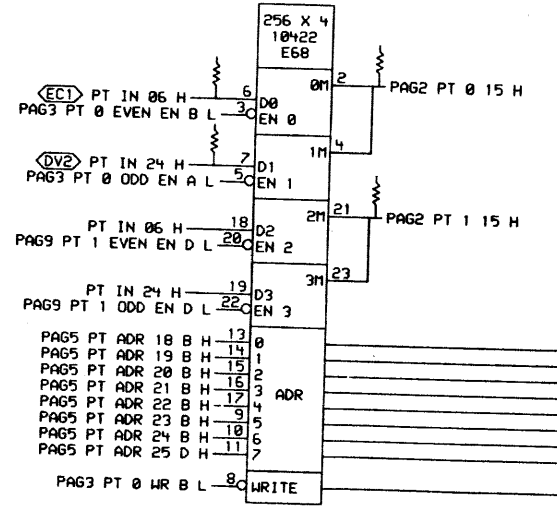
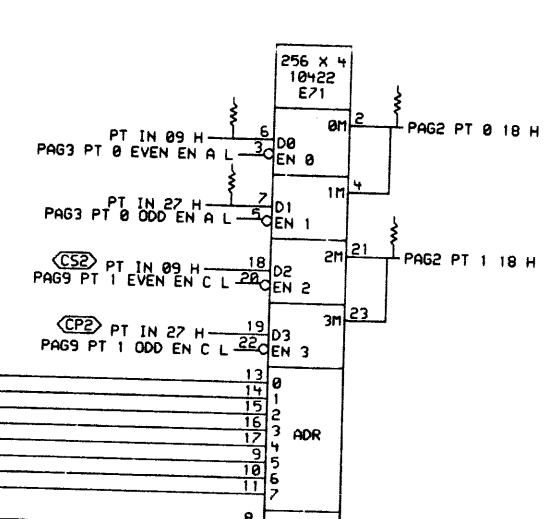
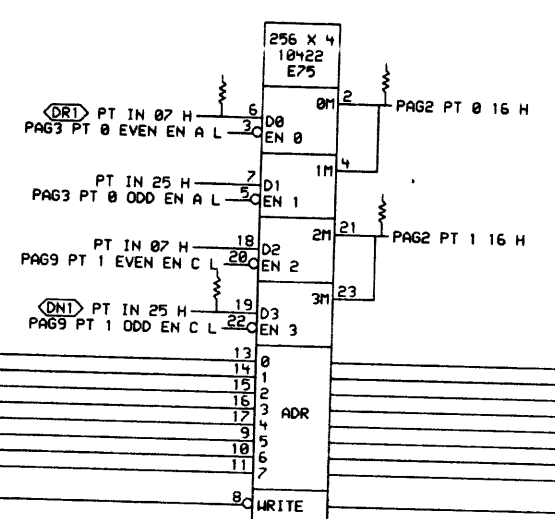
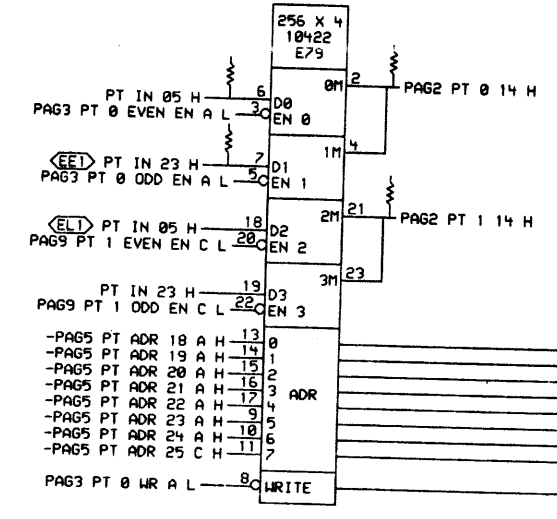
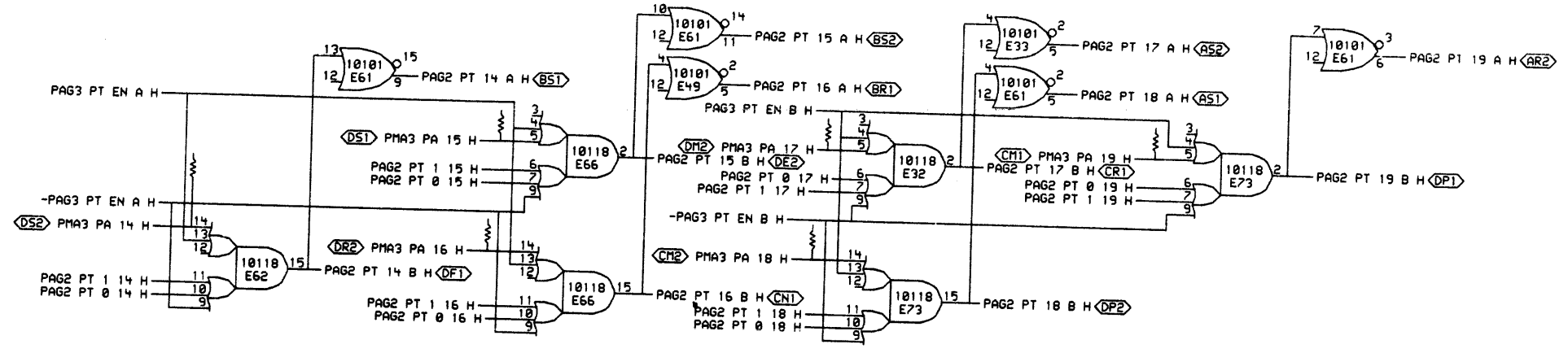
MRO 1



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REVISIONS	
CHK	CHANGE NO. REV

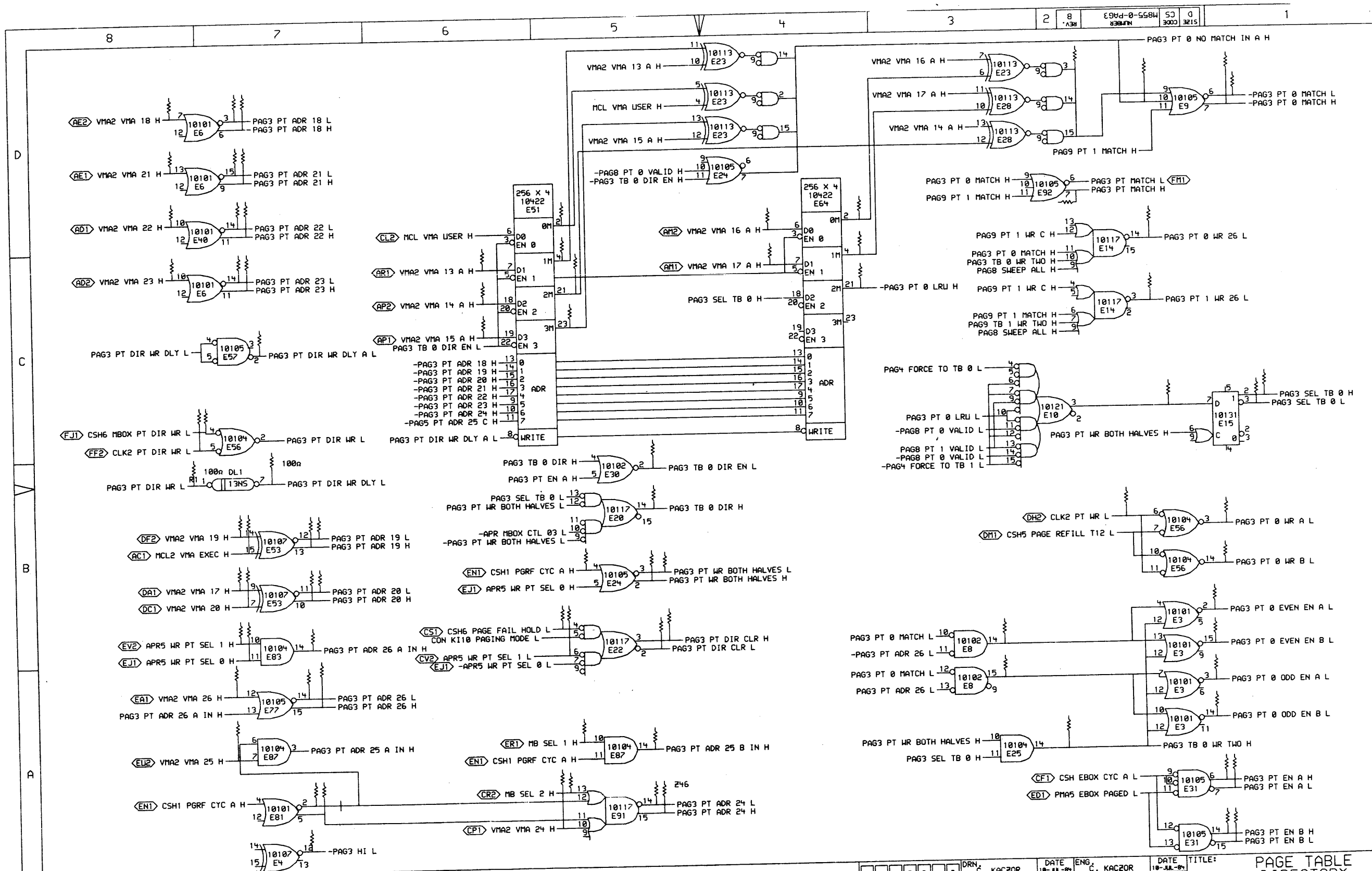
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 CHK'D. D. DELLORCO DATE 28-JUL-84 BOARD LOCATION: 4AF20
 XTRA: (M855-0-REV) M855-0-PAG1 116-JUL-84 11:32 NEXT HIGHER ASSEMBLY: SHEET 1 OF 1 SIZE CODE NUMBER REV.
 FIRST USED ON OPTION/MODEL: MCA25 B-DD-M855-0 D CS M855-0-PAG1 B
 MRO 1



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REVISIONS	
CHK	CHANGE NO. REV

digital DRN: C. KACZOR DATE: 10-JUL-84 ENG: C. KACZOR DATE: 10-JUL-84 TITLE: PAGE TABLE DATA PT 16 - PT 26
 CHK'D: D. DELLORCO DATE: 10-JUL-84 BOARD LOCATION: 4AF30
 FIRST USED ON OPTION/MODEL: MCA25 10-JUL-84 10:10 NEXT HIGHER ASSEMBLY: D-DD-M855-0 SIZE CODE: D CS NUMBER: M855-0-PAG2 REV: B

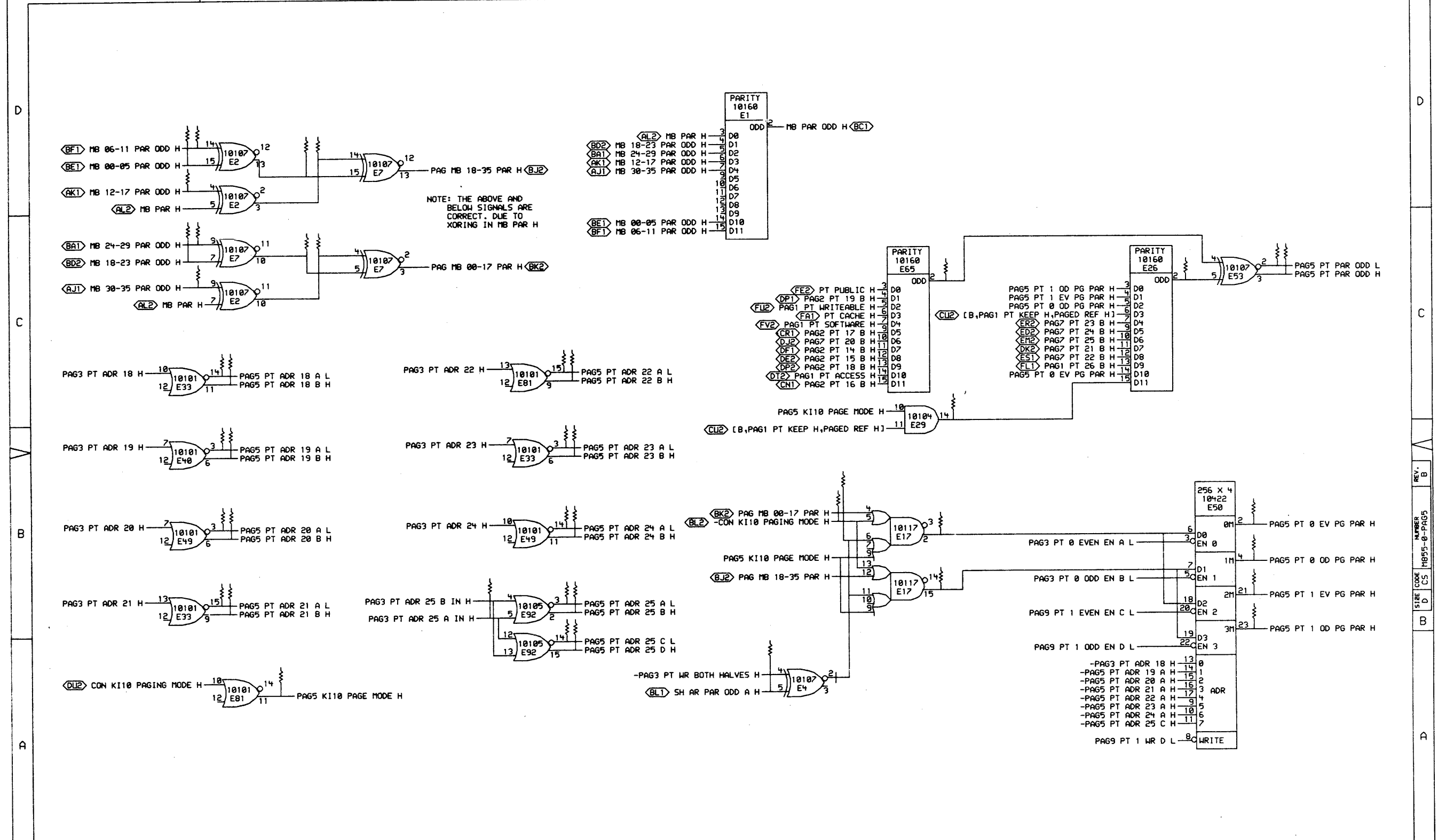


REV. B
 NUMBER M855-0-PAG3
 SIZE CODE CS
 D B
 A

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CHK	CHANGE NO.	REV

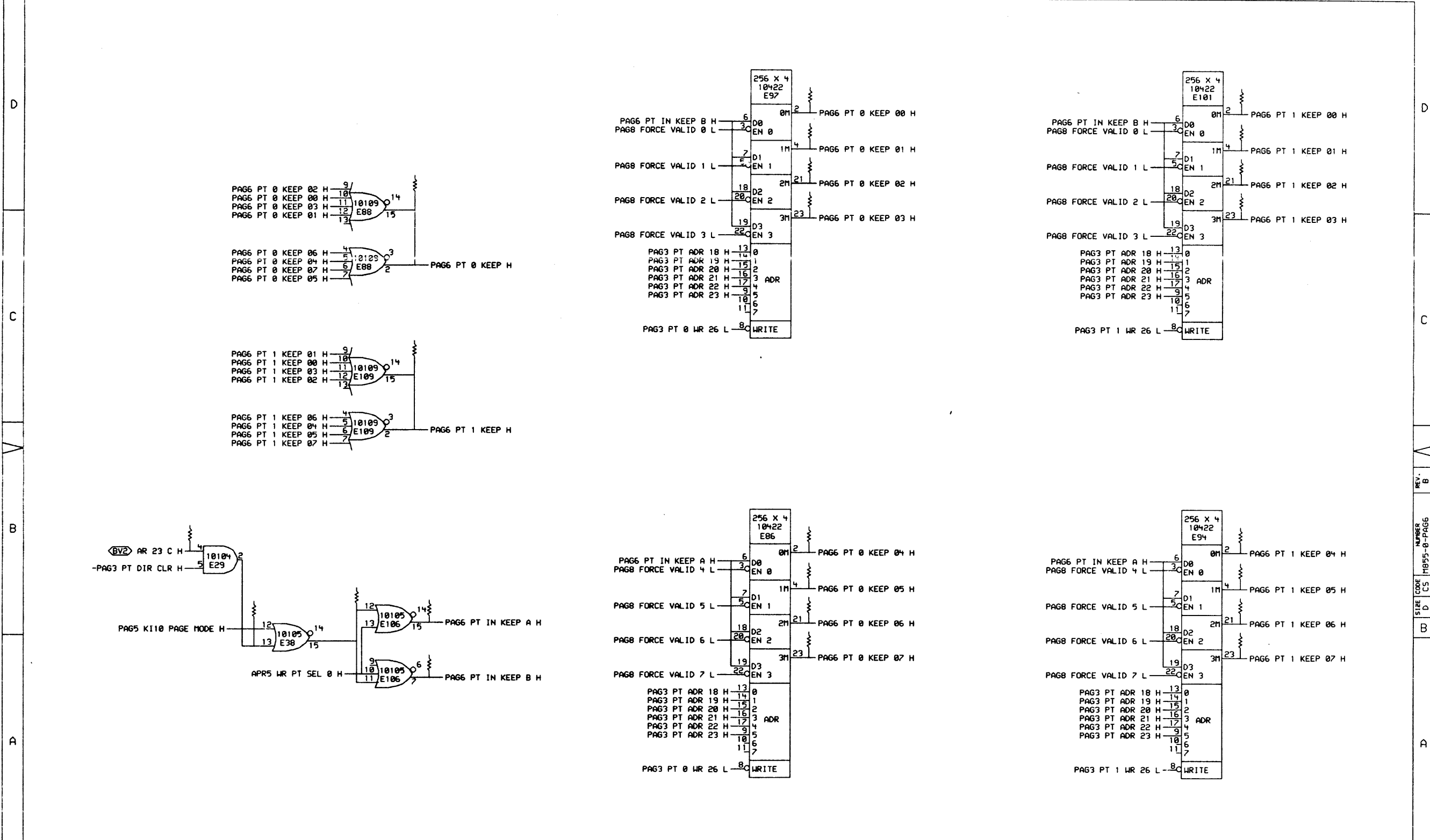
	DRN. C. KACZOR	DATE 10-JUL-84	ENG. C. KACZOR	DATE 10-JUL-84	TITLE: PAGE TABLE DIRECTORY
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FIRST USED ON OPTION/MODEL: MCA25 D-DD-M855-0					MRO 1

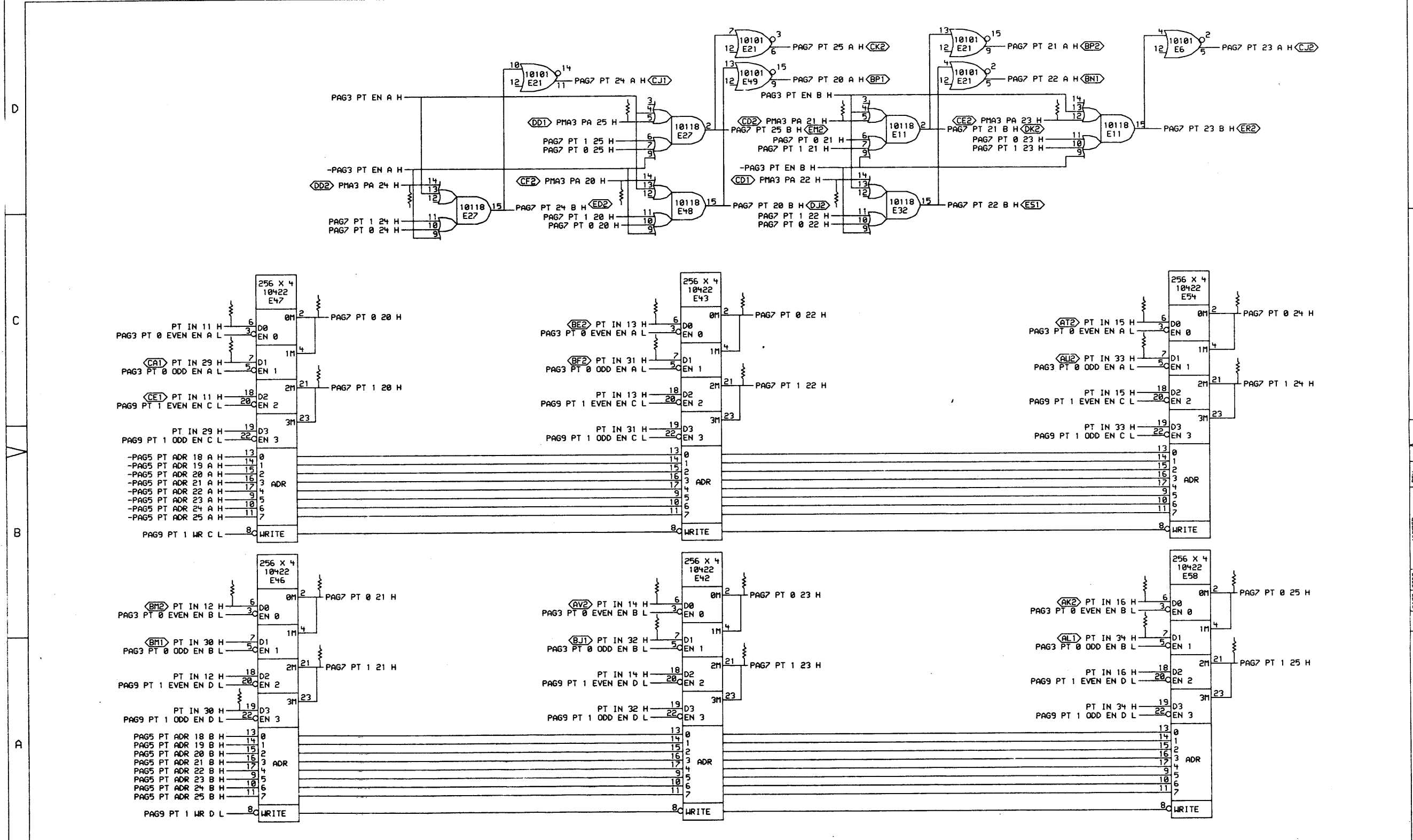


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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN: C. KACZOR	DATE: 20-JUL-84	ENG: C. KACZOR	DATE: 20-JUL-84	TITLE: PAGE TABLE PARITY LOGIC
	CHK'D: D. DELLORCO	DATE: 20-JUL-84	BOARD LOCATION: 4AF38	SHEET: 1 OF 1	
FIRST USED ON OPTION/MODEL: MCA25			NEXT HIGHER ASSEMBLY: B-DD-M855-0		
SIZE CODE: D CS	NUMBER: M855-0-PAG5	REV: B	MRO 1		

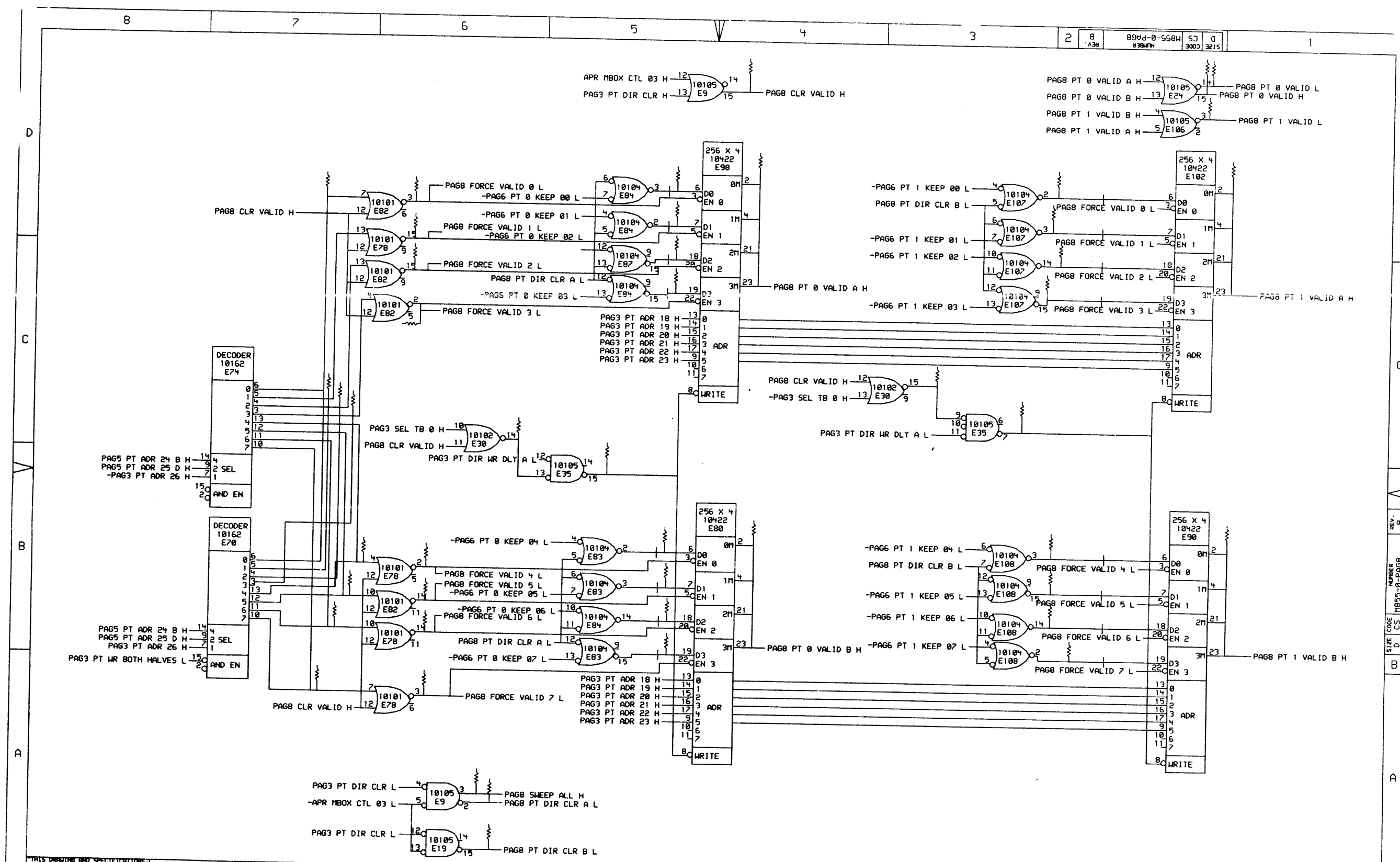




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REVISIONS		
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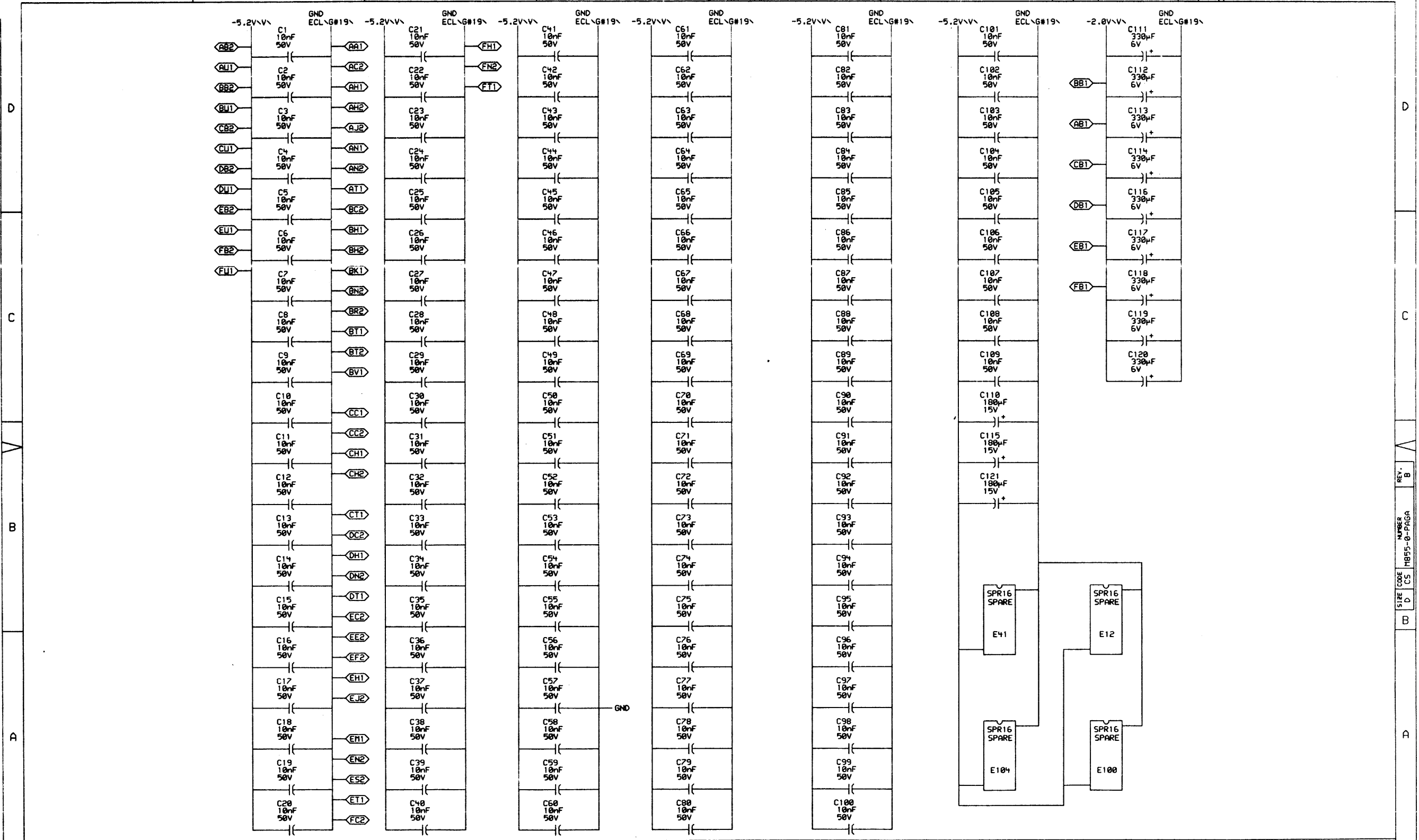
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FIRST USED ON OPTION/MODEL: MCA25					SIZE CODE D CS
NEXT HIGHER ASSEMBLY: D-DD-M855-0					NUMBER M855-0-PAG7
					REV. B



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REV.	CHG.	NO.	REV.

	DRN. C. KACZOR	DATE 10-JUL-84	ENG. C. KACZOR	DATE 10-JUL-84	TITLE: PAGE TABLE VALID BIT SELECTION
	CHK'D D. DELLORCO	DATE 10-JUL-84	BOARD LOCATION: 4A20	SHEET	OF
FIRST USED ON OPTION/MODEL: MCA25			NEXT HIGHER ASSEMBLY: D-DD-M855-0		SIZE CODE D CS
NUMBER M855-0-PAG8					REV. B



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. C. KACZOR	DATE 10-11-84	ENG. C. KACZOR	DATE 10-11-84	TITLE: PAGING BOARD POWER, GND, CAP
	CHK'D. D. DELLORCO	DATE 10-11-84	BOARD LOCATION: 4AF30	SHEET 1 OF 1	SIZE CODE D CS
FIRST USED ON OPTION/MODEL: MCA25					NUMBER M855-0-PAGA
NEXT HIGHER ASSEMBLY: D-DD-M855-0					REV. B

8 7 6 5 4 3 2 1

SIZE CODE NUMBER
D CS M855-0-PAGB
REV. B

D
C
B
A

D
C
B
A

- [B,GND ECL,
GND CLK\G#20\} —(CN2)
- [B,NC,GND
ECL\G#19\} —(AA2)
- [B,NC,GND
ECL\G#19\} —(AV1)
- [B,NC,GND
ECL\G#19\} —(BA2)
- [B,NC,GND
ECL\G#19\} —(CA2)
- [B,NC,GND
ECL\G#19\} —(CV1)
- [B,NC,GND
ECL\G#19\} —(DA2)
- [B,NC,GND
ECL\G#19\} —(DV1)
- [B,NC,GND
ECL\G#19\} —(EA2)
- [B,NC,GND
ECL\G#19\} —(EV1)
- [B,NC,GND
ECL\G#19\} —(FA2)
- [B,NC,GND CLK
TIEDOWN\G#20\} —(FV1)
- [B,GND
ECL\G#19\,NC\} —(EK2)

REVISIONS	
CHK	CHANGE NO. REV.

digital	DRN. C. KACZOR	DATE 10-JUL-84	ENG. C. KACZOR	DATE 10-JUL-84	TITLE: PAGING BOARD GND
	CHK'D. D. DELLORCO	DATE 10-JUL-84	BOARD LOCATION: 4AF30	SHEET 1 OF 1	
FIRST USED ON OPTION/MODEL: MCA25 D-DD-M855-0					SIZE CODE NUMBER REV. D CS M855-0-PAGB B

8 7 6 5 4 3 2 1

MRO 1

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
212(3)	PAG3	C2	68Ω	%E10K(3)	223(1)	PAG9	C5	68Ω	%E55K(21)	212(8)	PAG3	B2	68Ω	-CLK2 PT WR H	247(10)	PAG2	C6	68Ω	PAG2 PT 0 14 H
259K(3)	PAG4	A3	68Ω	%E10K(15)	223(3)	PAG9	C5	68Ω	%E55K(23)	238(6)	PAG5	B4	68Ω	-CON K110 PAGING MODE H	265(8)	PAG2	B6	68Ω	PAG2 PT 0 15 H
252(5)	PAG8	C2	68Ω	%E107(14)	246(6)	PAG9	C5	68Ω	%E55K(4)	211(3)	PAG3	B5	68Ω	CSH1 PGRF CYC A H	265(6)	PAG2	C4	68Ω	PAG2 PT 0 16 H
227(6)	PAG8	C3	68Ω	%E107(15)	249(5)	PAG4	C5	68Ω	%E56(9)	231(8)	PAG9	B2	68Ω	-CSH5 PAGE REFILL T12 H	232(8)	PAG2	B4	68Ω	PAG2 PT 0 17 H
227(10)	PAG8	D2	68Ω	%E107(2)	234(3)	PAG9	C4	68Ω	%E59K(2)	27(3)	PAG3	C7	68Ω	-CSH6 MBOX PT DIR WR H	250(5)	PAG2	C2	68Ω	PAG2 PT 0 18 H
252(8)	PAG8	D2	68Ω	%E107(3)	235(5)	PAG9	C4	68Ω	%E59K(4)	237(3)	PAG3	B5	68Ω	-CSH6 PAGE FAIL HOLD H	250(10)	PAG2	B2	68Ω	PAG2 PT 0 19 H
216(5)	PAG8	B2	68Ω	%E108(14)	27(6)	PAG4	B7	68Ω	%E60K(15)	258(1)	PAG4	C2	68Ω	CSH6 PAGE REFILL ERROR H	27(8)	PAG2	C6	68Ω	PAG2 PT 1 14 H
216(8)	PAG8	B2	68Ω	%E108(15)	261(3)	PAG4	B6	68Ω	%E60K(2)	258(3)	PAG4	C4	68Ω	-CSH6 PAGE REFILL ERROR H	265(10)	PAG2	A6	68Ω	PAG2 PT 1 15 H
216(6)	PAG8	B2	68Ω	%E108(2)	235(6)	PAG3	D4	68Ω	%E64(2)	239(1)	PAG5	D8	68Ω	MB 00-05 PAR ODD H	26(6)	PAG2	C4	68Ω	PAG2 PT 1 16 H
251(8)	PAG8	B2	68Ω	%E108(3)	234(1)	PAG3	C4	68Ω	%E64(4)	239(3)	PAG5	D7	68Ω	MB 06-11 PAR ODD H	232(1)	PAG2	A4	68Ω	PAG2 PT 1 17 H
267(10)	PAG9	A4	68Ω	%E13K(2)	221(1)	PAG5	C3	68Ω	%E65(2)	239(5)	PAG5	D8	68Ω	MB 12-17 PAR ODD H	250(3)	PAG2	C2	68Ω	PAG2 PT 1 18 H
271(5)	PAG9	A4	68Ω	%E13K(3)	238(3)	PAG5	C7	68Ω	%E7(10)	238(5)	PAG5	C8	68Ω	MB 18-23 PAR ODD H	250(8)	PAG2	A2	68Ω	PAG2 PT 1 19 H
241(10)	PAG1	D3	68Ω	%E16(14)	268(3)	PAG8	A7	68Ω	%E70K(10)	239K(10)	PAG5	C7	68Ω	MB 24-29 PAR ODD H	271(10)	PAG3	A7	68Ω	PAG3 HI H
238(3)	PAG5	B3	68Ω	%E17(15)	245(10)	PAG8	B7	68Ω	%E70K(11)	242(1)	PAG5	C7	68Ω	MB 30-35 PAR ODD H	214(5)	PAG3	B2	68Ω	-PAG3 PT 0 EVEN EN A H
256(1)	PAG5	B3	68Ω	%E17(2)	268(5)	PAG8	B7	68Ω	%E70K(12)	251(5)	PAG3	A5	68Ω	MB SEL 1 H	263(1)	PAG3	B2	68Ω	-PAG3 PT 0 EVEN EN B H
218(3)	PAG1	D3	68Ω	%E19K(3)	24(5)	PAG8	B6	68Ω	%E70K(13)	217(8)	PAG3	A5	68Ω	MB SEL 2 H	213(6)	PAG3	C4	68Ω	-PAG3 PT 0 LRU H
238(1)	PAG5	C7	68Ω	%E2(10)	24(10)	PAG8	C7	68Ω	%E70K(3)	271(8)	PAG4	D8	68Ω	MCL VMA USER H	244(8)	PAG3	D1	68Ω	PAG3 PT 0 MATCH H
239(6)	PAG5	D7	68Ω	%E2(13)	24(8)	PAG8	C7	68Ω	%E70K(4)	269(3)	PAG3	B7	68Ω	MCL2 VMA EXEC H	240(1)	PAG3	D1	68Ω	-PAG3 PT 0 MATCH H
239(8)	PAG5	D7	68Ω	%E2(3)	24(1)	PAG8	C7	68Ω	%E70K(5)	27(1)	PAG4	A4	68Ω	-MCL2 VMA WRITE H	266(10)	PAG3	D2	68Ω	PAG3 PT 0 NO MATCH IN A H
240(3)	PAG1	C3	68Ω	%E22K(14)	268(6)	PAG8	D7	68Ω	%E70K(6)	224(6)	PAG4	C2	68Ω	MCL3 PAGE ADDRESS COND H	215(3)	PAG3	A2	68Ω	-PAG3 PT 0 ODD EN A H
266(6)	PAG3	D2	68Ω	%E23(3)	266(1)	PAG3	B3	68Ω	%E8(14)	29(5)	PAG4	D7	68Ω	MCL3 PAGE ILL ENTRY H	263(3)	PAG3	A2	68Ω	-PAG3 PT 0 ODD EN B H
269(5)	PAG5	C2	68Ω	%E26(2)	266(5)	PAG3	A3	68Ω	%E8(15)	211(6)	PAG4	C4	68Ω	-MCL3 PAGE ILL' ENTRY H	216(3)	PAG3	D2	68Ω	-PAG3 PT 0 WR 26 H
212(5)	PAG9	D2	68Ω	%E28(2)	257(3)	PAG1	B3	68Ω	%E8(2)	236(6)	PAG4	B6	68Ω	MCL3 PAGE TEST PRIVATE H	214(1)	PAG3	B1	68Ω	-PAG3 PT 0 WR A H
240(5)	PAG5	C3	68Ω	%E29K(14)	257(5)	PAG1	A3	68Ω	%E8(3)	253(10)	PAG4	C2	68Ω	MCL5 VMA ADR ERR H	263(5)	PAG3	B1	68Ω	-PAG3 PT 0 WR B H
236(3)	PAG6	B7	68Ω	%E29K(2)	244(10)	PAG3	A7	68Ω	%E81(2)	248(1)	PAG4	D7	68Ω	MCL6 PAGE LIEBR REF H	253(6)	PAG3	C2	68Ω	-PAG3 PT 1 WR 26 H
229(8)	PAG8	C6	68Ω	%E30K(14)	217(10)	PAG3	A7	68Ω	%E81(5)	241(8)	PAG1	B2	68Ω	PAG1 PT 0 26 H	214(6)	PAG3	D7	68Ω	PAG3 PT ADR 18 H
211(10)	PAG8	C3	68Ω	%E30K(15)	263(6)	PAG8	B5	68Ω	%E83(15)	243(1)	PAG1	C7	68Ω	PAG1 PT 0 ACCESS H	230(10)	PAG3	D7	68Ω	-PAG3 PT ADR 18 H
22(8)	PAG4	C4	68Ω	%E34(15)	263(8)	PAG8	B5	68Ω	%E83(2)	259(8)	PAG1	C3	68Ω	PAG1 PT 0 CACHE H	224(3)	PAG3	B7	68Ω	PAG3 PT ADR 19 H
268(1)	PAG4	C7	68Ω	%E34(3)	263(10)	PAG8	B5	68Ω	%E83(3)	268(3)	PAG1	B7	68Ω	PAG1 PT 0 PUBLIC H	230(8)	PAG3	B7	68Ω	-PAG3 PT ADR 19 H
227(1)	PAG8	B5	68Ω	%E35(15)	262(8)	PAG8	B5	68Ω	%E84(14)	219(10)	PAG1	B5	68Ω	PAG1 PT 0 SOFTWARE H	214(8)	PAG3	B7	68Ω	PAG3 PT ADR 20 H
218(1)	PAG1	D3	68Ω	%E35(3)	215(10)	PAG8	C5	68Ω	%E84(15)	262(5)	PAG1	C5	68Ω	PAG1 PT 0 WRITABLE H	220(6)	PAG3	B7	68Ω	-PAG3 PT ADR 20 H
227(8)	PAG8	C3	68Ω	%E35(7)	227(3)	PAG8	D5	68Ω	%E84(2)	237(1)	PAG1	B1	68Ω	PAG1 PT 1 26 H	215(8)	PAG3	D7	68Ω	PAG3 PT ADR 21 H
224(10)	PAG6	B7	68Ω	%E38(15)	252(3)	PAG8	D5	68Ω	%E84(3)	259K(10)	PAG1	C7	68Ω	PAG1 PT 1 ACCESS H	264(1)	PAG3	D7	68Ω	-PAG3 PT ADR 21 H
238(10)	PAG5	B4	68Ω	%E4(2)	214(10)	PAG8	C5	68Ω	%E87(15)	259(6)	PAG1	C3	68Ω	PAG1 PT 1 CACHE H	227(5)	PAG3	D7	68Ω	PAG3 PT ADR 22 H
29(6)	PAG4	D4	68Ω	%E40K(2)	253(8)	PAG4	C2	68Ω	%E99K(6)	235(10)	PAG1	A3	68Ω	-PAG1 PT 1 EV EN 26 H	220(8)	PAG3	D7	68Ω	-PAG3 PT ADR 22 H
235(1)	PAG3	D5	68Ω	%E51(2)	211(5)	PAG3	B7	68Ω	APR5 WR PT SEL 0 H	246(1)	PAG1	A3	68Ω	-PAG1 PT 1 OD EN 26 H	253(5)	PAG3	C7	68Ω	PAG3 PT ADR 23 H
235(3)	PAG3	C5	68Ω	%E51(21)	251(1)	PAG3	B7	68Ω	APR5 WR PT SEL 1 H	268(6)	PAG1	A7	68Ω	PAG1 PT 1 PUBLIC H	264(5)	PAG3	C7	68Ω	-PAG3 PT ADR 23 H
271(1)	PAG3	C5	68Ω	%E51(23)	237(5)	PAG3	B5	68Ω	-APR5 WR PT SEL 1 H	219(8)	PAG1	A5	68Ω	PAG1 PT 1 SOFTWARE H	246(8)	PAG3	A5	68Ω	PAG3 PT ADR 24 H
271(6)	PAG3	C5	68Ω	%E51(4)	236(1)	PAG6	B7	68Ω	AR 23 C H	262(3)	PAG1	C5	68Ω	PAG1 PT 1 WRITABLE H	270(5)	PAG3	A5	68Ω	-PAG3 PT ADR 24 H
223(5)	PAG9	C5	68Ω	%E55K(2)	247(1)	PAG3	C7	68Ω	-CLK2 PT DIR WR H	243(3)	PAG1	D7	68Ω	-PAG1 PT ACCESS H	22(3)	PAG5	B6	68Ω	PAG3 PT ADR 25 A IN H

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

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REVISIONS	CHK	CHANGE NO.	REV

	DATE: 08-AUG-84 CHK'D: J. J. J. J. D. DELLORCO	ENG: C. KACZOR DATE: 08-AUG-84 BOARD LOCATION: 4AF-30	DATE: 08-AUG-84 SHEET: 1 OF 3	TITLE: ETCH PAGER TERMINATORS
	NEXT HIGHER ASSEMBLY: MCA25 FIRST USED ON OPTION/MODEL: MCA25	SIZE: D CODE: CS	NUMBER: M855-0-RES	REV: B

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
22(5)	PAG3	A5	68Ω	PAG3 PT ADR 25 B IN H	262(10)	PAG4	B3	68Ω	-PAG4 PF CODE 2X H	21(8)	PAG6	B1	68Ω	PAG6 PT 1 KEEP 04 H	267(3)	PAG9	D1	68Ω	-PAG9 PT 1 MATCH H
267(5)	PAG3	A7	68Ω	PAG3 PT ADR 26 H	238(8)	PAG5	A7	68Ω	PAG5 KI10 PAGE MODE H	225(10)	PAG6	B1	68Ω	PAG6 PT 1 KEEP 05 H	267(6)	PAG9	D2	68Ω	PAG9 PT 1 NO MATCH IN A H
266(8)	PAG3	A7	68Ω	-PAG3 PT ADR 26 H	240(6)	PAG5	B1	68Ω	PAG5 PT 0 EV PG PAR H	21(10)	PAG6	B1	68Ω	PAG6 PT 1 KEEP 06 H	226(10)	PAG9	A3	68Ω	-PAG9 PT 1 ODD EN C H
260(1)	PAG3	B7	68Ω	PAG3 PT ADR 26 A IN H	257(1)	PAG5	B1	68Ω	PAG5 PT 0 OD PG PAR H	243(10)	PAG6	A1	68Ω	PAG6 PT 1 KEEP 07 H	218(10)	PAG9	A3	68Ω	-PAG9 PT 1 ODD EN D H
213(3)	PAG3	B5	68Ω	PAG3 PT DIR CLR H	240(10)	PAG5	B1	68Ω	PAG5 PT 1 EV PG PAR H	216(1)	PAG6	B6	68Ω	PAG6 PT IN KEEP A H	212(1)	PAG9	B2	68Ω	PAG9 PT 1 WR C H
213(10)	PAG3	B5	68Ω	-PAG3 PT DIR CLR H	240(8)	PAG5	B1	68Ω	PAG5 PT 1 OD PG PAR H	253(1)	PAG6	A6	68Ω	PAG6 PT IN KEEP B H	256(5)	PAG9	B1	68Ω	-PAG9 PT 1 WR C H
R2(1)	PAG3	B7	100Ω	-PAG3 PT DIR WR H	226(3)	PAG5	C7	68Ω	-PAG5 PT ADR 18 A H	249(8)	PAG6	C7	68Ω	PAG6 PT 0 20 H	256(8)	PAG9	B1	68Ω	-PAG9 PT 1 WR D H
R1(1)	PAG3	B7	100Ω	-PAG3 PT DIR WR DLY H	218(3)	PAG5	C7	68Ω	PAG5 PT ADR 18 B H	241(5)	PAG7	B7	68Ω	PAG7 PT 0 21 H	211(8)	PAG9	B7	68Ω	PAG9 TB 1 DIR H
229(6)	PAG3	C7	68Ω	-PAG3 PT DIR WR DLY A H	254(5)	PAG5	B7	68Ω	-PAG5 PT ADR 19 A H	232(5)	PAG7	C4	68Ω	PAG7 PT 0 22 H	220(5)	PAG9	B7	68Ω	-PAG9 TB 1 DIR EN H
26(8)	PAG3	A1	68Ω	PAG3 PT EN A H	228(6)	PAG5	B7	68Ω	PAG5 PT ADR 19 B H	241(1)	PAG7	B4	68Ω	PAG7 PT 0 23 H	267(8)	PAG9	A4	68Ω	PAG9 TB 1 WR TWO H
265(5)	PAG3	A1	68Ω	-PAG3 PT EN A H	226(5)	PAG5	B7	68Ω	-PAG5 PT ADR 20 A H	232(3)	PAG7	C1	68Ω	PAG7 PT 0 24 H	27(10)	PAG2	D7	68Ω	PMA3 PA 14 H
245(6)	PAG3	A1	68Ω	PAG3 PT EN B H	228(8)	PAG5	B7	68Ω	PAG5 PT ADR 20 B H	232(6)	PAG7	B2	68Ω	PAG7 PT 0 25 H	26(10)	PAG2	D5	68Ω	PMA3 PA 15 H
250(6)	PAG3	A1	68Ω	-PAG3 PT EN B H	254(6)	PAG5	B7	68Ω	-PAG5 PT ADR 21 A H	223(8)	PAG7	C7	68Ω	PAG7 PT 1 20 H	26(5)	PAG2	D5	68Ω	PMA3 PA 16 H
27(5)	PAG3	D2	68Ω	PAG3 PT MATCH H	218(5)	PAG5	B7	68Ω	PAG5 PT ADR 21 B H	241(6)	PAG7	A7	68Ω	PAG7 PT 1 21 H	234(10)	PAG2	D4	68Ω	PMA3 PA 17 H
231(1)	PAG3	B5	68Ω	PAG3 PT WR BOTH HALVES H	246(5)	PAG5	C5	68Ω	-PAG5 PT ADR 22 A H	235(8)	PAG7	C4	68Ω	PAG7 PT 1 22 H	245(3)	PAG2	D4	68Ω	PMA3 PA 18 H
25(1)	PAG3	B5	68Ω	-PAG3 PT WR BOTH HALVES H	234(8)	PAG5	C5	68Ω	PAG5 PT ADR 22 B H	242(8)	PAG7	A4	68Ω	PAG7 PT 1 23 H	245(8)	PAG2	D3	68Ω	PMA3 PA 19 H
220(10)	PAG3	C1	68Ω	PAG3 SEL TB 0 H	216(10)	PAG5	B5	68Ω	-PAG5 PT ADR 23 A H	237(8)	PAG7	C1	68Ω	PAG7 PT 1 24 H	29(8)	PAG7	D5	68Ω	PMA3 PA 20 H
229(3)	PAG3	C1	68Ω	-PAG3 SEL TB 0 H	262(6)	PAG5	B5	68Ω	PAG5 PT ADR 23 B H	234(6)	PAG7	A2	68Ω	PAG7 PT 1 25 H	241(3)	PAG7	D4	68Ω	PMA3 PA 21 H
229(5)	PAG3	B5	68Ω	PAG3 TB 0 DIR H	226(8)	PAG5	B5	68Ω	-PAG5 PT ADR 24 A H	24(6)	PAG8	D4	68Ω	PAG8 CLR VALID H	234(5)	PAG7	D4	68Ω	PMA3 PA 22 H
264(3)	PAG3	B5	68Ω	-PAG3 TB 0 DIR EN H	25(3)	PAG5	B5	68Ω	PAG5 PT ADR 24 B H	224(5)	PAG8	D6	68Ω	-PAG8 FORCE VALID 0 H	242(10)	PAG7	D3	68Ω	PMA3 PA 23 H
266(3)	PAG3	A2	68Ω	PAG3 TB 0 WR TWO H	222(8)	PAG5	B5	68Ω	-PAG5 PT ADR 25 A H	253(3)	PAG8	D6	68Ω	-PAG8 FORCE VALID 1 H	237(6)	PAG7	D6	68Ω	PMA3 PA 24 H
233(1)	PAG4	D1	68Ω	PAG4 FORCE TB CLK H	257(6)	PAG5	B5	68Ω	PAG5 PT ADR 25 B H	224(1)	PAG8	C6	68Ω	-PAG8 FORCE VAL'D 2 H	237(10)	PAG7	D5	68Ω	PMA3 PA 25 H
233(8)	PAG4	A6	68Ω	-PAG4 FORCE TO TB 0 H	264(6)	PAG5	A5	68Ω	-PAG5 PT ADR 25 C H	252(6)	PAG8	C6	68Ω	-PAG8 FORCE VALID 3 H	226(6)	PAG1	C8	68Ω	PT IN 00 H
233(3)	PAG4	A6	68Ω	PAG4 FORCE TO TB 1 H	222(3)	PAG5	A5	68Ω	PAG5 PT ADR 25 D H	215(6)	PAG8	B6	68Ω	-PAG8 FORCE VALID 4 H	218(1)	PAG1	B8	68Ω	PT IN 01 H
220(1)	PAG4	D1	68Ω	-PAG4 MBOX CTL 03 H	22(10)	PAG5	C1	68Ω	PAG5 PT PAR ODD H	215(5)	PAG8	B6	68Ω	-PAG8 FORCE VALID 5 H	226(1)	PAG1	C6	68Ω	PT IN 02 H
260(5)	PAG4	D7	68Ω	PAG4 PAGE EXEC PAGED REF H	249(3)	PAG5	C1	68Ω	-PAG5 PT PAR ODD H	215(1)	PAG8	B6	68Ω	-PAG8 FORCE VALID 6 H	218(6)	PAG1	B6	68Ω	PT IN 03 H
225(3)	PAG4	D7	68Ω	-PAG4 PAGE EXEC PAGED REF H	229(10)	PAG6	D6	68Ω	PAG6 PT 0 KEEP H	214(3)	PAG8	A6	68Ω	-PAG8 FORCE VALID 7 H	220(10)	PAG1	C4	68Ω	PT IN 04 H
240(3)	PAG4	D7	68Ω	PAG4 PAGE EXEC REF H	244(1)	PAG6	D4	68Ω	PAG6 PT 0 KEEP 00 H	233(5)	PAG8	D2	68Ω	PAG8 PT 0 VALID H	255(8)	PAG2	C7	68Ω	PT IN 05 H
29(1)	PAG4	D7	68Ω	-PAG4 PAGE EXEC REF H	23(5)	PAG6	D4	68Ω	PAG6 PT 0 KEEP 01 H	271(3)	PAG8	D2	68Ω	-PAG8 PT 0 VALID H	264(10)	PAG2	B7	68Ω	PT IN 06 H
26(1)	PAG4	C3	68Ω	PAG4 PAGE FAIL A H	244(3)	PAG6	D4	68Ω	PAG6 PT 0 KEEP 02 H	211(1)	PAG8	D4	68Ω	PAG8 PT 0 VALID A H	255(6)	PAG2	C5	68Ω	PT IN 07 H
225(6)	PAG4	C3	68Ω	-PAG4 PAGE FAIL A H	23(1)	PAG6	C4	68Ω	PAG6 PT 0 KEEP 03 H	212(10)	PAG8	B4	68Ω	PAG8 PT 0 VALID B H	228(1)	PAG2	B5	68Ω	PT IN 08 H
265(3)	PAG4	B6	68Ω	PAG4 PAGE TEST PRIVATE H	23(10)	PAG6	B4	68Ω	PAG6 PT 0 KEEP 04 H	233(6)	PAG8	D2	68Ω	-PAG8 PT 1 VALID H	255(5)	PAG2	C2	68Ω	PT IN 09 H
225(5)	PAG4	B6	68Ω	-PAG4 PAGE TEST PRIVATE H	244(5)	PAG6	B4	68Ω	PAG6 PT 0 KEEP 05 H	224(8)	PAG8	D1	68Ω	PAG8 PT 1 VALID A H	255(1)	PAG2	B2	68Ω	PT IN 10 H
259(1)	PAG4	C6	68Ω	-PAG4 PAGE TEST WRITE H	23(8)	PAG6	B4	68Ω	PAG6 PT 0 KEEP 06 H	252(10)	PAG8	B1	68Ω	PAG8 PT 1 VALID B H	222(10)	PAG7	C7	68Ω	PT IN 11 H
29(3)	PAG4	C7	68Ω	-PAG4 PAGE UNPAGED REF H	244(6)	PAG6	A4	68Ω	PAG6 PT 0 KEEP 07 H	260(10)	PAG8	A6	68Ω	-PAG8 PT DIR CLR A H	257(10)	PAG7	B7	68Ω	PT IN 12 H
28(3)	PAG4	D7	68Ω	PAG4 PAGE USER PAGED REF H	212(6)	PAG6	C6	68Ω	PAG6 PT 1 KEEP H	225(8)	PAG8	A6	68Ω	-PAG8 PT DIR CLR B H	222(5)	PAG7	C5	68Ω	PT IN 13 H
225(1)	PAG4	D7	68Ω	-PAG4 PAGE USER PAGED REF H	243(8)	PAG6	D1	68Ω	PAG6 PT 1 KEEP 00 H	267(1)	PAG8	A6	68Ω	PAG8 SLEEP ALL H	257(8)	PAG7	B5	68Ω	PT IN 14 H
259(5)	PAG4	D4	68Ω	PAG4 PAGE WRITE OK H	243(6)	PAG6	D1	68Ω	PAG6 PT 1 KEEP 01 H	254(10)	PAG9	A3	68Ω	-PAG9 PT 1 EVEN EN C H	256(3)	PAG7	C2	68Ω	PT IN 15 H
249(1)	PAG4	D4	68Ω	-PAG4 PAGE WRITE OK H	21(6)	PAG6	D1	68Ω	PAG6 PT 1 KEEP 02 H	218(8)	PAG9	A3	68Ω	-PAG9 PT 1 EVEN EN D H	230(6)	PAG7	B2	68Ω	PT IN 16 H
252(1)	PAG4	A2	68Ω	-PAG4 PAGED REF H	243(5)	PAG6	C1	68Ω	PAG6 PT 1 KEEP 03 H	22(1)	PAG9	D1	68Ω	PAG9 PT 1 MATCH H	222(1)	PAG1	B3	68Ω	PT IN 17 H

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND (<) INDICATES PIN NUMBER

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REVISIONS	CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	06-AUG-84	C. KACZOR	06-AUG-84	ETCH PAGER TERMINATORS
CHK'D	DATE	BOARD LOCATION:	44F30	
D. DELLORCO	06-AUG-84	SHEET	2	
24-JUL-84 11:47	NEXT HIGHER ASSEMBLY:	SIZE CODE	D CS	NUMBER
FIRST USED ON OPTION MODEL: MCA25	D-DD-M855-0			M855-0-RES
				REV. B

D D

C C

B B

A A

RESISTOR LOC(PIN)	SHOWN ON DRW#	ON REF	VALUE	TERMINATES SIGNAL
254(8)	PAG1	C8	68n	PT IN 18 H
250(1)	PAG1	A8	68n	PT IN 19 H
254(3)	PAG1	C6	68n	PT IN 20 H
262(1)	PAG1	A6	68n	PT IN 21 H
254(1)	PAG1	C4	68n	PT IN 22 H
255(10)	PAG2	C7	68n	PT IN 23 H
254(8)	PAG2	A7	68n	PT IN 24 H
228(5)	PAG2	B5	68n	PT IN 25 H
256(10)	PAG2	A5	68n	PT IN 26 H
255(3)	PAG2	C2	68n	PT IN 27 H
228(3)	PAG2	A2	68n	PT IN 28 H
270(1)	PAG7	C7	68n	PT IN 29 H
230(1)	PAG7	A7	68n	PT IN 30 H
222(6)	PAG7	C5	68n	PT IN 31 H
232(10)	PAG7	B5	68n	PT IN 32 H
230(5)	PAG7	C2	68n	PT IN 33 H
256(6)	PAG7	B2	68n	PT IN 34 H
246(3)	PAG1	A3	68n	PT IN 35 H
213(5)	PAG5	A4	68n	SH AR PAR ODD A H
220(3)	PAG3	C6	68n	VMA2 VMA 13 A H
270(10)	PAG3	C6	68n	VMA2 VMA 14 A H
270(8)	PAG3	C6	68n	VMA2 VMA 15 A H
219(3)	PAG3	D4	68n	VMA2 VMA 16 A H
270(3)	PAG3	B7	68n	VMA2 VMA 17 H
219(5)	PAG3	C4	68n	VMA2 VMA 17 A H
242(6)	PAG3	D8	68n	VMA2 VMA 18 H
269(1)	PAG3	B7	68n	VMA2 VMA 19 H
270(6)	PAG3	B7	68n	VMA2 VMA 20 H
242(5)	PAG3	D8	68n	VMA2 VMA 21 H
236(8)	PAG3	D8	68n	VMA2 VMA 22 H
242(3)	PAG3	C8	68n	VMA2 VMA 23 H
251(10)	PAG3	A5	68n	VMA2 VMA 24 H
251(6)	PAG3	A7	68n	VMA2 VMA 25 H
261(1)	PAG3	A7	68n	VMA2 VMA 26 H

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

REV. B
 NUMBER M855-0-RES
 SIZE CODE D CS

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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE 06-AUG-84	ENG. C. KACZOR	DATE 06-AUG-84	TITLE: ETCH PAGER TERMINATORS
	CHK'D D. DELLORCO	BOARD LOCATION: 4AF30	SHEET 2 OF 2	
FIRST USED ON OPTION/MODEL: MCA25				NEXT HIGHER ASSEMBLY: D-DD-M855-0
SIZE	CODE	NUMBER	REV.	
D	CS	M855-0-RES	B	

DRAWING NUMBER	NO. OF SHT	PART NUMBER	DESCRIPTION	REVISION
		M856-00	MODULE REVISION	A1
E-UA-M856-0-0	2		CHD	A
K-PL-M856-0-DBP	2		PART LIST, M856	A
D-CS-M856-0-CHD1	1		CACHE DATA BITS [N+00],[N+01]	A
D-CS-M856-0-CHD2	1		CACHE DATA BITS [N+02],[N+03]	A
D-CS-M856-0-CHD3	1		CACHE DATA BITS [N+04],[N+05]	A
D-CS-M856-0-CHD4	1		CACHE DATA BITS [N+06],[N+07]	A
D-CS-M856-0-CHD5	1		CACHE DATA BIT [N+08]	A
D-CS-M856-0-CHD6	1		CACHE DATA ADDRESSING LOGIC	A
D-CS-M856-0-CHD7	1		CACHE SEL LOGIC TERMINATORS	A
D-CS-M856-0-CHD8	1		CACHE DATA TERMINATORS	A
D-CS-M856-0-RES	1		CACHE DATA TERMINATORS	A
D-CS-M856-0-RES	1		CACHE DATA TERMINATORS	A
K-PC-M856-0-DBI	-		P.C. DESIGN DATA BASE TAPE	A
D-DD-5017662-0	1		DRAWING DIRECTORY, 5017662	REF

D
C
B
A

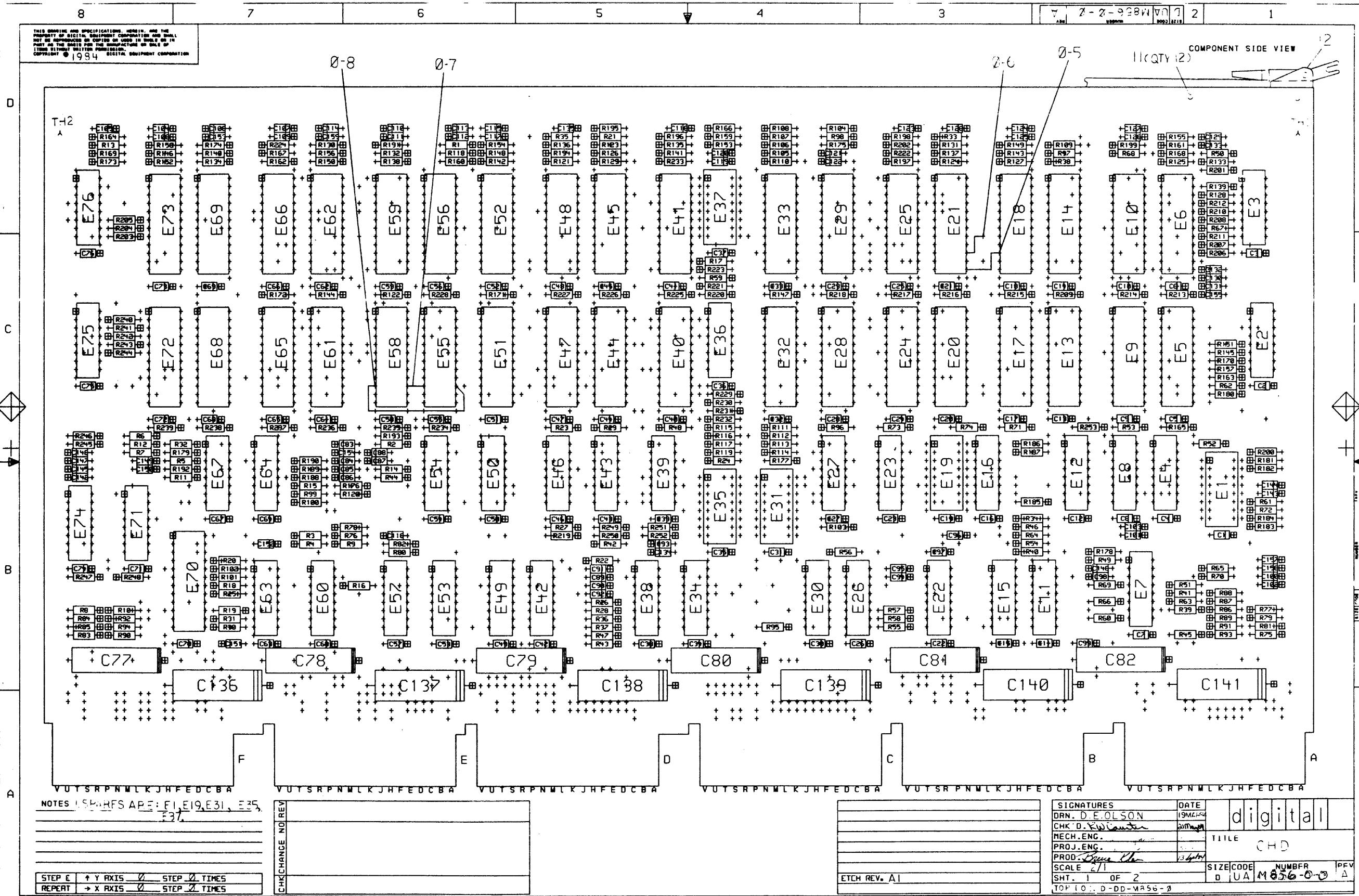
D
C
B
A

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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. <i>J. Jansky</i>	DATE 13-SEP-84	ENG. C.A. JENS	DATE 13-SEP-84	TITLE: M856
	CHK'D. D. DELLORCO	DATE 13-SEP-84	BOARD LOCATION: N/A	SHEET 1 OF 1	DRAWING DIRECTORY
DSK: M856A.T2PL4.21 113-SEP-84 14:24 NEXT HIGHER ASSEMBLY:					SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL: KL10-PW MCA25					D DD M856-0 A

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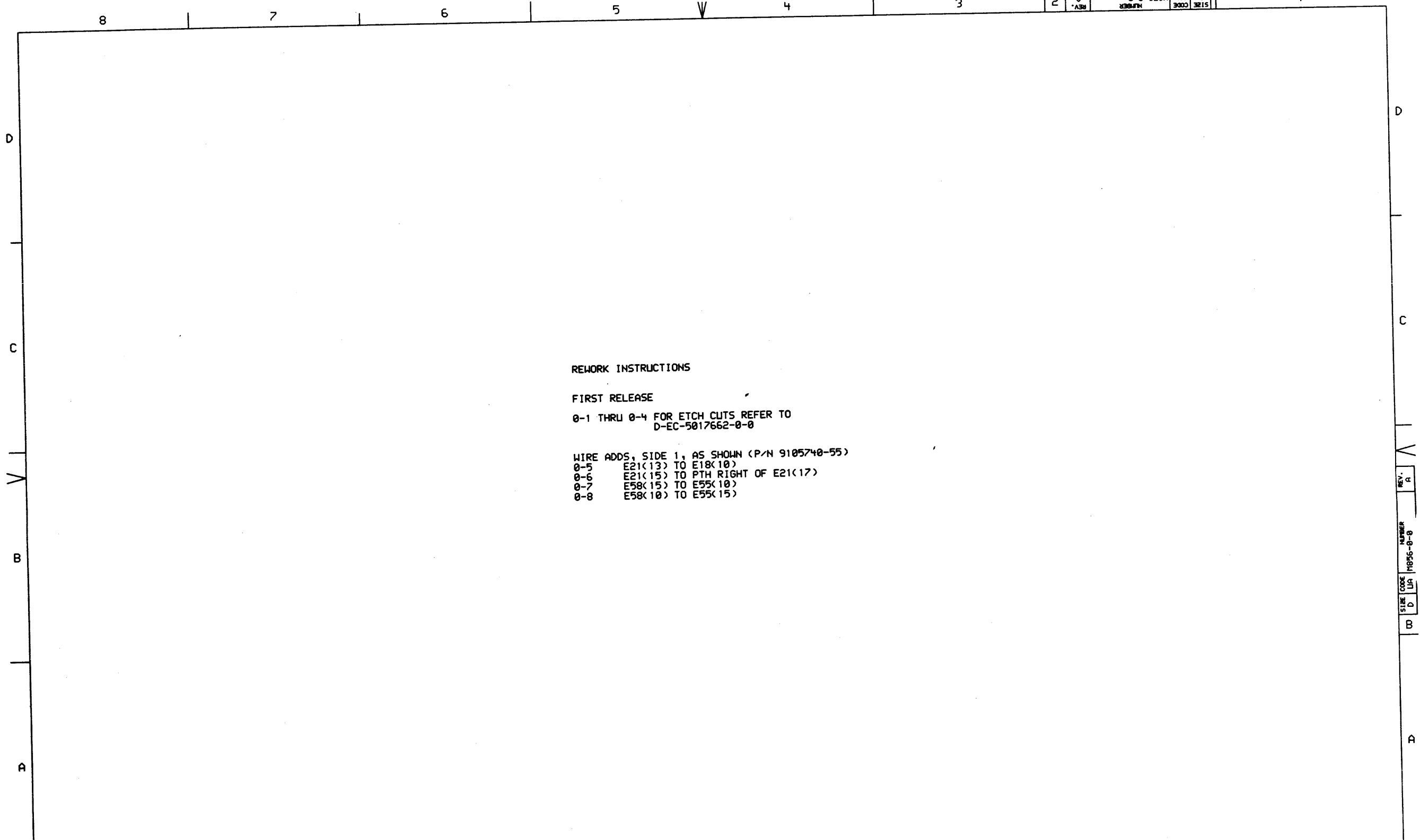
NOTES: PARALS ARE: E1, E19, E31, E35, E37.

STEP E	→ Y AXIS	0	STEP 0 TIMES
REPEAT	→ X AXIS	0	STEP 0 TIMES

CHK	CHANGE	NO	REV

ETCH REV. A1

SIGNATURES	DATE	digital
DRN. D.E. OLSON	19MAY84	
CHK. D. [Signature]	21MAY84	TITLE
MECH. ENG.		CHD
PROJ. ENG.		SIZE CODE
PROD. [Signature]	13 JUN 84	NUMBER
SCALE 2/1		d UA M856-0-0
SHT. 1 OF 2		PEV
TOP LO. D-DD-MA56-2		A



REWORK INSTRUCTIONS

FIRST RELEASE

0-1 THRU 0-4 FOR ETCH CUTS REFER TO
D-EC-5017662-0-0

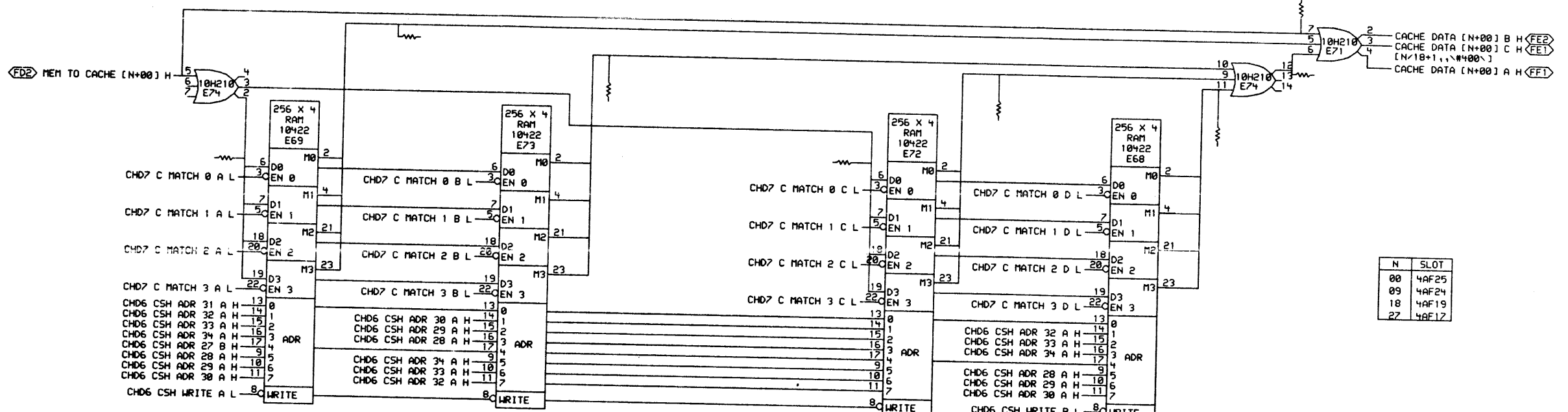
WIRE ADDS, SIDE 1, AS SHOWN (P/N 9105740-55)
 0-5 E21<13> TO E18<10>
 0-6 E21<15> TO PTH RIGHT OF E21<17>
 0-7 E58<15> TO E55<10>
 0-8 E58<10> TO E55<15>

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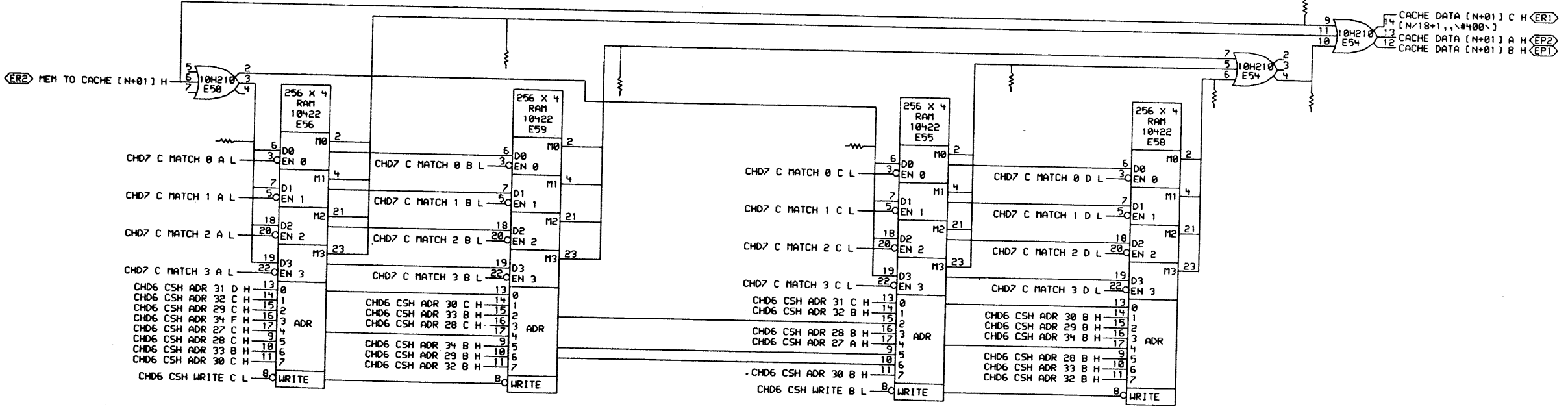
REVISIONS	
CHK	CHANGE NO. REV

digital	DRN: <i>J. J. J.</i>	DATE: 02-MAY-84	ENG. <i>Carb</i>	DATE: 12-Sep-84	TITLE: CHD
	CHK'D: <i>W. J. J.</i>	DATE: 12-Sep-84	BOARD LOCATION: 2	OF: 2	
FIRST USED ON OPTION/MODEL: D-DD-M856-0		NEXT HIGHER ASSEMBLY: D-DD-M856-0		SIZE CODE: D LIA	NUMBER: M856-0-0
				REV. A	

REV. A
NUMBER M856-0-0
SIZE CODE LIA
D
B



N	SLOT
00	4AF25
09	4AF24
18	4AF19
27	4AF12



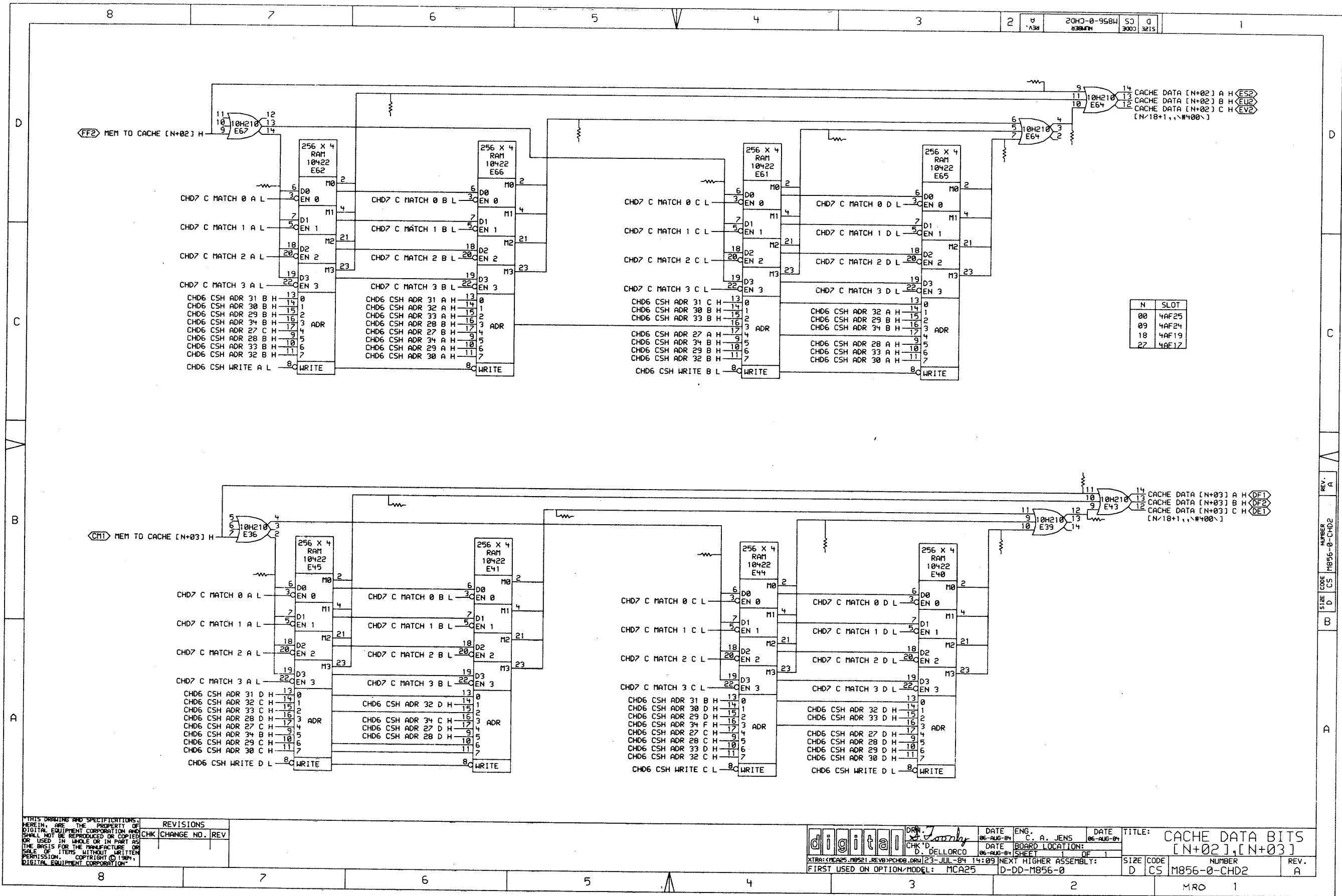
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REVISIONS		
CHK	CHANGE NO.	REV

digital *Don Jansky* DATE ENG. C. A. JENS DATE TITLE: CACHE DATA BITS [N+00], [N+01]
 06-AUG-84
 D. DELLORCO DATE BOARD LOCATION: 06-AUG-84
 23-JUL-84 14:09 NEXT HIGHER ASSEMBLY: 1 OF 1
 FIRST USED ON OPTION/MODEL: MCA25 D-DD-M856-0

SIZE	CODE	NUMBER	REV.
D	CS	M856-0-CHD1	A

MRO 1



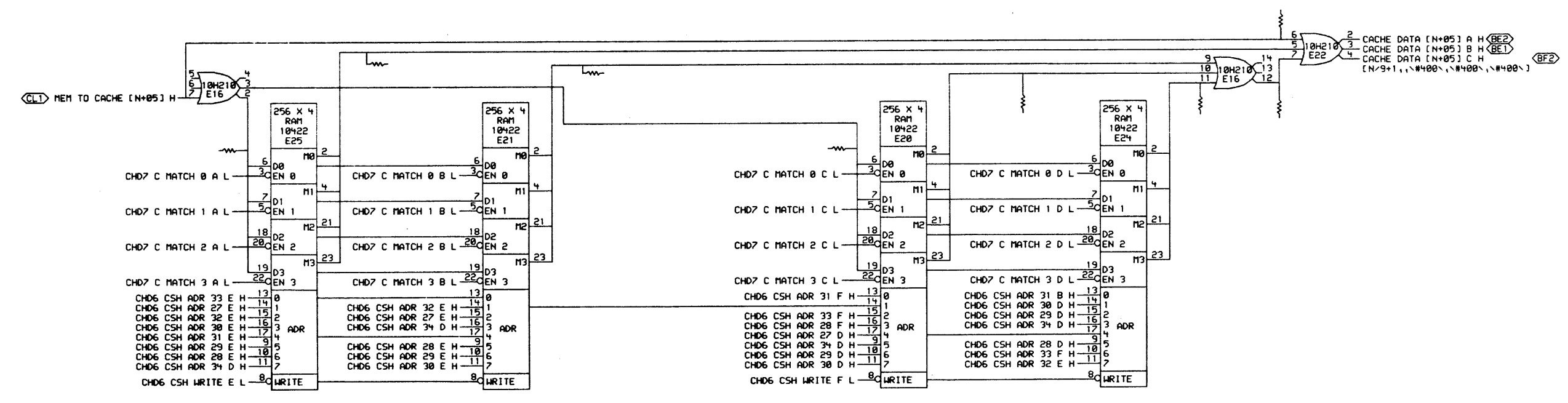
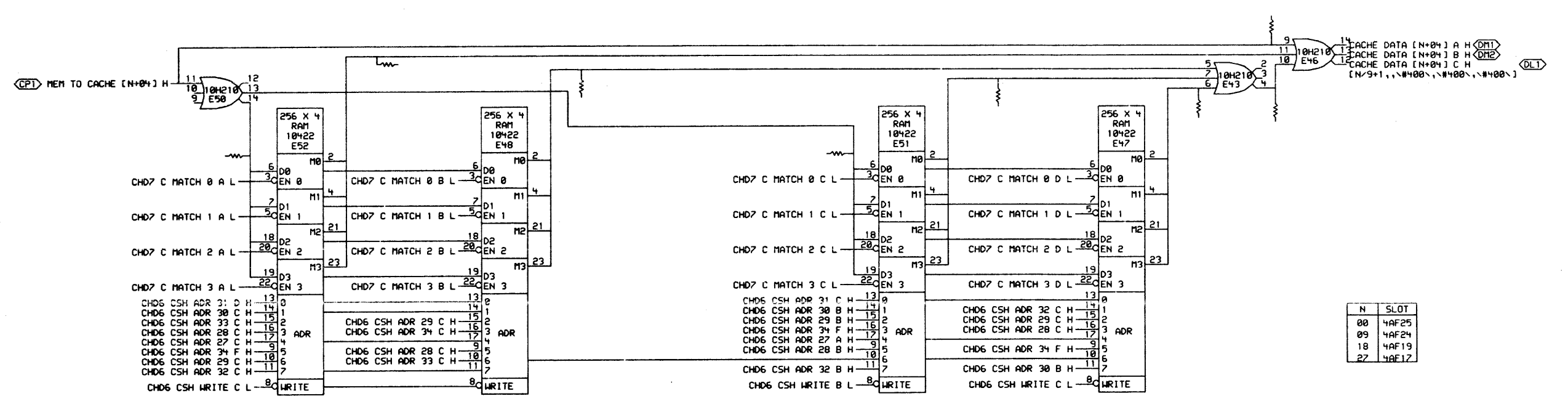
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REVISIONS
CHK CHANGE NO. REV

digital
 DATE 06-AUG-84
 ENG. C. A. JENS
 DATE 06-AUG-84
 BOARD LOCATION:
 SHEET 1 OF 1
 NEXT HIGHER ASSEMBLY:
 D-DD-M856-0

TITLE: CACHE DATA BITS [N+02],[N+03]
 SIZE CODE NUMBER REV.
 D CS M856-0-CHD2 A

REV. A
 NUMBER M856-0-CHD2
 SIZE CODE CS
 BOARD LOCATION
 SHEET 1 OF 1
 MRO 1

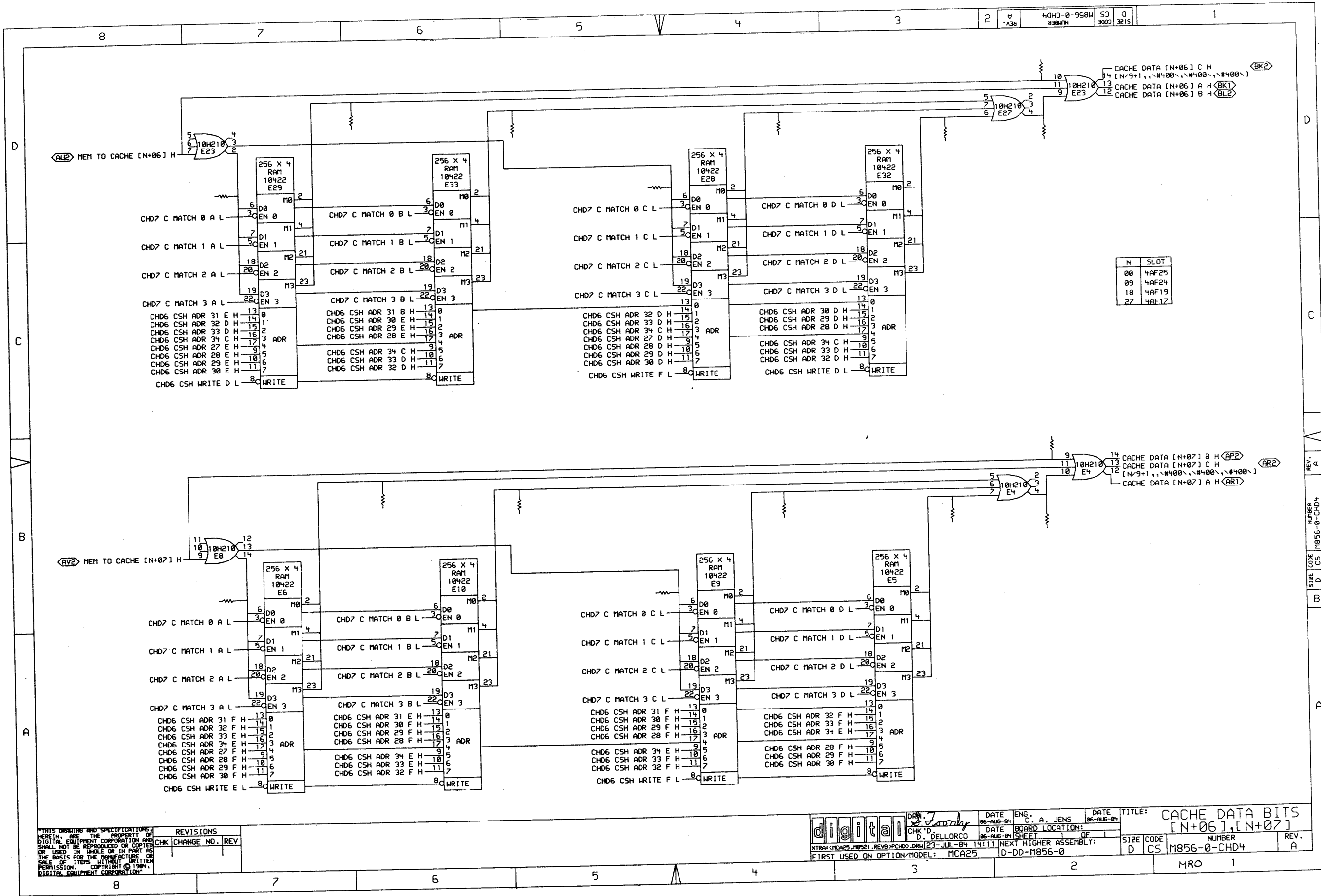


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REVISIONS	CHK	CHANGE NO.	REV.

digital
 DATE: 06-18-84
 ENG.: C. A. JENS
 DATE: 06-18-84
 BOARD LOCATION: SHEET 1 OF 1
 TITLE: CACHE DATA BITS [N+04],[N+05]
 SIZE CODE: D CS
 NUMBER: M856-0-CHD3
 REV.: A
 FIRST USED ON OPTION/MODEL: MCA25 D-DD-M856-0

REV. A
 NUMBER CHD3
 SIZE CODE CS
 M856-0-CHD3



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REVISIONS
CHK CHANGE NO. REV

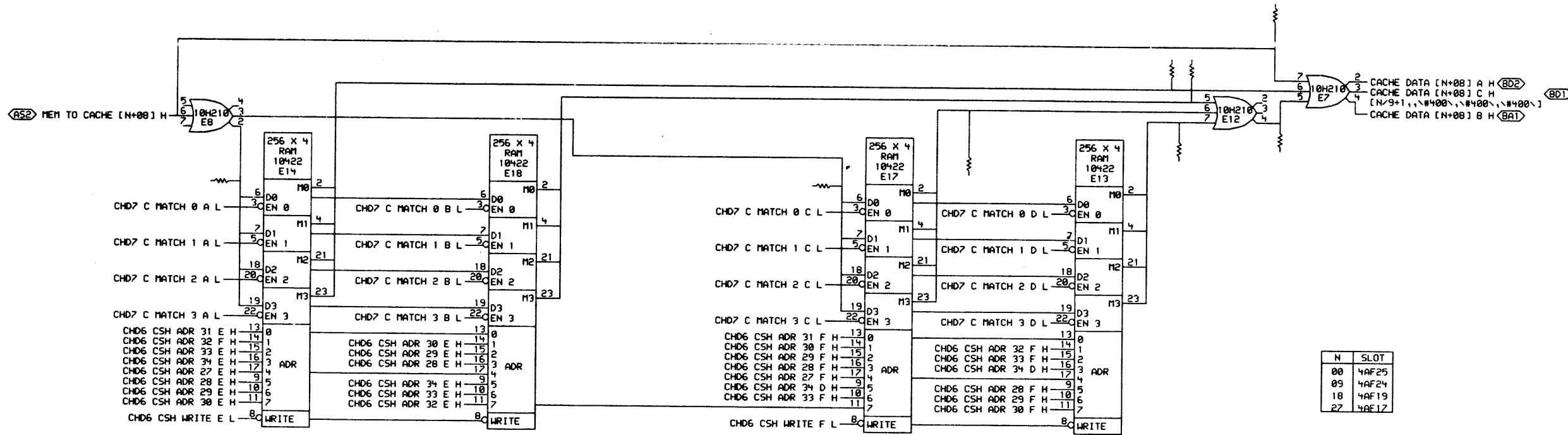
digital
 CHK'D
 D. DELLORCO
 06-AUG-84

DATE 06-AUG-84
 ENG. C. A. JENS
 DATE 06-AUG-84
 BOARD LOCATION: DE 1
 SHEET 1 OF 1
 NEXT HIGHER ASSEMBLY: D-DD-M856-0

TITLE: CACHE DATA BITS [N+06],[N+07]
 SIZE CODE NUMBER REV.
 D CS M856-0-CHD4 A

FIRST USED ON OPTION/MODEL: MCA25

MRO 1



N	SLOT
00	4AF25
09	4AF24
18	4AF19
27	4AF17

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CHK	CHANGE NO.	REV

digital DATA
D. DELLORCO
FIRST USED ON OPTION/MODEL: MCA25

DATE ENG. 06-AUG-84 C. A. JENS
DATE BOARD LOCATION: 06-AUG-84
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY: D-DD-M856-0

TITLE: CACHE DATA BIT [N+08]
SIZE CODE NUMBER REV.
D CS M856-0-CHD5 A

REV. A
NUMBER M856-0-CHD5
SIZE CODE CS

MRO 1

8

7

6

5

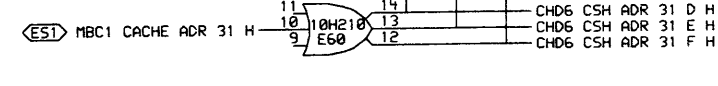
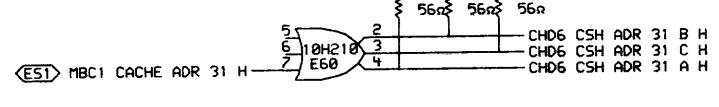
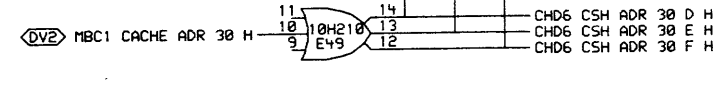
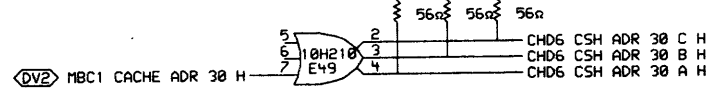
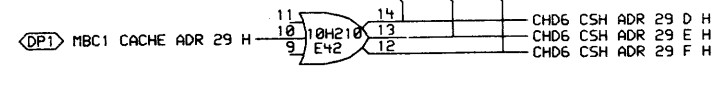
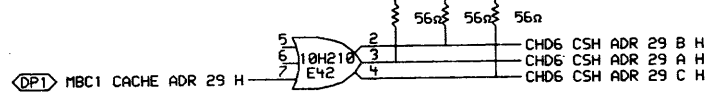
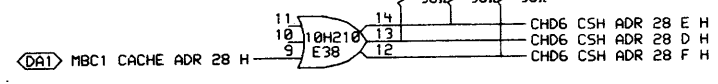
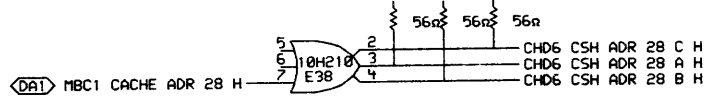
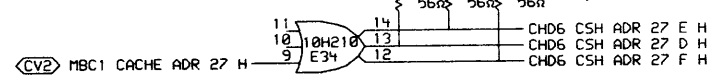
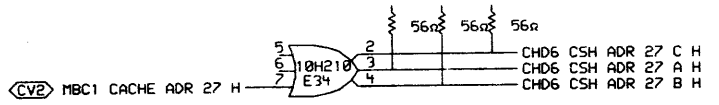
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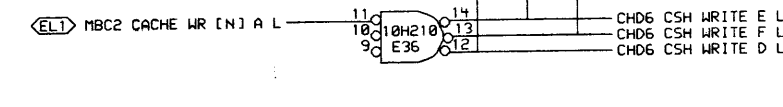
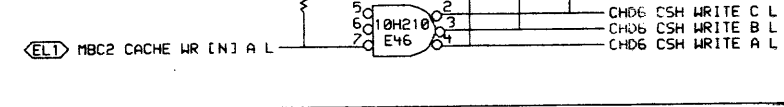
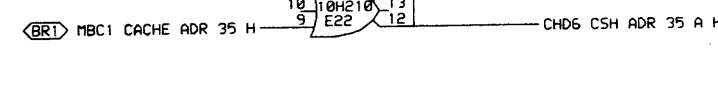
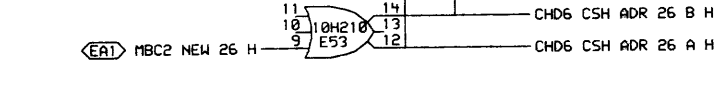
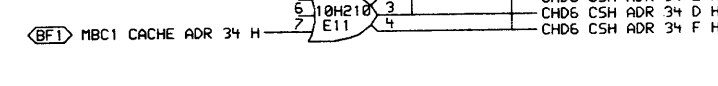
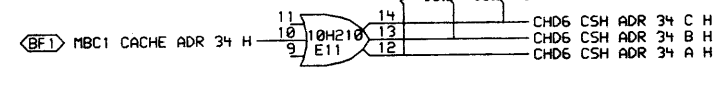
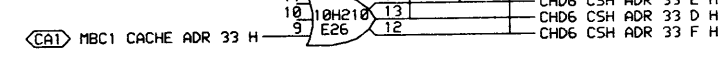
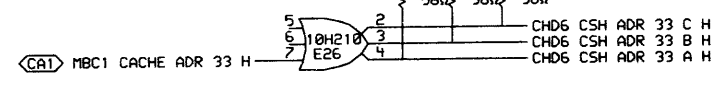
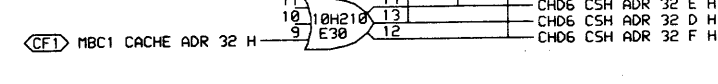
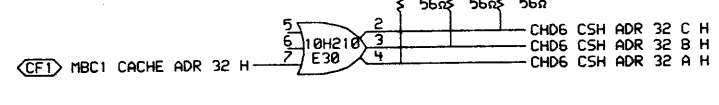
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REV. A
 NUMBER 99H3-0-9584
 CS 0
 SIZE 3215



N	SLOT
00	4AF25
09	4AF24
18	4AF19
27	4AF17



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REVISIONS		
CHK	CHANGE NO.	REV

digital
 DATE 06-AUG-84
 ENG. C. A. JENS
 DATE 06-AUG-84
 BOARD LOCATION: 1 OF 1
 SHEET 1 OF 1
 NEXT HIGHER ASSEMBLY: D-DD-M856-0
 FIRST USED ON OPTION/MODEL: MCA25

TITLE: CACHE DATA ADDRESSING LOGIC
 SIZE CODE NUMBER REV.
 D CS M856-0-CHD6 A

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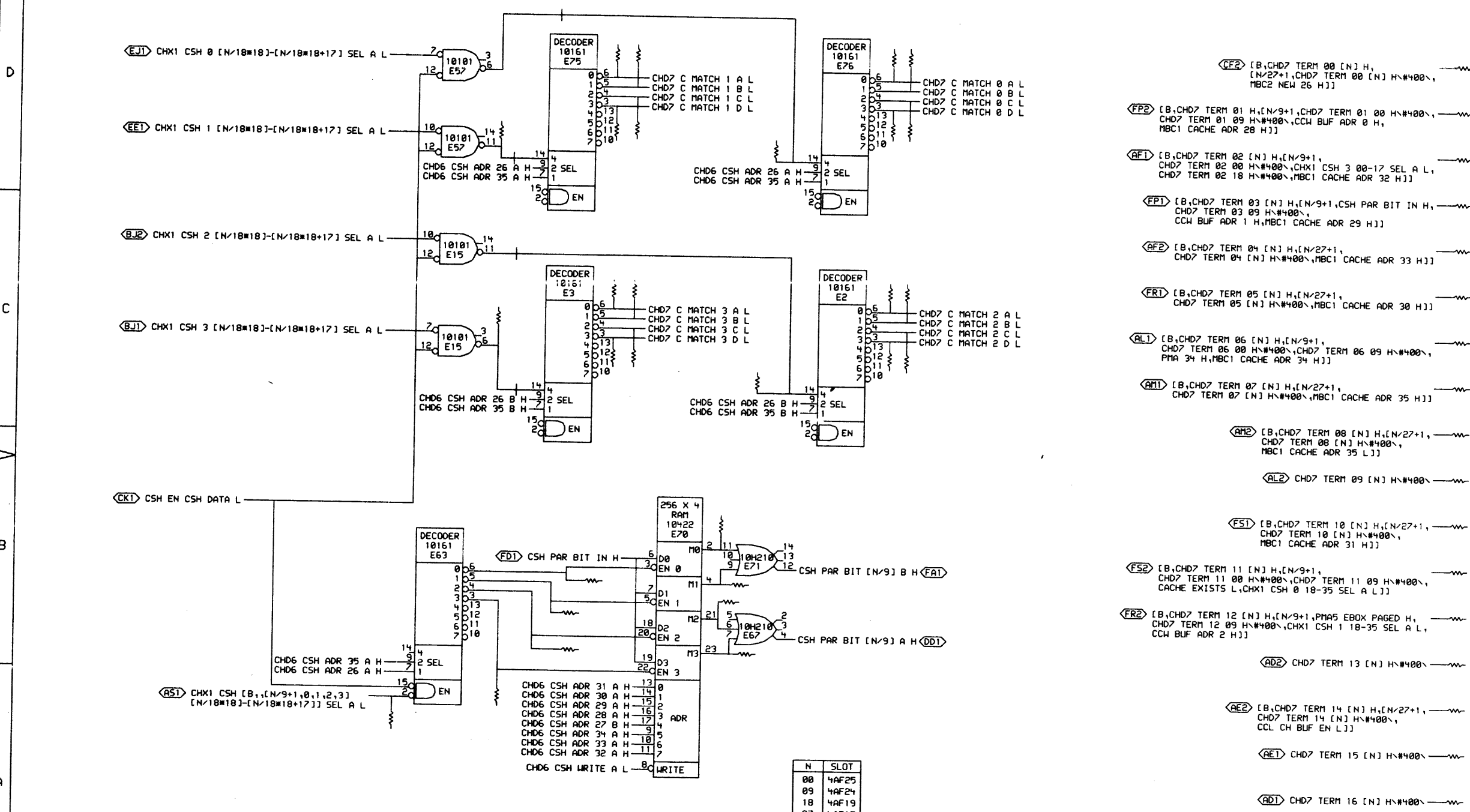
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MRO

REV. A
 NUMBER 99H3-0-9584
 CS 0
 SIZE 3215



- (CF2) [B,CHD7 TERM 00 [N] H, [N/27+1,CHD7 TERM 00 [N] H\#400\, MBC2 NEW 26 H]]
- (FP2) [B,CHD7 TERM 01 H,[N/9+1,CHD7 TERM 01 00 H\#400\, CHD7 TERM 01 09 H\#400\,CCW BUF ADR 0 H, MBC1 CACHE ADR 28 H]]
- (AF1) [B,CHD7 TERM 02 [N] H,[N/9+1, CHD7 TERM 02 00 H\#400\,CHX1 CSH 3 00-17 SEL A L, CHD7 TERM 02 18 H\#400\,MBC1 CACHE ADR 32 H]]
- (FP1) [B,CHD7 TERM 03 [N] H,[N/9+1,CHD7 TERM 03 09 H\#400\, CSH PAR BIT IN H, CCW BUF ADR 1 H,MBC1 CACHE ADR 29 H]]
- (AF2) [B,CHD7 TERM 04 [N] H,[N/27+1, CHD7 TERM 04 [N] H\#400\,MBC1 CACHE ADR 33 H]]
- (FR1) [B,CHD7 TERM 05 [N] H,[N/27+1, CHD7 TERM 05 [N] H\#400\,MBC1 CACHE ADR 30 H]]
- (AL1) [B,CHD7 TERM 06 [N] H,[N/9+1, CHD7 TERM 06 00 H\#400\,CHD7 TERM 06 09 H\#400\, PMA 34 H,MBC1 CACHE ADR 34 H]]
- (AM1) [B,CHD7 TERM 07 [N] H,[N/27+1, CHD7 TERM 07 [N] H\#400\,MBC1 CACHE ADR 35 H]]
- (AM2) [B,CHD7 TERM 08 [N] H,[N/27+1, CHD7 TERM 08 [N] H\#400\, MBC1 CACHE ADR 35 L]]
- (AL2) CHD7 TERM 09 [N] H\#400\
- (FS1) [B,CHD7 TERM 10 [N] H,[N/27+1, CHD7 TERM 10 [N] H\#400\, MBC1 CACHE ADR 31 H]]
- (FS2) [B,CHD7 TERM 11 [N] H,[N/9+1, CHD7 TERM 11 00 H\#400\,CHD7 TERM 11 09 H\#400\, CACHE EXISTS L,CHX1 CSH 0 18-35 SEL A L]]
- (FR2) [B,CHD7 TERM 12 [N] H,[N/9+1,PMA5 EBOX PAGED H, CHD7 TERM 12 09 H\#400\,CHX1 CSH 1 18-35 SEL A L, CCW BUF ADR 2 H]]
- (AD2) CHD7 TERM 13 [N] H\#400\
- (AE2) [B,CHD7 TERM 14 [N] H,[N/27+1, CHD7 TERM 14 [N] H\#400\, CCL CH BUF EN L]]
- (AE1) CHD7 TERM 15 [N] H\#400\
- (AD1) CHD7 TERM 16 [N] H\#400\
- (AF1) [B,CHD7 TERM 17 H,[N/9+1, CSH EN CSH DATA L, CHX1 CSH 2 00-17 SEL A L, PMA 35 H,MBC1 CACHE ADR 27 H]]

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CHK	CHANGE NO.	REV

digital
 DATE ENG. C. A. JENS DATE TITLE: CACHE SEL LOGIC TERMINATORS
 06-AUG-84
 DATE BOARD LOCATION: D. DELLORCO 06-AUG-84 SHEET 1 OF 1
 XTRAL (MCA25, M051, REV) PCHD6.DWG 23-JUL-84 14:13 NEXT HIGHER ASSEMBLY: FIRST USED ON OPTION/MODEL: MCA25 D-DD-M856-0 SIZE CODE NUMBER REV. D CS M856-0-CHD7 A

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538-0-9584 CS D
REV. 4 1333 3215

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
R52(1)	CHD4	B5	68n	%E10(2)
R69(1)	CHD5	C2	68n	%E12(4)
R178(1)	CHD5	C3	68n	%E13(2)
R66(1)	CHD5	C3	68n	%E14(2)
R151(1)	CHD7	C5	68n	%E15(11)
R50(1)	CHD7	C6	68n	%E15(6)
R55(1)	CHD3	B2	68n	%E16(12)
R197(1)	CHD3	B7	68n	%E16(2)
R218(1)	CHD3	B4	68n	%E16(3)
R185(1)	CHD5	C4	68n	%E17(2)
R186(1)	CHD5	C3	68n	%E18(2)
R34(1)	CHD3	B3	68n	%E20(2)
R46(1)	CHD3	B5	68n	%E21(2)
R118(1)	CHD4	D7	68n	%E23(2)
R229(1)	CHD4	D4	68n	%E23(3)
R187(1)	CHD3	B2	68n	%E24(2)
R58(1)	CHD3	B7	68n	%E25(2)
R57(1)	CHD4	D2	68n	%E27(4)
R114(1)	CHD4	D4	68n	%E28(2)
R103(1)	CHD4	D6	68n	%E29(2)
R113(1)	CHD4	D3	68n	%E32(2)
R177(1)	CHD4	D5	68n	%E33(2)
R129(1)	CHD2	B7	68n	%E36(2)
R227(1)	CHD2	B4	68n	%E36(4)
R250(1)	CHD2	B2	68n	%E39(12)
R61(1)	CHD4	B2	68n	%E4(4)
R251(1)	CHD2	B2	68n	%E40(2)
R24(1)	CHD2	B5	68n	%E41(2)
R27(1)	CHD3	D2	68n	%E43(4)
R252(1)	CHD2	B4	68n	%E44(2)
R249(1)	CHD2	B6	68n	%E45(2)
R26(1)	CHD3	D2	68n	%E47(2)
R29(1)	CHD3	D5	68n	%E48(2)
R51(1)	CHD4	B3	68n	%E5(2)
R23(1)	CHD3	D4	68n	%E50(13)
R126(1)	CHD3	D7	68n	%E50(14)
R235(1)	CHD1	B4	68n	%E50(2)
R142(1)	CHD1	B7	68n	%E50(3)
R42(1)	CHD3	D3	68n	%E51(2)
R22(1)	CHD3	D7	68n	%E52(2)

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
R82(1)	CHD1	B2	68n	%E54(4)
R14(1)	CHD1	B3	68n	%E55(2)
R2(1)	CHD1	B6	68n	%E56(2)
R242(1)	CHD7	D6	68n	%E57(11)
R173(1)	CHD7	D5	68n	%E57(6)
R44(1)	CHD1	B2	68n	%E58(2)
R120(1)	CHD1	B5	68n	%E59(2)
R182(1)	CHD4	B6	68n	%E6(2)
R190(1)	CHD2	D4	68n	%E61(2)
R188(1)	CHD2	D6	68n	%E62(2)
R28(1)	CHD7	A6	68n	%E63(3)
R181(1)	CHD7	B5	68n	%E63(4)
R18(1)	CHD7	B6	68n	%E63(5)
R182(1)	CHD7	B6	68n	%E63(6)
R99(1)	CHD2	D2	68n	%E64(4)
R15(1)	CHD2	D2	68n	%E65(2)
R189(1)	CHD2	D5	68n	%E66(2)
R236(1)	CHD2	D4	68n	%E67(13)
R158(1)	CHD2	D7	68n	%E67(14)
R245(1)	CHD1	D2	68n	%E68(2)
R12(1)	CHD1	D6	68n	%E69(2)
R7(1)	CHD7	B5	68n	%E70(2)
R192(1)	CHD7	B5	68n	%E70(21)
R11(1)	CHD7	B5	68n	%E70(23)
R10(1)	CHD7	B5	68n	%E70(4)
R8(1)	CHD1	D3	68n	%E72(2)
R247(1)	CHD1	D5	68n	%E73(2)
R248(1)	CHD1	D2	68n	%E74(12)
R174(1)	CHD1	D7	68n	%E74(2)
R239(1)	CHD1	D4	68n	%E74(3)
R157(1)	CHD4	B4	68n	%E8(13)
R218(1)	CHD4	B7	68n	%E8(14)
R143(1)	CHD5	C7	68n	%E8(2)
R217(1)	CHD5	C4	68n	%E8(3)
R181(1)	CHD4	B4	68n	%E9(2)
R240(1)	CHD6	A6	56n	CHD6 CSH ADR 26 A H
R281(1)	CHD6	A6	56n	CHD6 CSH ADR 26 B H
R238(1)	CHD6	D6	56n	CHD6 CSH ADR 27 A H
R31(1)	CHD6	D6	56n	CHD6 CSH ADR 27 B H
R156(1)	CHD6	D6	56n	CHD6 CSH ADR 27 C H

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
R221(1)	CHD6	D2	56n	CHD6 CSH ADR 27 D H
R109(1)	CHD6	D2	56n	CHD6 CSH ADR 27 E H
R214(1)	CHD6	D2	56n	CHD6 CSH ADR 27 F H
R30(1)	CHD6	D6	56n	CHD6 CSH ADR 28 A H
R167(1)	CHD6	D6	56n	CHD6 CSH ADR 28 B H
R138(1)	CHD6	D6	56n	CHD6 CSH ADR 28 C H
R233(1)	CHD6	D2	56n	CHD6 CSH ADR 28 D H
R216(1)	CHD6	D2	56n	CHD6 CSH ADR 28 E H
R213(1)	CHD6	D2	56n	CHD6 CSH ADR 28 F H
R152(1)	CHD6	C6	56n	CHD6 CSH ADR 29 A H
R162(1)	CHD6	C6	56n	CHD6 CSH ADR 29 B H
R122(1)	CHD6	C6	56n	CHD6 CSH ADR 29 C H
R73(1)	CHD6	C2	56n	CHD6 CSH ADR 29 D H
R97(1)	CHD6	C2	56n	CHD6 CSH ADR 29 E H
R211(1)	CHD6	C2	56n	CHD6 CSH ADR 29 F H
R172(1)	CHD6	C6	56n	CHD6 CSH ADR 30 A H
R144(1)	CHD6	C6	56n	CHD6 CSH ADR 30 B H
R132(1)	CHD6	C6	56n	CHD6 CSH ADR 30 C H
R74(1)	CHD6	C2	56n	CHD6 CSH ADR 30 D H
R38(1)	CHD6	C2	56n	CHD6 CSH ADR 30 E H
R212(1)	CHD6	C2	56n	CHD6 CSH ADR 30 F H
R224(1)	CHD6	C6	56n	CHD6 CSH ADR 31 A H
R96(1)	CHD6	C6	56n	CHD6 CSH ADR 31 B H
R219(1)	CHD6	C6	56n	CHD6 CSH ADR 31 C 4
R226(1)	CHD6	C2	56n	CHD6 CSH ADR 31 D H
R168(1)	CHD6	C2	56n	CHD6 CSH ADR 31 E H
R286(1)	CHD6	C2	56n	CHD6 CSH ADR 31 F H
R284(1)	CHD6	B6	56n	CHD6 CSH ADR 32 A H
R191(1)	CHD6	B6	56n	CHD6 CSH ADR 32 B H
R228(1)	CHD6	B6	56n	CHD6 CSH ADR 32 C H
R232(1)	CHD6	B2	56n	CHD6 CSH ADR 32 D H
R137(1)	CHD6	B2	56n	CHD6 CSH ADR 32 E H
R215(1)	CHD6	B2	56n	CHD6 CSH ADR 32 F H
R283(1)	CHD6	B6	56n	CHD6 CSH ADR 33 A H
R160(1)	CHD6	B6	56n	CHD6 CSH ADR 33 B H
R223(1)	CHD6	B6	56n	CHD6 CSH ADR 33 C H
R48(1)	CHD6	B2	56n	CHD6 CSH ADR 33 D H
R287(1)	CHD6	B2	56n	CHD6 CSH ADR 33 E H
R180(1)	CHD6	B2	56n	CHD6 CSH ADR 33 F H
R6(1)	CHD6	B6	56n	CHD6 CSH ADR 34 A H

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
R237(1)	CHD6	B6	56n	CHD6 CSH ADR 34 B H
R171(1)	CHD6	B6	56n	CHD6 CSH ADR 34 C H
R222(1)	CHD6	B2	56n	CHD6 CSH ADR 34 D H
R67(1)	CHD6	B2	56n	CHD6 CSH ADR 34 E H
R118(1)	CHD6	B2	56n	CHD6 CSH ADR 34 F H
R205(1)	CHD6	A2	56n	CHD6 CSH ADR 35 A H
R139(1)	CHD6	A2	56n	CHD6 CSH ADR 35 B H
R25(1)	CHD6	A6	56n	-CHD6 CSH WRITE A H
R32(1)	CHD6	A6	56n	-CHD6 CSH WRITE B H
R121(1)	CHD6	A6	56n	-CHD6 CSH WRITE C H
R194(1)	CHD6	A2	56n	-CHD6 CSH WRITE D H
R288(1)	CHD6	A2	56n	-CHD6 CSH WRITE E H
R53(1)	CHD6	A2	56n	-CHD6 CSH WRITE F H
R125(1)	CHD7	D4	68n	-CHD7 C MATCH 0 A H
R199(1)	CHD7	D4	68n	-CHD7 C MATCH 0 B H
R289(1)	CHD7	D4	68n	-CHD7 C MATCH 0 C H
R145(1)	CHD7	D4	68n	-CHD7 C MATCH 0 D H
R128(1)	CHD7	D5	68n	-CHD7 C MATCH 1 A H
R68(1)	CHD7	D5	68n	-CHD7 C MATCH 1 B H
R253(1)	CHD7	D5	68n	-CHD7 C MATCH 1 C H
R178(1)	CHD7	D5	68n	-CHD7 C MATCH 1 D H
R148(1)	CHD7	C4	68n	-CHD7 C MATCH 2 A H
R146(1)	CHD7	C4	68n	-CHD7 C MATCH 2 B H
R244(1)	CHD7	C4	68n	-CHD7 C MATCH 2 C H
R179(1)	CHD7	C4	68n	-CHD7 C MATCH 2 D H
R134(1)	CHD7	C5	68n	-CHD7 C MATCH 3 A H
R158(1)	CHD7	C5	68n	-CHD7 C MATCH 3 B H
R241(1)	CHD7	C5	68n	-CHD7 C MATCH 3 C H
R243(1)	CHD7	C5	68n	-CHD7 C MATCH 3 D H
R91(1)	CHD7	B1	68n	CHD7 TERM 09 [N] H-N#400
R81(1)	CHD7	B1	68n	CHD7 TERM 13 [N] H-N#400
R79(1)	CHD7	A1	68n	CHD7 TERM 15 [N] H-N#400
R75(1)	CHD7	A1	68n	CHD7 TERM 16 [N] H-N#400
R19(1)	CHD7	A6	68n	-CHX1 CSH [B, (N/9+1,0,1,2,3) [N/18*18]-(N/18*18+17)] SEL A H
R220(1)	CHD6	A7	68n	-MBC2 CACHE WR [N] A H
R246(1)	CHD1	D2	68n	MEM TO CACHE [N+00] H
R193(1)	CHD1	B2	68n	MEM TO CACHE [N+01] H
R180(1)	CHD2	D2	68n	MEM TO CACHE [N+02] H
R238(1)	CHD2	B2	68n	MEM TO CACHE [N+03] H
R234(1)	CHD3	D2	68n	MEM TO CACHE [N+04] H

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND (<) INDICATES PIN NUMBER

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	06-AUG-84	C. A. JENS	06-AUG-84	CACHE DATA TERMINATORS
	DATE	BOARD LOCATION:		
	06-AUG-84			
		SHEET	1	OF 2
		NEXT HIGHER ASSEMBLY:		
		D-DD-M856-0		
		SIZE	D	CS
		NUMBER	M856-0-RES	
		REV.	A	

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MRO 1

REV. A
 SITE CODE NUMBER
 D CS M856-0-RES

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R64<1>	CHD3	B2	68Ω	MEM TO CACHE [N+05] H	R111<1>	CHD8	A5	68Ω	NC	R94<1>	CHD7	D1	68Ω	[B,CHD7 TERM 01 H, [N/9+1,CHD7 TERM 01 00 H\#400\, CHD7 TERM 01 09 H\#400\,CCW BUF ADR 0 H, MBC1 CACHE ADR 28 H]]
R56<1>	CHD4	D2	68Ω	MEM TO CACHE [N+06] H	R112<1>	CHD8	A5	68Ω	NC	R86<1>	CHD7	D1	68Ω	[B,CHD7 TERM 02 [N] H, [N/9+1, CHD7 TERM 02 00 H\#400\,CHX1 CSH 3 00-17 SEL A L, CHD7 TERM 02 18 H\#400\,MBC1 CACHE ADR 32 H]]
R72<1>	CHD4	B2	68Ω	MEM TO CACHE [N+07] H	R115<1>	CHD8	A5	68Ω	NC	R98<1>	CHD7	D1	68Ω	[B,CHD7 TERM 03 [N] H, [N/9+1,CSH PAR BIT IN H, CHD7 TERM 03 09 H\#400\, CCW BUF ADR 1 H,MBC1 CACHE ADR 29 H]]
R71<1>	CHD5	C2	68Ω	MEM TO CACHE [N+08] H	R116<1>	CHD8	A5	68Ω	NC	R87<1>	CHD7	C1	68Ω	[B,CHD7 TERM 04 [N] H, [N/27+1, CHD7 TERM 04 [N] H\#400\,MBC1 CACHE ADR 33 H]]
R1<1>	CHD8	B5	68Ω	NC	R117<1>	CHD8	A5	68Ω	NC	R92<1>	CHD7	C1	68Ω	[B,CHD7 TERM 05 [N] H, [N/27+1, CHD7 TERM 05 [N] H\#400\,MBC1 CACHE ADR 30 H]]
R3<1>	CHD8	B5	68Ω	NC	R119<1>	CHD8	A5	68Ω	NC	R93<1>	CHD7	C1	68Ω	[B,CHD7 TERM 06 [N] H, [N/9+1, CHD7 TERM 06 00 H\#400\,CHD7 TERM 06 09 H\#400\, PMA 34 H,MBC1 CACHE ADR 34 H]]
R4<1>	CHD8	B5	68Ω	NC	R123<1>	CHD8	A5	68Ω	NC	R45<1>	CHD7	C1	68Ω	[B,CHD7 TERM 07 [N] H, [N/27+1, CHD7 TERM 07 [N] H\#400\,MBC1 CACHE ADR 35 H]]
R5<1>	CHD8	B5	68Ω	NC	R124<1>	CHD8	A5	68Ω	NC	R89<1>	CHD7	C1	68Ω	[B,CHD7 TERM 08 [N] H, [N/27+1, CHD7 TERM 08 [N] H\#400\, MBC1 CACHE ADR 35 L]]
R9<1>	CHD8	B5	68Ω	NC	R127<1>	CHD8	A5	68Ω	NC	R85<1>	CHD7	B1	68Ω	[B,CHD7 TERM 10 [N] H, [N/27+1, CHD7 TERM 10 [N] H\#400\, MBC1 CACHE ADR 31 H]]
R13<1>	CHD8	B5	68Ω	NC	R130<1>	CHD8	A5	68Ω	NC	R84<1>	CHD7	B1	68Ω	[B,CHD7 TERM 11 [N] H, [N/9+1, CHD7 TERM 11 00 H\#400\,CHD7 TERM 11 09 H\#400\, CACHE EXISTS L,CHX1 CSH 0 18-35 SEL A L]]
R16<1>	CHD8	B5	68Ω	NC	R131<1>	CHD8	A5	68Ω	NC	R83<1>	CHD7	B1	68Ω	[B,CHD7 TERM 12 [N] H, [N/9+1,PMA5 EBOX PAGED H, CHD7 TERM 12 09 H\#400\,CHX1 CSH 1 18-35 SEL A L, CCW BUF ADR 2 H]]
R17<1>	CHD8	B5	68Ω	NC	R133<1>	CHD8	A5	68Ω	NC	R77<1>	CHD7	A1	68Ω	[B,CHD7 TERM 14 [N] H, [N/27+1, CHD7 TERM 14 [N] H\#400\, CCL CH BUF EN L]]
R21<1>	CHD8	B5	68Ω	NC	R135<1>	CHD8	A5	68Ω	NC	R39<1>	CHD7	A1	68Ω	[B,CHD7 TERM 17 H, [N/9+1, CSH EN CSH DATA L, CHX1 CSH 2 00-17 SEL A L, PMA 35 H,MBC1 CACHE ADR 27 H]]
R28<1>	CHD8	B5	68Ω	NC	R141<1>	CHD8	A1	68Ω	NC					
R33<1>	CHD8	A5	68Ω	NC	R147<1>	CHD8	A1	68Ω	NC					
R35<1>	CHD8	B5	68Ω	NC	R148<1>	CHD8	A1	68Ω	NC					
R36<1>	CHD8	B5	68Ω	NC	R149<1>	CHD8	A1	68Ω	NC					
R37<1>	CHD8	B5	68Ω	NC	R153<1>	CHD8	A1	68Ω	NC					
R40<1>	CHD8	B5	68Ω	NC	R154<1>	CHD8	A1	68Ω	NC					
R41<1>	CHD8	B5	68Ω	NC	R155<1>	CHD8	A1	68Ω	NC					
R43<1>	CHD8	B5	68Ω	NC	R159<1>	CHD8	A5	68Ω	NC					
R47<1>	CHD8	B5	68Ω	NC	R161<1>	CHD8	A5	68Ω	NC					
R49<1>	CHD8	B5	68Ω	NC	R163<1>	CHD8	A1	68Ω	NC					
R54<1>	CHD8	B5	68Ω	NC	R164<1>	CHD8	A1	68Ω	NC					
R59<1>	CHD8	B5	68Ω	NC	R165<1>	CHD8	A1	68Ω	NC					
R60<1>	CHD8	A5	68Ω	NC	R166<1>	CHD8	A1	68Ω	NC					
R62<1>	CHD8	B5	68Ω	NC	R169<1>	CHD8	A1	68Ω	NC					
R63<1>	CHD8	B5	68Ω	NC	R175<1>	CHD8	A1	68Ω	NC					
R65<1>	CHD8	B5	68Ω	NC	R176<1>	CHD8	A1	68Ω	NC					
R70<1>	CHD8	B5	68Ω	NC	R183<1>	CHD8	A1	68Ω	NC					
R76<1>	CHD8	A5	68Ω	NC	R184<1>	CHD8	A2	68Ω	NC					
R78<1>	CHD8	A5	68Ω	NC	R195<1>	CHD8	A2	68Ω	NC					
R80<1>	CHD8	A5	68Ω	NC	R196<1>	CHD8	A2	68Ω	NC					
R88<1>	CHD8	A5	68Ω	NC	R198<1>	CHD8	A2	68Ω	NC					
R98<1>	CHD8	A5	68Ω	NC	R200<1>	CHD8	A2	68Ω	NC					
R104<1>	CHD8	A5	68Ω	NC	R202<1>	CHD8	A2	68Ω	NC					
R105<1>	CHD8	A5	68Ω	NC	R225<1>	CHD8	A2	68Ω	NC					
R106<1>	CHD8	A5	68Ω	NC	R231<1>	CHD8	A2	68Ω	NC					
R107<1>	CHD8	A5	68Ω	NC	R95<1>	CHD7	D1	68Ω	[B,CHD7 TERM 00 [N] H, [N/27+1,CHD7 TERM 00 [N] H\#400\, MBC2 NEW 26 H]]					
R108<1>	CHD8	A5	68Ω	NC										

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND (<) INDICATES PIN NUMBER

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	SIZE CODE: D CS	NUMBER: M856-0-RES		REV. A	

DRAWING NUMBER	NO. OF SHT	PART NUMBER	DESCRIPTION	REVISION
		M857-00	MODULE REVISION	A1
D-UA-M857-0-0	1		MBOX CONTROL #3	A
K-PL-M857-0-DBP	2		PART LIST, M857	A
D-CS-M857-0-MBC1	1		CSH ADR MIX, EBUS & PMA HOLD REG	A
D-CS-M857-0-MBC2	1		FORCE VAL MATCH & MBC DIAG MIX	A
D-CS-M857-0-MBC3	1		DATA VAL OUT, CLK, PHS, MEM START	A
D-CS-M857-0-MBC4	1		MEM REQUESTS, ACK & DAT VAL CTRS	A
D-CS-M857-0-MBC5	1		FORCE VAL MATCH & MBC DIAG MIX	A
D-CS-M857-0-MBC6	1		MBC MBOX CONTROL PWR, GND, CAPS	A
D-CS-M857-0-RES	2		MBOX CONTROL 3 TERMINATORS	A
K-PC-M857-0-DBI	-		P.C. DESIGN DATA BASE TAPE	A
D-DD-5017663-0	1		DRAWING DIRECTORY, 5017663	REF

REVISIONS		
CHK	CHANGE NO.	REV

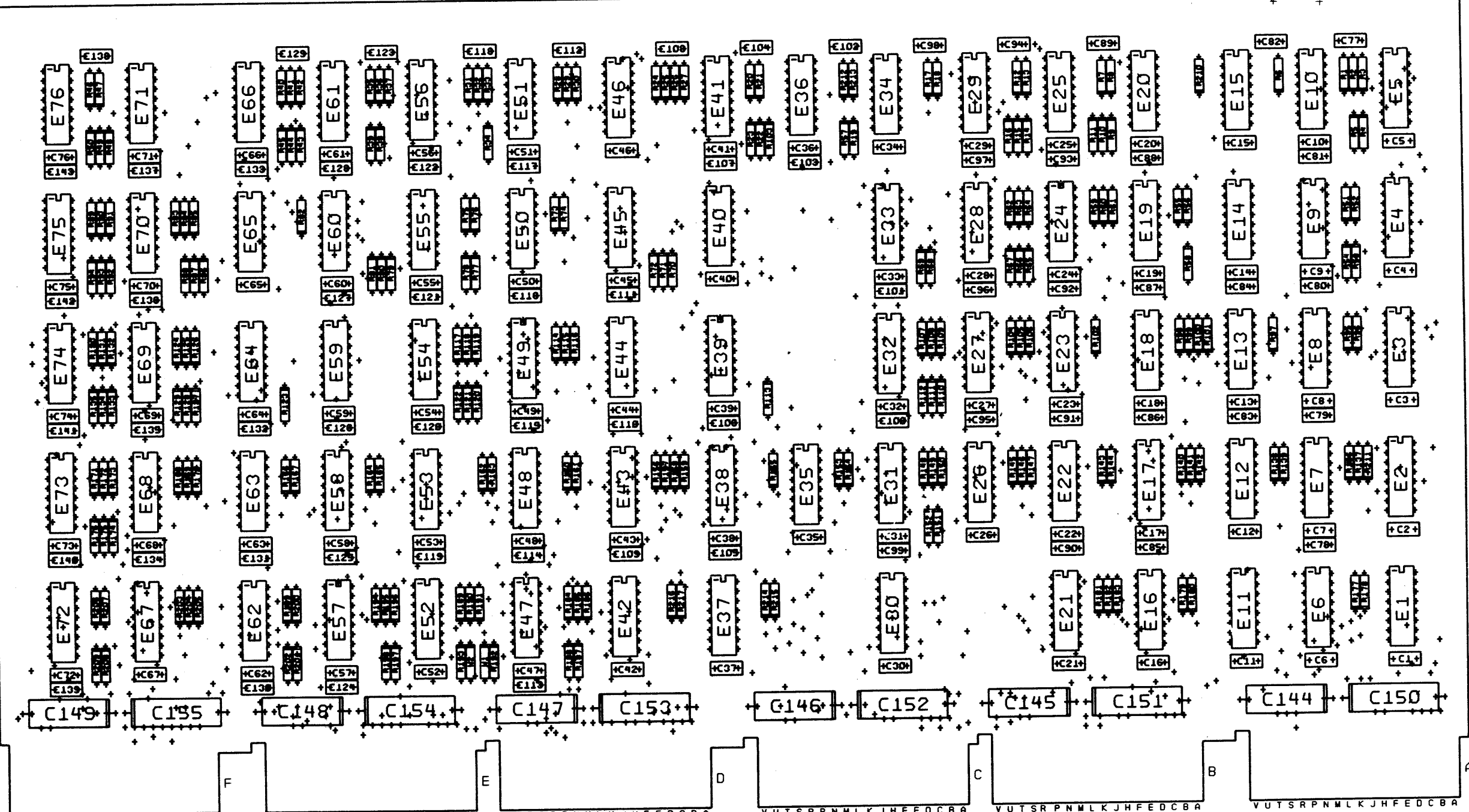
digital

DRN. <i>W. Wilson</i>	DATE 20-AUG-84	ENG. <i>W. Wilson</i>	DATE 20-AUG-84
CHK'D <i>W. Wilson</i>	DATE 20-AUG-84	BOARD LOCATION: N/A	
DSK: M857A.T2PL4.105		NEXT HIGHER ASSEMBLY: MCA25	

TITLE: M857			
DRAWING DIRECTORY			
SIZE	CODE	NUMBER	REV.
D	DD	M857-0	A

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22 (QTY 12) COMPONENT SIDE VIEW



NOTES:

STEP E → Y AXIS 0 STEP 0 TIMES
 REPEAT → X AXIS 0 STEP 0 TIMES

CHG	CHANGE NO	REV

ETCH REV. AI

SIGNATURES	DATE	digital
DRN. <i>[Signature]</i>	7/17/84	
CHK'D BY <i>[Signature]</i>	8/20/84	TITLE MBOX CONTROL 3
MECH. ENG. <i>[Signature]</i>	7-20-84	
PROJ. ENG. <i>[Signature]</i>	7-20-84	SIZE CODE NUMBER REV
PROD. <i>[Signature]</i>	8/24/84	
SCALE 2/1	OF	D U A M 857-0-0 A
TOP DOC. NO: D-DD-M857-0		

MRO 1 WO #410668 A

8

7 D U A M 857-0-0 A

6

5

4

3

2

1

D
C
B
A

D
C
B
A

8

7

6

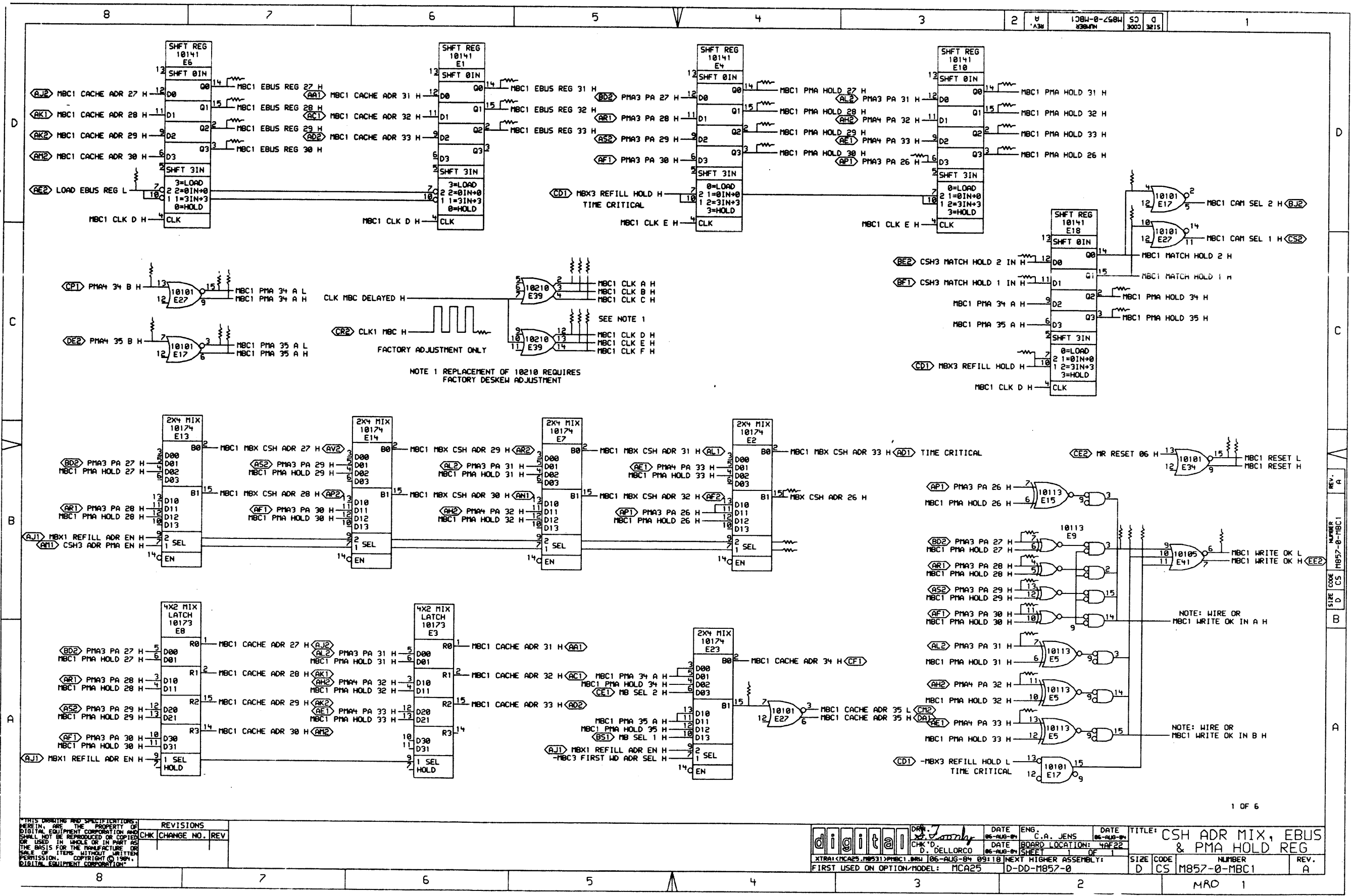
5

4

3

2

1



NOTE 1 REPLACEMENT OF 10210 REQUIRES FACTORY DESKEW ADJUSTMENT

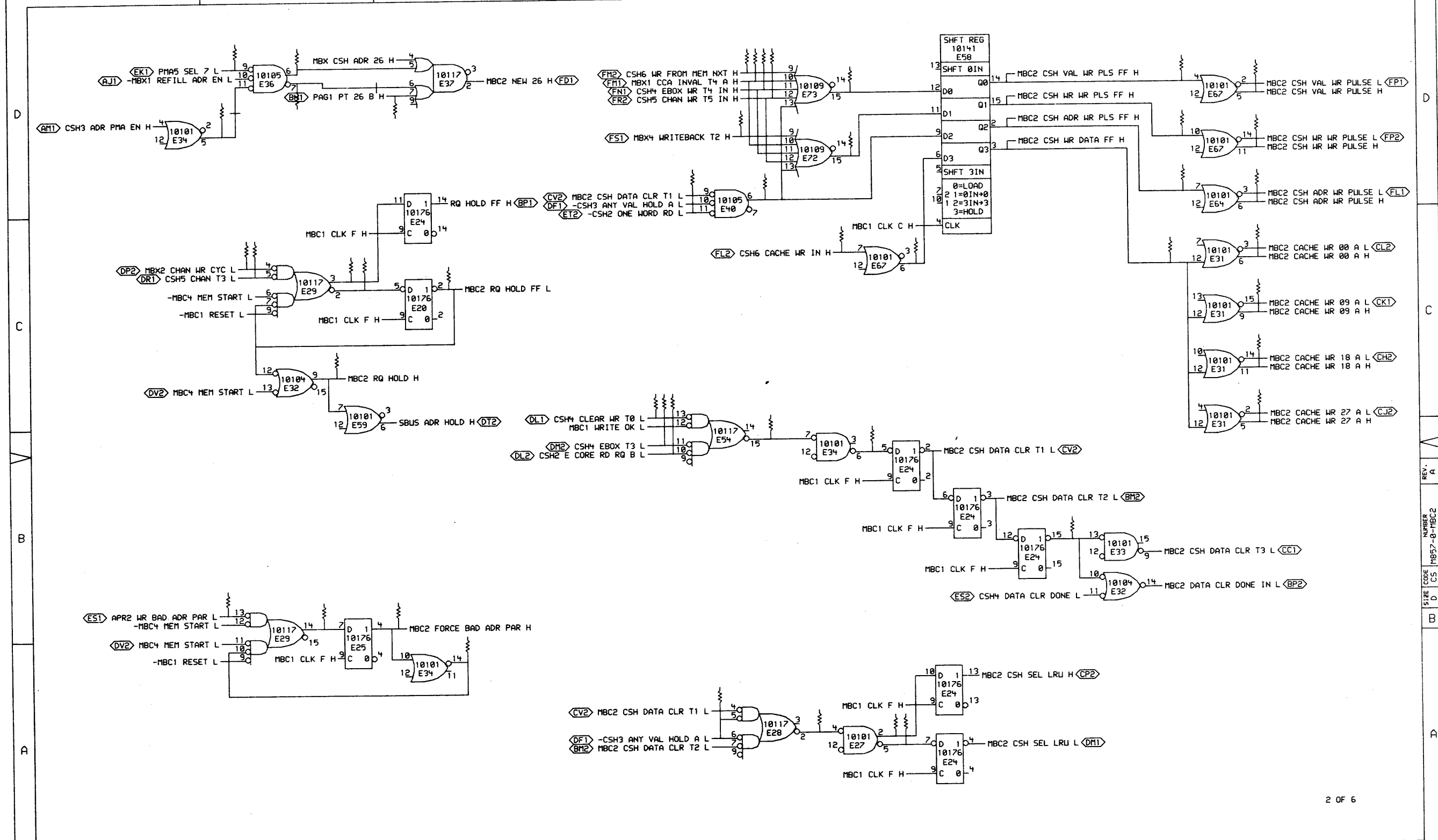
NOTE: WIRE OR MBC1 WRITE OK IN A H

NOTE: WIRE OR MBC1 WRITE OK IN B H

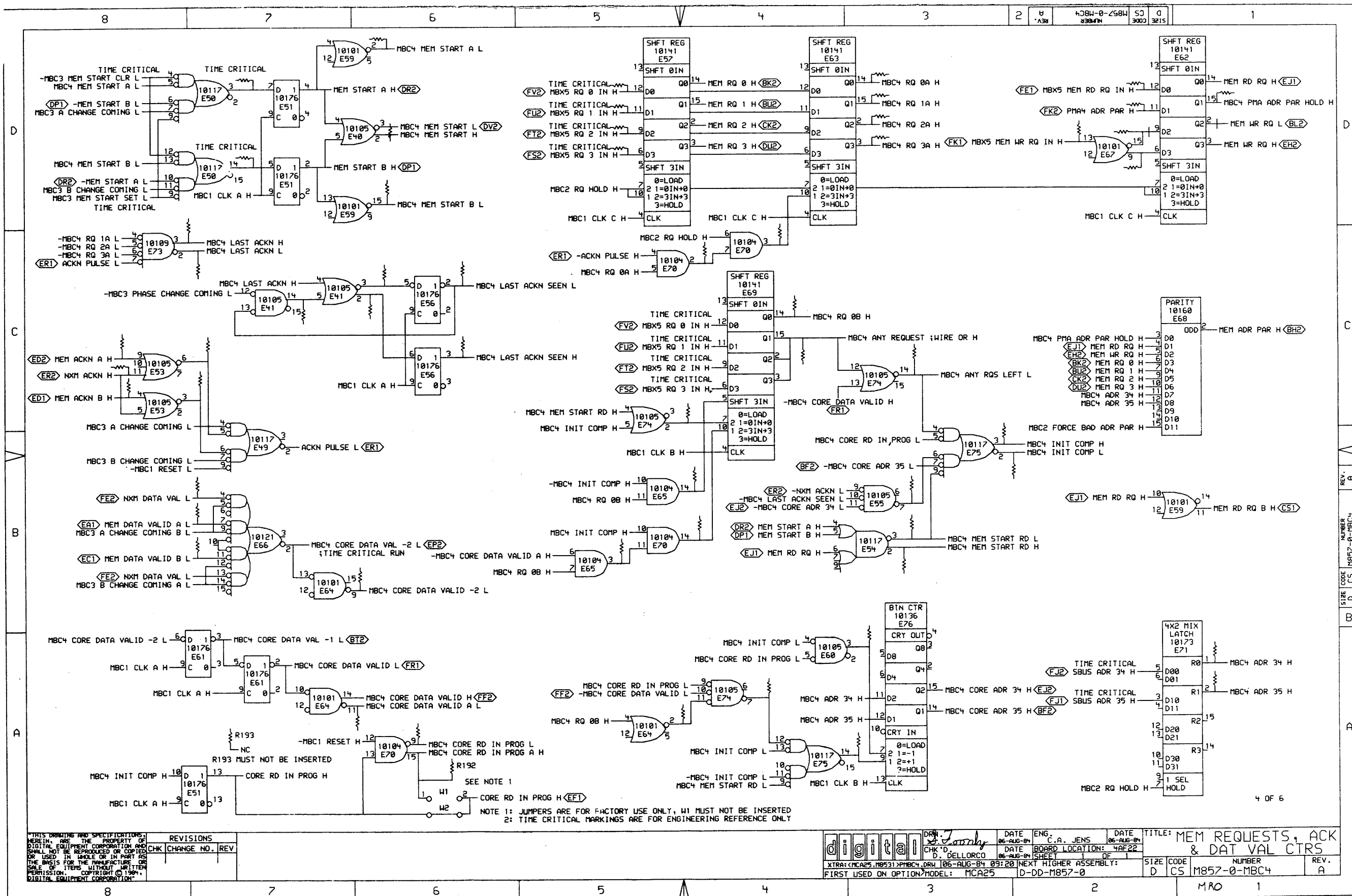
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REV	CHK	CHANGE NO.	REV

	DATE: 06-AUG-84	ENG. C.A. JENS	DATE: 06-AUG-84	TITLE: CSH ADR MIX, EBUS & PMA HOLD REG
	CHK'D: D. DELLORCO	DATE: 06-AUG-84	BOARD LOCATION: 4AF22	SHEET: 1 OF 1
XTRAI: MCA25, M8571, M8572, M8573, M8574, M8575, M8576, M8577, M8578, M8579, M8580, M8581, M8582, M8583, M8584, M8585, M8586, M8587, M8588, M8589, M8590, M8591, M8592, M8593, M8594, M8595, M8596, M8597, M8598, M8599, M8600		NEXT HIGHER ASSEMBLY: D-DD-M857-0		SIZE CODE: D CS M857-0-MBC1
FIRST USED ON OPTION MODEL: MCA25				REV. A



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	MRO 1						

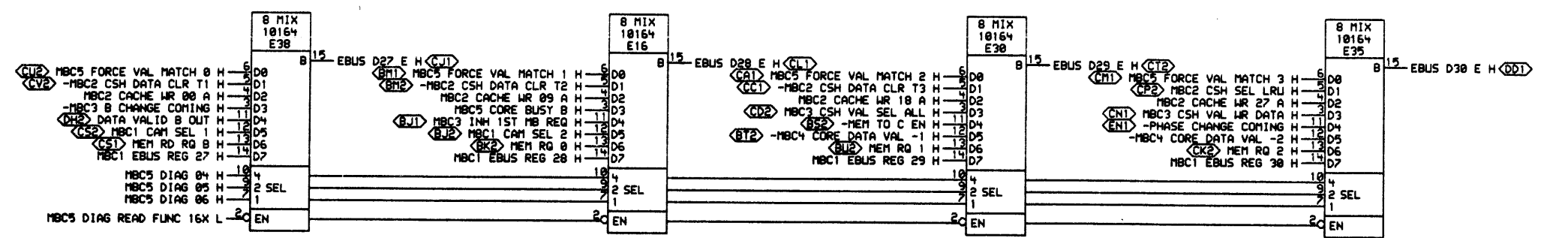


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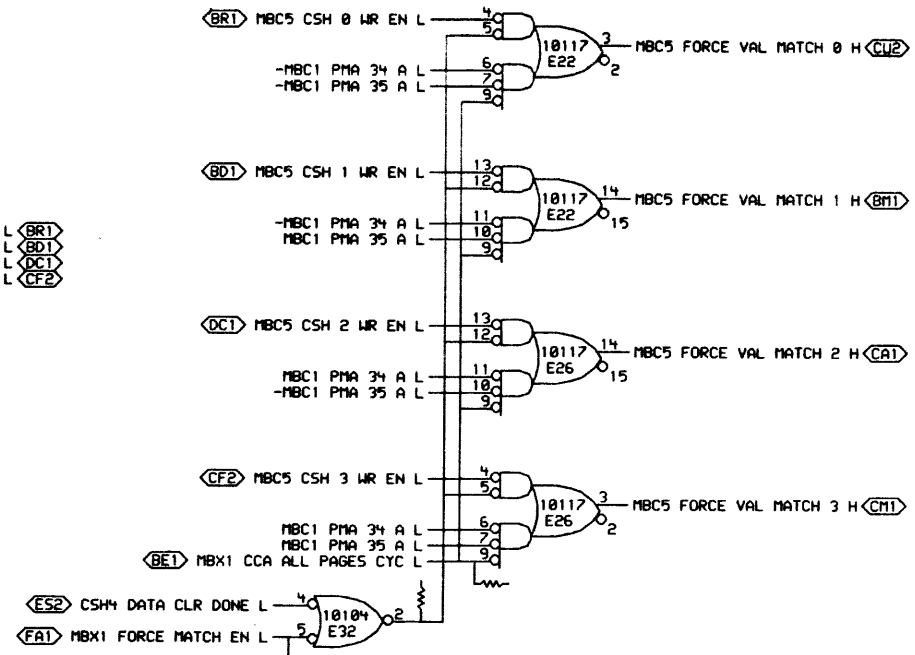
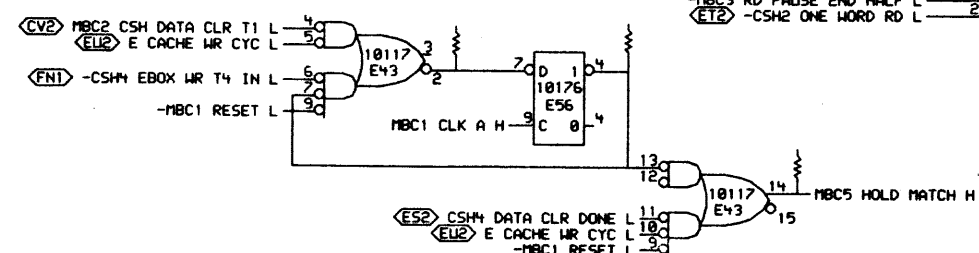
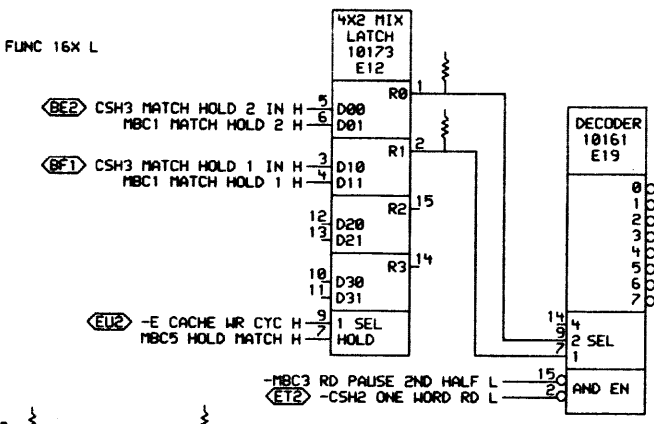
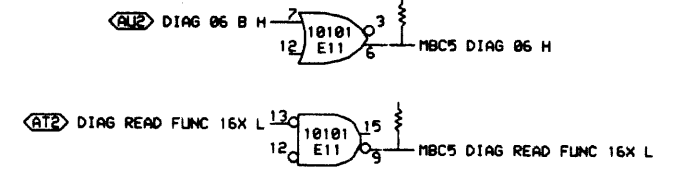
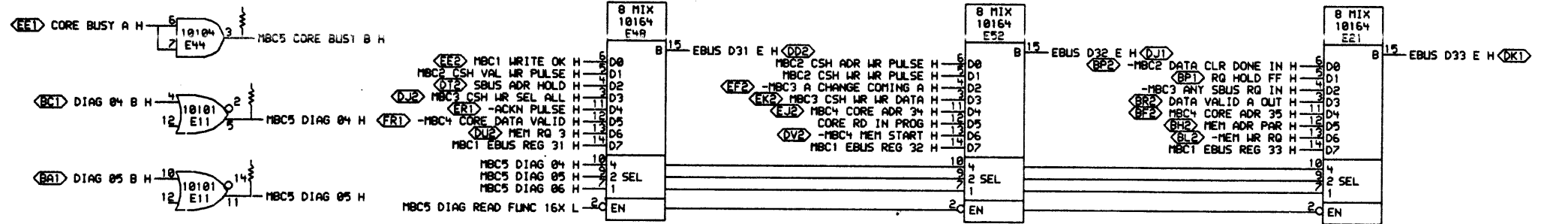
REVISIONS	CHK	CHANGE NO.	REV

NOTE 1: JUMPERS ARE FOR FACTORY USE ONLY, W1 MUST NOT BE INSERTED
 NOTE 2: TIME CRITICAL MARKINGS ARE FOR ENGINEERING REFERENCE ONLY

	DATE	06-AUG-84	ENG.	C.A. JENS	DATE	06-AUG-84	TITLE:	MEM REQUESTS, ACK & DAT VAL CTRS
	CHK'D	D. DELLORCO	DATE	06-AUG-84	BOARD LOCATION:	4AF22	SIZE	D
XTRA: (MCA25, M8531) PMBC4.DRW		06-AUG-84	09:20	NEXT HIGHER ASSEMBLY:	D-DD-M857-0	SHEET	1	OF
FIRST USED ON OPTION/MODEL:	MCA25	D-DD-M857-0	SIZE	D	CODE	C5	NUMBER	M857-0-MBC4
REV.	A							



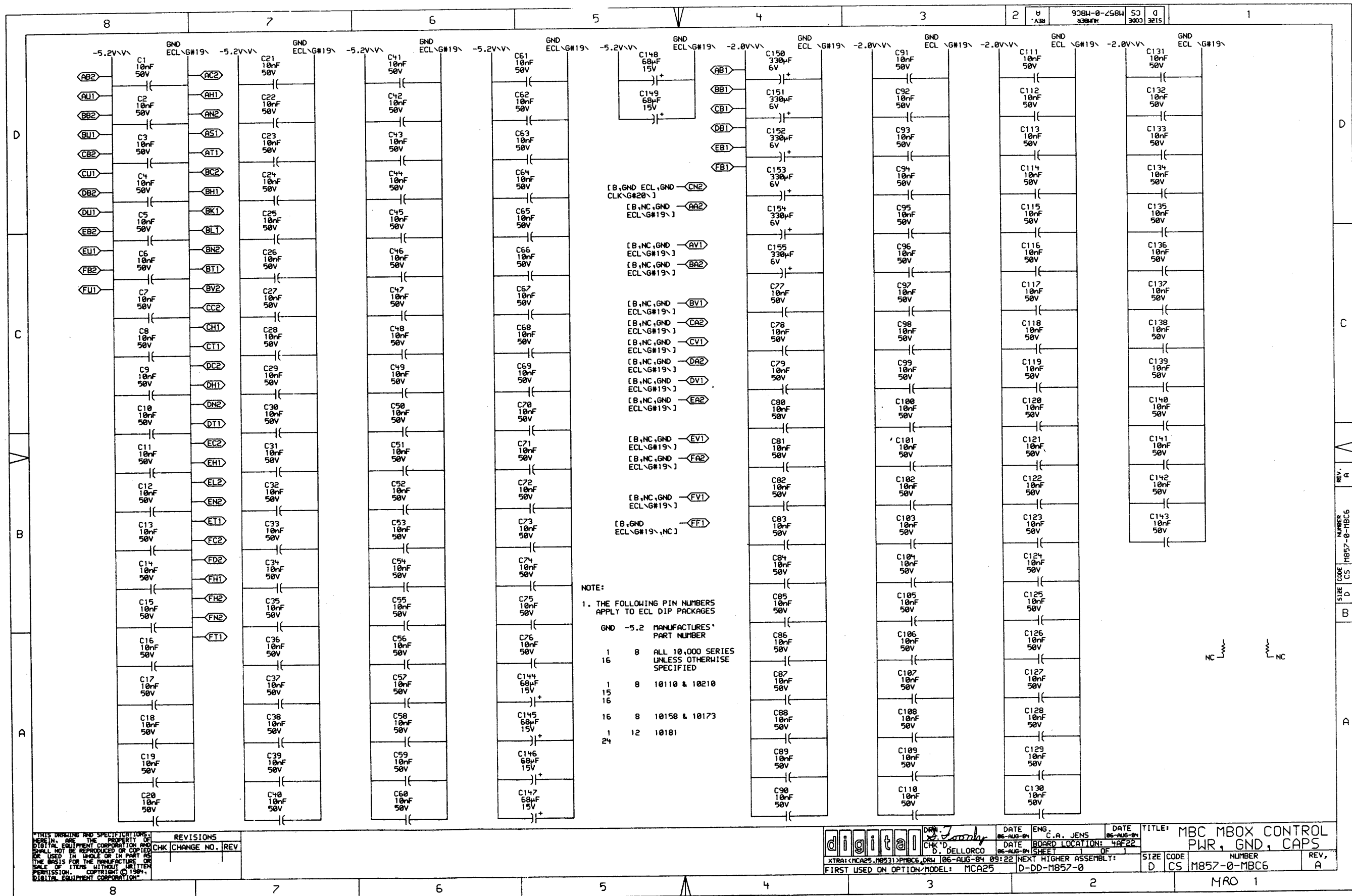
NOTE: DIAGNOSTIC MIXERS USE FUNCTIONS 168-167



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REVISIONS	CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	06-AUG-84	C.A. JENS	06-AUG-84	FORCE VAL MATCH & MBC DIAG MIX
CHK'D	DATE	BOARD LOCATION:	SHEET	1 OF 1
D. DELLORCO	06-AUG-84 09:21	4AF22	1	
FIRST USED ON OPTION/MODEL:	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
MCA25	D-DD-M857-0	D	CS	M857-0-MBC5
				REV.
				A



NOTE:
 1. THE FOLLOWING PIN NUMBERS APPLY TO ECL DIP PACKAGES

GND	-5.2	MANUFACTURES' PART NUMBER
1	8	ALL 10,000 SERIES UNLESS OTHERWISE SPECIFIED
15	8	10110 & 10210
16	8	10158 & 10173
1	12	10181
24		

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE: 06-AUG-84	ENG: C.A. JENS	DATE: 06-AUG-84	TITLE: MBC MBOX CONTROL PWR, GND, CAPS
	CHK'D: D. DELLORCO	DATE: 06-AUG-84	BOARD LOCATION: 48F22	
FIRST USED ON OPTION MODEL: MCA25				REV: A
NEXT HIGHER ASSEMBLY: D-DD-M857-0				
SIZE CODE: D CS				NUMBER: M857-0-MBC6
MRO				1

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R56(1)	MBC5	B5	68n	%E12(1)	R80(1)	MBC3	D5	68n	%E56(13)	R174(1)	MBC2	D4	68n	CSH6 WR FROM MEM NXT H	R155(1)	MBC2	C1	68n	MBC2 CACHE WR 27 A H
R60(1)	MBC5	B5	68n	%E12(2)	R156(1)	MBC5	A6	68n	%E56(4)	R138(1)	MBC3	B2	68n	-E CACHE WR CYC H	R123(1)	MBC2	D2	68n	MBC2 CSH ADR WR PLS FF H
R21(1)	MBC1	B2	68n	%E17(15)	R50(1)	MBC4	A3	68n	%E60(3)	R177(1)	MBC1	D8	68n	-LOAD EBUS REG H	R198(1)	MBC2	D1	68n	MBC2 CSH ADR WR PULSE H
R75(1)	MBC3	D3	68n	%E20(15)	R67(1)	MBC3	B5	68n	%E60(6)	R39(1)	MBC1	C5	68n	MBC1 CLK A H	R207(1)	MBC2	D2	68n	MBC2 CSH VAL WR PLS FF H
R110(1)	MBC1	A4	68n	%E23(15)	R131(1)	MBC4	A4	68n	%E64(2)	R47(1)	MBC1	C5	68n	MBC1 CLK B H	R114(1)	MBC2	D1	68n	MBC2 CSH VAL WR PULSE H
R112(1)	MBC2	B3	68n	%E24(15)	R128(1)	MBC4	B4	68n	%E65(14)	R200(1)	MBC1	C5	68n	MBC1 CLK C H	R149(1)	MBC2	C2	68n	MBC2 CSH WR DATA FF H
R66(1)	MBC2	A4	68n	%E27(2)	R30(1)	MBC3	A5	68n	%E65(2)	R178(1)	MBC1	C5	68n	MBC1 CLK D H	R204(1)	MBC2	D2	68n	MBC2 CSH WR WR PLS FF H
R65(1)	MBC2	A3	68n	%E27(5)	R80(1)	MBC4	B5	68n	%E65(3)	R6(1)	MBC1	C5	68n	MBC1 CLK E H	R196(1)	MBC2	D1	68n	MBC2 CSH WR WR PULSE H
R11(1)	MBC3	B4	68n	%E28(15)	R3(1)	MBC3	D4	68n	%E65(9)	R10(1)	MBC1	C5	68n	MBC1 CLK F H	R170(1)	MBC2	B6	68n	MBC2 FORCE BAD ADR PAR H
R109(1)	MBC2	A4	68n	%E28(2)	R201(1)	MBC4	D2	68n	%E67(15)	R157(1)	MBC1	D7	68n	MBC1 EBUS REG 27 H	R48(1)	MBC2	C7	68n	MBC2 RQ HOLD H
R14(1)	MBC2	B7	68n	%E29(14)	R167(1)	MBC2	C3	68n	%E67(6)	R180(1)	MBC1	D7	68n	MBC1 EBUS REG 28 H	R108(1)	MBC2	C6	68n	-MBC2 RQ HOLD FF H
R8(1)	MBC2	C7	68n	%E29(2)	R205(1)	MBC4	D2	68n	%E67(9)	R151(1)	MBC1	D7	68n	MBC1 EBUS REG 29 H	R62(1)	MBC3	D7	68n	-MBC3 A CHANGE COMING H
R59(1)	MBC2	C7	68n	%E29(3)	R129(1)	MBC4	B4	68n	%E70(14)	R154(1)	MBC1	D7	68n	MBC1 EBUS REG 30 H	R63(1)	MBC3	C7	68n	MBC3 A CHANGE COMING A H
R143(1)	MBC5	A2	68n	%E32(2)	R92(1)	MBC4	C4	68n	%E70(2)	R160(1)	MBC1	D6	68n	MBC1 EBUS REG 31 H	R44(1)	MBC3	D7	68n	-MBC3 A CHANGE COMING B H
R16(1)	MBC2	A6	68n	%E34(14)	R166(1)	MBC4	C4	68n	%E70(3)	R195(1)	MBC1	D6	68n	MBC1 EBUS REG 32 H	R87(1)	MBC3	D2	68n	MBC3 A PHASE H
R213(1)	MBC2	D7	68n	%E34(5)	R165(1)	MBC2	D4	68n	%E72(15)	R182(1)	MBC1	D6	68n	MBC1 EBUS REG 33 H	R181(1)	MBC3	A1	68n	-MBC3 ANY SBUS RQ IN H
R64(1)	MBC2	B4	68n	%E34(6)	R164(1)	MBC2	D4	68n	%E73(15)	R140(1)	MBC1	D2	68n	MBC1 MATCH HOLD 1 H	R24(1)	MBC3	C7	68n	MBC3 B CHANGE COMING H
R215(1)	MBC2	D7	68n	%E36(6)	R133(1)	MBC4	C4	68n	%E74(2)	R142(1)	MBC1	D2	68n	MBC1 MATCH HOLD 2 H	R158(1)	MBC3	C7	68n	-MBC3 B CHANGE COMING H
R214(1)	MBC2	D7	68n	%E36(7)	R89(1)	MBC4	A4	68n	%E74(7)	R100(1)	MBC1	C7	68n	MBC1 PMA 34 A H	R40(1)	MBC3	B7	68n	-MBC3 B CHANGE COMING A H
R32(1)	MBC3	C7	68n	%E40(14)	R49(1)	MBC4	A3	68n	%E75(14)	R150(1)	MBC1	C7	68n	-MBC1 PMA 34 A H	R4(1)	MBC3	D3	68n	MBC3 CLK A PHASE COMING H
R35(1)	MBC3	C7	68n	%E40(15)	R15(1)	MBC2	B7	68n	-APR2 WR BAD ADR PAR H	R146(1)	MBC1	C7	68n	MBC1 PMA 35 A H	R191(1)	MBC3	A3	68n	MBC3 CORE BUSY H
R206(1)	MBC2	D4	68n	%E40(6)	R106(1)	MBC3	A7	68n	-CCL START MEM H	R145(1)	MBC1	C7	68n	-MBC1 PMA 35 A H	R105(1)	MBC3	C2	68n	-MBC3 FIRST MD ADR SEL H
R27(1)	MBC4	C7	68n	%E41(14)	R113(1)	MBC1	C6	68n	CLK1 MBC H	R210(1)	MBC1	D3	68n	MBC1 PMA HOLD 26 H	R115(1)	MBC3	C2	68n	-MBC3 INH 1ST MB REQ H
R36(1)	MBC4	C6	68n	%E41(2)	R121(1)	MBC2	C5	68n	-CSH2 E CORE RD RQ B H	R101(1)	MBC1	D4	68n	MBC1 PMA HOLD 27 H	R73(1)	MBC3	C4	68n	MBC3 MEM START CLR H
R37(1)	MBC4	C6	68n	%E41(3)	R61(1)	MBC2	D5	68n	CSH2 ONE WORD RD H	R97(1)	MBC1	D4	68n	MBC1 PMA HOLD 28 H	R74(1)	MBC3	B6	68n	-MBC3 MEM START SET H
R105(1)	MBC3	A6	68n	%E42(2)	R69(1)	MBC3	A5	68n	-CSH2 RD PAUSE 2ND HALF H	R95(1)	MBC1	D4	68n	MBC1 PMA HOLD 29 H	R20(1)	MBC3	D1	68n	MBC3 PHASE CHANGE COMING H
R30(1)	MBC5	A6	68n	%E43(2)	R57(1)	MBC1	B4	68n	CSH3 ADR PMA EN H	R96(1)	MBC1	D4	68n	MBC1 PMA HOLD 30 H	R55(1)	MBC3	A5	68n	MBC3 RD PAUSE 2ND HALF H
R20(1)	MBC3	C7	68n	%E44(15)	R68(1)	MBC2	A5	68n	CSH3 ANY VAL HOLD A H	R139(1)	MBC1	D3	68n	MBC1 PMA HOLD 31 H	R46(1)	MBC4	A1	68n	MBC4 ADR 34 H
R13(1)	MBC3	B4	68n	%E46(15)	R99(1)	MBC1	C2	68n	CSH3 MATCH HOLD 1 IN H	R136(1)	MBC1	D3	68n	MBC1 PMA HOLD 32 H	R160(1)	MBC4	A1	68n	MBC4 ADR 35 H
R31(1)	MBC3	A4	68n	%E46(2)	R98(1)	MBC1	C2	68n	CSH3 MATCH HOLD 2 IN H	R137(1)	MBC1	D3	68n	MBC1 PMA HOLD 33 H	R132(1)	MBC4	C4	68n	MBC4 ANY REQUEST; WIRE OR H
R9(1)	MBC3	C3	68n	%E49(15)	R118(1)	MBC2	C5	68n	-CSH4 CLEAR WR T0 H	R106(1)	MBC1	C2	68n	MBC1 PMA HOLD 34 H	R90(1)	MBC4	C3	68n	-MBC4 ANY RQS LEFT H
R7(1)	MBC3	D2	68n	%E5(2)	R107(1)	MBC3	A2	68n	-CSH4 DATA CLR DONE H	R102(1)	MBC1	C2	68n	MBC1 PMA HOLD 35 H	R153(1)	MBC4	B6	68n	-MBC4 CORE DATA VALID -2 H
R33(1)	MBC4	D7	68n	%E50(14)	R122(1)	MBC2	C5	68n	-CSH4 EBOX T3 H	R79(1)	MBC1	B1	68n	MBC1 RESET H	R85(1)	MBC4	A6	68n	-MBC4 CORE DATA VALID A H
R34(1)	MBC4	D7	68n	%E50(3)	R161(1)	MBC2	D4	68n	CSH4 EBOX WR T4 IN H	R83(1)	MBC1	B1	68n	-MBC1 RESET H	R135(1)	MBC4	A6	68n	-MBC4 CORE RD IN PROG H
R120(1)	MBC4	C7	68n	%E53(3)	R107(1)	MBC3	A7	68n	-CSH5 CHAN RD T5 H	R117(1)	MBC1	B1	68n	-MBC1 WRITE OK H	R192(1)	MBC4	A6	68n	MBC4 CORE RD IN PROG A H
R119(1)	MBC4	C7	68n	%E53(6)	R18(1)	MBC2	C7	68n	-CSH5 CHAN T3 H	R22(1)	MBC1	B2	68n	MBC1 WRITE OK IN A H	R29(1)	MBC4	B3	68n	MBC4 INIT COMP H
R19(1)	MBC2	B4	68n	%E54(15)	R171(1)	MBC2	D4	68n	CSH5 CHAN WR T5 IN H	R23(1)	MBC1	B2	68n	MBC1 WRITE OK IN B H	R82(1)	MBC4	B2	68n	-MBC4 INIT COMP H
R84(1)	MBC3	D4	68n	%E55(2)	R180(1)	MBC3	A7	68n	-CSH5 PAGE REFILL T9 H	R159(1)	MBC2	C1	68n	MBC2 CACHE WR 00 A H	R25(1)	MBC4	C7	68n	MBC4 LAST ACKN H
R76(1)	MBC3	D4	68n	%E55(3)	R200(1)	MBC2	C4	68n	CSH6 CACHE WR IN H	R179(1)	MBC2	C1	68n	MBC2 CACHE WR 09 A H	R72(1)	MBC4	C7	68n	-MBC4 LAST ACKN H
R93(1)	MBC4	B3	68n	%E55(7)	R140(1)	MBC3	B2	68n	-CSH6 CHAN WR CACHE H	R152(1)	MBC2	C1	68n	MBC2 CACHE WR 10 A H	R70(1)	MBC4	C6	68n	MBC4 LAST ACKN SEEN H

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN: J. Jansky	DATE: 06-AUG-84	ENG.: C. A. JENS	DATE: 06-AUG-84	TITLE: MBOX CONTROL TERMINATORS
	CHK'D: D. DELLORCO	DATE: 06-AUG-84	BOARD LOCATION: 40F22	DATE: 06-AUG-84	SHEET: 1 OF 2
FIRST USED ON OPTION/MODEL: MCA25		NEXT HIGHER ASSEMBLY: D-DD-M857-0		SIZE CODE: D CS	NUMBER: M857-0-RES
				REV: A	

RESISTOR LOC(PIN)	SHOWN ON DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	ON REF	VALUE	TERMINATES SIGNAL
R26<1>	MBC4	C6	68Ω	-MBC4 LAST ACKN SEEN H	R126<1>	MBC6	A1	68Ω	NC
R12<1>	MBC4	D6	68Ω	MBC4 MEM START H	R193<1>	MBC4	A7	68Ω	NC
R71<1>	MBC4	D6	68Ω	-MBC4 MEM START A H	R77<1>	MBC4	C8	68Ω	NMN ACKN H
R70<1>	MBC4	D6	68Ω	-MBC4 MEM START B H	R41<1>	MBC4	B7	68Ω	-NMN DATA VAL H
R130<1>	MBC4	B3	68Ω	MBC4 MEM START RD H	R216<1>	MBC2	D6	68Ω	PMA3 PA 26 H
R94<1>	MBC4	B3	68Ω	-MBC4 MEM START RD H	R211<1>	MBC1	D3	68Ω	PMA3 PA 26 H
R169<1>	MBC4	D1	68Ω	MBC4 PMA ADR PAR HOLD H	R52<1>	MBC1	B2	68Ω	PMA3 PA 27 H
R91<1>	MBC4	D3	68Ω	MBC4 RQ 0A H	R51<1>	MBC1	B2	68Ω	PMA3 PA 28 H
R86<1>	MBC4	C4	68Ω	MBC4 RQ 0B H	R58<1>	MBC1	B2	68Ω	PMA3 PA 29 H
R172<1>	MBC4	D3	68Ω	MBC4 RQ 1A H	R54<1>	MBC1	B2	68Ω	PMA3 PA 30 H
R175<1>	MBC4	D3	68Ω	MBC4 RQ 2A H	R1<1>	MBC1	A2	68Ω	PMA3 PA 31 H
R173<1>	MBC4	D3	68Ω	MBC4 RQ 3A H	R104<1>	MBC1	C8	68Ω	PMA4 34 B H
R183<1>	MBC5	C7	68Ω	MBC5 CORE BUSY B H	R144<1>	MBC1	C8	68Ω	PMA4 35 B H
R189<1>	MBC5	C7	68Ω	MBC5 DIAG 04 H	R202<1>	MBC4	D2	68Ω	PMA4 ADR PAR H
R190<1>	MBC5	C7	68Ω	MBC5 DIAG 05 H	R2<1>	MBC1	A2	68Ω	PMA4 PA 32 H
R197<1>	MBC5	C7	68Ω	MBC5 DIAG 06 H	R5<1>	MBC1	A2	68Ω	PMA4 PA 33 H
R194<1>	MBC5	B7	68Ω	-MBC5 DIAG READ FUNC 16X H	R212<1>	MBC2	D7	68Ω	-PMA5 SEL 7 H
R141<1>	MBC5	A5	68Ω	MBC5 HOLD MATCH H					
R217<1>	MBC1	B4	68Ω	MBX CSH ADR 26 H					
R147<1>	MBC5	A2	68Ω	-MBX1 CCA ALL PAGES CYC H					
R176<1>	MBC2	D4	68Ω	MBX1 CCA INVAL T4 A H					
R111<1>	MBC5	A3	68Ω	-MBX1 FORCE MATCH EN H					
R103<1>	MBC1	B4	68Ω	MBX1 REFILL ADR EN H					
R17<1>	MBC2	C7	68Ω	-MBX2 CHAN WR CYC H					
R53<1>	MBC1	C2	68Ω	MBX3 REFILL HOLD H					
R81<1>	MBC3	B5	68Ω	-MBX4 CACHE TO MB T2 H					
R184<1>	MBC3	B6	68Ω	-MBX4 CACHE TO MB T4 A H					
R209<1>	MBC2	D4	68Ω	MBX4 WRITEBACK T2 H					
R199<1>	MBC4	D2	68Ω	MBX5 MEM RD RQ IN H					
R116<1>	MBC3	A2	68Ω	-MBX5 MEM TO C EN H					
R203<1>	MBC4	D2	68Ω	MBX5 MEM WR RQ IN H					
R124<1>	MBC4	D5	68Ω	MBX5 RQ 0 IN H					
R125<1>	MBC4	D5	68Ω	MBX5 RQ 1 IN H					
R127<1>	MBC4	D5	68Ω	MBX5 RQ 2 IN H					
R134<1>	MBC4	D5	68Ω	MBX5 RQ 3 IN H					
R163<1>	MBC4	C8	68Ω	MEM ACKN A H					
R162<1>	MBC4	C8	68Ω	MEM ACKN B H					
R45<1>	MBC4	B7	68Ω	-MEM DATA VALID A H					
R43<1>	MBC4	B7	68Ω	-MEM DATA VALID B H					
R42<1>	MBC6	A1	68Ω	NC					

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND (<) INDICATES PIN NUMBER

D
C
B
A

D
C
B
A

REV. A
 NUMBER M857-0-RES
 SIZE CODE D CS

REVISIONS	CHK	CHANGE NO.	REV

digital
 DATE 06-AUG-84 ENG. C. A. JENS DATE 06-AUG-84
 CHK'D D. DELLORCO DATE 06-AUG-84 BOARD LOCATION: 4AF22
 SHEET 2 OF 2
 TITLE: MBOX CONTROL TERMINATORS
 FIRST USED ON OPTION/MODEL: MCA25 D-DD-M857-0
 SIZE CODE D CS NUMBER M857-0-RES REV. A
 MRO 1