SYSTEM TIMINGS
Key to abbreviations used in formulas
$L_{\Delta}=$ Length of the $A$-field
$L_{B}=$ Length of the $B$-field
$\mathbf{L}_{\mathrm{c}}=$ Length of Multiplicand field
$\mathbf{L}_{\mathbf{I}}=$ Length of Instruction
$\mathrm{L}_{\mathrm{xr}}=$ Length of Multiplier field
$L_{Q}=$ Length of Quotient field
$\mathbf{L}_{\mathrm{B}}=$ Length of Divisor field
$\mathrm{L}_{\mathrm{s}}=$ Number of significant digits in Divisor (Excludes high-
$L_{W}=$ Length of $A$ - or $B$-field, whichever is shorter
$\mathbf{L}_{\mathbf{x}}=$ Number of characters to be cleared
$\mathbf{L}_{\mathbf{Y}}=$ Number of characters back to right-most " 0 " in control field
$L_{z}=$ Number of 0 's inserted in a field
$\mathrm{I} / \mathrm{O}=$ Timing for Input or Output cycle
$\mathbf{F}_{\mathrm{m}}=$ Forms movement times. Allow 20 ms for first space, plus 5 ms for each additional space
$\mathrm{T}_{\mathbf{m}}=$ Tape movement times
$\Sigma=$ Number of fields included in an operation

Add (no recomplement) Add (recomplement) Branch
Branch if Bit Equal*
Branch if Character Equal
Branch if Indicator On
Branch if Word Mark
and/or Zone
Clear Storage
Clear Word Mark
Compare
Control Carriage
Control Unit
Divide (aver.)*
Halt
Load Characters to A
Word Mark
Modify Address*
Move Characters to A or
B Word Mark
Move Characters and Edit
Move Characters to Record
or Word Mark*
Move Characters and Suppress Zeros
Move and Insert Zeros*
Move Numeric
Move Zone
Multiply (aver.)*
No Operation
$0115\left(L_{I}+3+L_{A}+L_{B}\right)$
$.0115\left(L_{I}+3+L_{A}+4 L_{B}\right)$
$.0115\left(L_{1}+1\right)$
$.0115\left(L_{I}+2\right)$
$.0115\left(L_{1}+2\right)$
$.0115\left(L_{I}+1\right)$
$.0115\left(L_{1}+2\right)$
$.0115\left(L_{1}+1+L_{x}\right)$
$.0115\left(L_{1}+3\right)$
$.0115\left(L_{1}+1+L_{A}+L_{B}\right)$
$.0115\left(L_{L}+1\right)+F_{m}$ $.0115\left(L_{1}+1\right)+T_{m}$
$.0115\left(L_{1}+2+7 L_{12} L_{Q}+8 L_{Q}\right)$ $.0115\left(L_{1}+1\right)$
$.0115\left(L_{I}+1+2 L_{A}\right)$
$.0115\left(L_{1}+9\right)$
$.0115\left(L_{I}+1+2 L_{W}\right)$ $0115\left(L_{I}+1+L_{A}+L_{B}+L_{Y}\right)$
$.0115\left(L_{1}+1+2 L_{A}\right)$
$.0115\left(L_{1}+1+3 L_{\Delta}\right)$
$.0115\left(L_{I}+1+2 \Sigma L_{\Delta}+\Sigma L_{z}\right)$
$.0115\left(L_{I}+3\right)$
$.0115\left(L_{I}+3\right)$
$.0115\left(L_{I}+3+2 L_{C}+5 L_{C} L_{M}+7 L_{M}\right)$
$.0115\left(L_{I}+1\right)$

## TAPE OPERATIONS

$\mathrm{T}_{\mathrm{m}}$ - Tape movement can be determined from the following: $\mathrm{N}=$ Number of Characters
$\mathrm{C}=$ Character Rate
729 11 at $200 \mathrm{spi}=.067 \mathrm{~ms}$
729 at $556 \mathrm{cpi}=.024 \mathrm{~ms}$
$\begin{aligned} 729 \mathrm{IV} \text { at } 200 \mathrm{cpi} & =.044 \mathrm{~ms} \\ \text { at } 556 \mathrm{cpi} & =.016 \mathrm{~ms}\end{aligned}$
$7330 \begin{aligned} & \text { at } 200 \mathrm{cpi}=.139 \mathrm{~ms} \\ & \text { at } \\ & \text { at } 556 \mathrm{cpi}=.050 \mathrm{~ms}\end{aligned}$
Write, Read Tape 729 Model II $=10.8+\mathrm{CN}$ ms 729 Model IV $=7.3+\mathrm{CN}$ ms $7330 \operatorname{Read} 7.6+C(N+7)=\mathrm{ms}$ if processing exceeds 13.2 ms $20.8+\mathrm{C}(\mathrm{N}+7)=\mathrm{ms}$ if processing is less than 13.2 ms Write $13.3+\mathrm{C}(\mathrm{N}+4)=\mathrm{ms}$ if processing exceeds 7.5 ms

## Rewind

 $20.8+\mathrm{C}(\mathrm{N}+4)=\mathrm{ms}$ if processing is less than 7.5 ms729 Model II $=1.2$ minutes/reel
729 Model IV $=.9$ minutes $/$ reel
7330 (High Speed) $\stackrel{.2}{=} 2.2$ minutes/reel
Skip and Blank Tape
(add to subsequent write time)
729 Model II $=108 \mathrm{~ms}$
729 Model IV $=72 \mathrm{~ms}$
$7330=108 \mathrm{~ms}$
$7330=108 \mathrm{~ms}$
Backspace (after Read)
729 Model II $=46+\mathrm{CN}$ ms
729 Model IV $=33+\mathrm{CN}$ ms
$7330=436.1+$ CN ms
Backspace (after Write)
729 Model II $=52+\mathrm{CN}$ ms
729 Model IV $=37+\mathrm{CN} \mathrm{ms}$
$7330=452.1+\mathrm{CN} \mathrm{ms}$

## INSTRUCTION FORMAT

The IBM 1401 Data Processing System uses a variable wordlength concept; the length of an instruction can vary from one to eight characters.
$\frac{\text { OP CODE }}{\mathbf{x}} \frac{\text { A- or I-ADDRESS }}{\mathbf{x X x}} \frac{\text { B-ADDRESS }}{\mathbf{x x x}} \frac{\text { d-CHARACTER }}{\mathbf{x}}$
Op Code: This is always a single character which defines the basic operation being performed. A word mark is always associated with the operation code position of an instruction.
A-Address: This always consists of three characters. It can identify the units position of the A-field, or it can be used to select a special unit or feature (tape unit, 1412 magnetic character reader, column binary feature, disk storage, inquiry, etc.).
l-Address: Instructions that can cause program branches use the l-address to specify the location of the next instruction to be executed if a branch occurs.
B-Address: This is a three-character storage address associated with the B-field. It usually addresses the units position of the with the B-field. It usually addresses the units position of the
B-field, but in some operations, such as tape or disk record read and write, it specifies the high-order position of a read and write,
d-Character: The d-character is used to modify an operation code. It is a single alphabetic, numerical, or special characer, positioned as the last character of an instruction. It can be used with instructions of any length

## RAMAC 1401

## INSTRUCTION FORMAT


DISK ADDRESS FORMAT

| ACCESS ARM | DISK <br> UNIT | DISK <br> FACE | TRACK | SECTOR | CONSTANT |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathbf{X}$ | $\mathbf{X}$ | $\mathbf{X X}$ | $\mathbf{X X}$ | $\mathbf{X}$ | $\mathbf{X}$ |
| $\mathbf{0 - 1}$ | $\mathbf{0}$ | $\mathbf{0 0 - 9 9}$ | $\mathbf{0 0 - 9 9}$ | $\mathbf{0 - 9}$ | $\mathbf{0}$ |

TIMINGS (Model 2)
Disk to Disk
Track to Track
Track to Track
Record to Record, same Track

MAX. 800 ms 800 ms

250 ms 250 ms

AVG
AVG. MIN.

International Business Machines Corporation Data Processing Division
112 East Post Road White Plains, N. Y.

INPUT-OUTPUT CODES

| 1 | Read a Card | R | 1 | 1 |
| :---: | :---: | :---: | :---: | :---: |
| 2 | Write a Line | W | 2 | 2 |
| 2口 | Write Word Marks | $\square$ is modifier |  |  |
| 3 | Writ-Read | WR | C21 | 3 |
| 4 | Punch a Card | P | 4 | 4 |
| 4R | Read-Punch Feed* | $R$ is modifier |  |  |
| 4(1)R | Read-Punch Feed and Branch* | R is modifier |  |  |
| 5 | Read-Punch | RP | C41 | 5 |
| 6 | Write-Punch | WP | C42 | 6 |
| 6R | Write-Read Punch Feed* | R is modifier |  |  |
| 6(1)R | Write-Read Punch Feed and Branch* | R is modifier |  |  |
| 7 | Write-Read-Punch | WRP | 421 | 7 |
| 8 | Start Read Feed* | SRF | 8 | 8 |
| 9 | Start Punch Feed* | SPF | C81 | 9 |


| ARITHMETIC CODES |  |  |  |  |
| :--- | :--- | :---: | :---: | :---: |
| A | Add | A | BA1 | $12-1$ |
| $\mathbf{S}$ | Subtract | S | CA2 | $0-2$ |
| $\stackrel{+}{0}$ | Zero and Add | ZA | CBA82 | $12-0$ |
| $\overline{0}$ | Zero and Subtract | ZS | B82 | $11-0$ |
| $@$ | Multiply | M | C84 | $4-8$ |
| $\%$ | Divide $^{*}$ | M | D | A84 |
| $\%$ | $0-4-8$ |  |  |  |

## LOGIC OPERATION CODES

| B(I) | Branch | B | BA2 | 12-2 |
| :--- | :--- | :--- | :--- | :---: |
| B(I)d | Branch if Indicator ON | d is modifier |  |  |
| B(I)(B)d | Branch if Character is Equal | Contents of B compared to d |  |  |
| V(I)(B)d | Branch if WM and/or Zone | BWZ | A41 | $0-5$ |

## MOVE AND LOAD CODES

| D | Move Numerical | MN | BA4 | $12-4$ |
| :---: | :--- | :--- | :--- | :---: |
| L | Load Character to A Word <br> Mark | LCA | B21 | $11-3$ |
| M | Move Characters to A or B <br> Word Mark | MCW | CB4 | $11-4$ |
| Y | Move Zone | MZ | CA8 | 0.8 |
| Z | Move Characters and <br> Suppress Zeros | MCS | A81 | 0.9 |
| , | Set Word Mark | SW | CA821 | $0-3-8$ |
| $\square$ | Clear Word Mark | CW | CBA84 | $12-4-8$ |

## MISCELLANEOUS OPERATION CODES

| C | Compare | C | CBA21 | $12-3$ |
| :--- | :--- | :--- | :--- | :---: |
| E | Move Characters and Edit | MCE | CBA41 | $12-5$ |
| F | Control Carriage | CC | CBA42 | $12-6$ |
| H | Store B-Address Register* | SBR | BA8 | $12-8$ |
| K | Select Stacker | SS | CB2 | $11-2$ |
| N | No Operation | NOP | B41 | $11-5$ |
| $\mathbf{Q}$ | Store A-Address Register* | SAR | CB8 | 11.8 |
| $\boldsymbol{l}$ | Clear Starage | CS | CA1 | $0-1$ |
| $*$ | Halt | H | BA821 | $12-3-8$ |
| $\#$ | Modify Address* | MA | 821 | $3-8$ |

CHARACTER AT 』 FOR Bİd BRANCH

| d |  | BRANCH ON | d | BRANCH ON |
| :---: | :---: | :---: | :---: | :---: |
| bl | Unconditional |  | R | Carriage Busy* |
| 9 | Carr. Chan. \#9 |  | $T$ | Low Compare B $<\mathrm{A}^{*}$ |
| A | "Last Card" Switch |  | U | High Compare B $>\mathrm{A}^{*}$ |
| B | Sense Switch B* |  | z | Overflow |
| C | Sense Switch C* |  | $\begin{aligned} & + \\ & 0 \end{aligned}$ | Reader Error if I/O Check Stop Switch OFF |
| D | Sense Switch D* |  |  |  |
| E | Sense Switch E* |  | $\overline{0}$ | Punch Error if I/O Check Stop Switch OFF |
| F | Sense Switch $\mathrm{F}^{*}$ |  |  |  |
| G | Sense Switch G* |  | $+$ | Printer Error if I/O Check Stop Switch OFF |
| K | End of Reel* |  | @ | Carr. Chan. \#12 |
| L | Tape Error* |  | \% | Processing Check with Process Check Switch OFF |
| S | Equal C | Compare $\mathbf{B}=\mathbf{A}^{*}$ |  |  |
| P | Printer Busy* |  | 1 | Unequal Compare B $\neq A$ |
| Q | Inquiry Request (1407) |  | * | Inquiry Clear (1407) |
| COLUMN BINARY |  |  |  |  |
| 1 C |  | Read Column Binary | C is Modifier |  |
| 4C |  | Punch Column Binary | $C$ is Modifier |  |
| M(A)(B)A |  | Move and Binary Decod | $A$ is Modifier |  |
| M(A)(B)B |  | Move Binary Code | $B$ is Modifier |  |
| M(\%BX)(A)R |  | Read Binary Tape | \% BX is Address of tape unit |  |
| M(\%BX)(A)W |  | Write Binary Tape |  |  |  |
| W(I)(B)d |  | Branch if Bit Equal | BBE is mnemonic |  |

## 1407 INOUIRY \%T0 ADDRESS

##  <br> $M$ $M$ $L$ $L$ $L$



