

IBM

**Customer Engineering
Intermediate
Level Diagrams**

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1620

Data Processing System, Model 2

PREFACE

This manual is intended for instructional purposes. It does not replace any field machine system diagrams. The diagrams enclosed within the machine should always be used for field analysis. These diagrams are intended for use with the Customer Engineering Manual of Instruction, 1620 Data Processing System, Model 2.

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This form, S27-0500, supplements the IBM 1620 Model 2 Customer Engineering Intermediate Level Diagrams manual (Form 227-5857-0). This supplement contains changes applicable to all 1620 Model 2 machines.

The pages in the 1620 Model 2 Customer Engineering Intermediate Level Diagrams manual that have been changed can be replaced with the pages in this supplement in the following manner.

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| 1. Contents Page | |
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| 3. Pages 10.00.08.1 and 10.00.09.1 | 14. Pages 10.01.40.1 and 10.01.41.1 |
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| 8. Pages 10.00.52.1 and 10.00.53.1 | 19. Pages 10.01.64.1 and 10.01.66.1 |
| 9. Pages 10.00.54.1 and 10.00.55.1 | 20. Pages 10.01.68.1 and 10.01.70.1 |
| 10. Pages 10.00.86.1 and 10.00.87.1 | 21. Pages 10.01.90.1 and 10.01.91.1 |
| 11. Pages 10.01.20.1 and 10.01.21.1 | 22. Pages 10.01.94.1 and 10.01.95.1 |
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INSERT THE FOLLOWING PAGES INTO THE MANUAL:

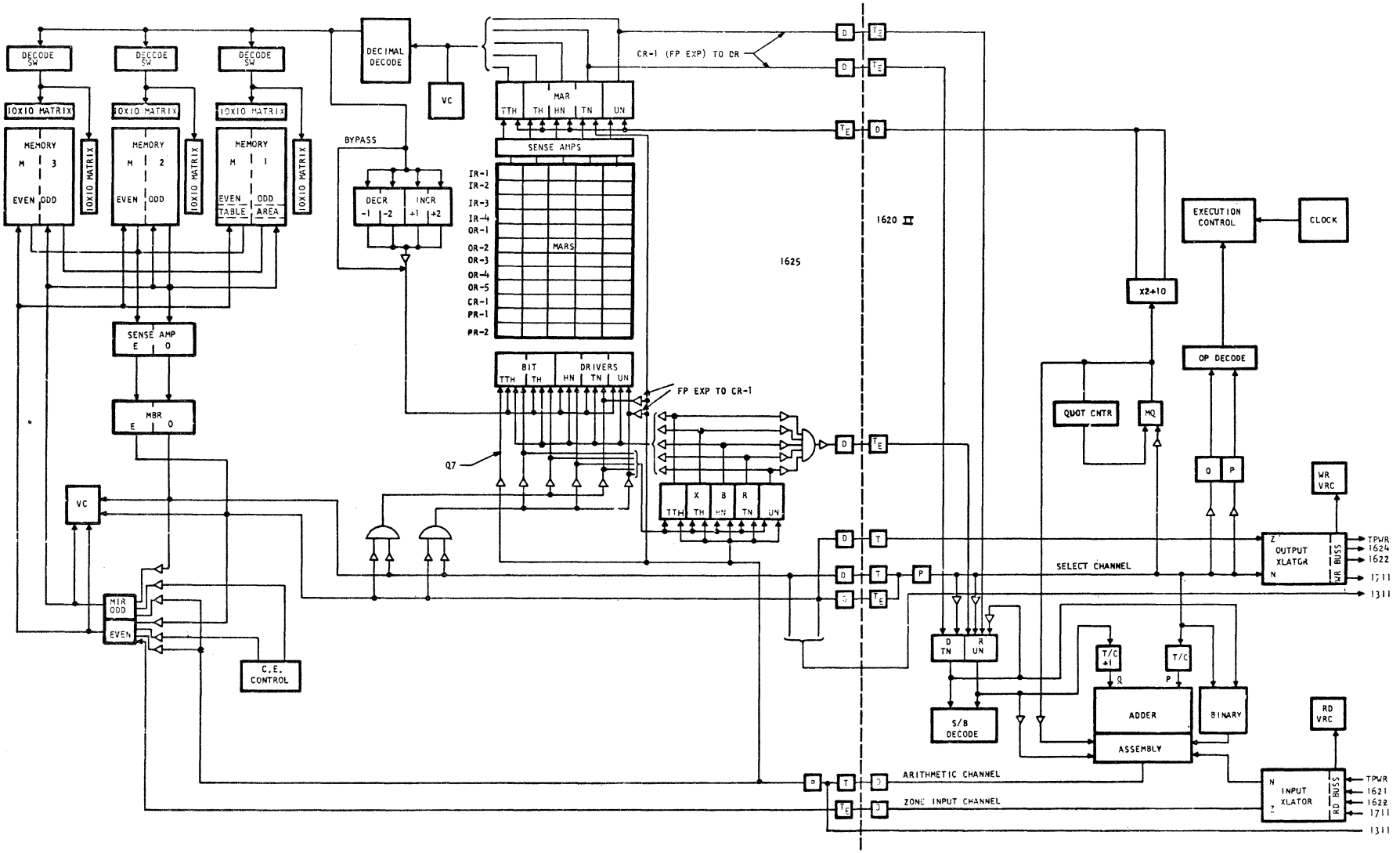
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| 4. Pages 10.00.20.1 and 10.00.21.1 | 18. Pages 10.01.40.1 and 10.01.41.1 |
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| 15. Pages 10.01.20.1 and 10.01.21.1 | 29. Pages 10.01.96.1 and 10.01.97.1 |

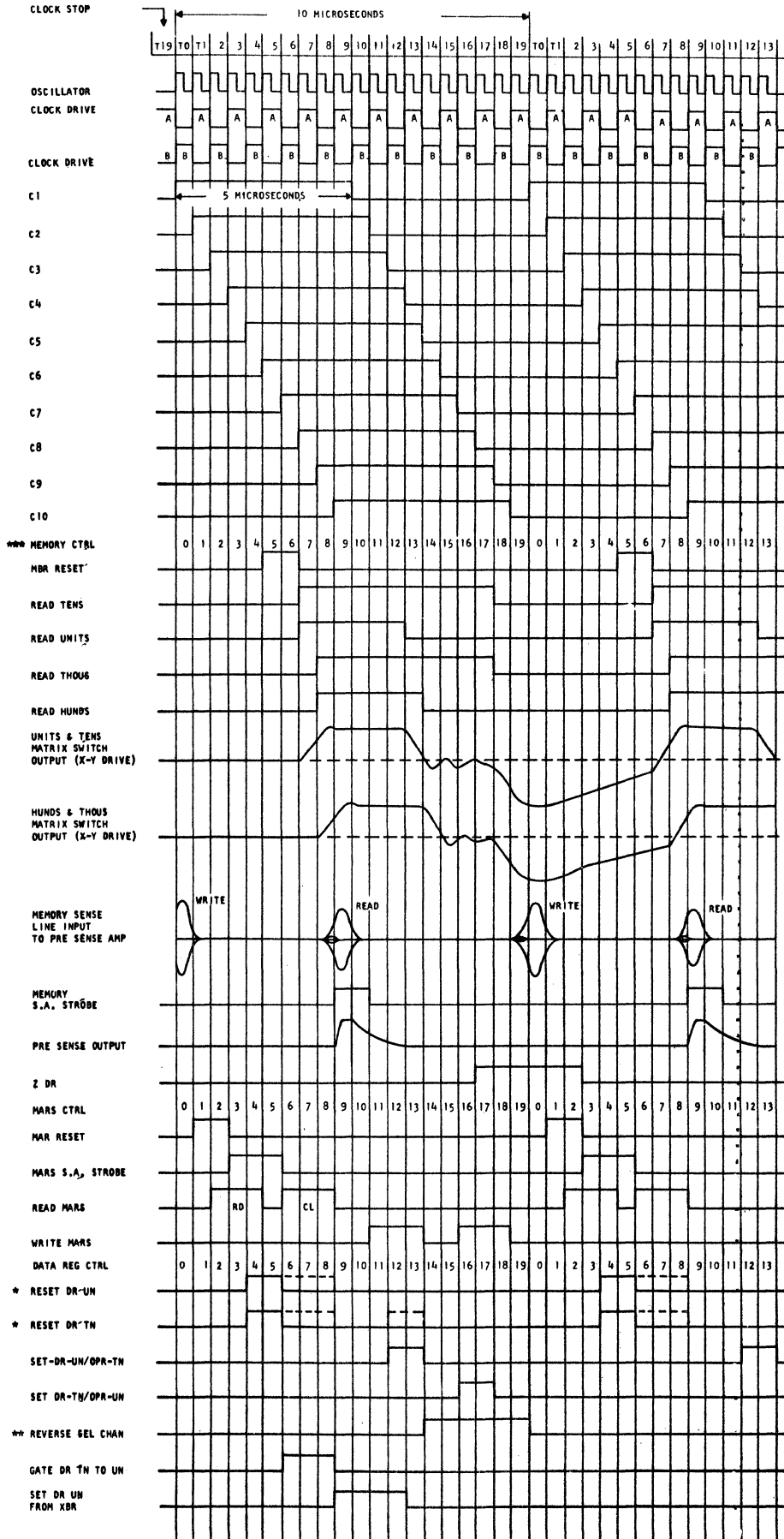
After completing the above steps, file this instruction page between the Preface page (page ii) and the new Contents page (page iii) and destroy all the leaves removed from the manual. This page will serve as a revision notice.

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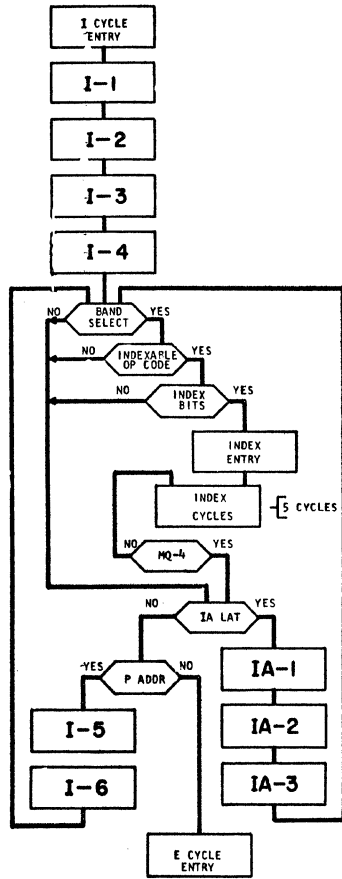
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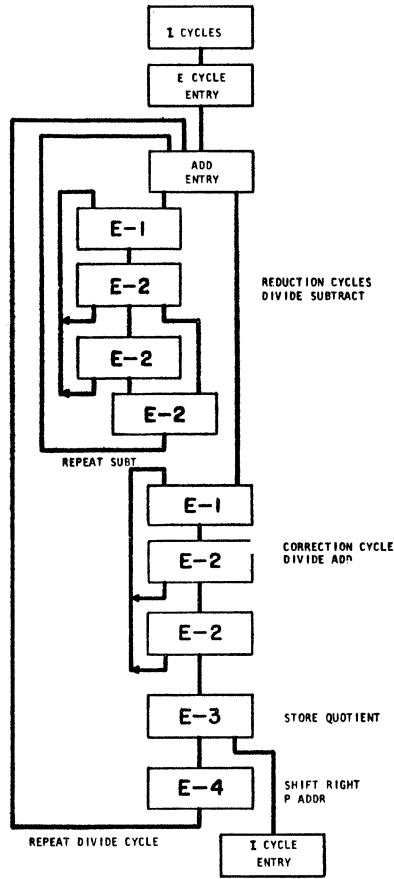


* SEE 10.01.22.1
 ** SEE 10.01.21.1
 *** SEE CE REFERENCE MANUAL FOR TYPICAL WAVE FORMS
 E/C 408504-V 7-9-63 DA 0A06000

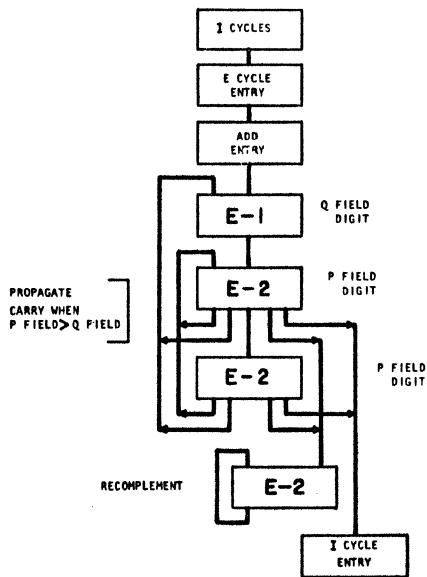
I CYCLES - INDEXING - IA



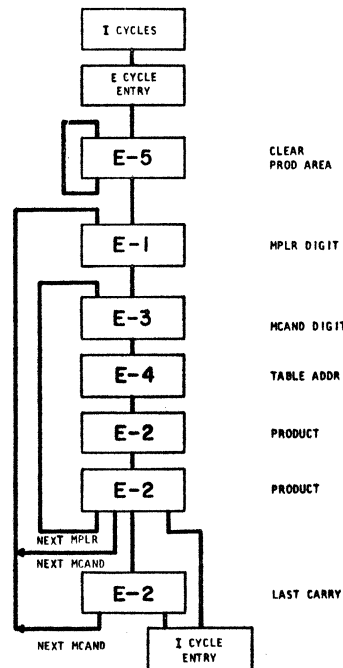
DIVIDE

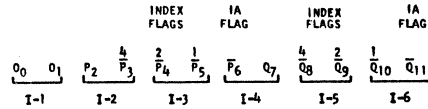


ADD - SUBTRACT - COMPARE



MULTIPLY



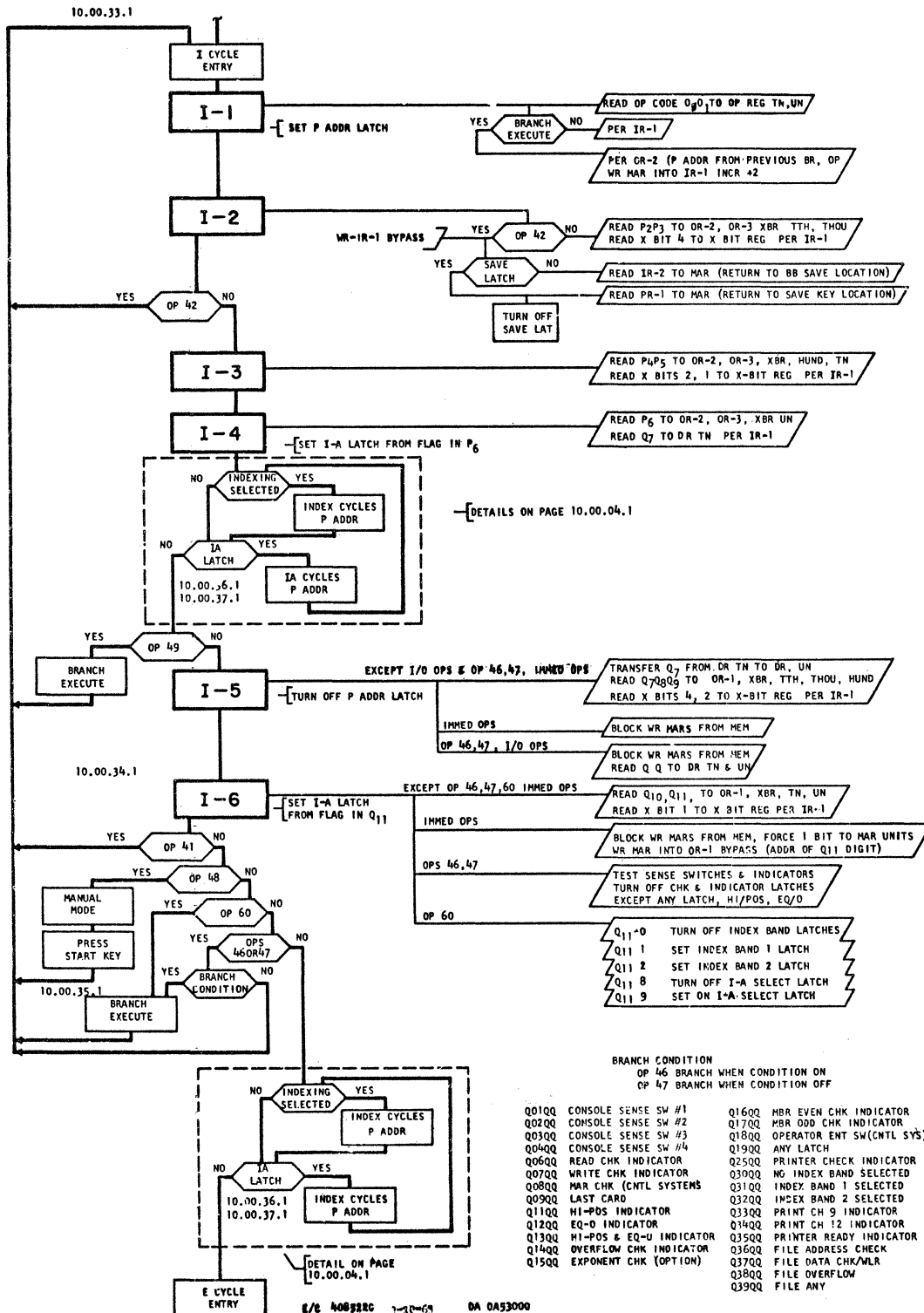


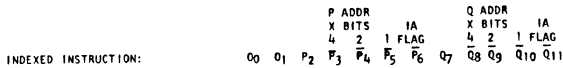
PURPOSE: STORE INSTR OP CODE, P & Q ADDR, AND FLAGS TO RESPECTIVE REGISTERS
EXECUTE THOSE OP CODES NOT REQUIRING E CYCLE
PERFORM INDEXING AND INDIRECT ADDRESSING

OP CODES EXECUTED ON I CYCLES

NON-BRANCH CODES	NOP	41	0 0 0 0 0	0 0 0 0 0	P & Q READ OUT BUT NOT USED
	H	48	0 0 0 0 0	0 0 0 0 0	P & Q READ OUT BUT NOT USED
UNCONDITIONAL BRANCH	BB	42	X X X X X	X X X X X	P & Q NOT READ OUT
	B	49	P P P P P	X X X X X	Q NOT READ OUT
	BB	60	P P P P P	0 0 0 Q ₁₁	
CONDITIONAL BRANCH	B1	46	P P P P P	X Q ₈ Q ₉ X X	Q ₈ Q ₉ SENSE CODES 01-19
	BNI	47	P P P P P	X Q ₈ Q ₉ X X	Q ₈ Q ₉ SENSE CODES 01-19

FUNCTION CHART	PAGE REF
I CYCLES I ENTRY I-1	10.00.30.1
I CYCLES I-2 → I-5	10.00.31.1
I CYCLES I-6	10.00.32.1
I-15 BRANCH	10.00.33.1
I-16 BRANCH	10.00.34.1
BRANCH EXEC	10.00.35.1
INDIRECT ADDR	10.00.36.1
	10.00.37.1
I-14 XBR LOAD	10.00.40.1
I-16 XBR LOAD	10.00.41.1
XBR SET & RESET	10.01.30.1



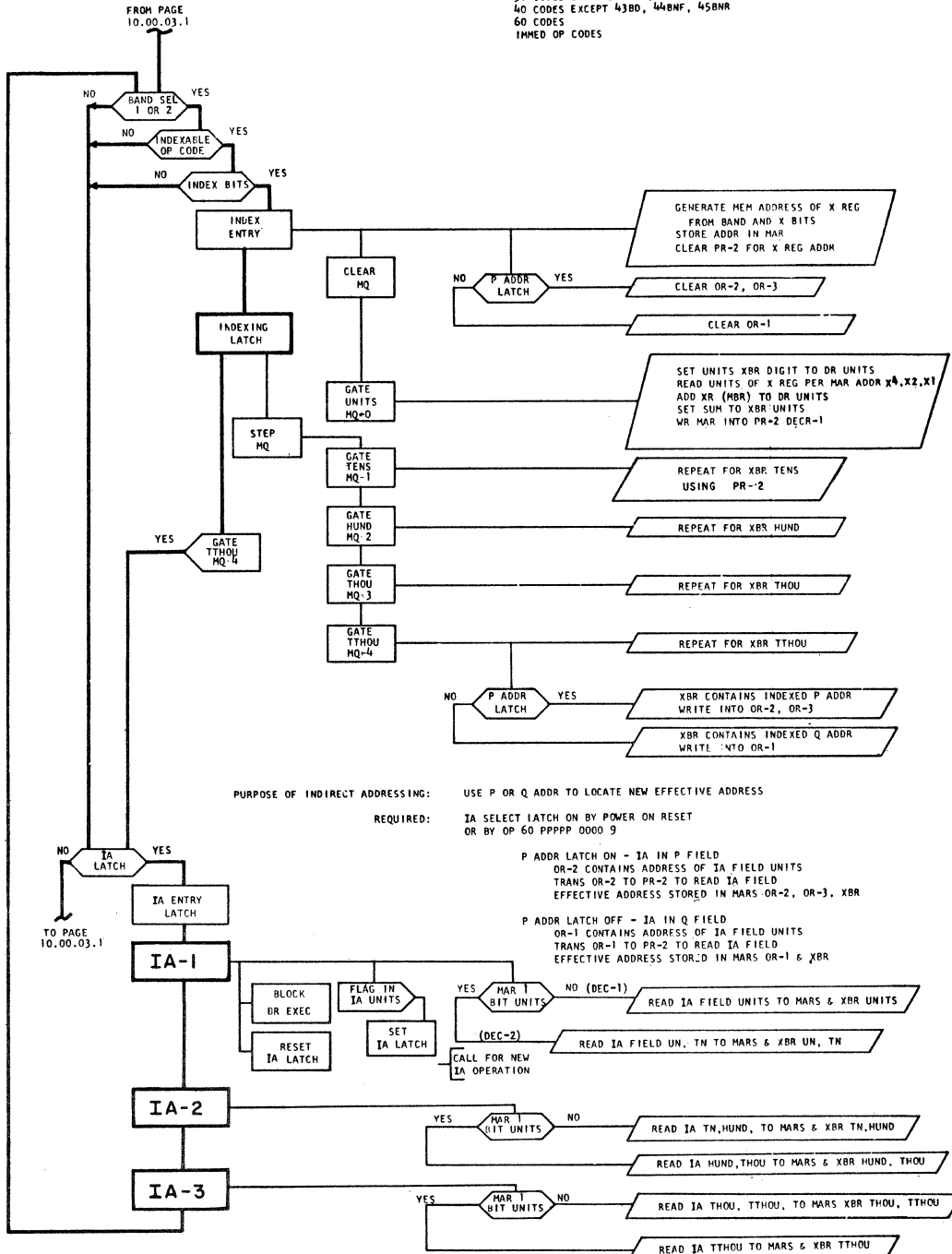


PURPOSE OF INDEXING: SELECT AN X REGISTER IN MEMORY BANDS 1 OR 2 WITH P OR Q X BITS
 ADD CONTENTS OF SELECTED X REGISTER TO P OR Q ADDRESS IN XBR
 STORE NEW EFFECTIVE ADDRESS IN MARS

- REQUIRED:
- BAND 1 OR 2 SELECTED BY OP 60 P P P P P 0 0 0 0 X
 - AT LEAST ONE X BIT PRESENT
 - AN INDEXABLE OP CODE

FUNCTION CHART	PAGE REF
XBR I-A LOAD	10.00.42.1
INDEX SEL P ENTRY	10.00.43.1
INDEX CYCLES	10.00.44.1
MQ REG AND COUNTER	10.01.23.1
XBR SET & RESET	10.01.30.1

CODES WITH P ADDRESS NON INDEXABLE
 34K, 41NOP, 42BB, 4BH
 CODES WITH Q ADDRESS NON-INDEXABLE
 30 CODES EXCEPT 30TRNM, 31TR
 40 CODES EXCEPT 43BD, 44BNF, 45BNR
 60 CODES
 IMMEDIATE OP CODES



PURPOSE OF INDIRECT ADDRESSING: USE P OR Q ADDR TO LOCATE NEW EFFECTIVE ADDRESS

- REQUIRED:
- IA SELECT LATCH ON BY POWER ON RESET OR BY OP 60 P P P P P 0 0 0 0 9

P ADDR LATCH ON - IA IN P FIELD
 OR-2 CONTAINS ADDRESS OF IA FIELD UNITS
 TRANS OR-2 TO PR-2 TO READ IA FIELD
 EFFECTIVE ADDRESS STORED IN MARS OR-2, OR-3, XBR

P ADDR LATCH OFF - IA IN Q FIELD
 OR-1 CONTAINS ADDRESS OF IA FIELD UNITS
 TRANS OR-1 TO PR-2 TO READ IA FIELD
 EFFECTIVE ADDRESS STORED IN MARS OR-1 & XBR

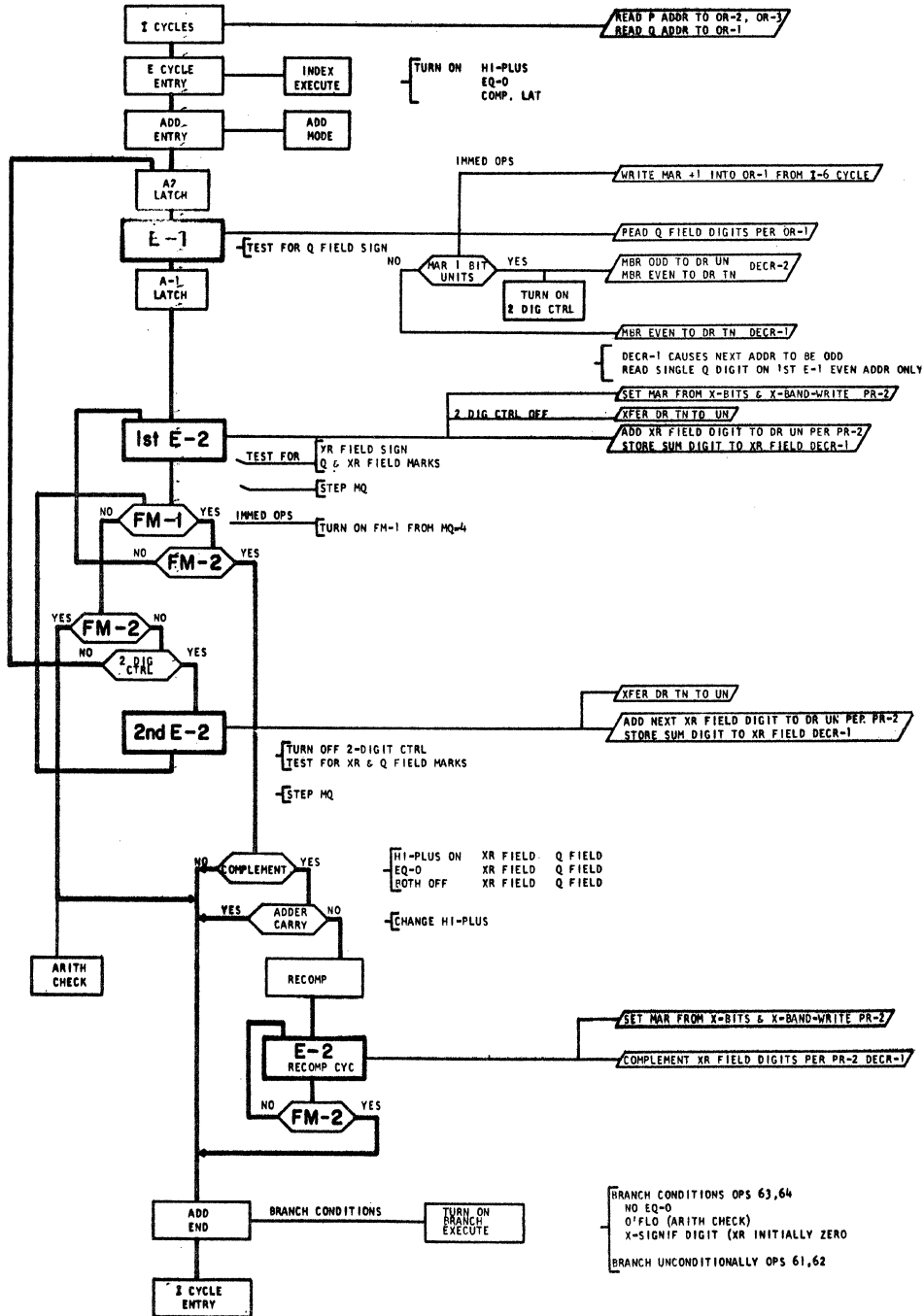
- 61 BX BRANCH AND MODIFY INDEX REGISTER
- 62 BXM BRANCH AND MODIFY INDEX REGISTER IMMED
- 63 BCX BRANCH CONDITIONALLY AND MODIFY INDEX REGISTER
- 64 BCXM BRANCH CONDITIONALLY AND MODIFY INDEX REGISTER IMMED

INSTRUCTION: OPS 61,63 0 0 P P P P P Q Q Q Q Q
 OPS 62,64 0 0 P P P P P Q Q Q Q Q

PURPOSE: ADD Q FIELD TO SPECIFIED INDEX REGISTER OBSERVING SIGNS OF BOTH FIELDS
 BRANCH TO P ADDRESS

X-BITS
 X-BITS SIGN
 FM-2 X X X X X PR-2
 FM-1 Q Q Q OR-1
 SUM S S S S S PR-2
 RECOMP R R R R R PR-2

FUNCTION CHART	PAGE REF
INDEX EXEC	10.00.45.1
ADD ENTRY & MODE	10.00.56.1
ADD, SUBT, COMPARE	10.00.57.1
DR & 2 DIGIT CTRL	10.01.22.1
MQ REG & COUNTER	10.01.23.1

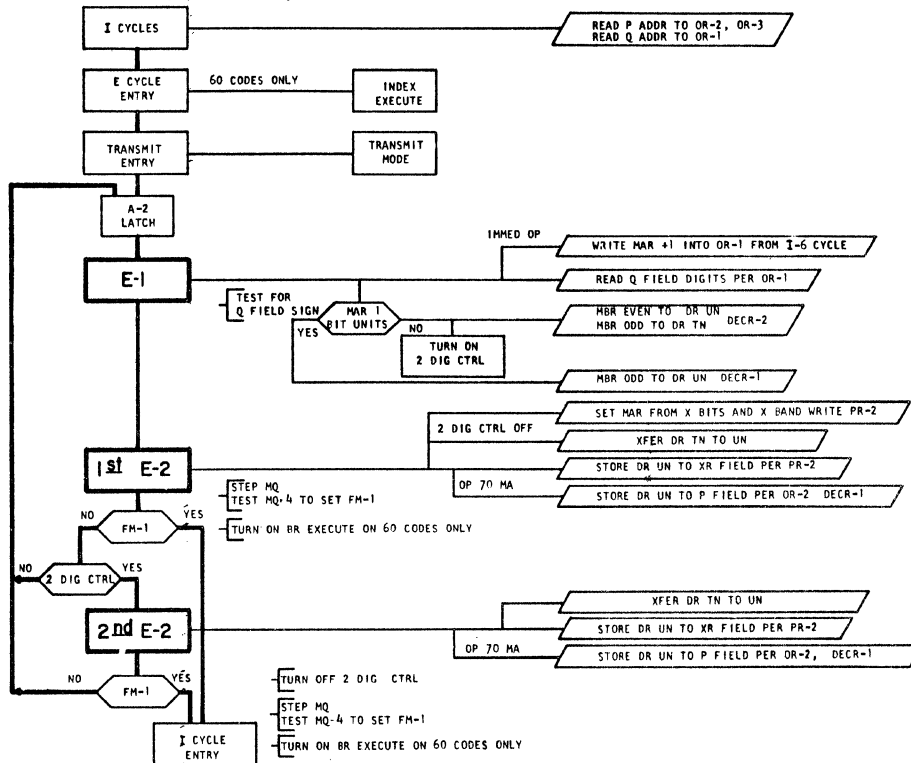


65 BLX BRANCH AND LOAD INDEX REGISTER
66 BLXM BRANCH AND LOAD INDEX REGISTER IMMED
70 MA MOVE ADDRESS

INSTRUCTION: 65 P P P P P X Q Q Q Q Q
 66 P P P P P X Q Q Q Q Q
 70 P P P P P X Q Q Q Q Q

PURPOSE: OP 65, 66 TRANSMIT 5 DIGITS AND SIGN FROM Q FIELD TO SPECIFIED XR
 STORE FM ON HIGH ORDER XR, STRIP ALL OTHER FLAGS
 BRANCH UNCONDITIONALLY TO P ADDR
 OP 70 TRANSMIT 5 Q FIELD DIGITS TO P FIELD. LEAVE P FIELD FLAGS UNCHANGED

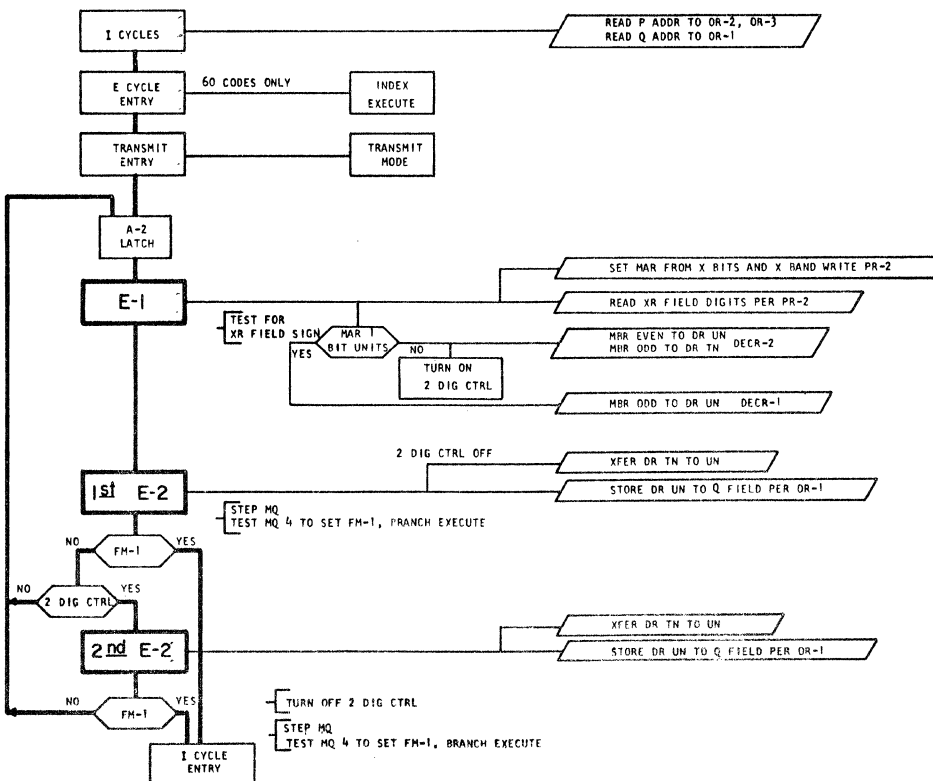
FUNCTION CHART	PAGE REF
INDEX EXEC	10.00.45.1
XMIT ENTRY & MODE	10.00.53.1
XMIT E1, E2	10.00.54.1
DR & 2 DIGIT CTRL	10.01.22.1
HQ REG & COUNTER	10.01.23.1



67 BSX BRANCH AND STORE INDEX REGISTER

INSTRUCTION: 67 P P P P P X Q Q Q Q Q
 PURPOSE: TRANSMIT SPECIFIED XR FIELD AND SIGN TO Q FIELD
 FORCE FM TO HIGH ORDER Q FIELD
 BRANCH UNCONDITIONALLY TO P ADDR

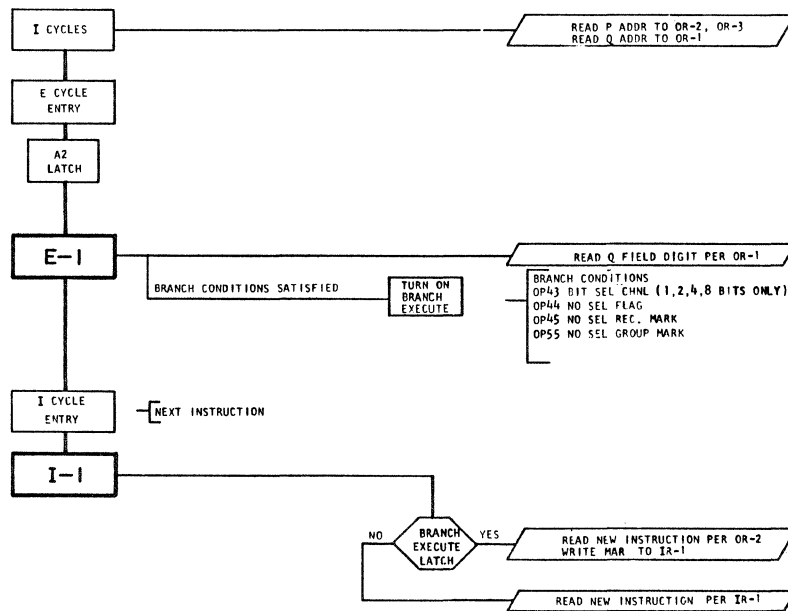
FUNCTION CHART	PAGE REF
INDEX EXEC	10.00.45.1
XMIT ENTRY & MODE	10.00.53.1
XMIT E1, E2	10.00.54.1
DR & 2 DIGIT CTRL	10.01.22.1
HQ REG & COUNTER	10.01.23.1



- 43 BD BRANCH ON DIGIT
- 44 BNF BRANCH ON NO FLAG
- 45 BNR BRANCH ON NO RECORD MARK
- 55 BNG BRANCH ON NO GROUP MARK

PURPOSE: INTERROGATE CHARACTER AT Q
IF CONDITION SATISFIED, BRANCH TO P ADDR

FUNCTION CHART	PAGE REF
OPS 43,44,45,55	10.00.51.1
BRANCH EXECUTE	10.00.35.1



- 06 TFL TRANSMIT FLOATING
- 26 TF TRANSMIT FIELD
- 16 TFM TRANSMIT FIELD IMMED

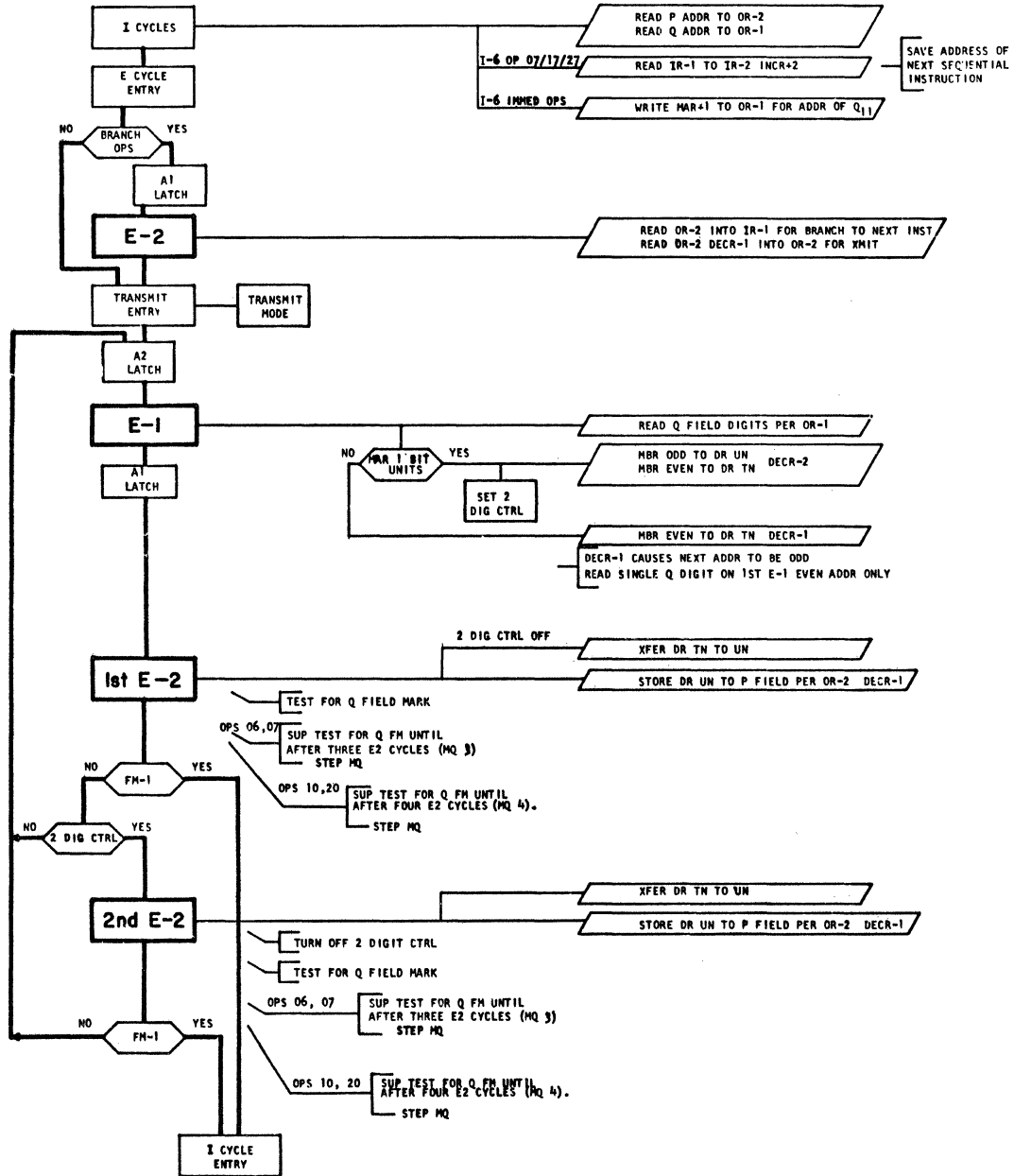
- 07 BTFL BRANCH AND TRANSMIT FLOATING
- 27 BT BRANCH AND TRANSMIT
- 17 BTM BRANCH AND TRANSMIT IMMED
- 20 BTA BRANCH AND TRANSMIT ADDRESS
- 10 BTAM BRANCH AND TRANSMIT ADDRESS IMMED

INSTRUCTION: 0p1 P P P P P Q Q Q Q Q P AND Q ADDRESS LOW ORDER POSITIONS
OPERATION TERMINATED BY Q FIELD MARK

PURPOSE OF TRANSMIT: TRANSMIT Q FIELD TO P FIELD

PURPOSE OF BRANCH AND TRANSMIT: TRANSMIT FIELD AT Q ADDR TO FIELD AT P ADDR MINUS 1
SAVE ADDRESS OF NEXT SEQUENTIAL INSTRUCTION
BRANCH UNCONDITIONALLY TO LOCATION P

FUNCTION CHART	PAGE REF
XMIT OPS	10.00.52.1
XMIT ENTRY & MODE	10.00.53.1
XMIT E1, E2	10.00.54.1
DR & 2 DIGIT CTRL	10.01.22.1



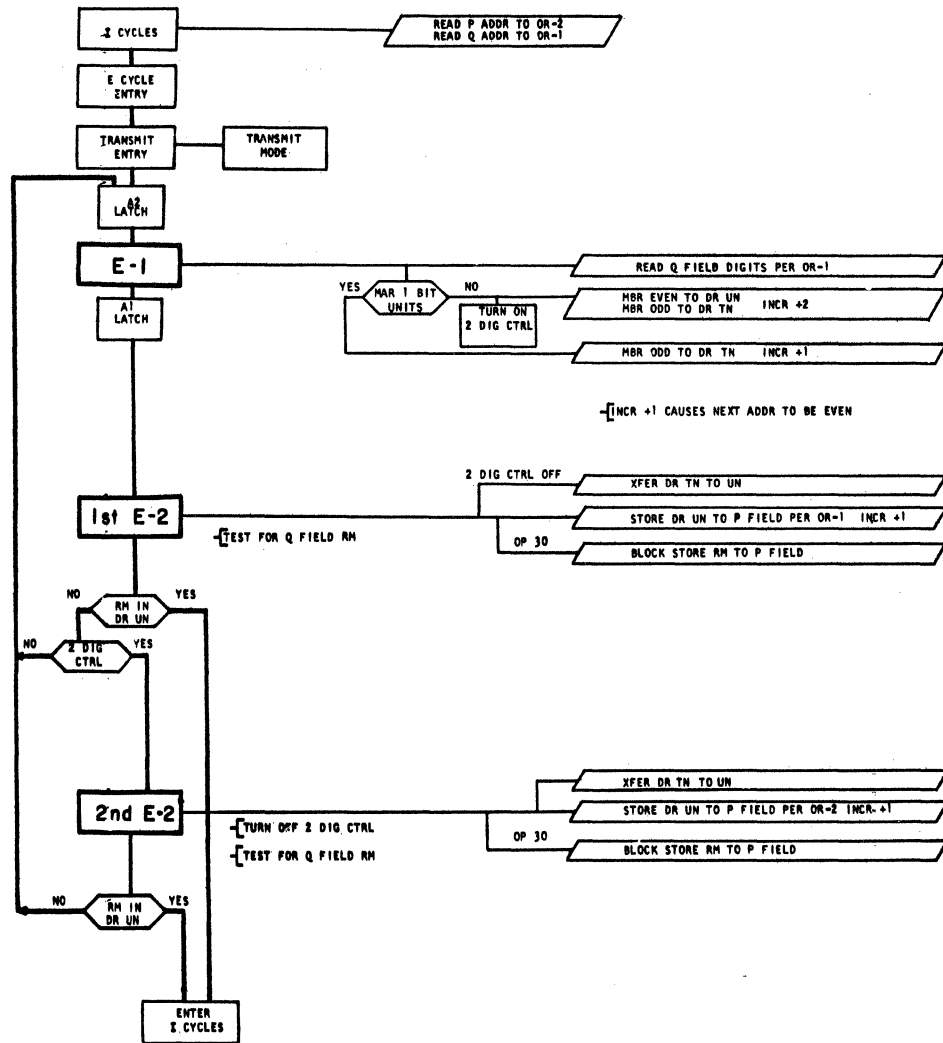
31 TR TRANSMIT RECORD
30 TRNM TRANSMIT RECORD NO RECORD MARK

INSTRUCTION: 0001 P P P P P Q Q Q Q Q

P AND Q ADDRESS HIGH ORDER POSITIONS
OPERATION TERMINATED BY Q FIELD RECORD MARK

PURPOSE: TRANSMIT RECORD AT Q ADDR TO P ADDR
OMIT RECORD MARK IN P FIELD ON OP 30

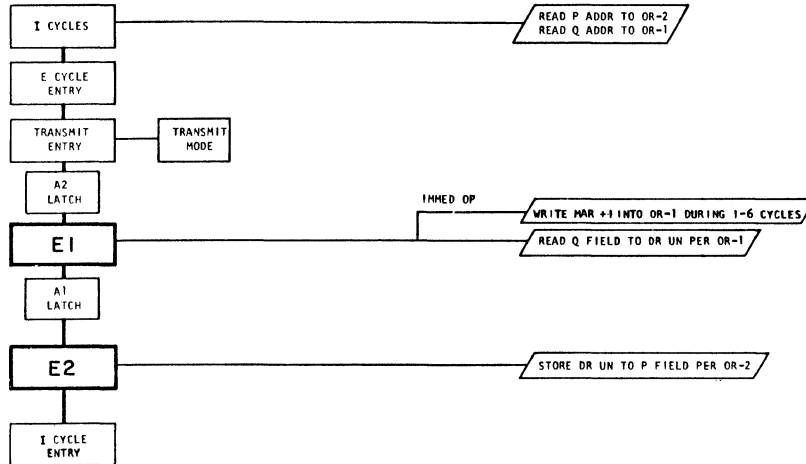
FUNCTION CHART	PAGE REF
XMIT OPS	10.00.52.1
XMIT ENTRY & MODE	10.00.53.1
XMIT E1, E2	10.00.54.1
DR & 2 DIGIT CTRL	10.01.22.1



**25 TD TRANSMIT DIGIT
15 TDM TRANSMIT DIGIT IMMED**

PURPOSE: TRANSMIT SINGLE DIGIT AT Q ADDR TO P ADDR
ON IMMED OP TRANSMIT Q11

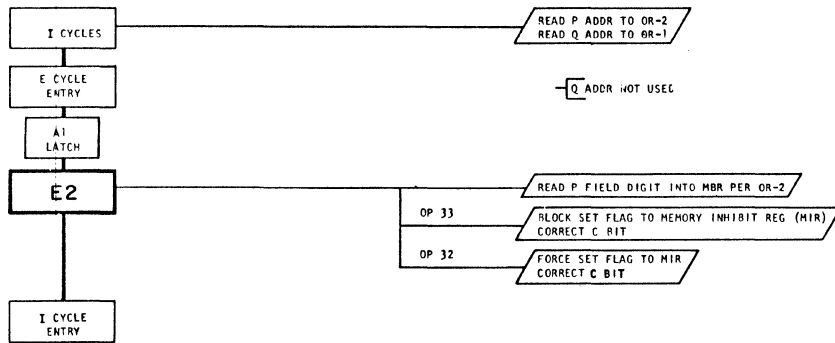
FUNCTION CHART	PAGE REF
XMIT ENTRY & MODE	10.00.53.1
XMIT E1, E2	10.00.54.1



**32 SF SET FLAG
33 CF CLEAR FLAG**

PURPOSE: STORE OR CLEAR FLAG BIT OVER DIGIT AT P ADDR

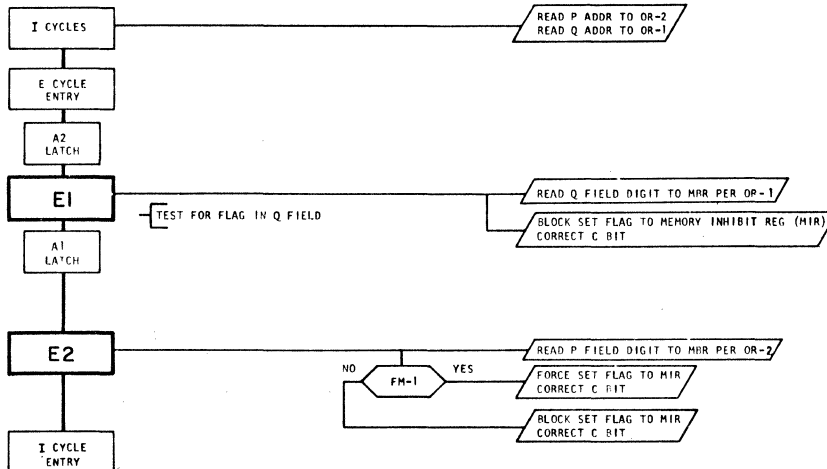
FUNCTION CHART	PAGE REF
OPS 32, 33	10.00.55.1



71 MF MOVE FLAG

PURPOSE: MOVE FLAG BIT FROM Q ADDR TO P ADDR

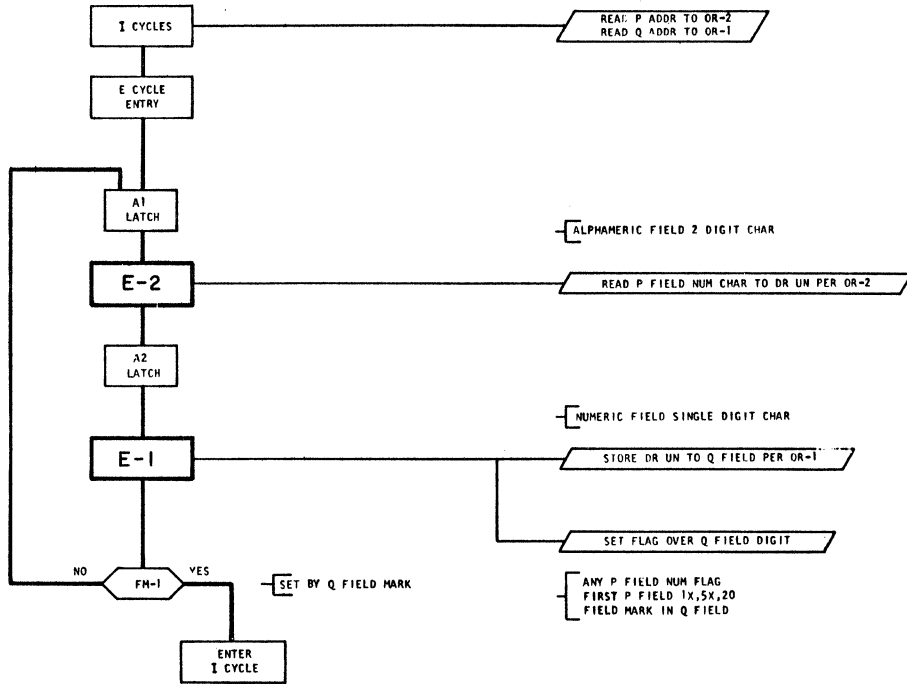
FUNCTION CHART	PAGE REF
OP 71 MF	10.00.64.1



72 TNS TRANSMIT NUMERIC STRIP

FUNCTION CHART	PAGE REF
OP 72 TNS	10.00.65.1

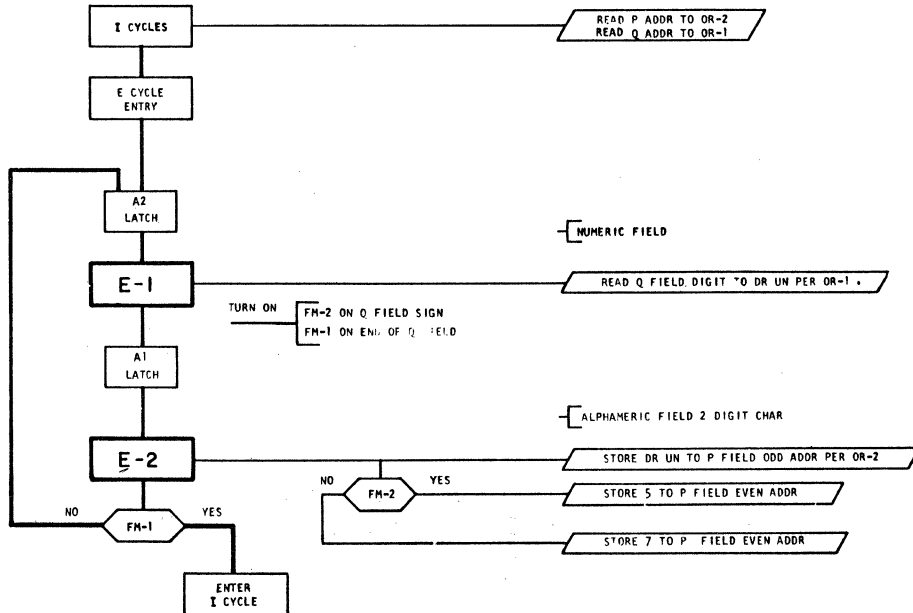
INSTRUCTION: 72 P P P P P Q Q Q Q Q P-ODD ADDR ONLY
 NUM, FIELD
 PURPOSE: STRIP NUMERIC CHARACTERS FROM P FIELD AND STORE IN Q FIELD (NOTE-THIS IS ONLY OP WHEN FIELD IS MOVED FROM P TO Q)



73 TNF TRANSMIT NUMERIC FILL

FUNCTION CHART	PAGE REF
OP 73 TNF	10.00.65.1

INSTRUCTION: 73 P P P P P Q Q Q Q Q P-ODD ADDR ONLY
 NUM FIELD
 PURPOSE: READ CONTENTS OF NUMERIC FIELD AT Q AND STORE WITH ZONE DIGITS INTO ALPHAMERIC FIELD AT P

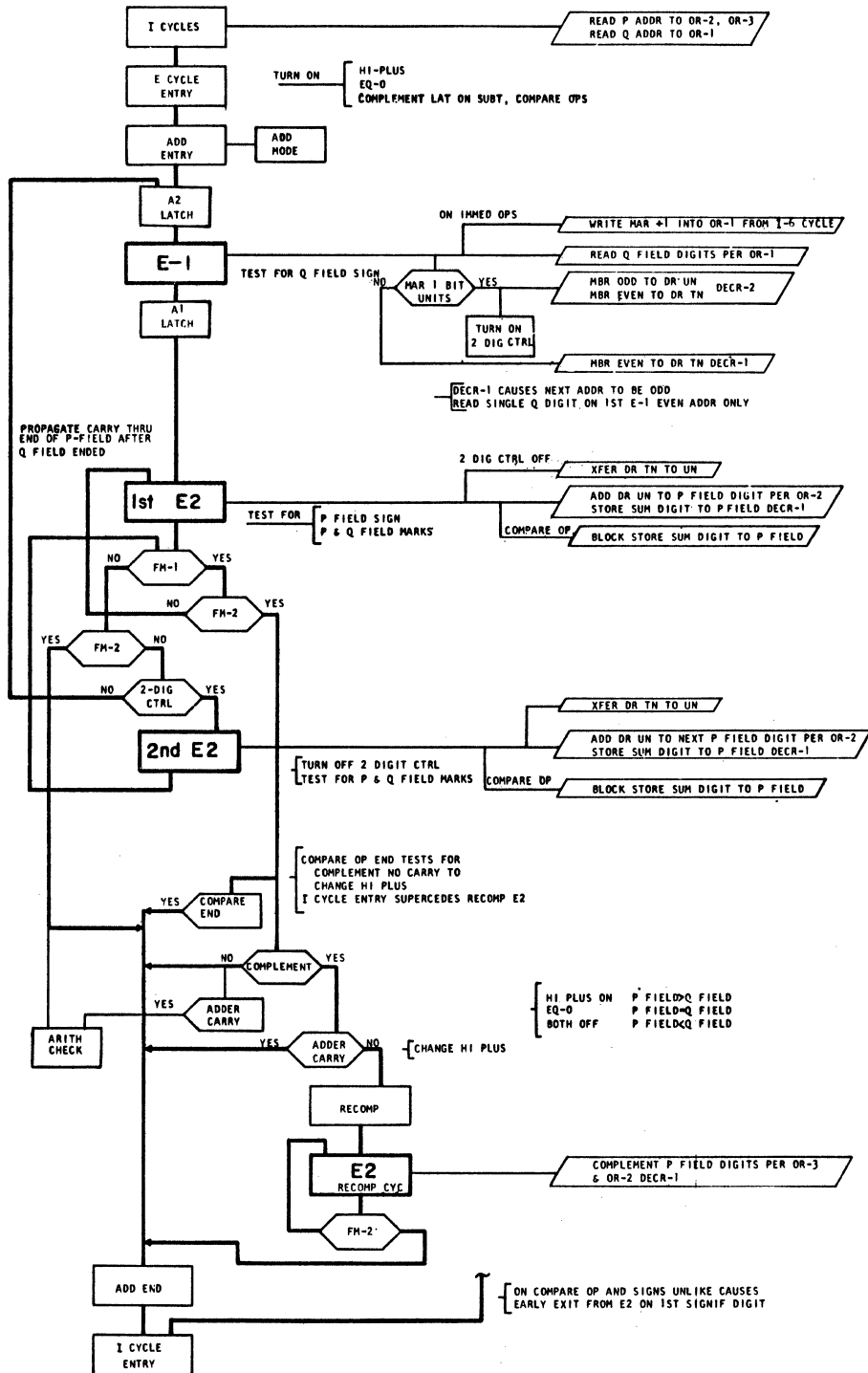


21A 11AM ADD, ADD IMMED.
 22S 12SM SUBTRACT, SUBTRACT IMMED.
 24C 14CM COMPARE, COMPARE IMMED.

PURPOSE OF ADD SUBT: AUGEND IN MBR FM-2 P P P P DR-2
 ADDEND IN MBR FM-1 Q Q Q DR-1
 SUM IN P FIELD S S S DR-2
 RECOMP SUM R R R DR-3

FUNCTION CHART	PAGE REF
ADD ENTRY & MODE	10.00.56.1
ADD, SUBT, COMPARE	10.00.57.1
DR & 2 DIGIT CTRL	10.01.22.1

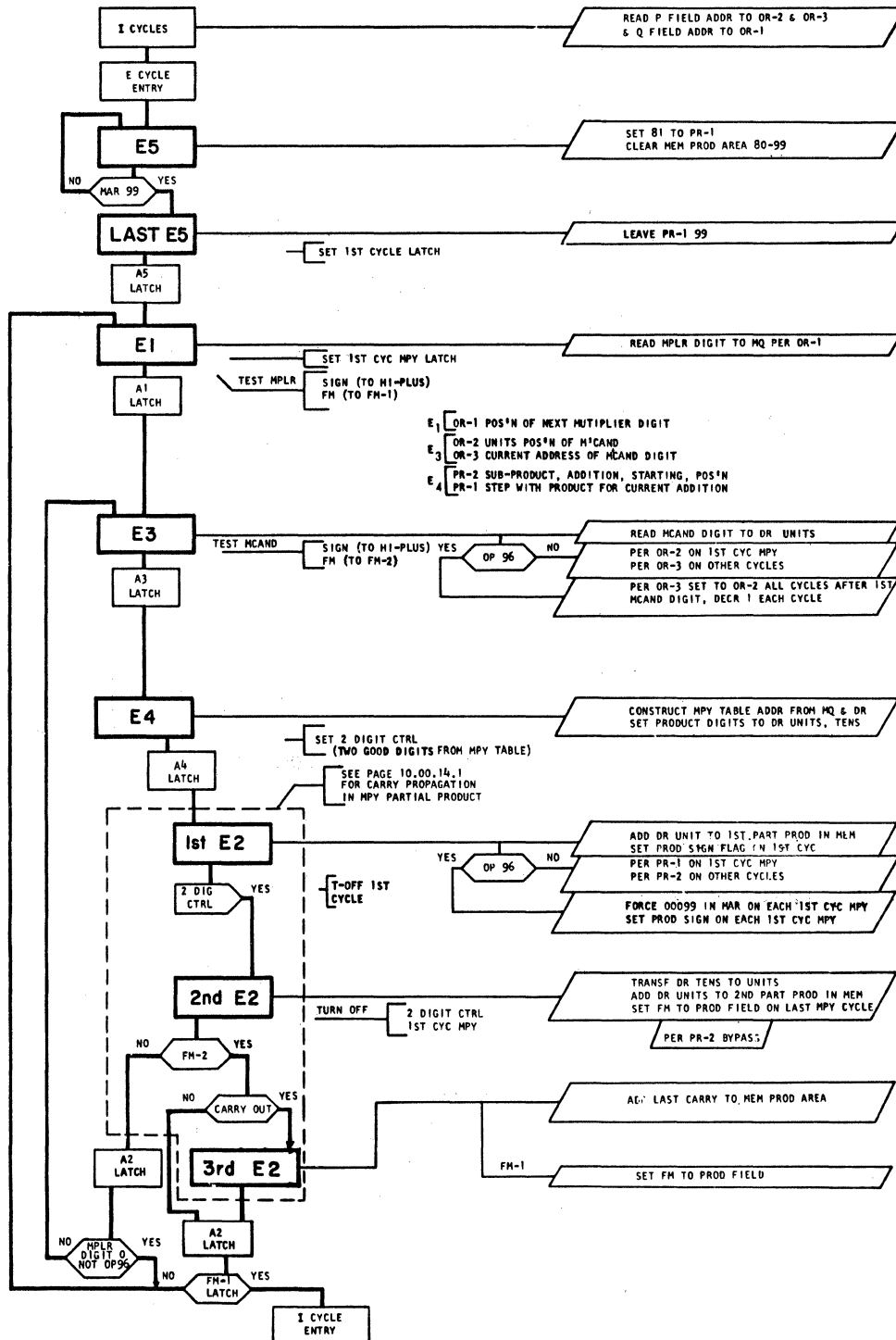
PURPOSE OF COMPARE: SUBTRACT Q FIELD DIGITS FROM P FIELD DIGITS
 TURN OFF EQ-0 ON DIFF-0
 Q & P FIELDS UNCHANGED
 SIGNS UNLIKE-EARLY EXIT ON FIRST SIGNIFICANT DIGIT

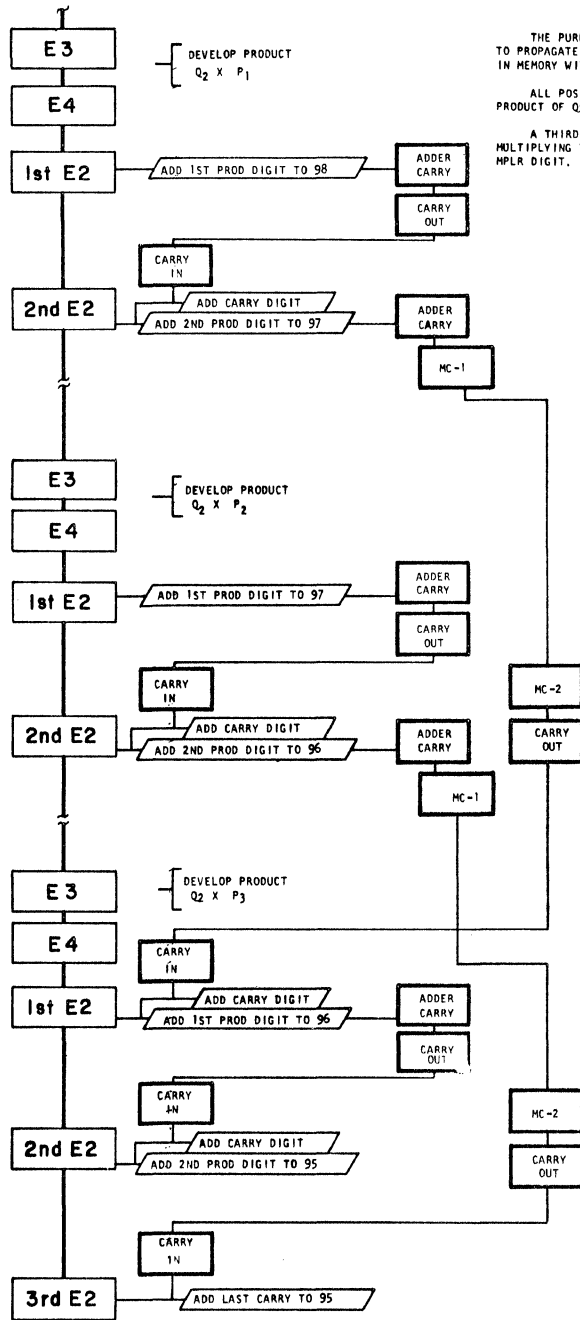


23 M MULTIPLY
13 MM MULTIPLY IMMED
96 OTD OCTAL TO DECIMAL CONVERSION

INSTRUCTION: 23 P P P P P Q Q Q Q Q
PURPOSE: MULTIPLY FIELD AT P ADDR BY FIELD AT Q ADDR
INSTRUCTION: 13 P P P P P Q Q Q Q Q
PURPOSE: MULTIPLY FIELD AT P ADDR BY Q DIGITS
INSTRUCTION: 96 P P P P P Q Q Q Q Q
PURPOSE: MULTIPLY OCTAL TO DECIMAL CONVERSION TABLE AT P ADDR BY OCTAL FIELD AT Q
CONVERSION TABLE AT P ADDR-----5 1 2 5 4 0 8 0 1

FUNCTION CHART	PAGE REF
CLEAR PR-1 AREA E5	10.00.58.1
MPY E1, E3, E4	10.00.59.1
MPY E2	10.00.60.1
OP 96 OTD	10.01.02.1
MQ REG & COUNTER	10.01.23.1

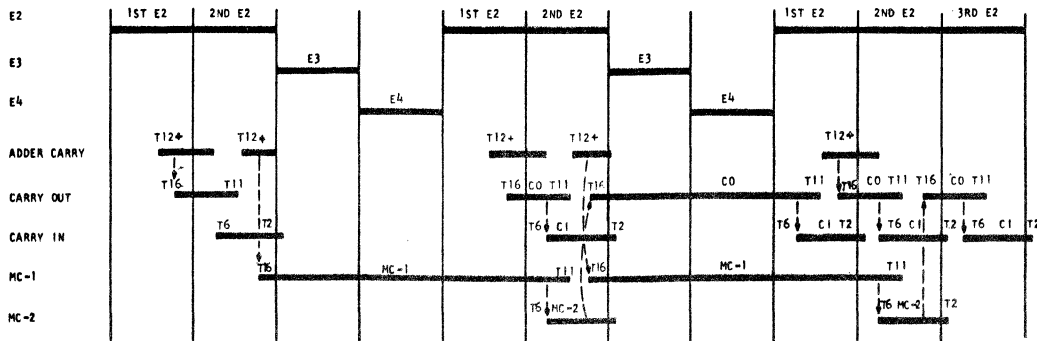
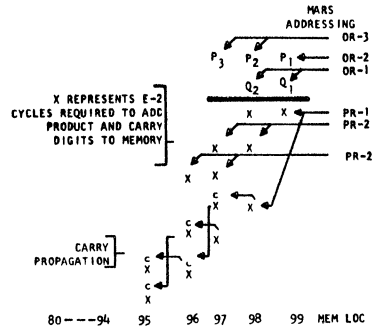




THE PURPOSE OF MC-1 AND MC-2 LATCHES IS TO PROPAGATE CARRIES ACROSS THE PARTIAL PRODUCT IN MEMORY WITHOUT USING ADDITIONAL MACHINE CYCLES.

ALL POSSIBLE CARRIES ARE SHOWN FOR THE PRODUCT OF Q_2 AND THE MCAND DIGITS.

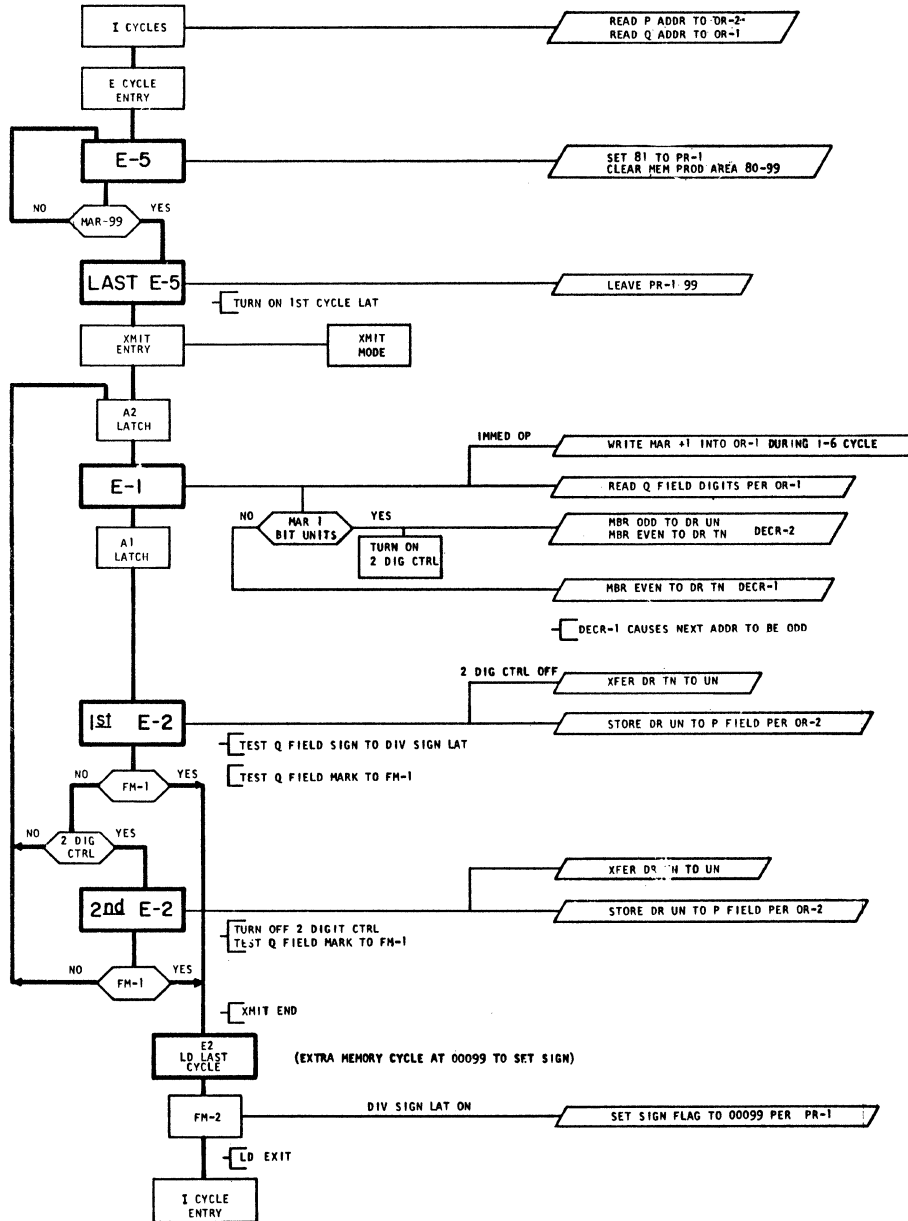
A THIRD E2 MAY ONLY OCCUR AFTER MULTIPLYING THE LAST MCAND DIGIT BY EACH MPLR DIGIT.



28 LD LOAD DIVIDEND
18 LDM LOAD DIVIDEND IMMED.

FUNCTION CHART	PAGE REF
CLEAR PROD AREA E5	10.00.58.1
LOAD	10.00.61.1
XMIT ENTRY & MODE	10.00.53.1
XMIT E1, E2	10.00.54.1
DR & 2 DIGIT CTRL	10.01.22.1

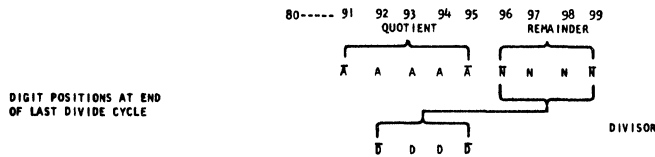
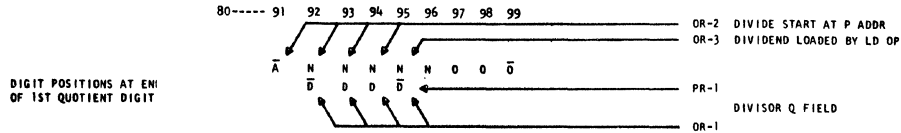
PURPOSE: CLEAR PRODUCT AREA 80-99
TRANSMIT Q FIELD TO P FIELD (USUALLY WITHIN PROD AREA)
STORE Q FIELD SIGN AT 00099



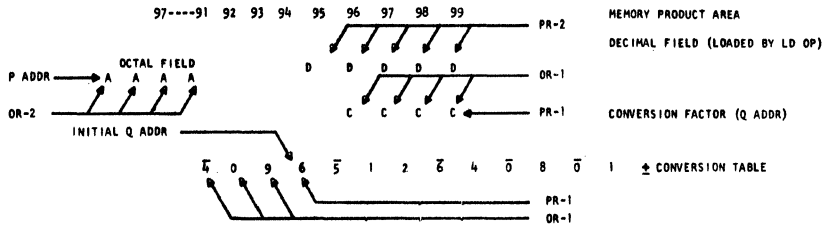
29 D DIVIDE
19 DM DIVIDE IMMED
97 DTO DECIMAL TO OCTAL CONVERSION

PURPOSE OF OP 29D, 19 DM: DIVIDE DIVIDEND IN PRODUCT AREA BY Q FIELD
SEE PAGE 10.00.17.1 STORE QUOTIENT IN P FIELD
LEAVE REMAINDER AT RIGHT OF QUOTIENT
REMAINDER SIGN SAME AS QUOTIENT SIGN

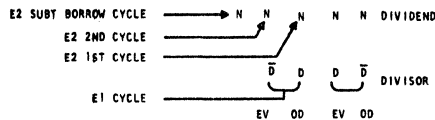
FUNCTION CHART	PAGE REF
ADD ENTRY & MODE	10.00.56.1
DIVIDE E1, E2	10.00.62.1
DIVIDE E3, E4	10.00.63.1
OP 97 DTO	10.01.02.1
MQ REG & COUNTER	10.01.23.1



PURPOSE OF OP 97 DTO: CONVERT DECIMAL FIELD AT 00099 TO OCTAL BY SUCCESSIVE SUBTRACTION
SEE PAGE 10.00.19.1 STORE OCTAL DIGITS IN P FIELD, HIGH ORDER AT P ADDR
Q ADDR LOCATES UNITS POSITION OF HIGHER ORDER CONVERSION FROM TABLE

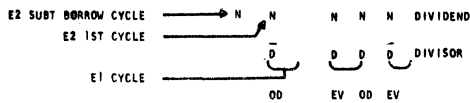


SUBTRACT BORROW CYCLES A: END OF E2 REDUCTION CYCLES
Q FIELD MARK IN EVEN ADDRESSES



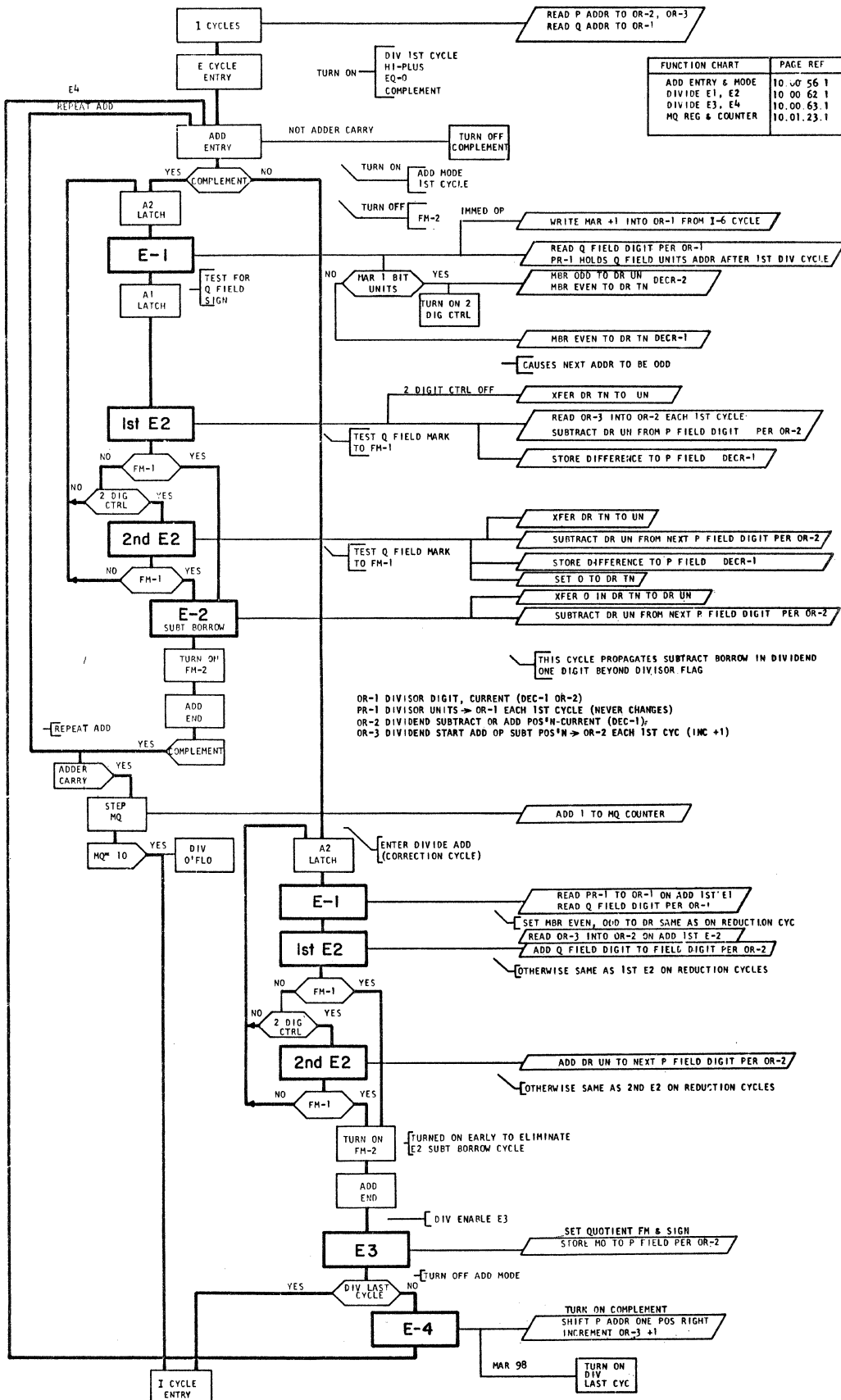
	E1	E2 1st CYCLE	E2 2nd CYCLE	E2 SUBT BORROW	E1
FL-2				T12D2	
FL-1			T6D3	T4D2	
TURN ON FM-1					T2D3
TURN ON FM-2			T2D3		
FM-2 TURN ON ADD ENTRY					T16D3

Q FIELD MARK IN ODD ADDRESS



	E1	E2 1st CYCLE	E2 SUBT BORROW	E1
FL-1			T4D2	
TURN ON FM-1		T6D3		T2D3
TURN ON FM-2		T2D3		
FM-2 TURN ON ADD ENTRY				T16D3

29D DIVIDE I9DM DIVIDE IMMED

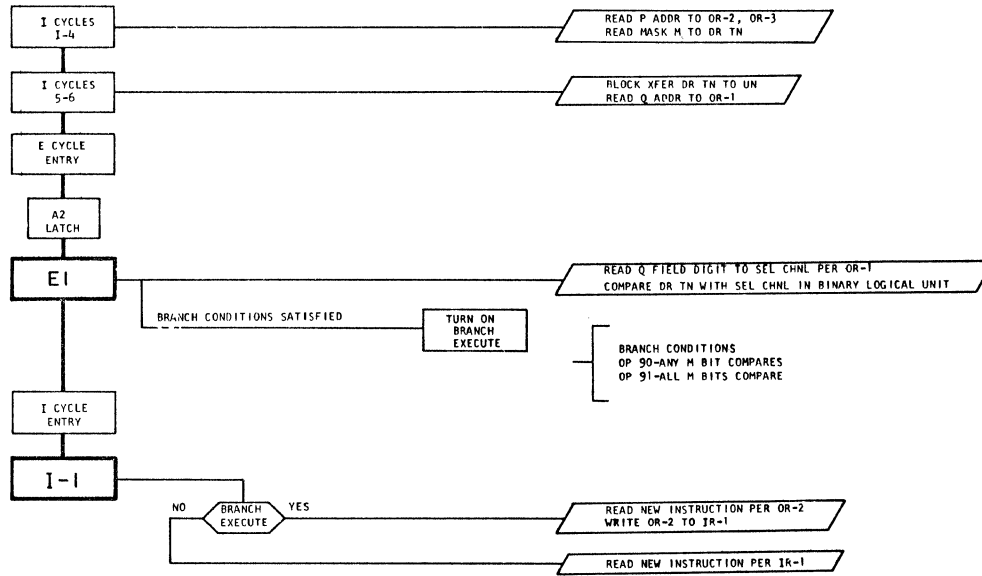


90 BBT BRANCH ON BIT
91 BMK BRANCH ON MASK

INSTRUCTION: 0 0 P P P P P M Q Q Q Q

PURPOSE: COMPARE Q FIELD DIGIT WITH MASK DIGIT M
OP 90 BRANCH TO P ADDR IF ANY BIT COMPARES
OP 91 BRANCH TO P ADDR IF ALL BITS COMPARE

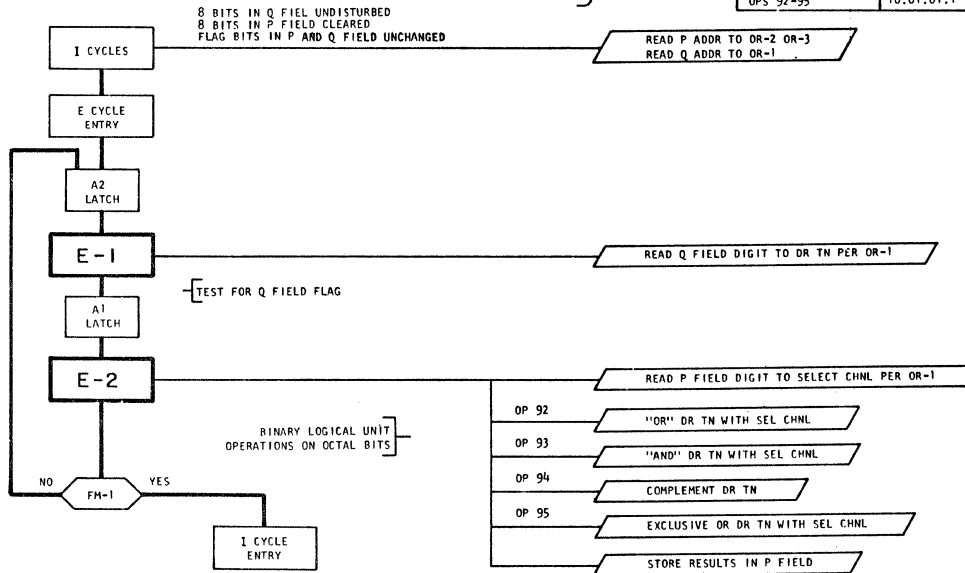
FUNCTION CHART	PAGE REF
OPS 90, 91 BRANCH EXEC	10.01.00.1 10.00.35.1



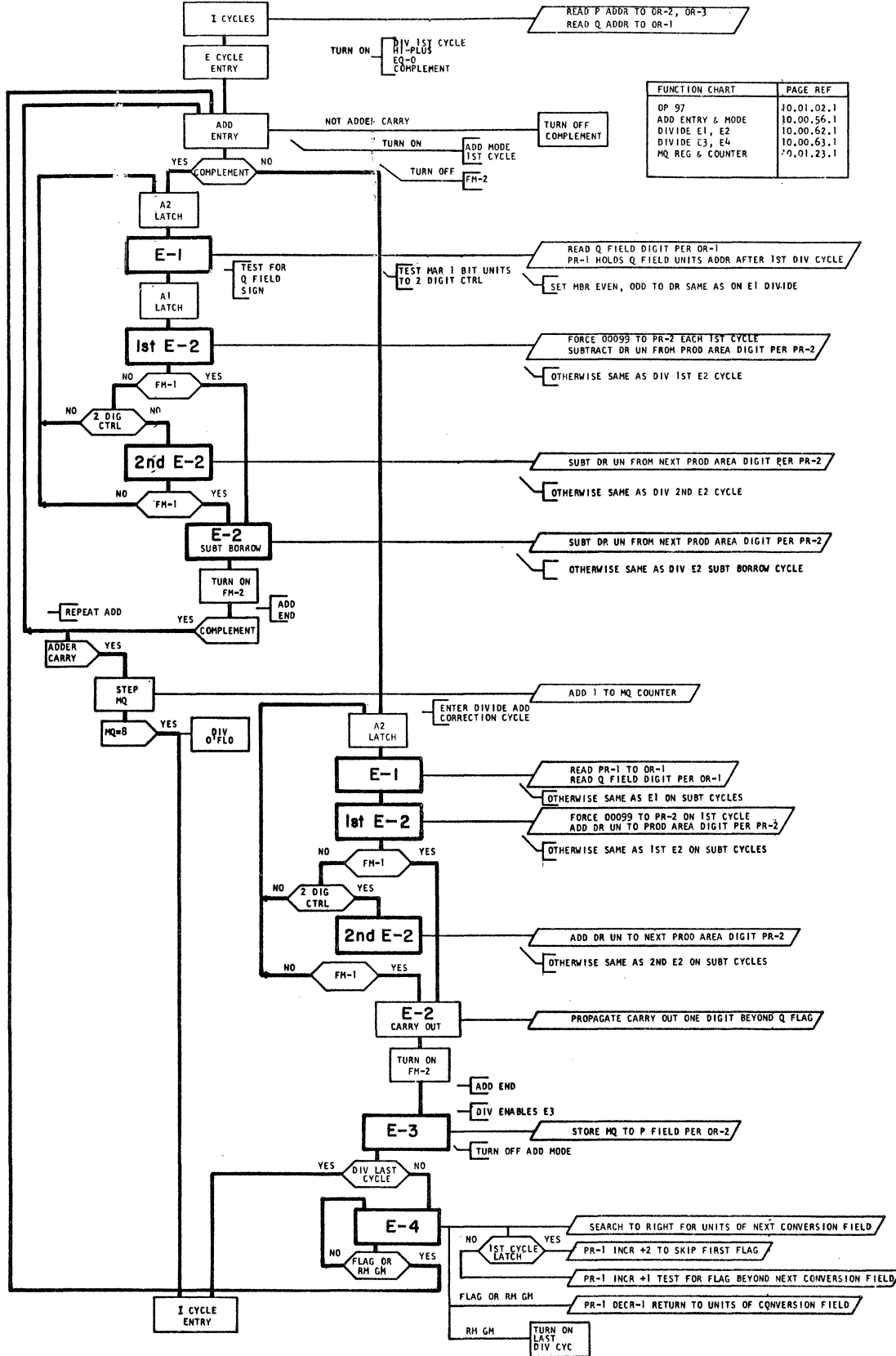
92 ORF "OR" TO FIELD
93 ANDF "AND" TO FIELD
94 CPLF COMPLEMENT TO FIELD
95 EORF EXCLUSIVE OR TO FIELD

PURPOSE: OP 92-"OR" Q FIELD OCTAL BITS (4,2,1) WITH P FIELD OCTAL BITS
OP 93-"AND" Q FIELD OCTAL BITS (4,2,1) WITH P FIELD OCTAL BITS
OP 94-COMPLEMENT Q FIELD OCTAL BITS
OP 95-EXCLUSIVE OR Q FIELD OCTAL BITS WITH P FIELD OCTAL BITS } STORE RESULT IN P FIELD

FUNCTION CHART	PAGE REF
OPS 92-95	10.01.01.1



SEE PAGE 10.00.13.1 FOR OP 96 DTO OCTAL TO DECIMAL CONVERSION

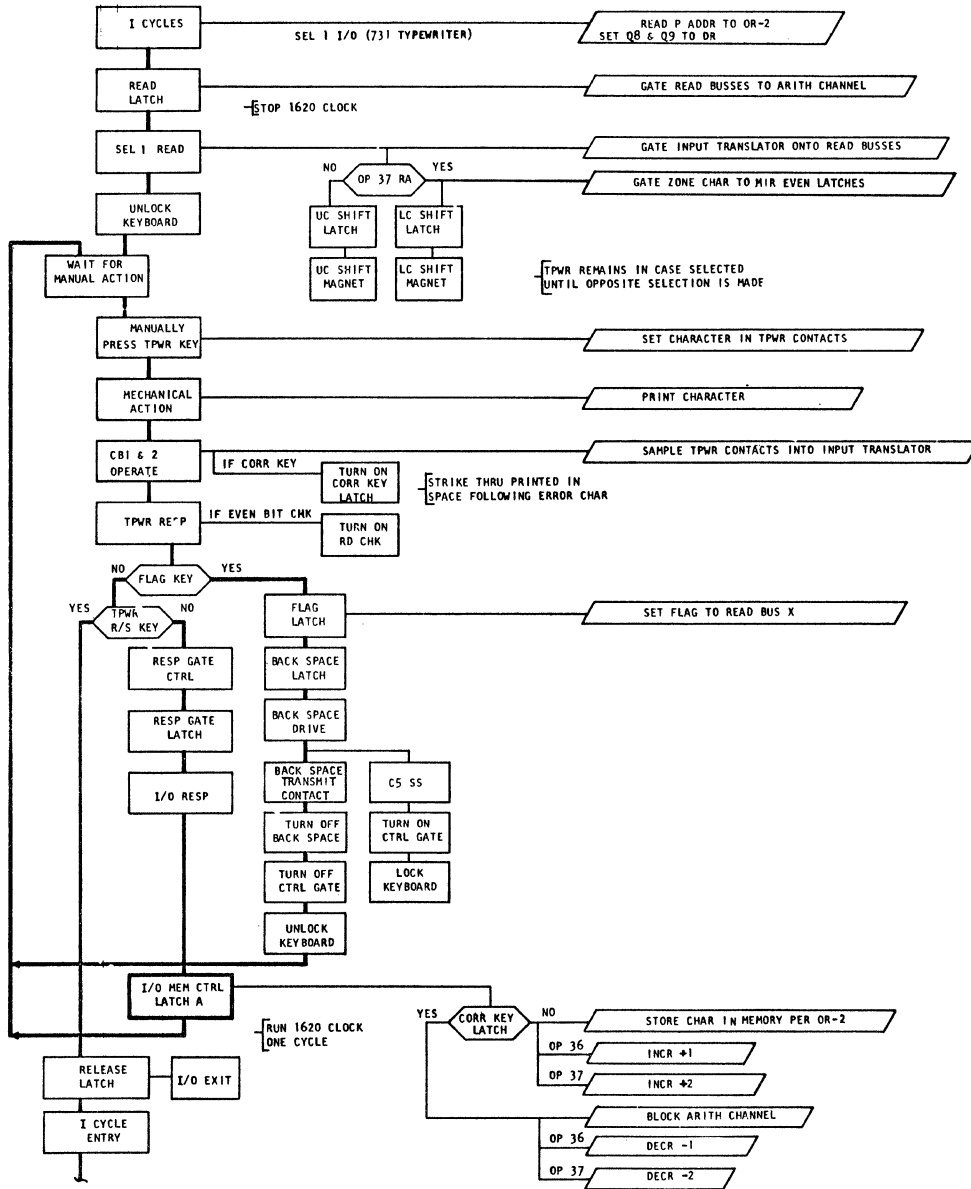


36 RN READ NUMERICALLY
37 RA READ ALPHAMERICALLY

FUNCTION CHART	PAGE REF
RELEASE	10.01.49.1
READ LATCH	10.01.50.1
TYPEWRITER	10.01.52.1
RESP GATE, I/O MEM CTRL	10.01.53.1
CORRECTION KEY & PARITY	10.01.54.1
FLAGGED CHARACTER	10.01.55.1
TYPEWRITER CONTROL	10.01.56.1

INSTRUCTION: 00 01 02 03 04 05 06 07 08 09 10 11 01 SELECTS TYPEWRITER

PURPOSE: STORE CHAR MANUALLY ENTERED FROM KEYBOARD INTO MEMORY AT P ADDR AND SUCCESSIVELY HIGHER ADDR
OP 36 RN KEYBOARD IN UPPER CASE SHIFT
OP 37 RA KEYBOARD IN LOWER CASE SHIFT, TWO MEM POSITION PER CHAR P ODD & P-1 EVEN ADDR

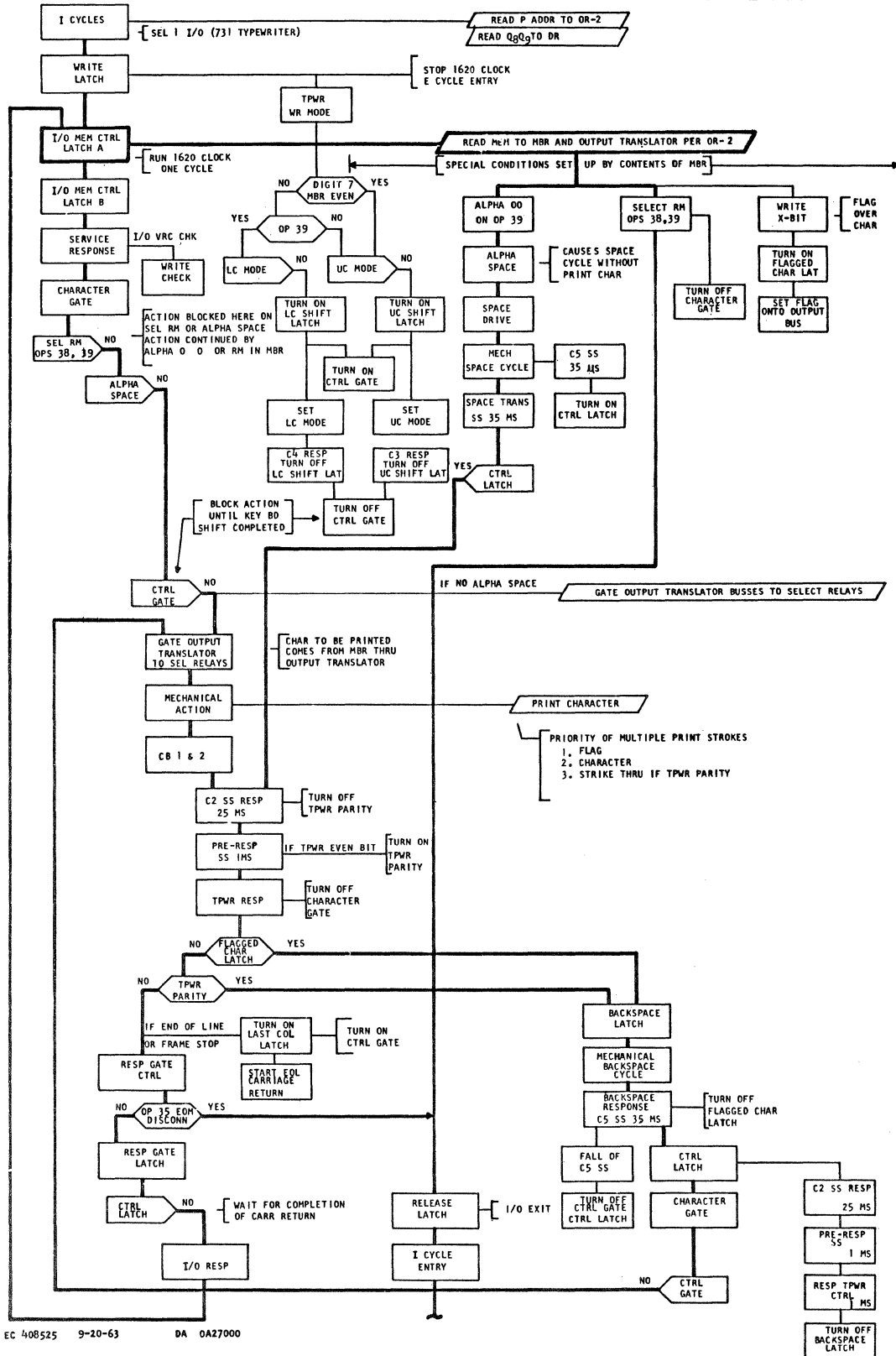


35 DN DUMP NUMERICALLY
 38 WN WRITE NUMERICALLY
 39 WA WRITE ALPHAMERICALLY

INSTRUCTION: 0001 P₂P₃P₄P₅P₆ Q₇Q₈Q₉Q₁₀Q₁₁ OF SELECTS TYPEWRITER

PURPOSE: OP 35 DN - PRINT ALL CHAR, INCLUDING FLAGS FROM P ADDR TO END OF MEMORY MODULE
 OP 38 WN - PRINT NUMERIC CHAR AND FLAGS FROM P ADDR TO RM
 OP 39 WA - PRINT ALPHAMERIC CHAR AND FLAG FROM P ODD AND P-1 EVEN ADDR TO RM

FUNCTION CHART	PAGE REF
RELEASE	10.01.49.1
WRITE LATCH	10.01.51.1
TYPEWRITER	10.01.52.1
RESP GATE, I/O MEM CTRL	10.01.53.1
PARITY	10.01.54.1
FLAGGED CHARACTER	10.01.55.1
TYPEWRITER CONTROL	10.01.56.1



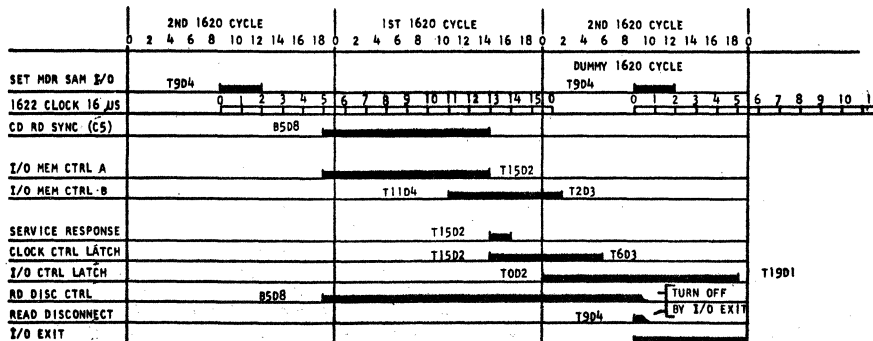
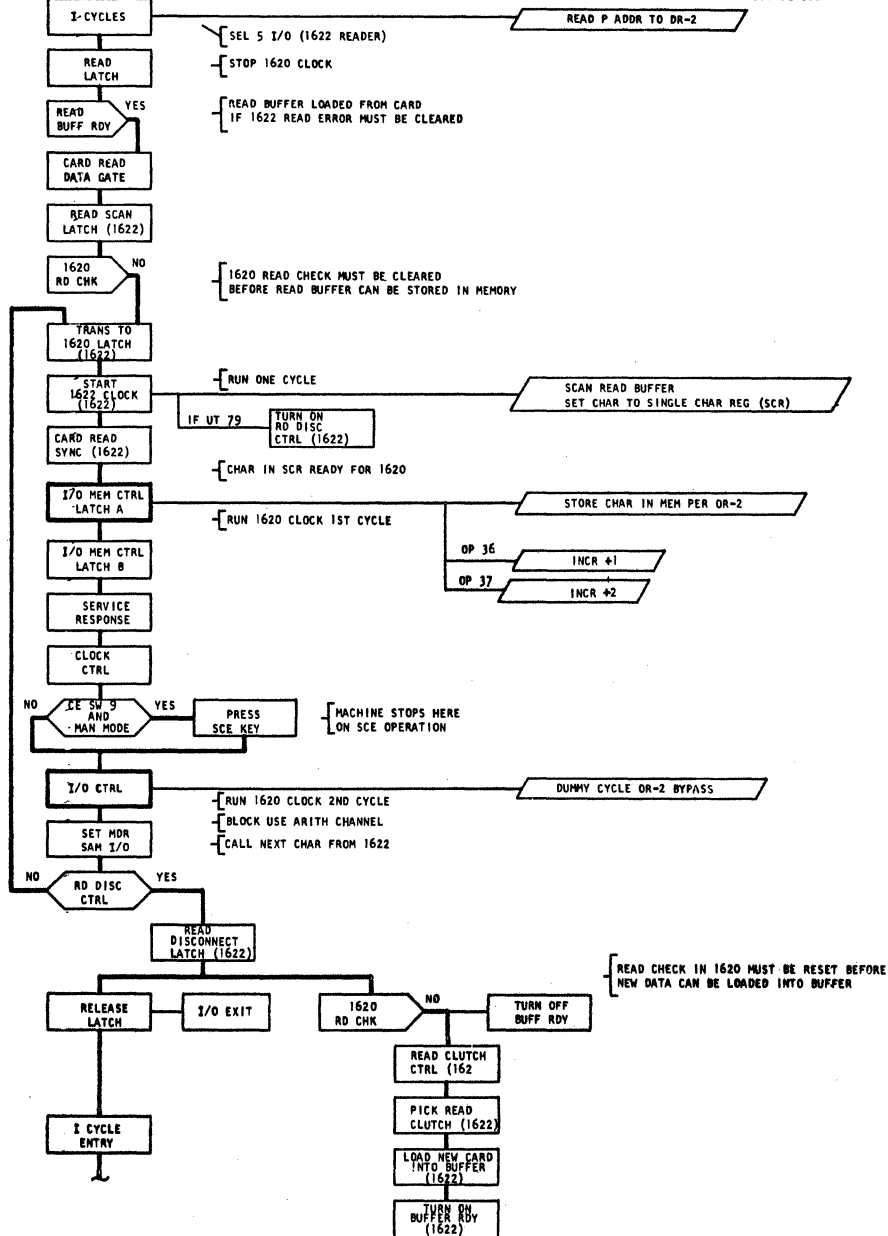
36 RN READ NUMERICALLY 37 RA REA ALPHAMERICALLY

FUNCTION CHART	PAGE REF
RELEASE	10.01.49.1
READ LATCH	10.01.50.1
RESP GATE, I/O MEM CTRL	10.01.53.1
1622 CARD READER	10.01.70.1

INSTRUCTION: 0001 2 3 4 5 6 7 8 9 10 11 05 SELECTS 1622 READER

PURPOSE: READ 80 CHAR FROM A CARD
STORE IN MEMORY STARTING FROM P ADDR THRU SUCCESSIVELY HIGHER NUMBERED LOCATIONS

NUMERIC INSTRUCTION CARD COL 1 2 --- 79 80
MEM LOC P P+1 P+78 P+79
ALPHAMERIC INSTRUCTION CARD COL P-1 P P+1 P+2 P+155 P+156 P+157 P+158 P ADDR MUST BE ODD
MEM LOC P-1 P P+1 P+2 P+155 P+156 P+157 P+158



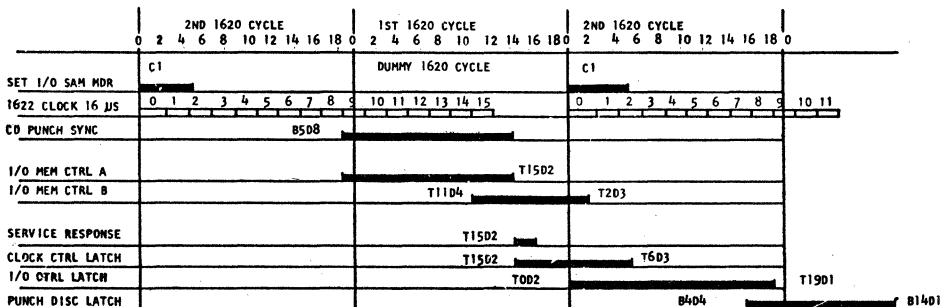
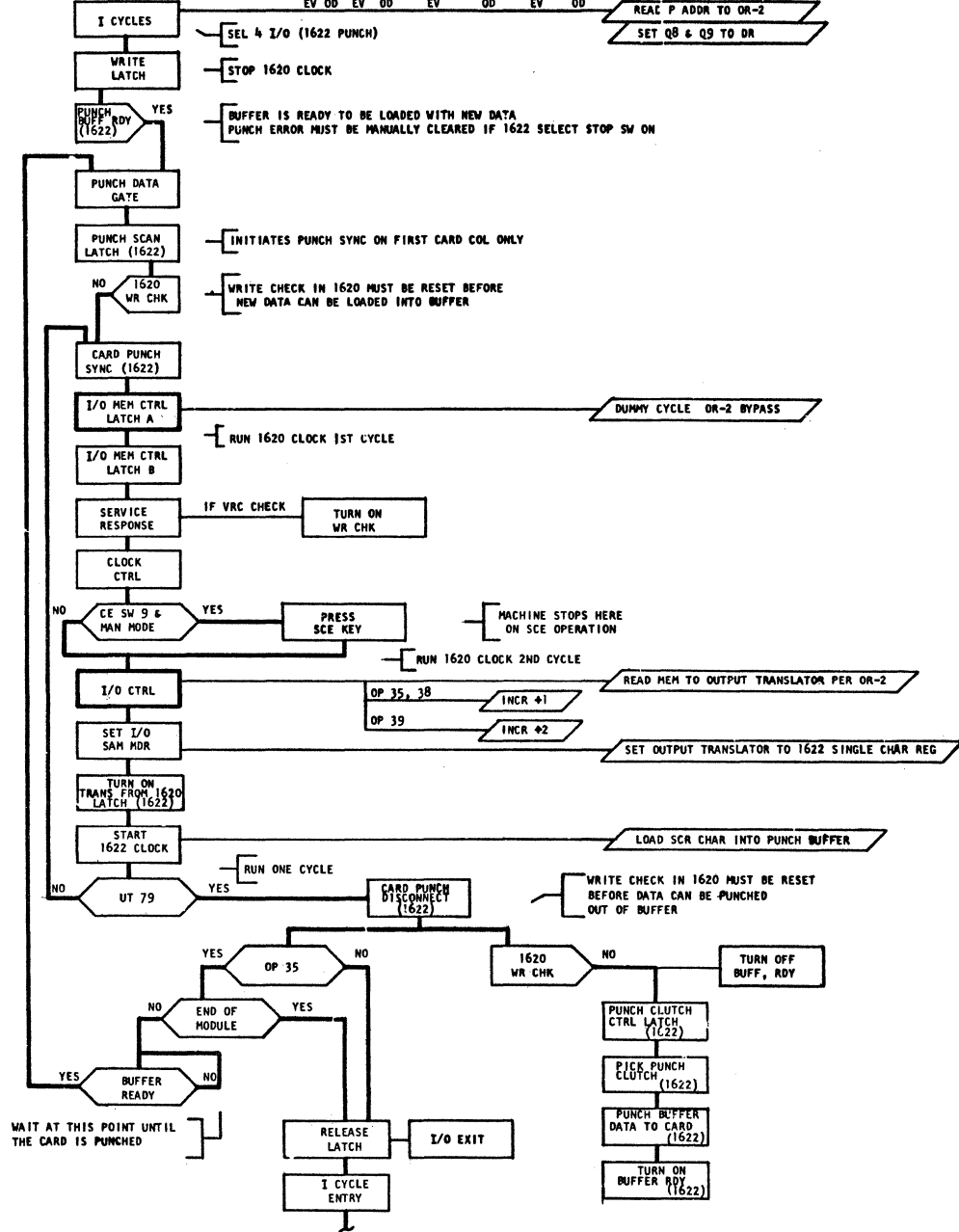
35 DN DUMP NUMERICALLY
38 WN WRITE NUMERICALLY
39 WA WRITE ALPHAMERICALLY

FUNCTION CHART	PAGE REF
RELEASE	10.01.49.1
WRITE LATCH	10.01.51.1
RESP GATE, I/O MEM CTRL	10.01.53.1
1622 CARD PUNCH	10.01.70.1

INSTRUCTION: 0 0 1 P P P P P P Q Q Q Q Q Q 04 SELECTS 1622 PUNCH

PURPOSE: PUNCH 80 CHAR INTO CARD STARTING FROM P ADDR THRU SUCCESSIVELY HIGHER NUMBERED LOCATIONS IN MEMORY

NUMERIC INSTRUCTION: CARD COL 1 2 ----- 79 80 MEM ADDR=P+CC-1
 MEM LOC P P+1 P+2 P+3 P+4 P+5 P+6 P+7 P+8 P+9 P+10 P+11
 ALPHAMERIC INSTRUCTION: CARD COL P P+1 P+2 P+3 P+4 P+5 P+6 P+7 P+8 P+9 P+10 P+11
 MEM LOC P-1 P P+1 P+2 P+3 P+4 P+5 P+6 P+7 P+8 P+9 P+10 P+11
 MEM ADDR=P+2 (CC-1)
 P ADDR MUST BE ODD
 REAL P ADDR TO OR-2
 SET Q8 & Q9 TO DR

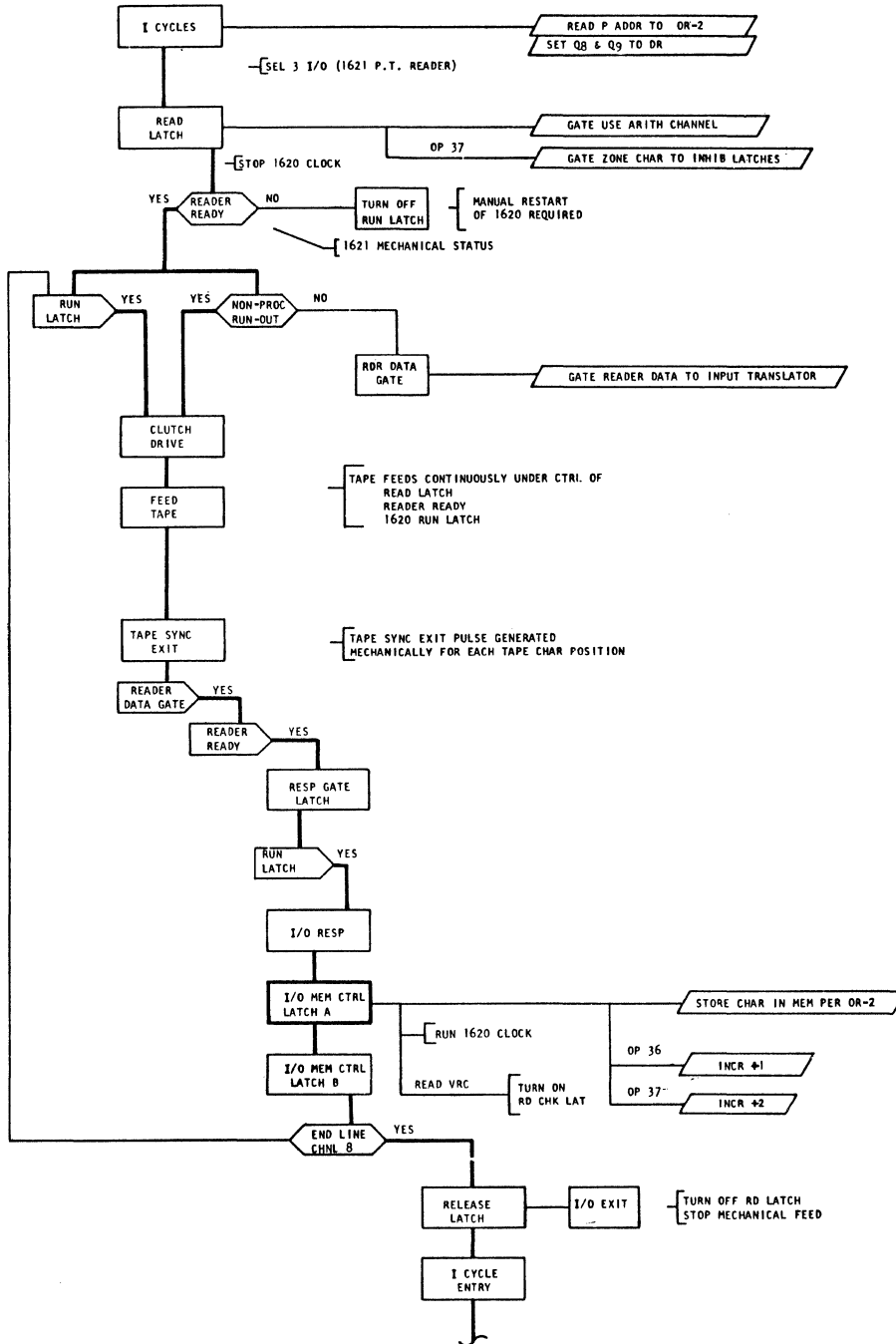


36 RN READ NUMERICALLY
37 RA READ ALPAMERICALLY

FUNCTION CHART	PAGE REF
RELEASE, R/S	10.01.49.1
READ LATCH	10.01.50.1
RESP GATE, I/O MEM CTRL	10.01.53.1
1621	10.01.66.1

INSTRUCTION: 0001 P₂P₃P₄P₅P₆ Q₇Q₈Q₉Q₁₀Q₁₁ 03 SELECTS 1621 PAPER TAPE READER

PURPOSE: READ CHARACTERS FROM TAPE INTO MEMORY STARTING AT P ADDR
OP 37 READ ALPHAMERIC CHAR INTO P ODD AND P-1 EVEN ADDR

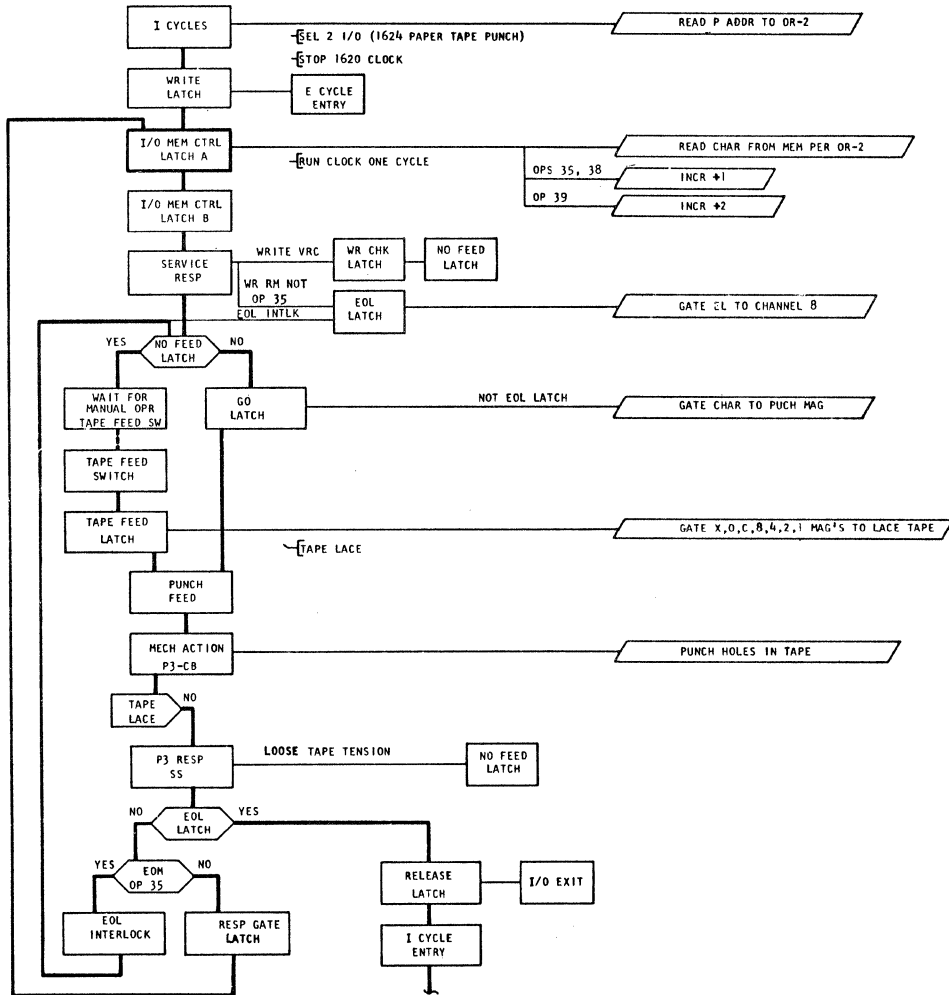


35 DN DUMP NUMERICALLY
 38 WN WRITE NUMERICALLY
 39 WA WRITE ALPHAMERICALLY

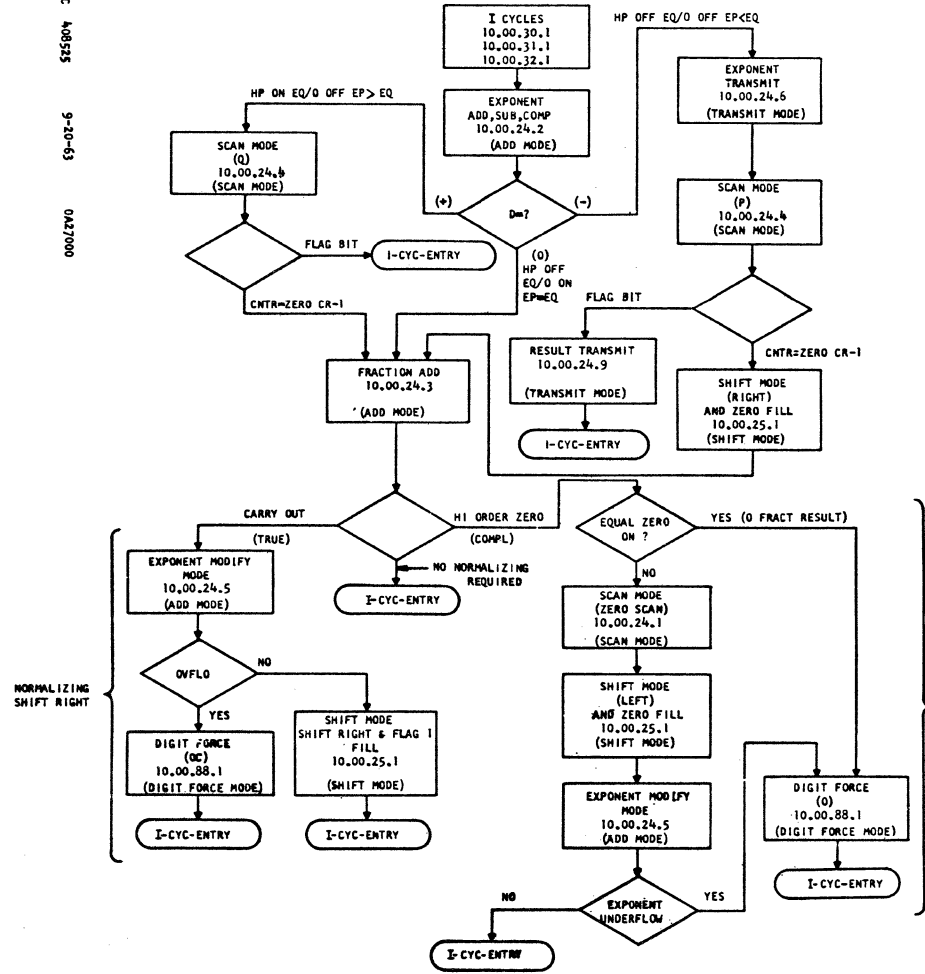
FUNCTION CHART	PAGE REF
RELEASE, R/S	10.01.49.1
WRITE LATCH	10.01.51.1
RESP GATE, I/O MEM CTRL	10.01.53.1
1624	10.01.68.1

INSTRUCTION: 0001 P2P3P4P5P6 Q7Q8Q9Q10Q11 02 SELECTS 1624 PAPER TAPE PUNCH

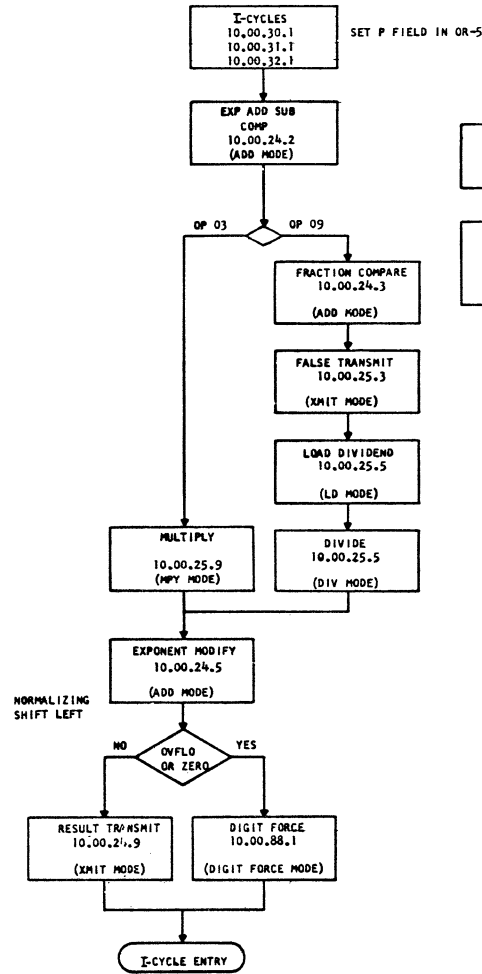
PURPOSE: OP 35 DN PUNCH ALL CHAR INCLUDING FLAGS FROM P ADDR TO END OF MEMORY MODULE
 OP 38 WN SAME AS OP 35 EXCEPT TERMINATE PUNCHING ON RM IN MEM
 OP 39 WA PUNCH ALPHAMERIC CHAR FROM P ODD AND P-1 EVEN ADDR. TERMINATE ON RM IN MEM.



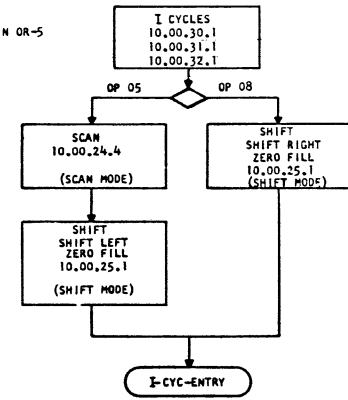
FADD, FSUB



FLOATING MULTIPLY & DIVIDE



F.P. SHIFT OPS



PURPOSE OF EXP ADD,SUB:
COMBINE IN
FMUL & FDIV

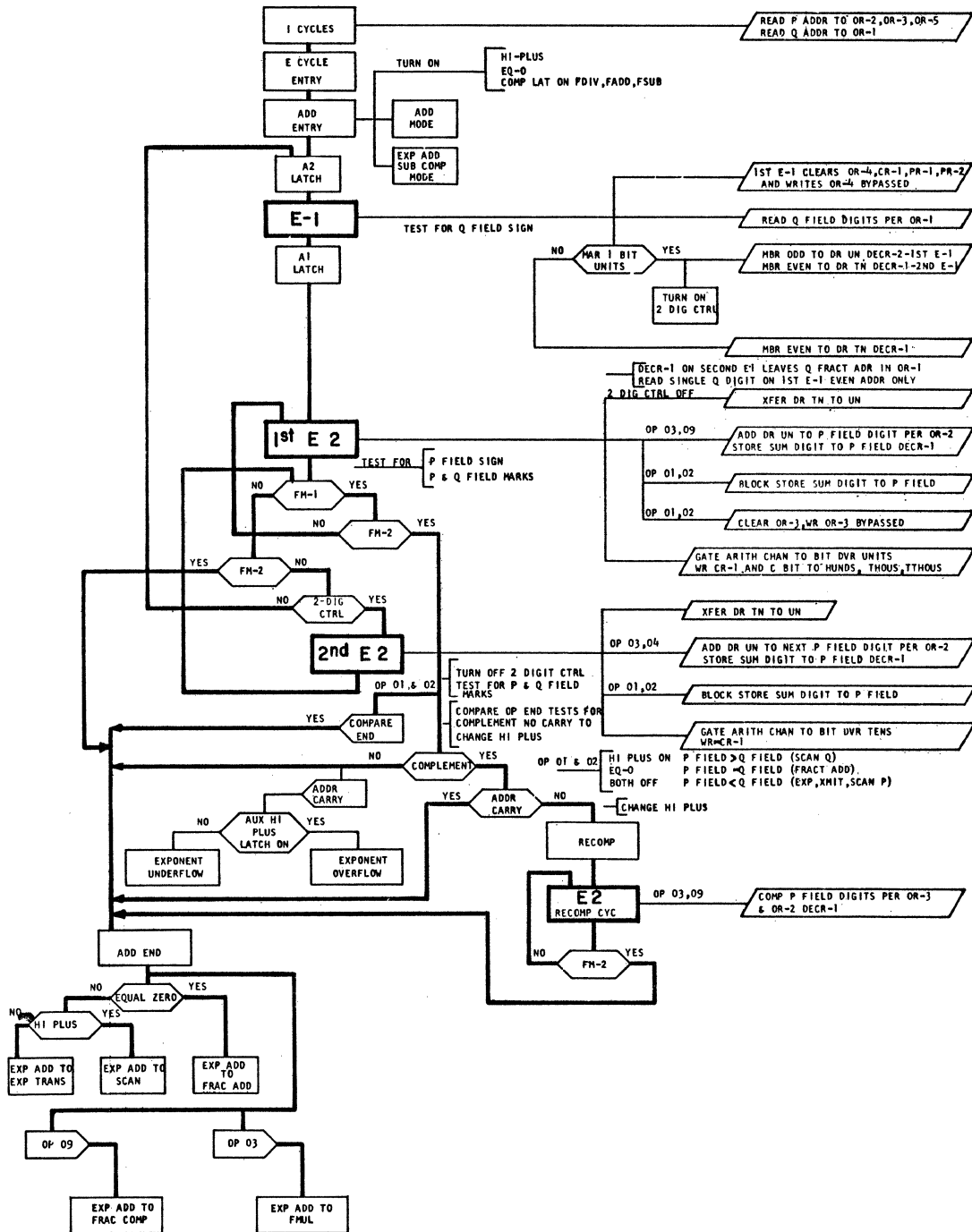
PURPOSE OF COMPARE
PLACE EXP DIFFERENCE
IN CR-1 FOR SCAN
IN FADD,FSUB

BASIC MODE CONTROL:
ADD MODE E1 2 DIG E2
MARS USED OR-1 OR-2 (OR-3)

LEAVE OR-4 Q EXP
OR-1 Q FRACT
OR-5 P EXP

OP 01,02
OR-3 P FRACT
OR-2 P FRACT
CR-1 EXP DIFFERENCE

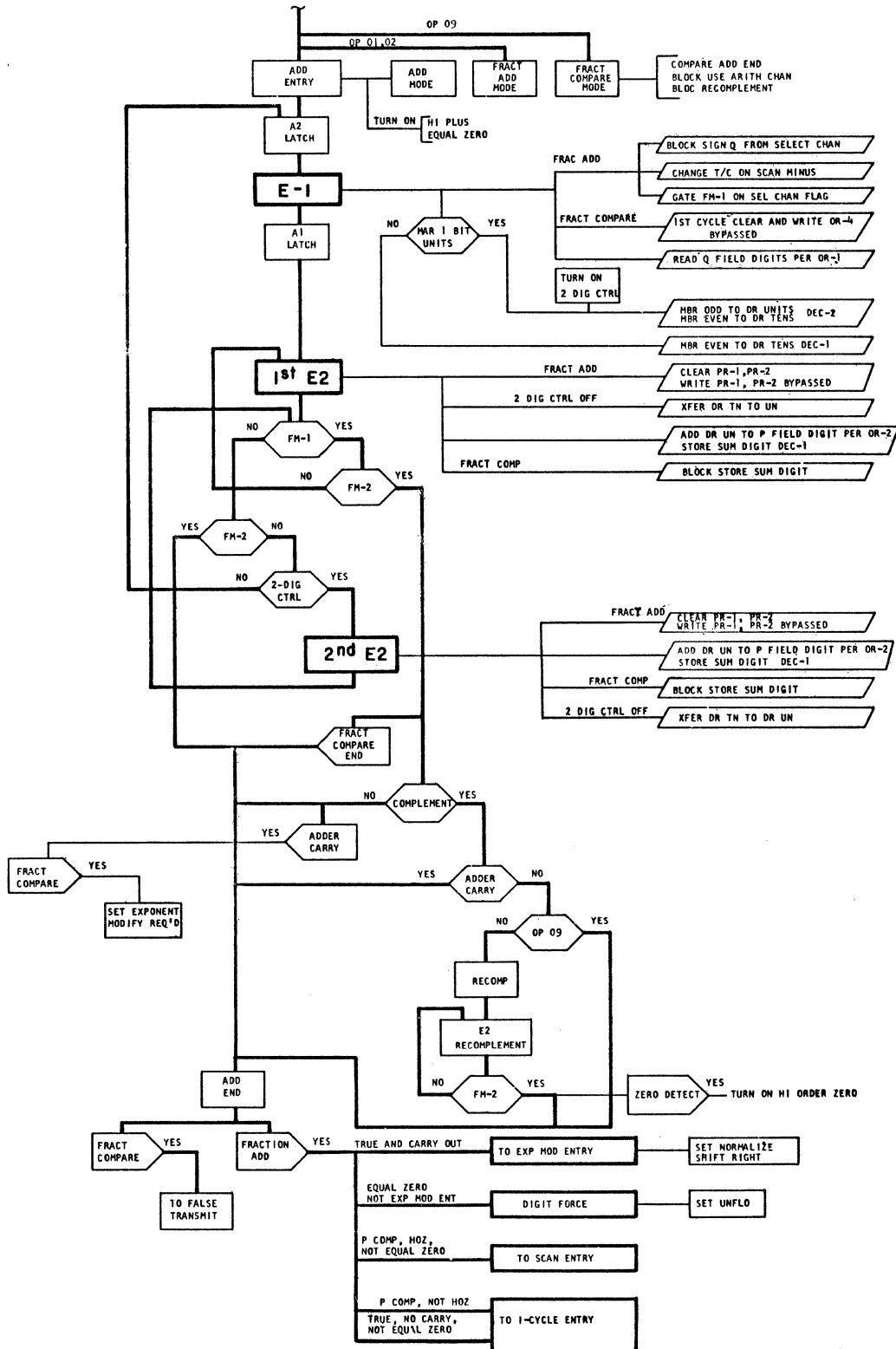
FUNCTION CHART	PAGE REF
EXP ADD, SUB, COMP	10,00,80,1



PURPOSE OF FRACTION ADD: FADD, FSUB OPS ADD THE PRESHIFTED FRACTIONS TO P FIELD
BASIC MODE CONTROL: ADD MODE E1 E2
MARS USED OP-1 OR-2 (OR-3)
LEAVE PR-1, PR-2 TO HI ORDER POS OF FRACTION

PURPOSE OF FRACTION COMPARE: FDIV OP COMPARES FRACTIONS TO DETERMINE IF EXP MOD IS REQUIRED
BASIC MODE CONTROL: ADD MODE E1 E2
MARS USED OR-1 OR-2
SAVE OR-1 IN OR-4 FOR 1ST DIV SUB CYCLE

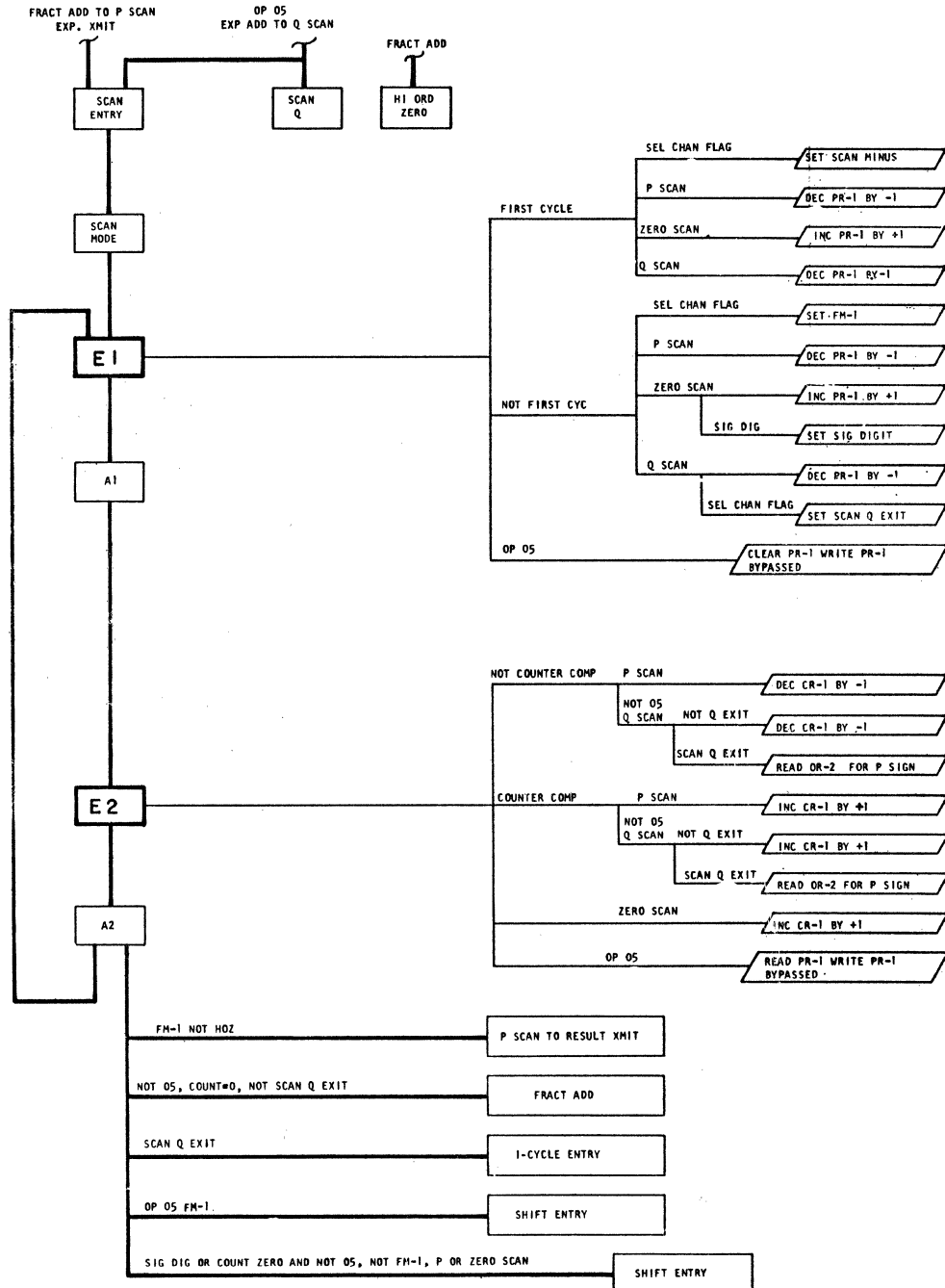
FUNCTION CHART	PAGE REF
FRACTION ADD MODE	10,00,85,1
FRACTION COMPARE MODE	10,00,90,1



PURPOSE: 1) TO SCAN P OR Q FIELDS TO ADJUST FRACTIONS FOR ADD SUBTRACT
 2) TO COUNT THE NUMBER OF LEADING ZEROS FOR NORMALIZATION
 3) TO SCAN FRACTION FOR SIGN AND HI ORDER DIGIT FOR SHIFT OP

MARS USED: E1 E2 SCAN Q LATCH CONTROL FOR SCAN Q OR OP 05
 HI ORDER ZERO LATCH CONTROL FOR ZERO SCAN
 P OR ZERO SCAN PR-1 CR-1
 Q SCAN OR-1 CR-1
 OP 05 OR-1 PR-1

FUNCTION CHART	PAGE REF
SCAN MODE	10.00.82.1



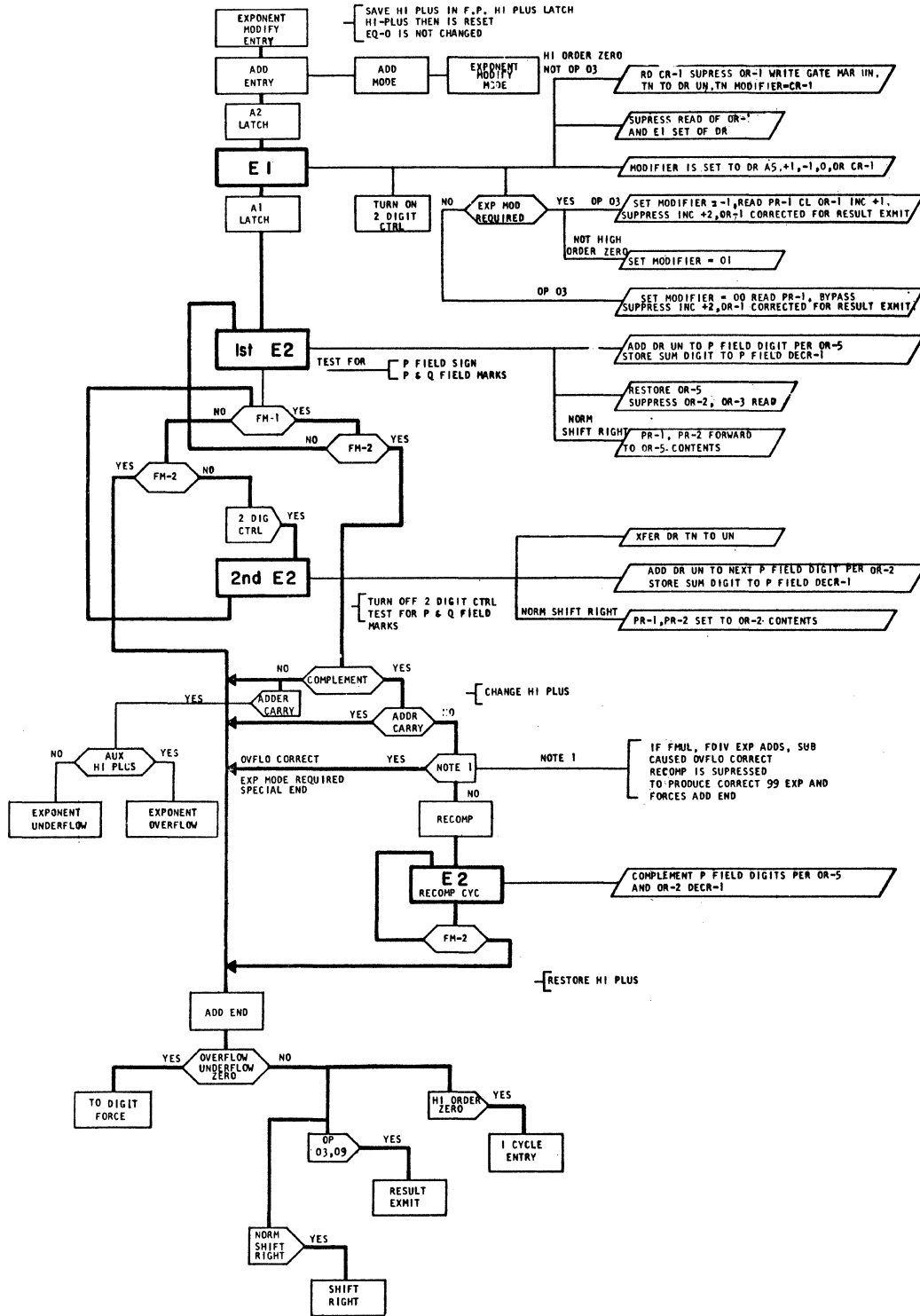
PURPOSE OF EXP MODIFY MODE:

- 1) MODIFY P EXP BY 0, +1, -1, OR CR-1
- 2) CORRECT OR-1 FOR RESULT XMIT
- 3) ON NORM SHIFT RIGHT LEAVE PR-1, PR-2 TO P FRACT

BASIC MODE CONTROLS:
 ADD MODE E1, E2
 MARS USED:

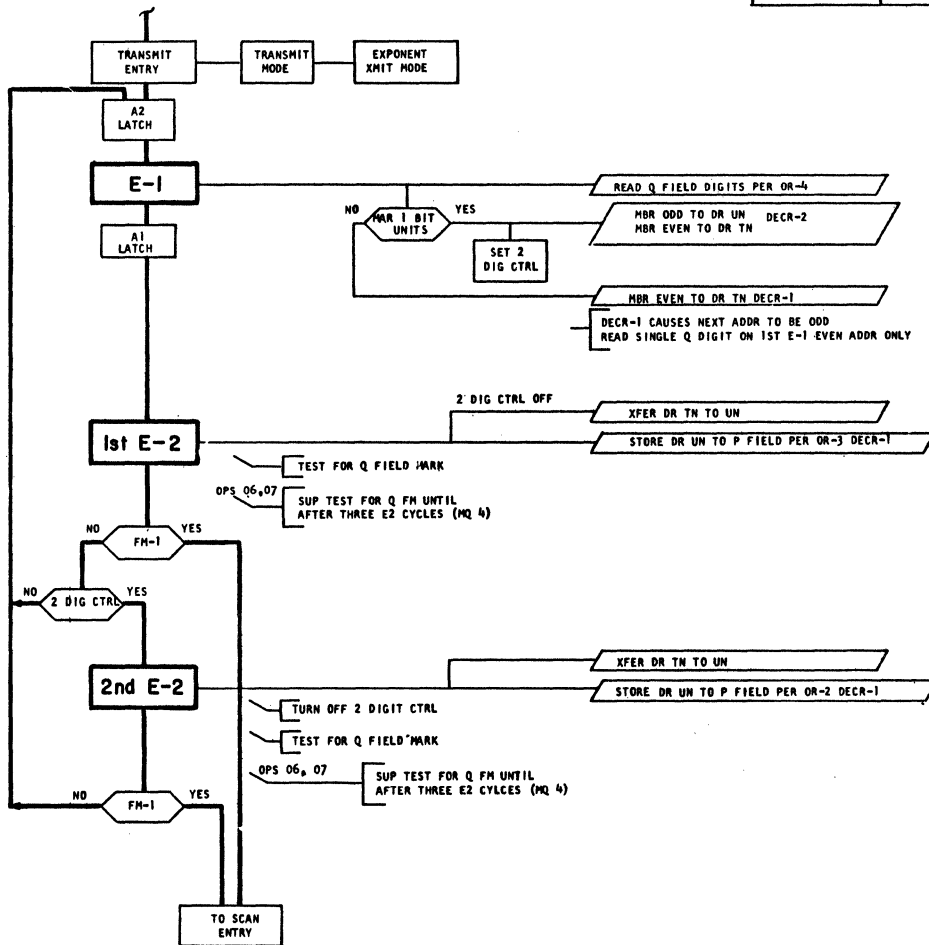
OP 03, PR-1, OR-2, OR-5 1ST CYC
 NOT OP 03, CR-1, OR-2, OR-5 1ST CYC

FUNCTION CHART	PAGE REF
EXPONENT MODIFY ENTRY	10.00.86.1
EXPONENT MODIFY MODE	10.00.87.1



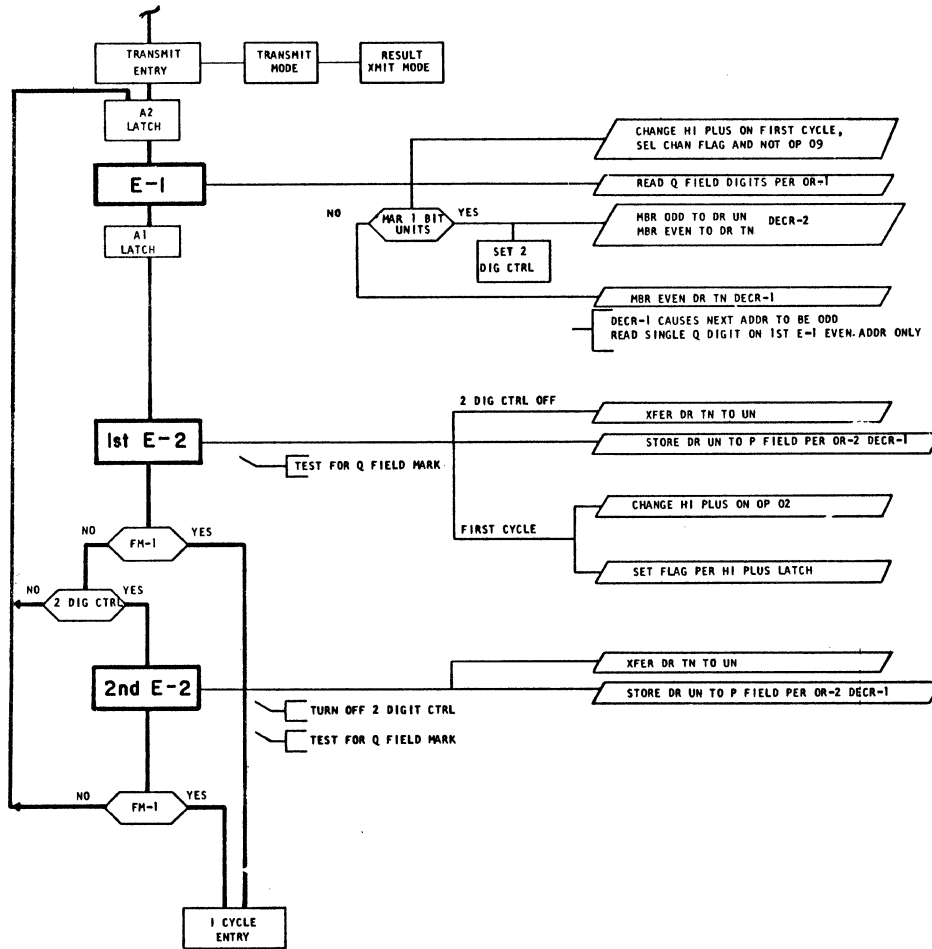
PURPOSE: TRANSMIT EXP TO P ADDRESS
 BASIC TRANSMIT MODE E1 E2
 MARS USED OR-4 OR-3

FUNCTION CHART	PAGE REF
EXP XMIT	10.00.81.1
XMIT OPS	10.00.52.1
XMIT ENTRY & MODE	10.00.53.1
XMIT E1, E2	10.00.54.1
DR & 2 DIGIT CTRL	10.01.22.1



PURPOSE: TRANSMIT FRACTION RESULT TO P ADDRESS
 BASIC MODE CONTROLS: TRANSMIT E1 E2
 MARS USED OR-1 OR-2

FUNCTION CHART	PAGE REF
RESULT XMIT	10.00.83.1
XMIT OPS	10.00.57.1
XMIT ENTRY & MODE	10.00.53.1
XMIT E1,E2	10.00.54.1
DR & 2 DIGIT CTRL	10.01.22.1

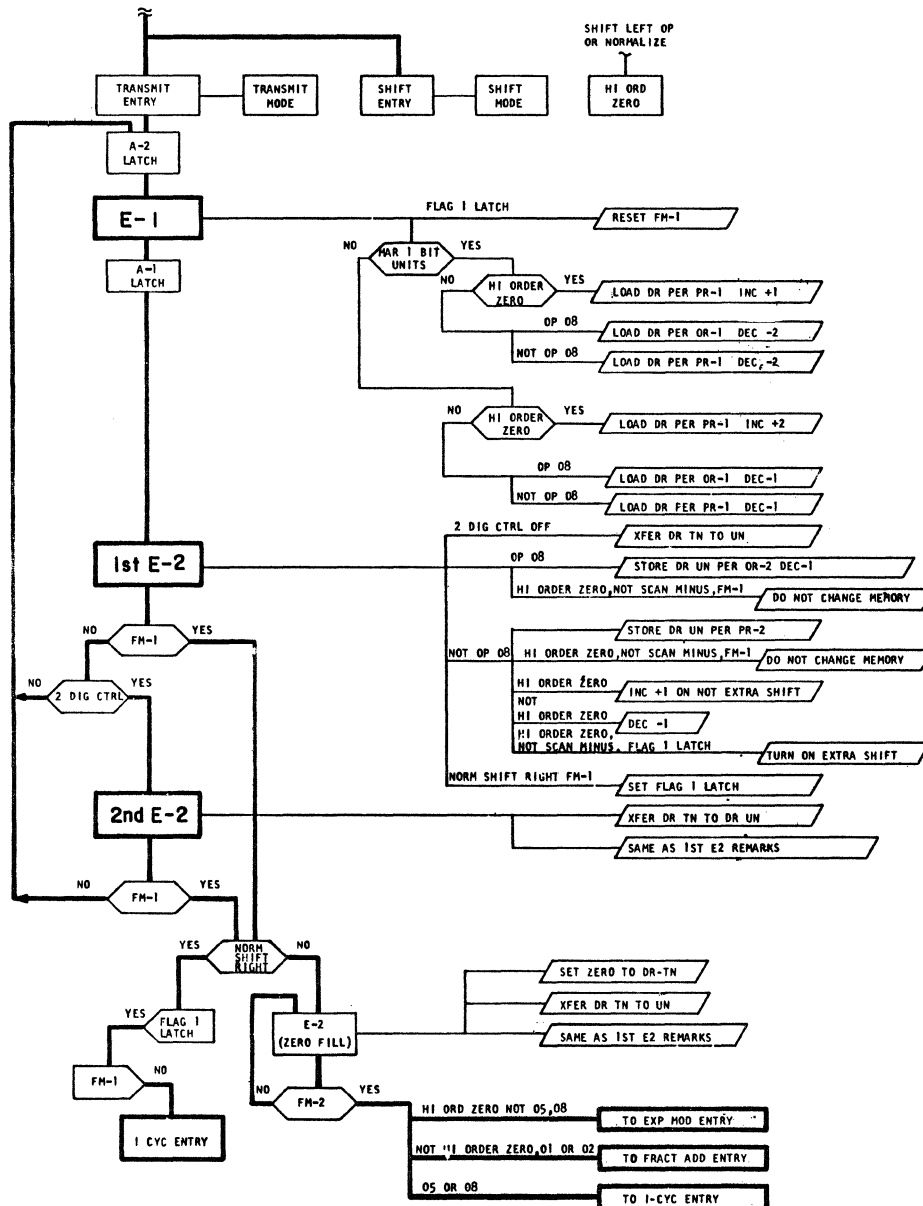


- PURPOSE: 1) SHIFT P FRACTION RIGHT AND ZERO FILL TO ALIGN FRACTIONS FOR ADDING
- 2) SHIFT FRACT ADD SUM RIGHT 1 POS AND FORCE FLAG ONE ON FRACT ADD WITH CARRY OUT
- 3) SHIFT UNNORMALIZED SUM LEFT TO NORMALIZE AND ZERO FILL REST OF FIELD
- 4) OP 05/08 SHIFT FRACTION LEFT/RIGHT AND ZERO FILL TO LENGTHEN/SHORTEN FRACTION

BASIC MODE CONTROL: TRANSMIT MODE E1 E2

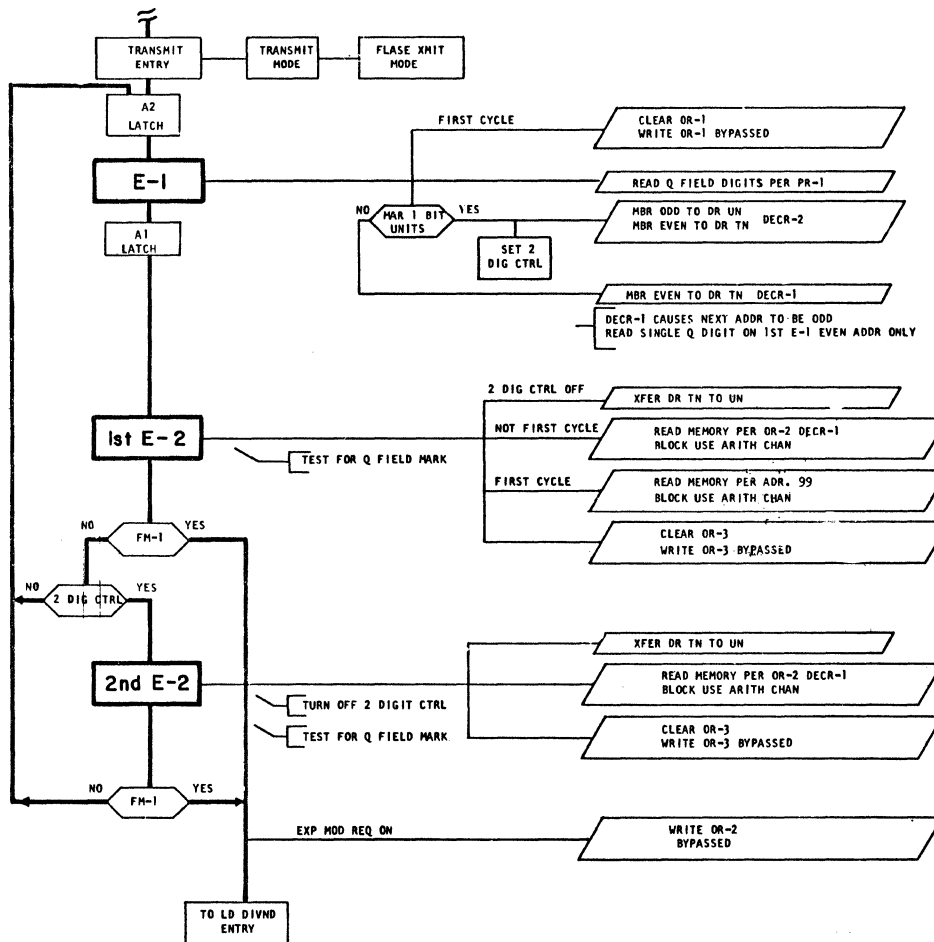
MARS USED: NOT 08 PR-1 PR-2
 08 OR-1 OR-2
 ZERO FILL 08 PR-2
 08 OR-2

FUNCTION CHART	PAGE REF
F.P. SHIFT AND ZERO FILL	10.00.84.1



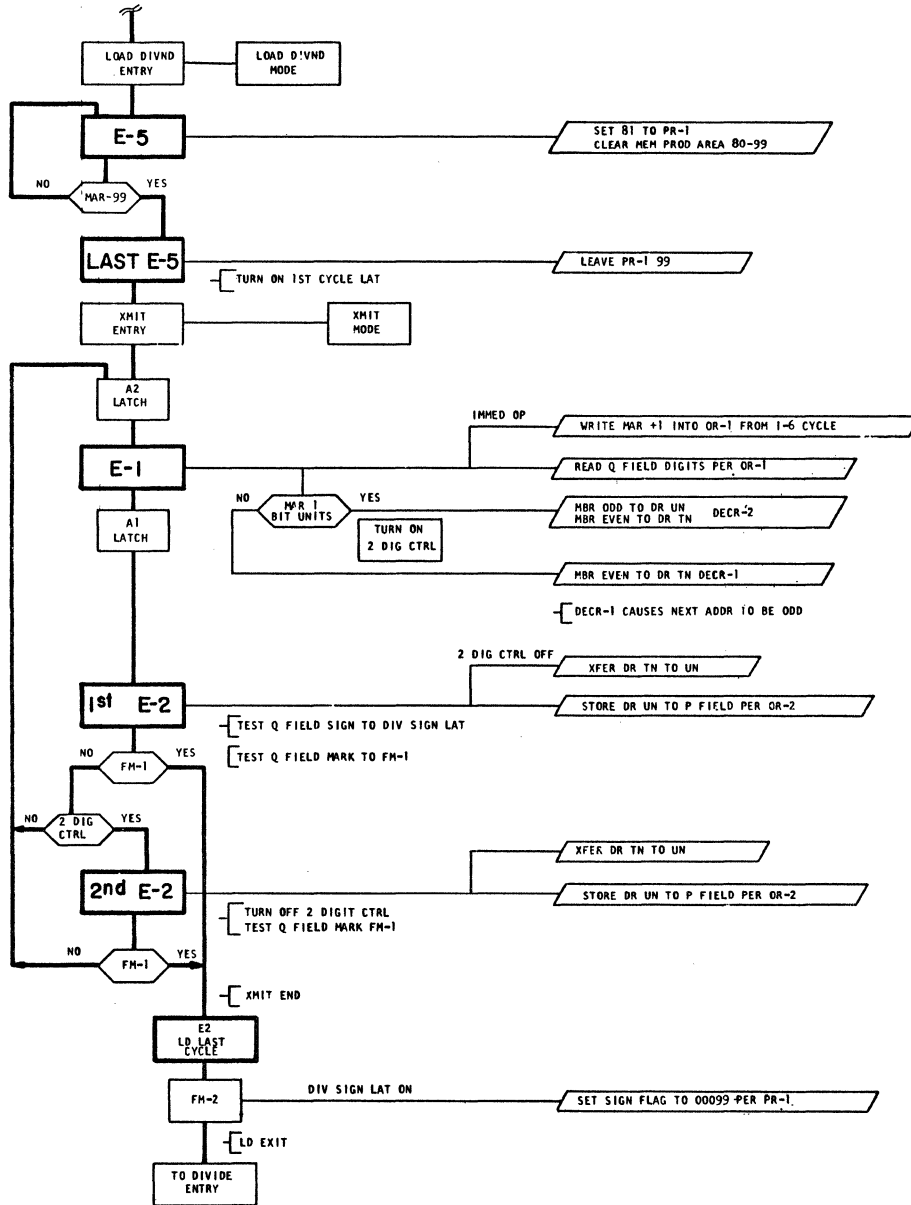
PURPOSE: DETERMINE "L" FOR LOAD DIVIDEND
 BASIC MODE: XMIT E1 E2
 MARS USED PR-1 OR-2
 FALSE XMIT LEAVES OR-2 AT 100-L IF EXP MOD REQ
 99-L IF NOT EXP MOD REQ
 OR-3 AT 100-L
 OR-1 IS RESTORED TO P FRACTION

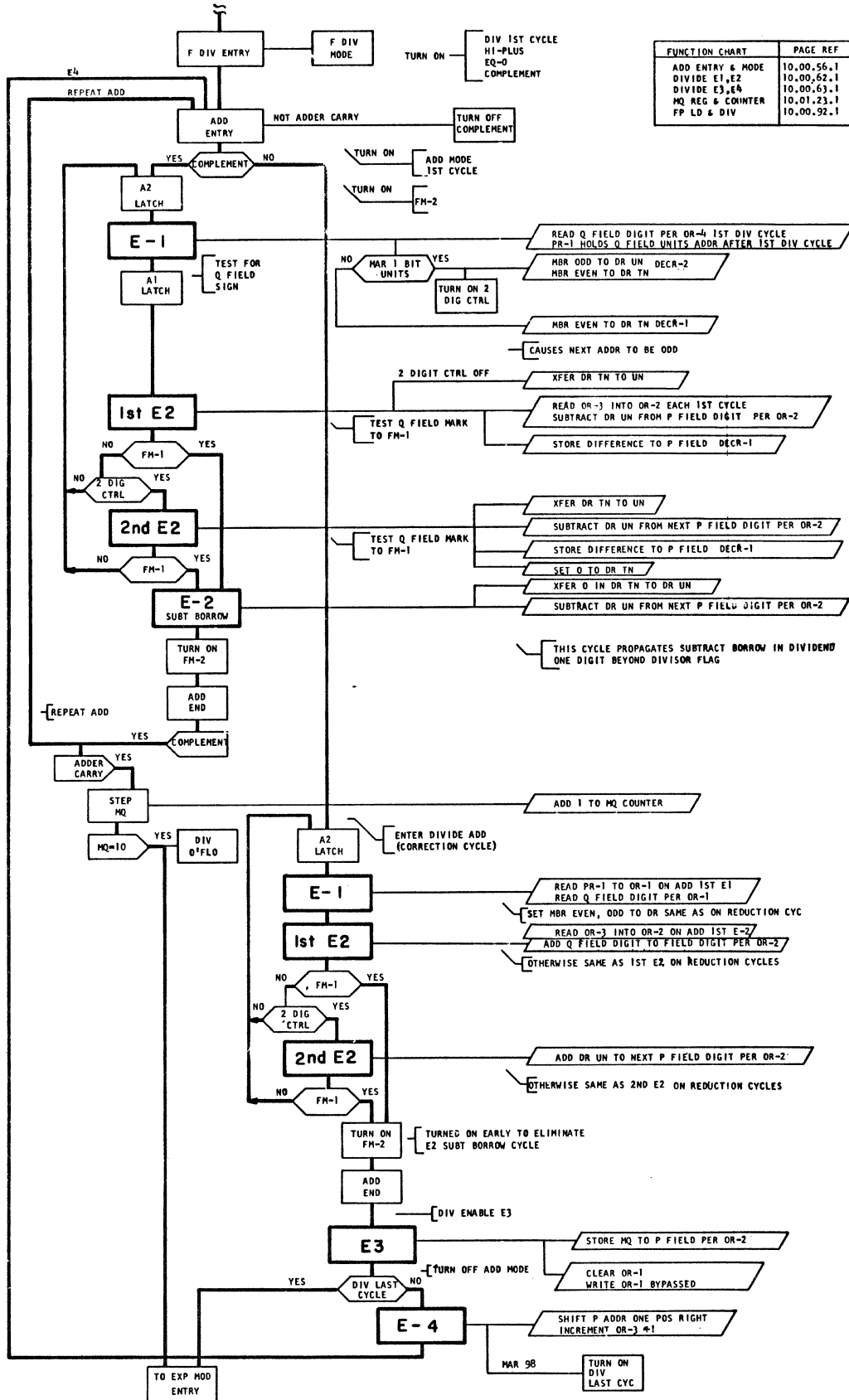
FUNCTION CHART	PAGE REF
F.P. FALSE XMIT	10.00.91.1



PURPOSE: CLEAR PRODUCT AREA
LOAD DIVIDEND TO
PRODUCE NORMALIZED QUOTIENT

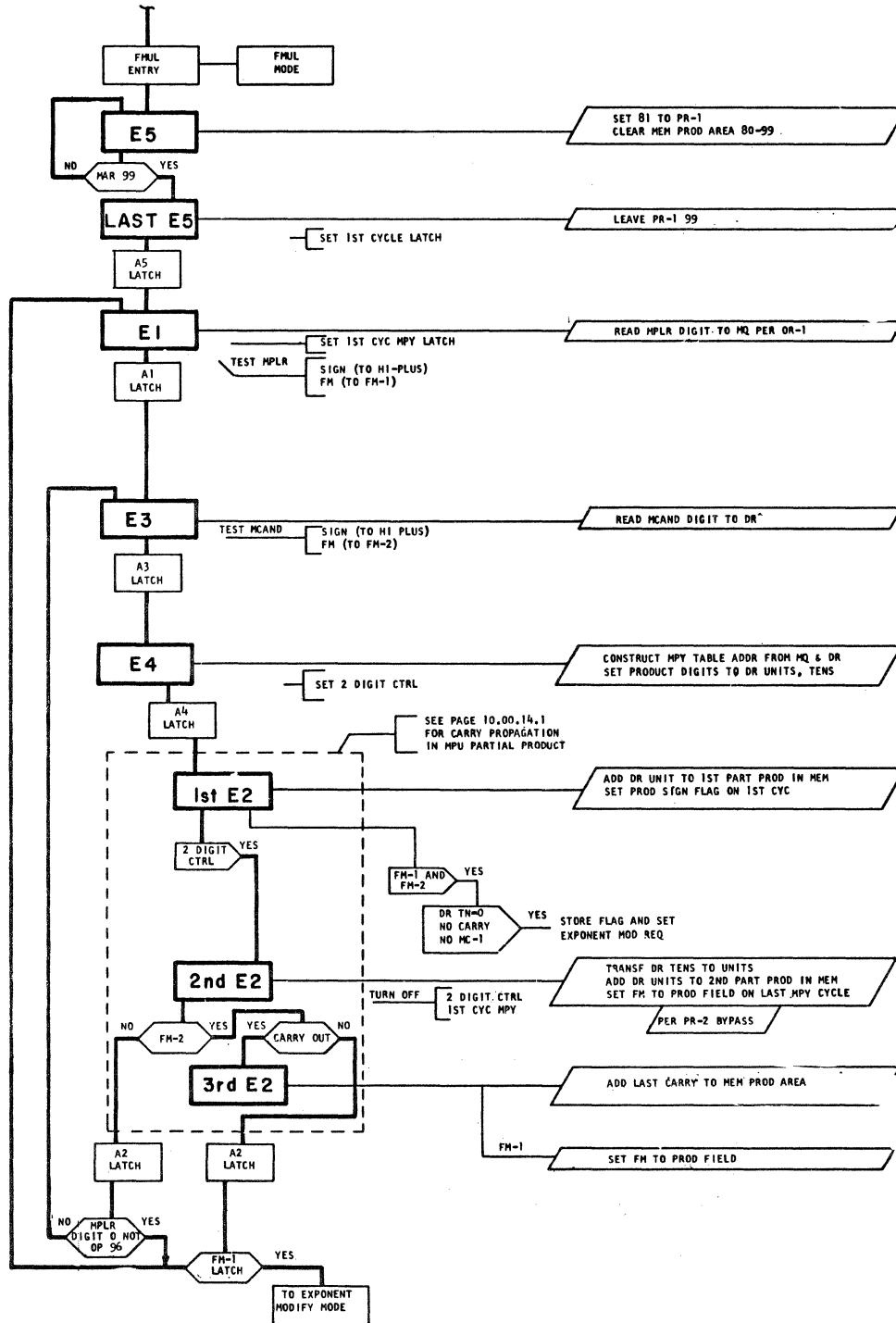
FUNCTION CHART	PAGE REF
CLEAR PROD AREA E5	10.00.58.1
LOAD	10.00.61.1
XMIT ENTRY & MODE	10.00.53.1
XMIT E1, E2	10.00.54.1
DR & 2 DIGIT CTRL	10.01.22.1
F.P. LD AND DIV	10.00.92.1





PURPOSE: MULTIPLY FRACTIONS TOGETHER TO PP AREA
 DETERMINE 2L OR 2L-1 PRODUCT AND IF EXP MOD IS REQ
 SET FLAG ON HI ORDER DIGIT (NORMALIZE)

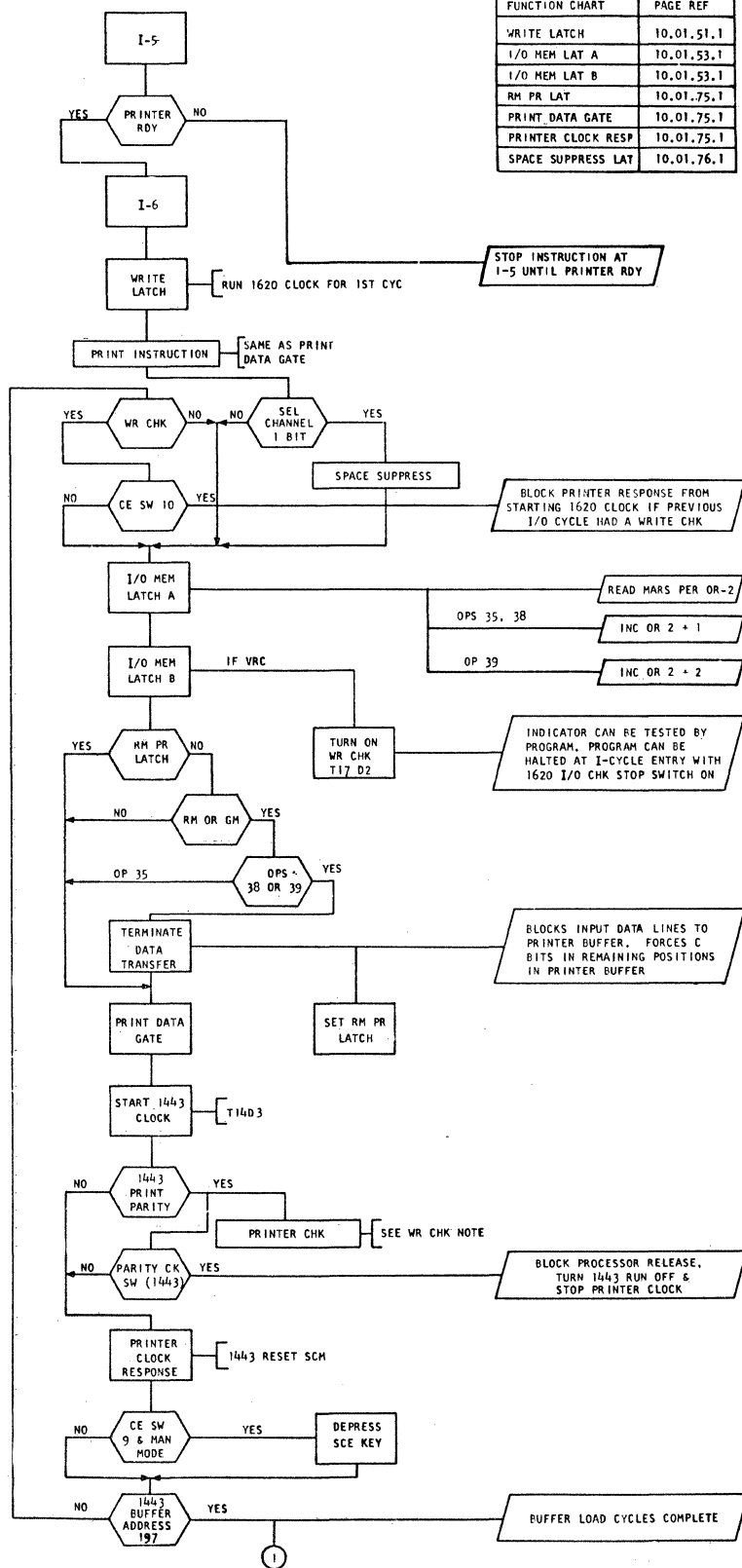
FUNCTION CHART	PAGE REF
FMUL	10.00.89.1
CLEAR PROD AREA E5	10.00.58.1
MPY E1, E3, E4	10.00.59.1
MPY E2	10.00.60.1
MQ REG & COUNTER	10.01.23.1
MPY PROP CARRY	10.00.13.1

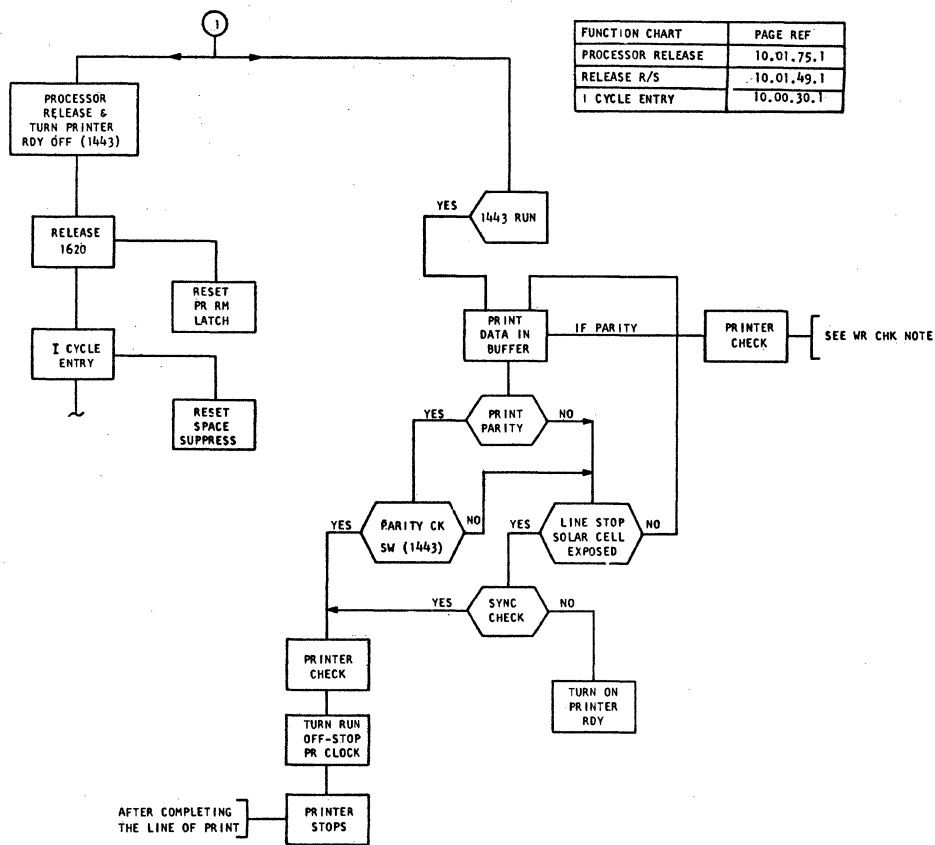


5 DN DUMP NUMERICALLY
 8 WN WRITE NUMERICALLY
 39 WA WRITE NUMERICALLY

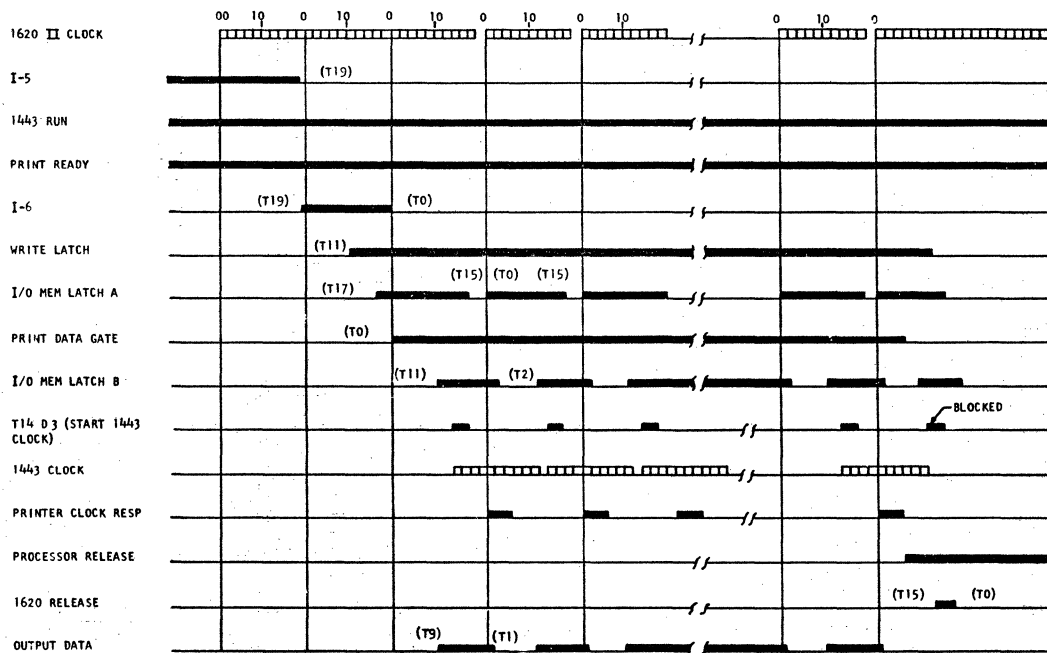
INSTRUCTION: $O_0 O_1 P_2 P_3 P_4 P_5 P_6 Q_7 Q_8 Q_9 Q_{10} Q_{11}$ 09 IN Q_8 & Q_9 SELECTS PRINTER
 PURPOSE: TO PRINT ONE LINE (120 CHAR) FROM P ADDRESS 1 IN Q_{11} WILL SUPPRESS THE SPACE AFTER PRINT THRU SUCCESSIVELY HIGHER LOCATIONS IN MEMORY.

FUNCTION CHART	PAGE REF
WRITE LATCH	10.01.51.1
I/O MEM LAT A	10.01.53.1
I/O MEM LAT B	10.01.53.1
RM PR LAT	10.01.75.1
PRINT DATA GATE	10.01.75.1
PRINTER CLOCK RESP	10.01.75.1
SPACE SUPPRESS LAT	10.01.76.1





PRINT WRITE TIMING (LOAD 1443 BUFFER)

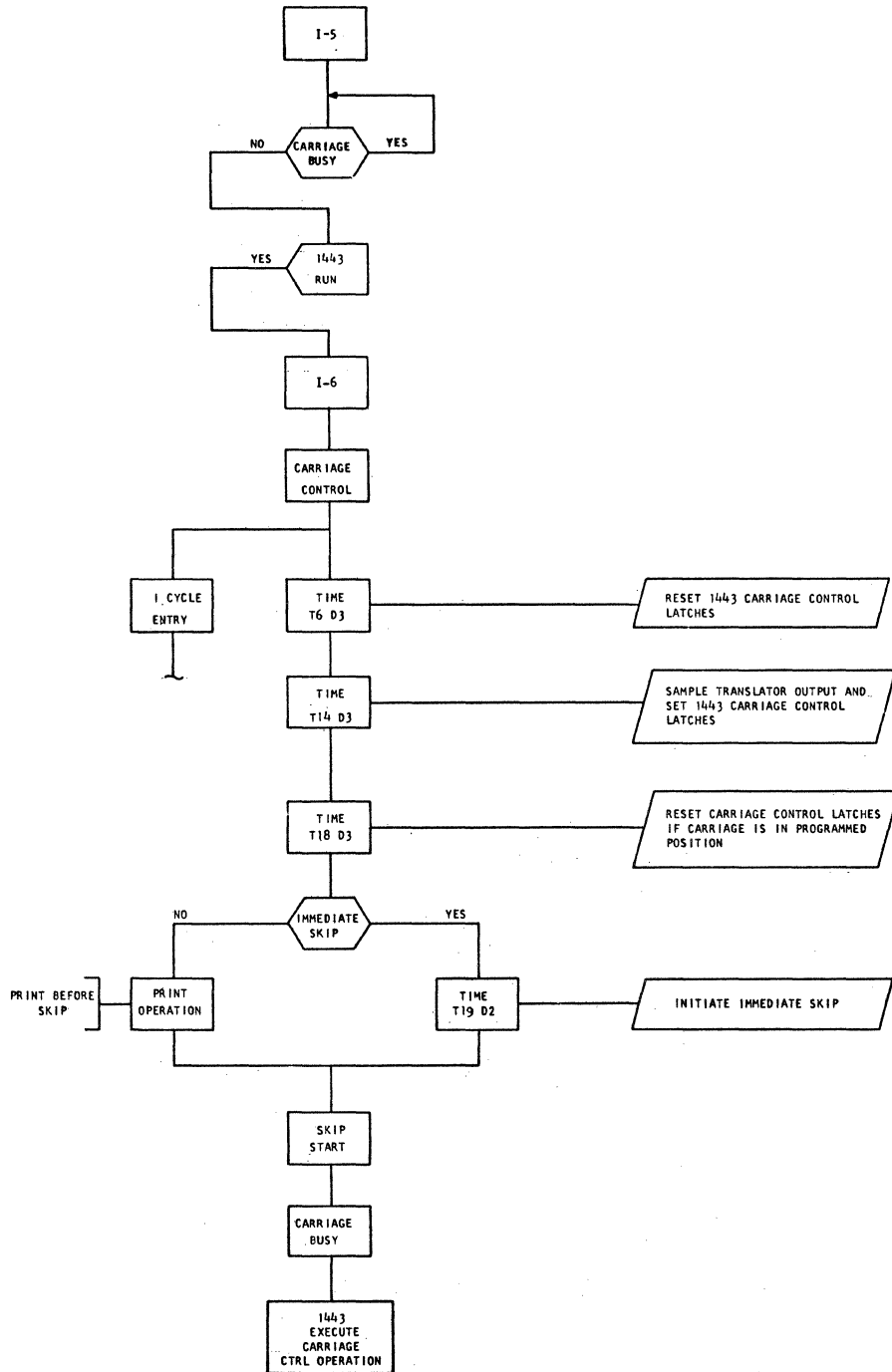


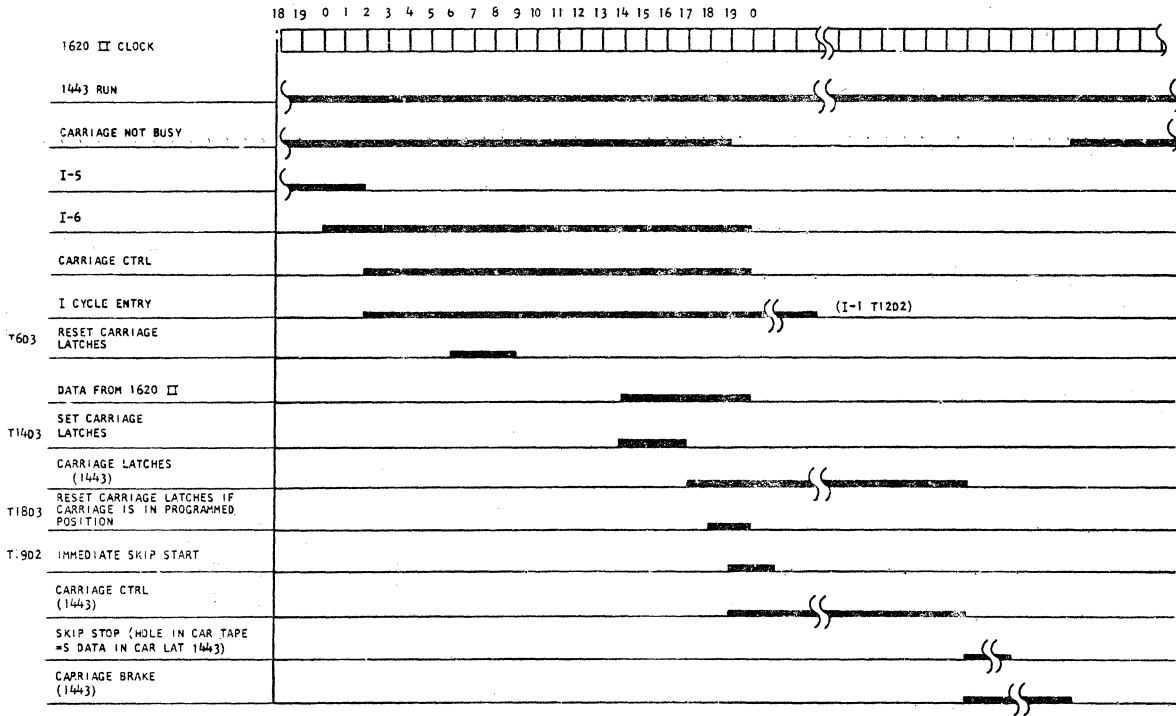
34 K CONTROL

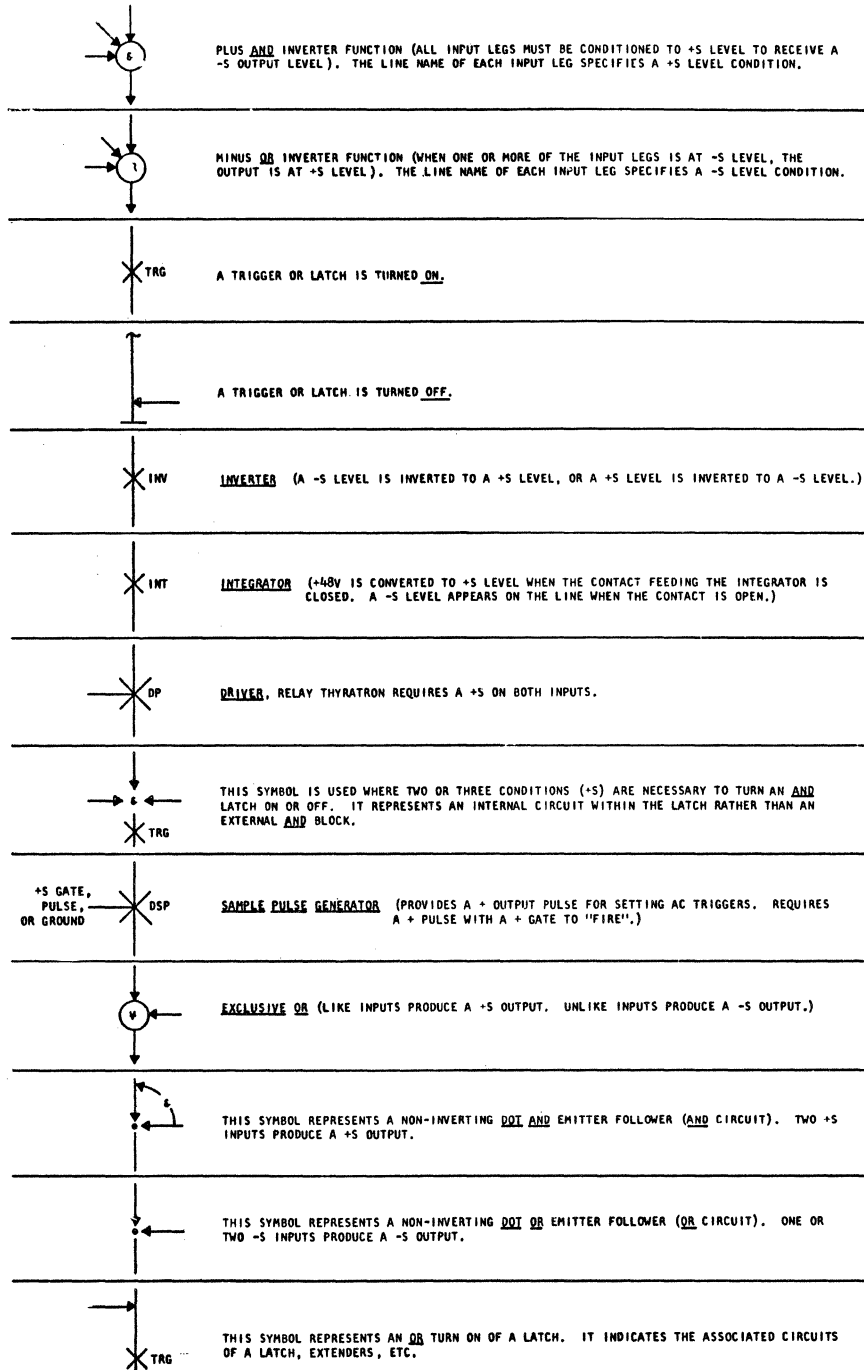
0₀ 0₁ P₂ P₃ P₄ P₅ P₆ Q₇ 0₈ 0₉ Q₁₀ Q₁₁ 0₉ SELECTS 1443 PRINTER

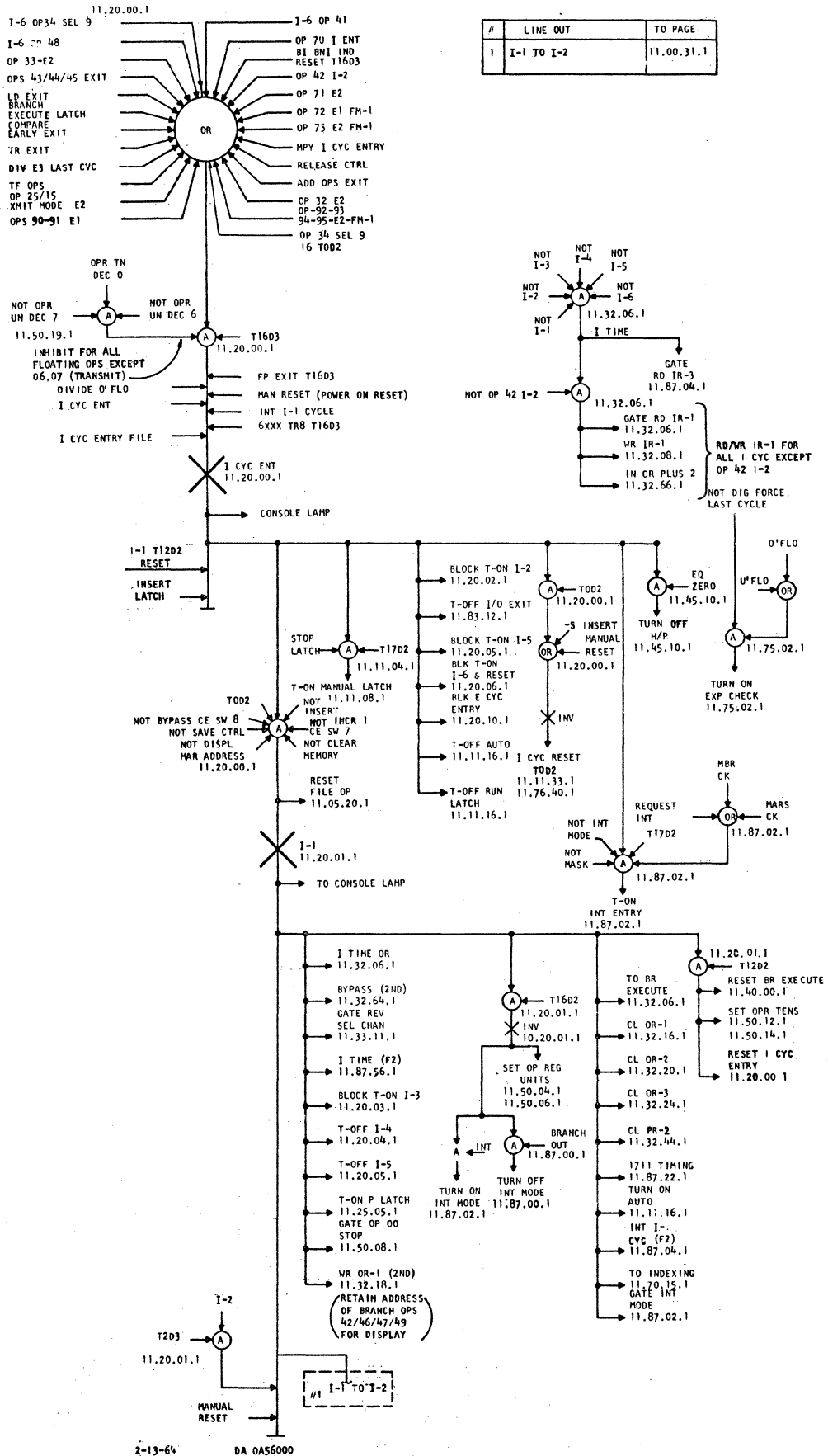
PURPOSE: CONTROL SPACE AND SKIP OPERATIONS

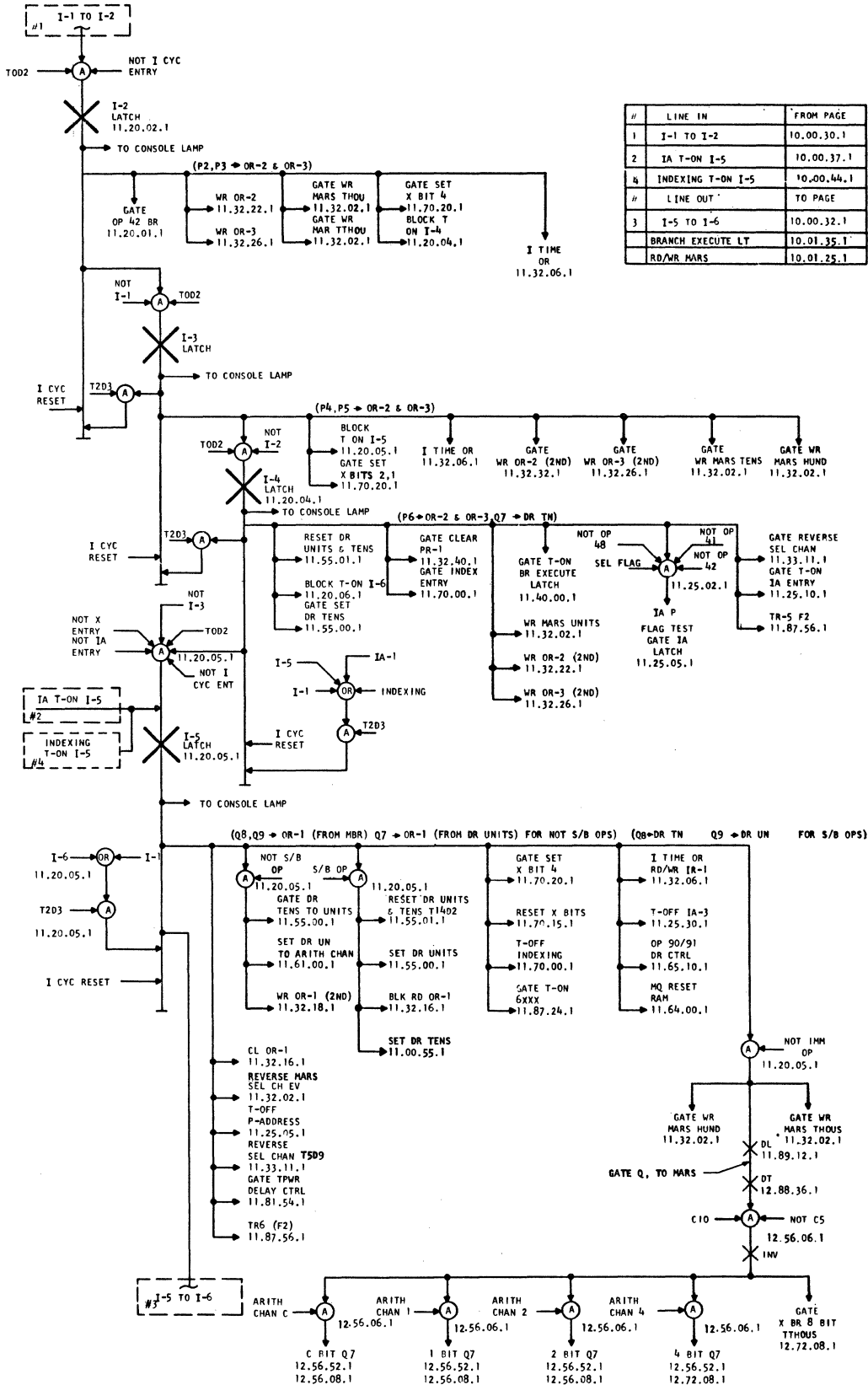
FUNCTION CHART	REF PAGE
CARRIAGE CONTROL	10.01.76.1
I CYCLE ENTRY	10.00.30.1

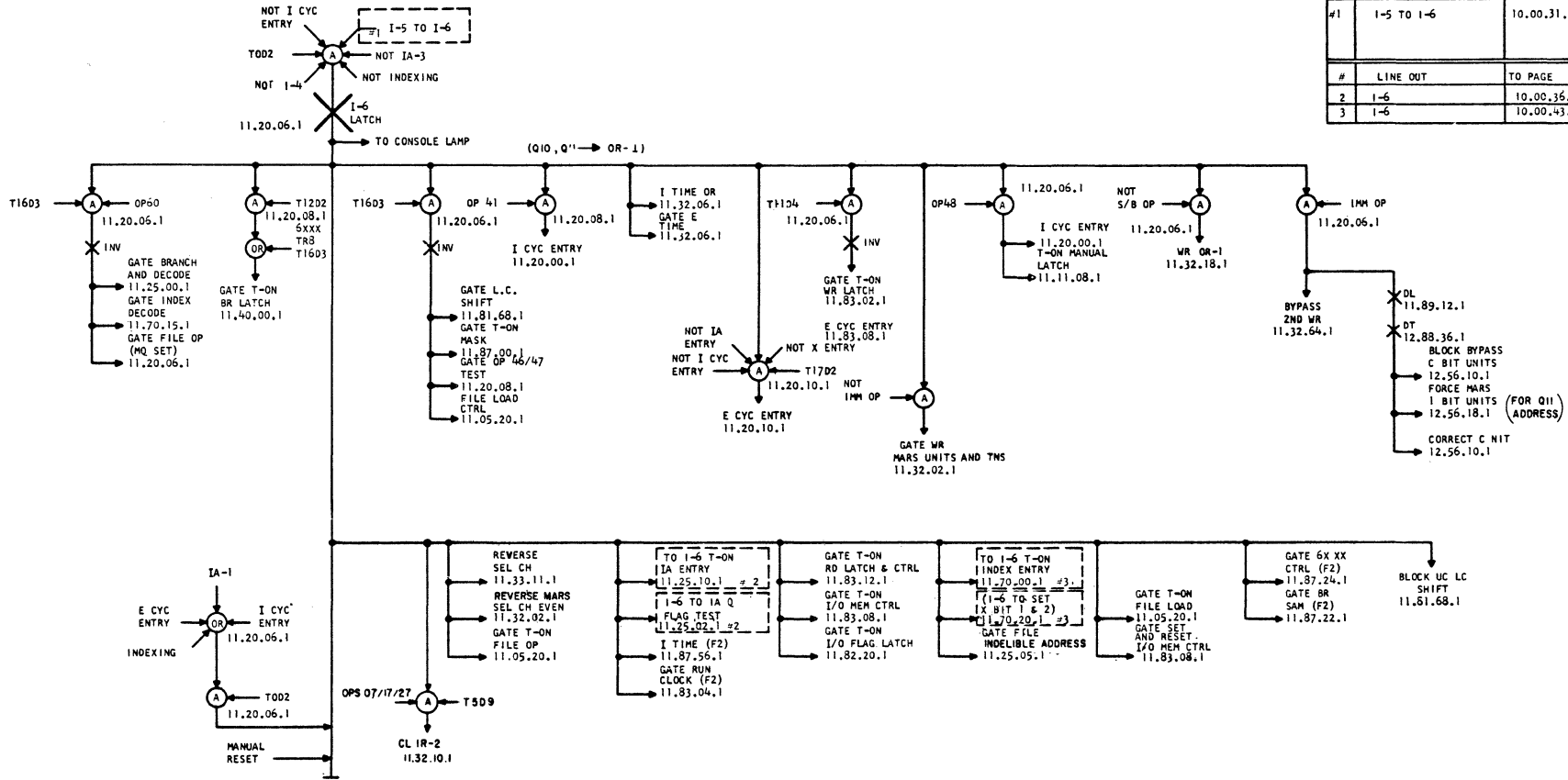






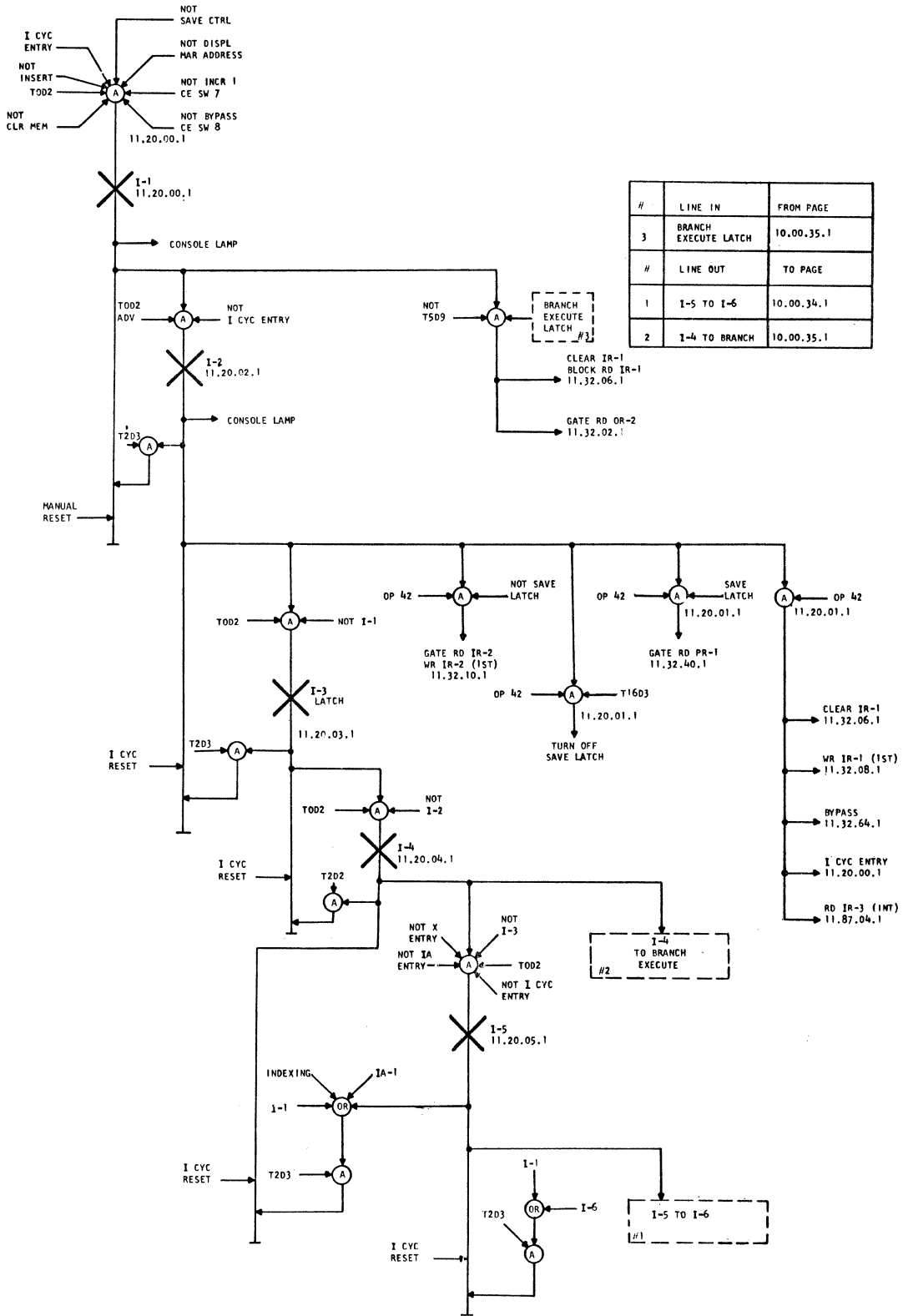


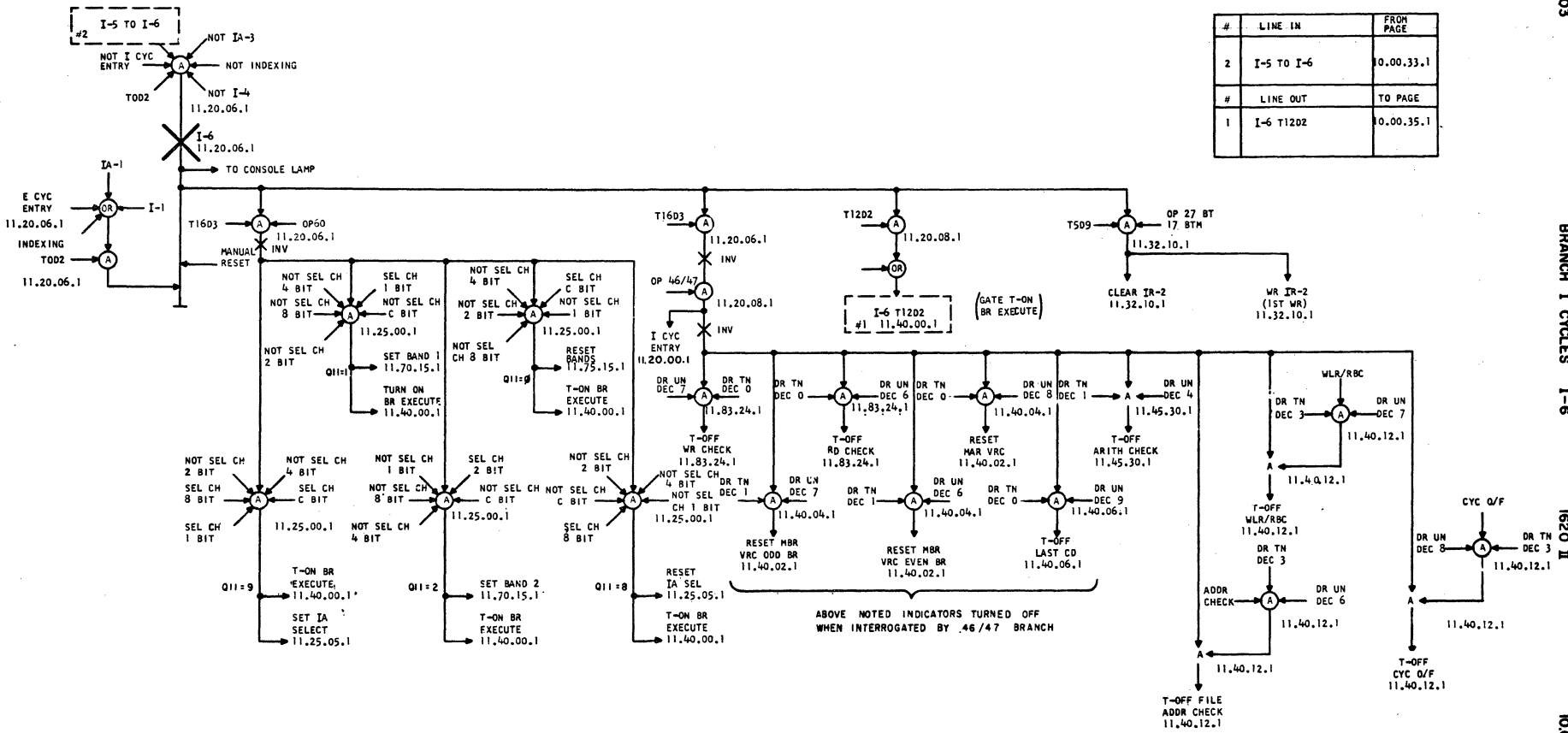


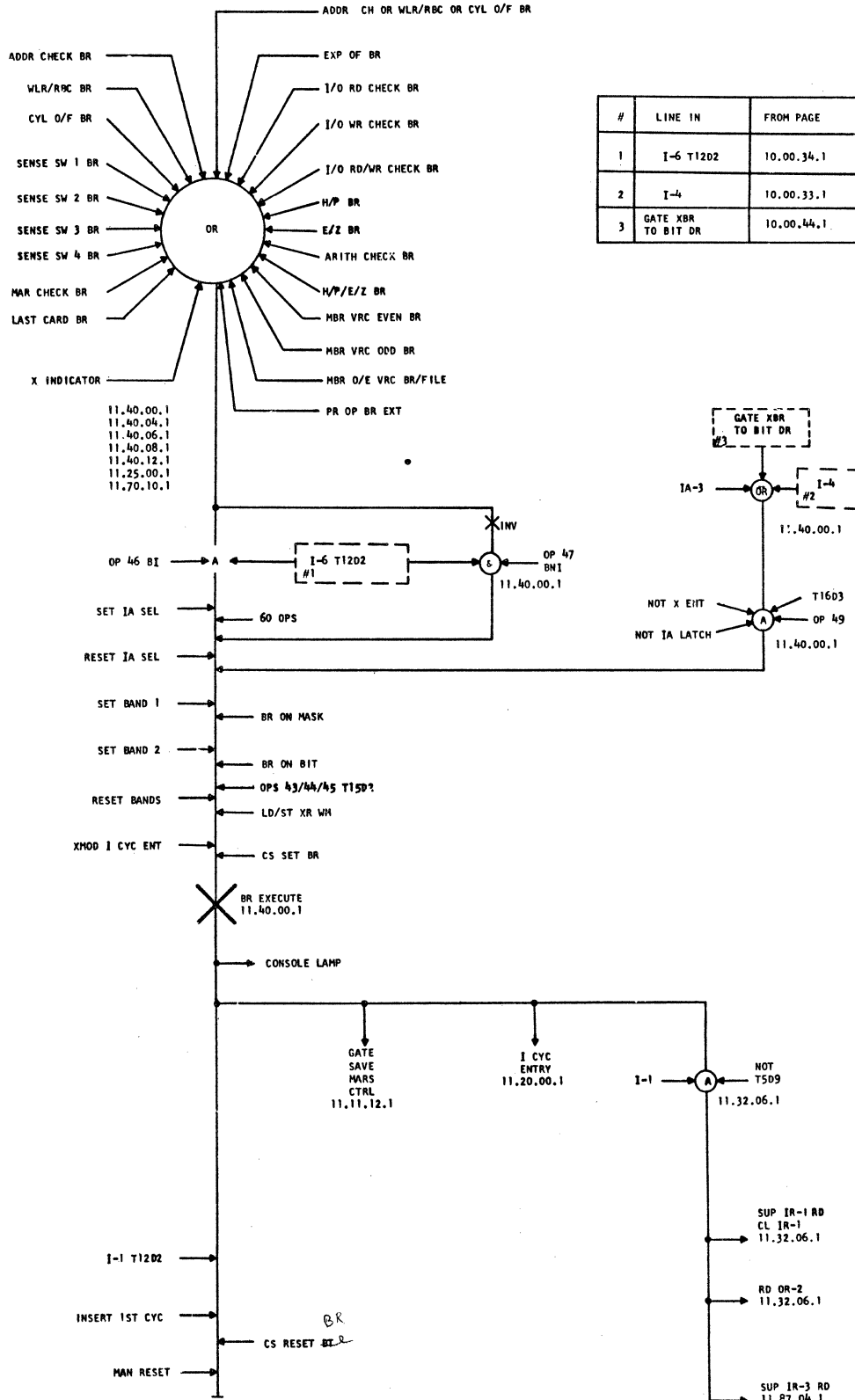


#	LINE IN	FROM PAGE
1	I-5 TO I-6	10.00.31.1
#	LINE OUT	TO PAGE
2	I-6	10.00.36.1
3	I-6	10.00.43.1

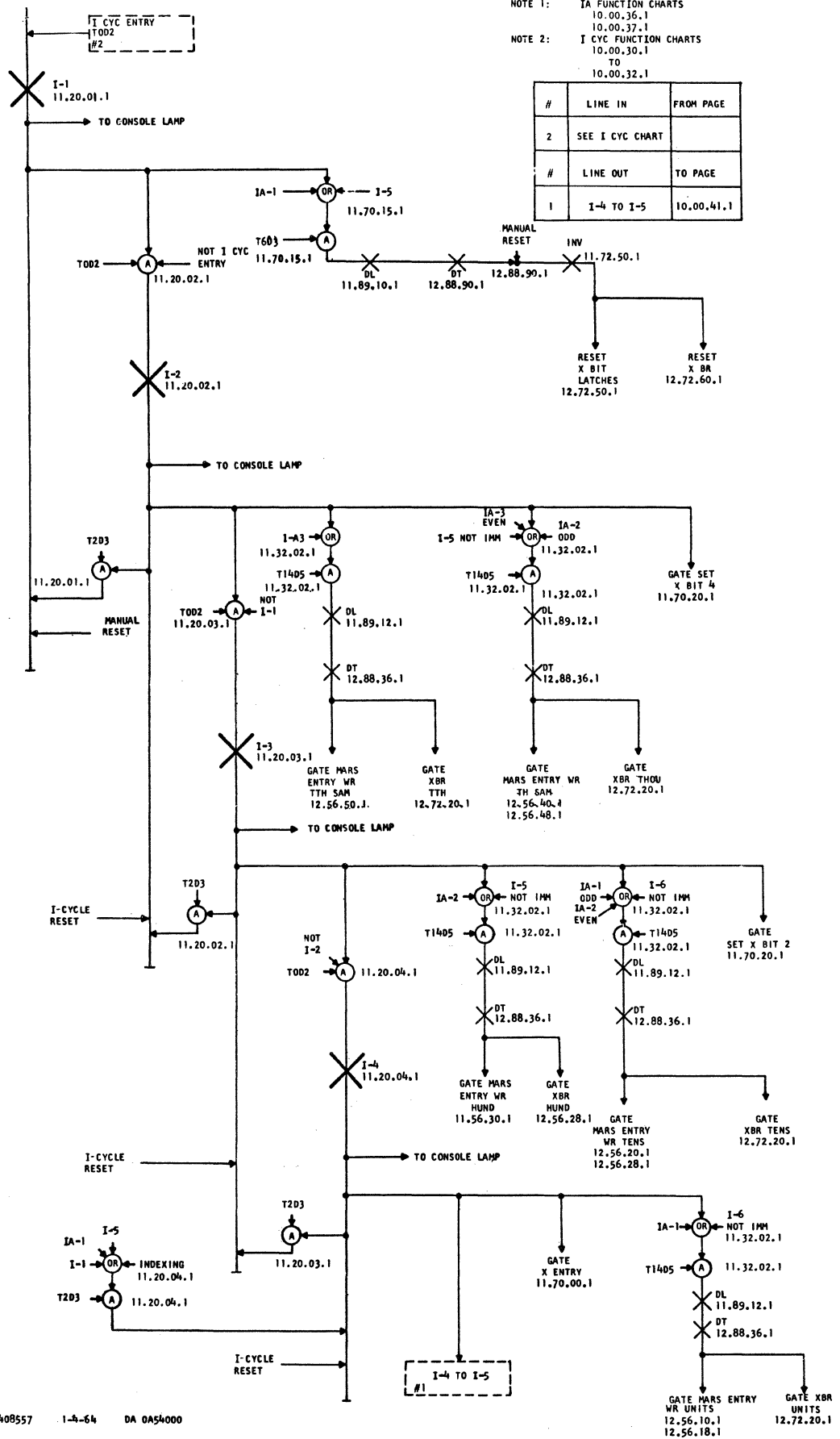
I-1 → I-5







#	LINE IN	FROM PAGE
1	I-6 T12D2	10.00.34.1
2	I-4	10.00.33.1
3	GATE XBR TO BIT DR	10.00.44.1



NOTE 1: IA FUNCTION CHARTS

10.00.36.1

10.00.37.1

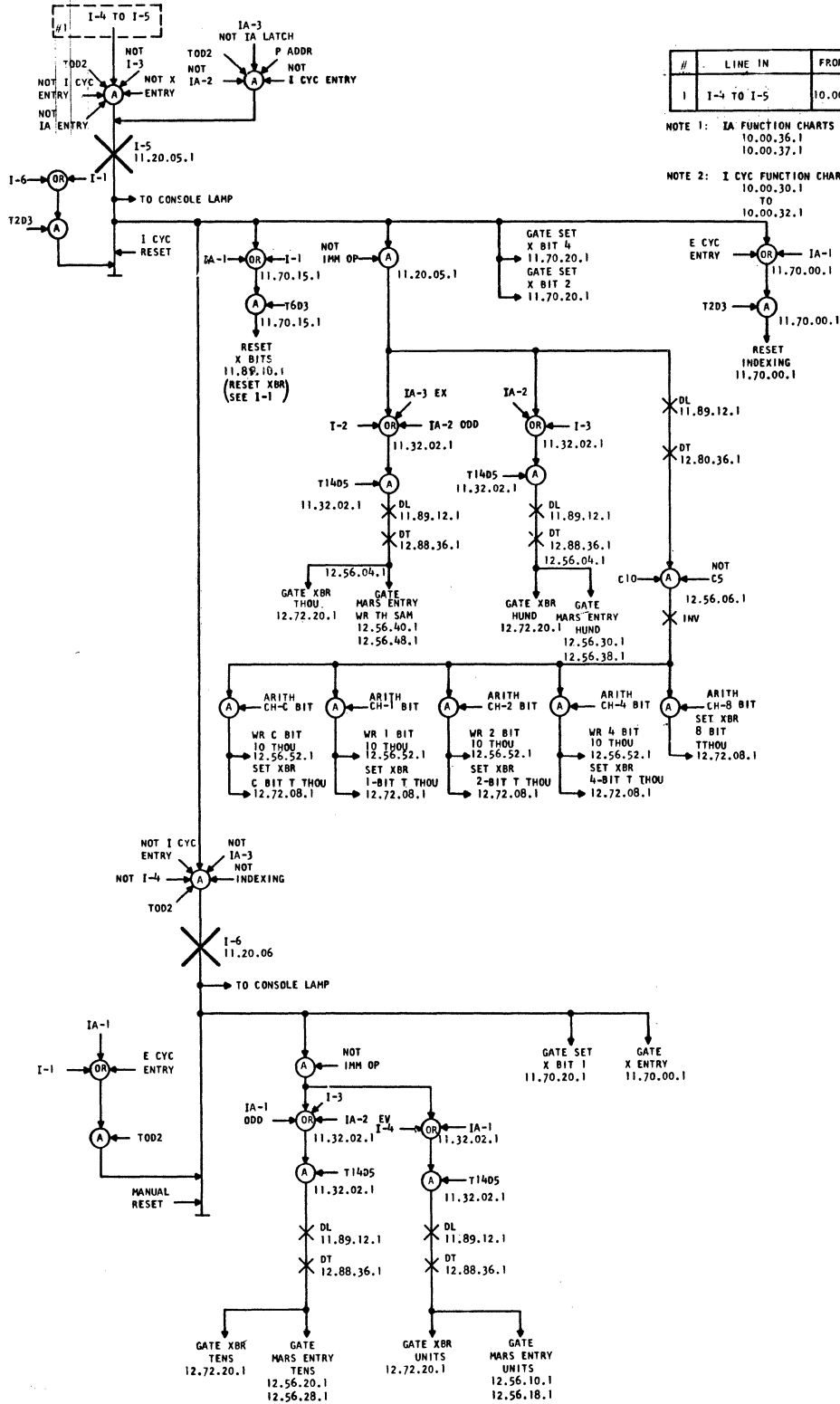
NOTE 2: I CYC FUNCTION CHARTS

10.00.30.1

TO

10.00.32.1

#	LINE IN	FROM PAGE
2	SEE I CYC CHART	
#	LINE OUT	TO PAGE
1	I-4 TO I-5	10.00.41.1



#	LINE IN	FROM PAGE
1	I-4 TO I-5	10.00.40.1

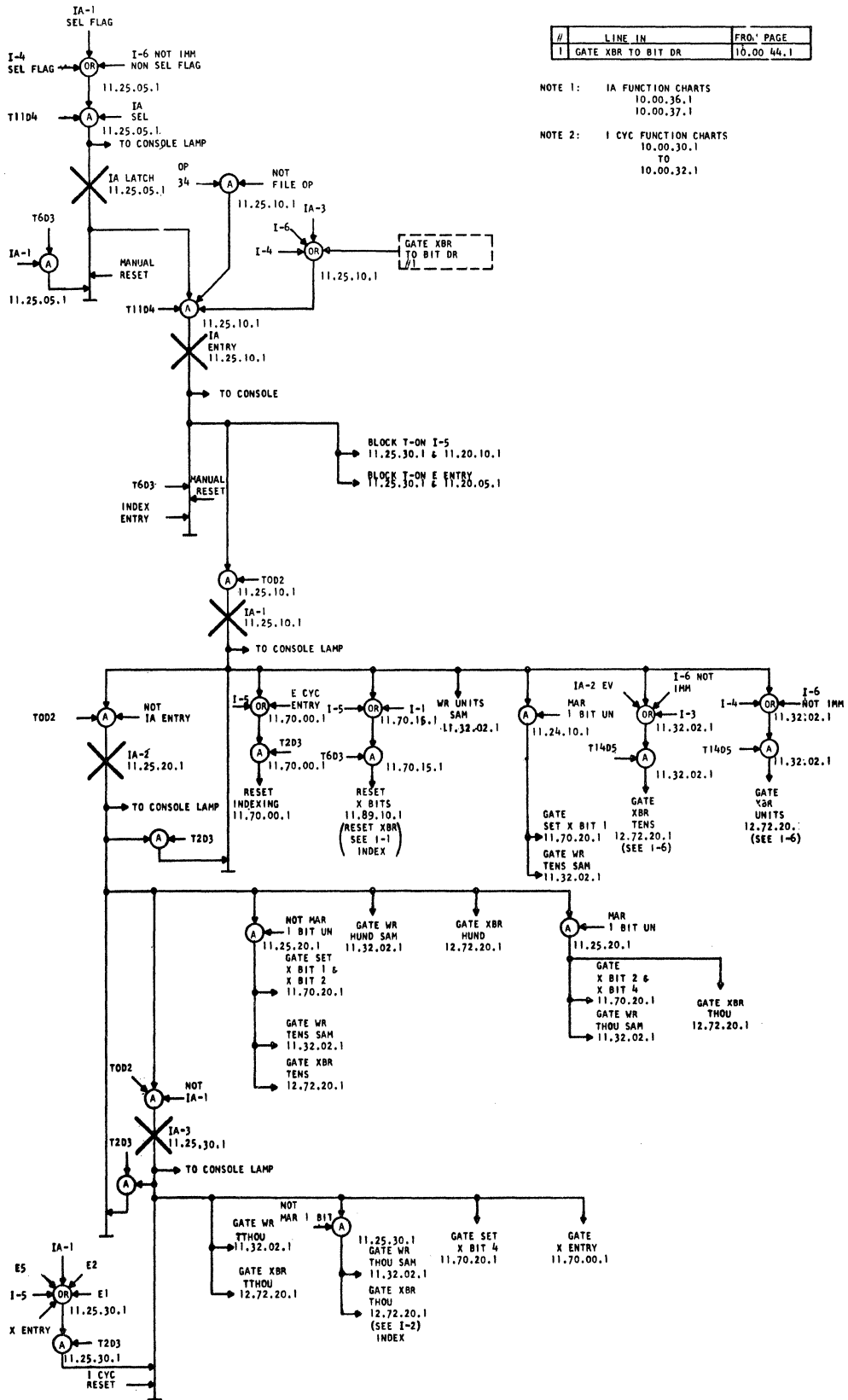
NOTE 1: IA FUNCTION CHARTS
10.00.36.1
10.00.37.1

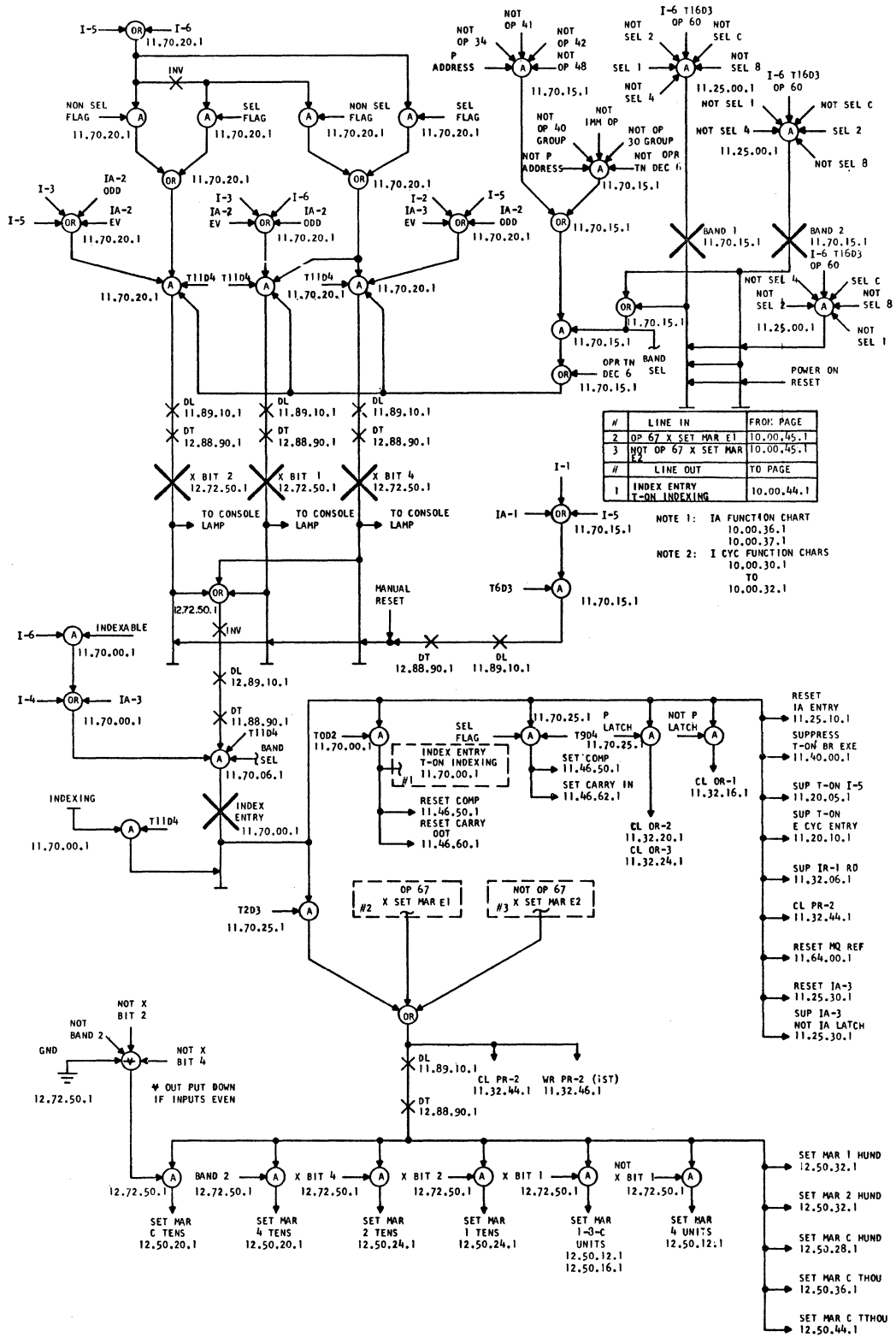
NOTE 2: I CYC FUNCTION CHARTS
10.00.30.1
TO 10.00.32.1

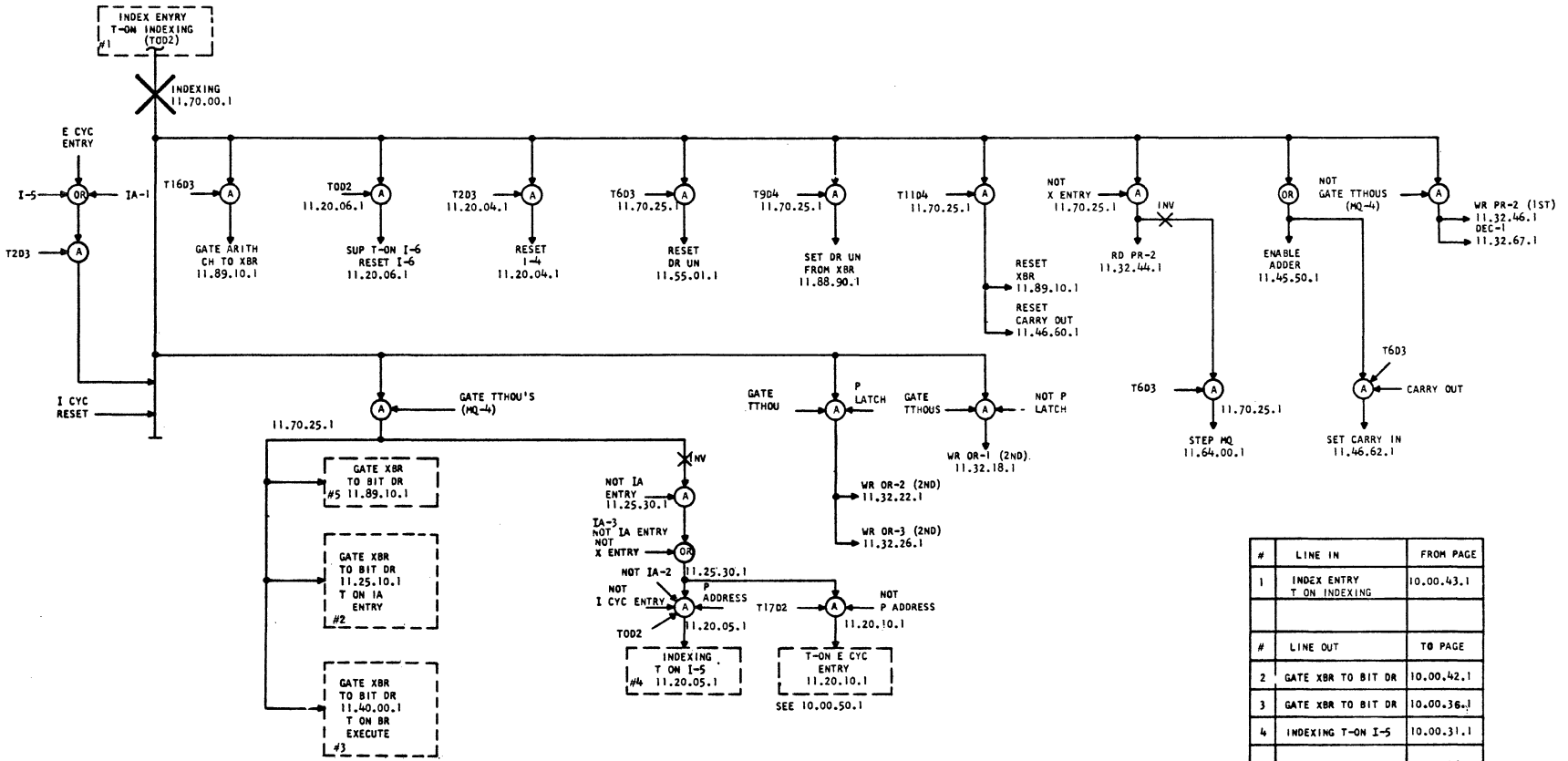
#	LINE IN	FRON. PAGE
1	GATE XBR TO BIT DR	10.00.44.1

NOTE 1: IA FUNCTION CHARTS
10.00.36.1
10.00.37.1

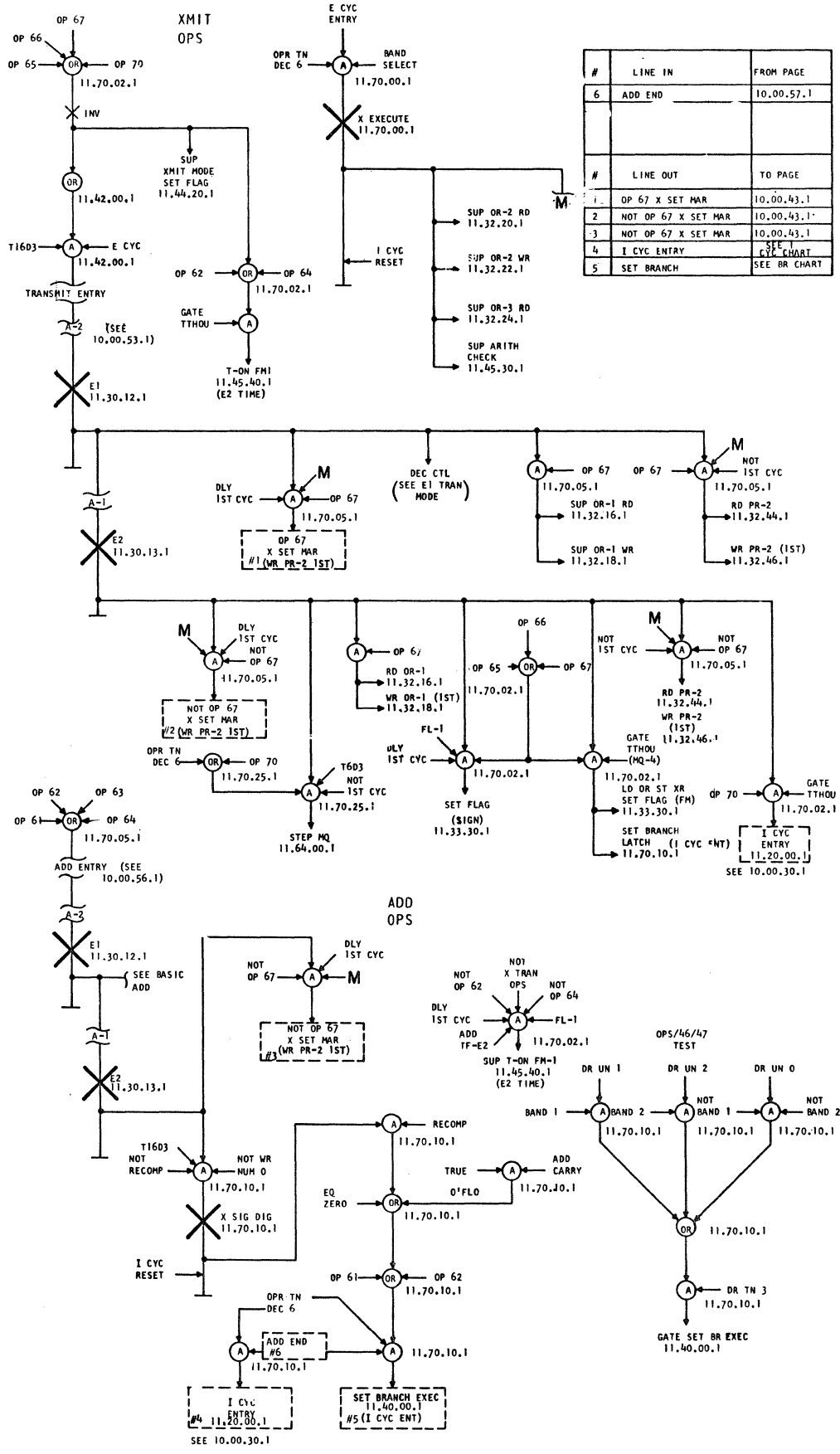
NOTE 2: I CYC FUNCTION CHARTS
10.00.30.1
TO
10.00.32.1

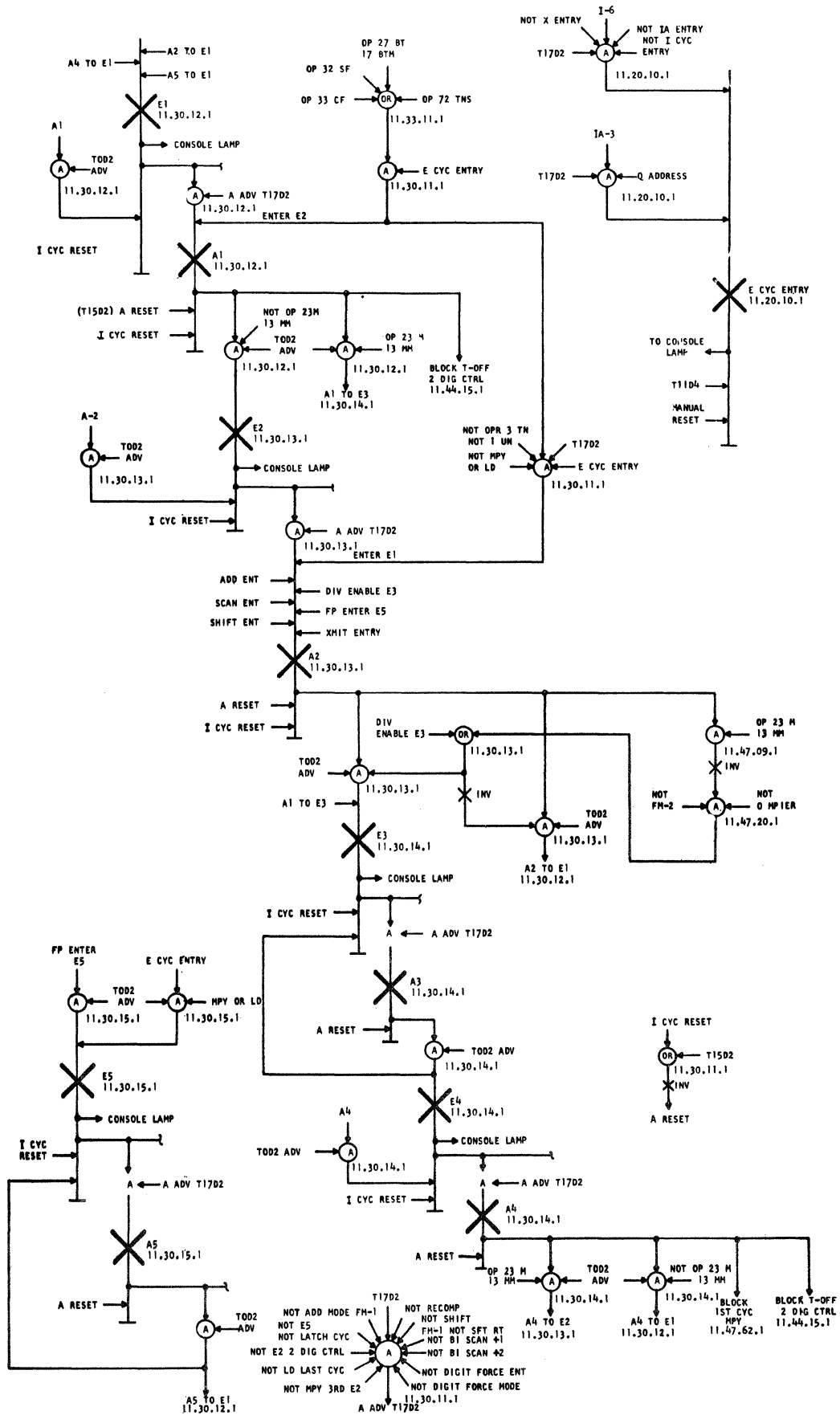


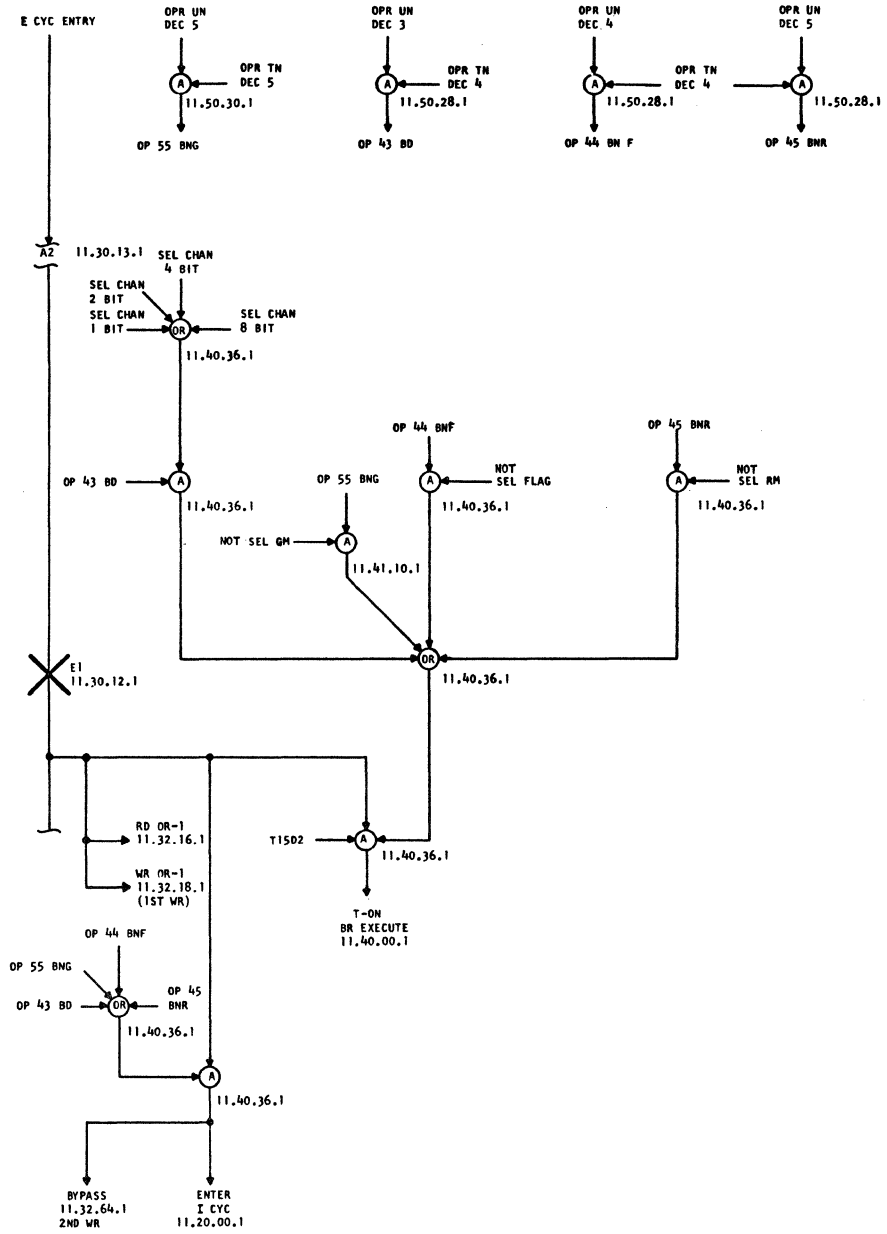


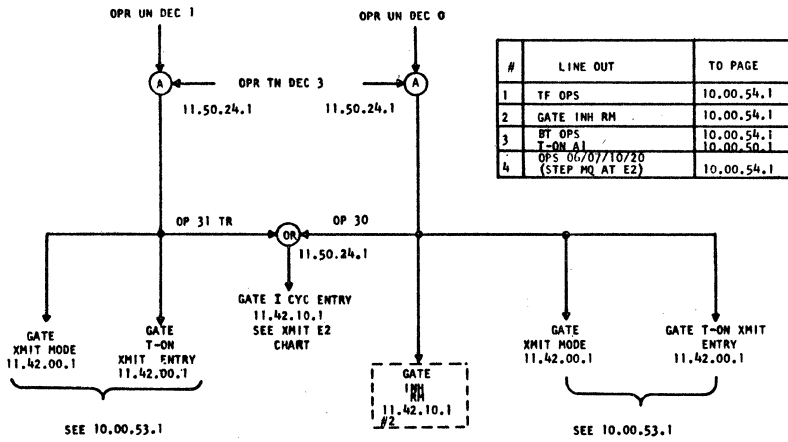
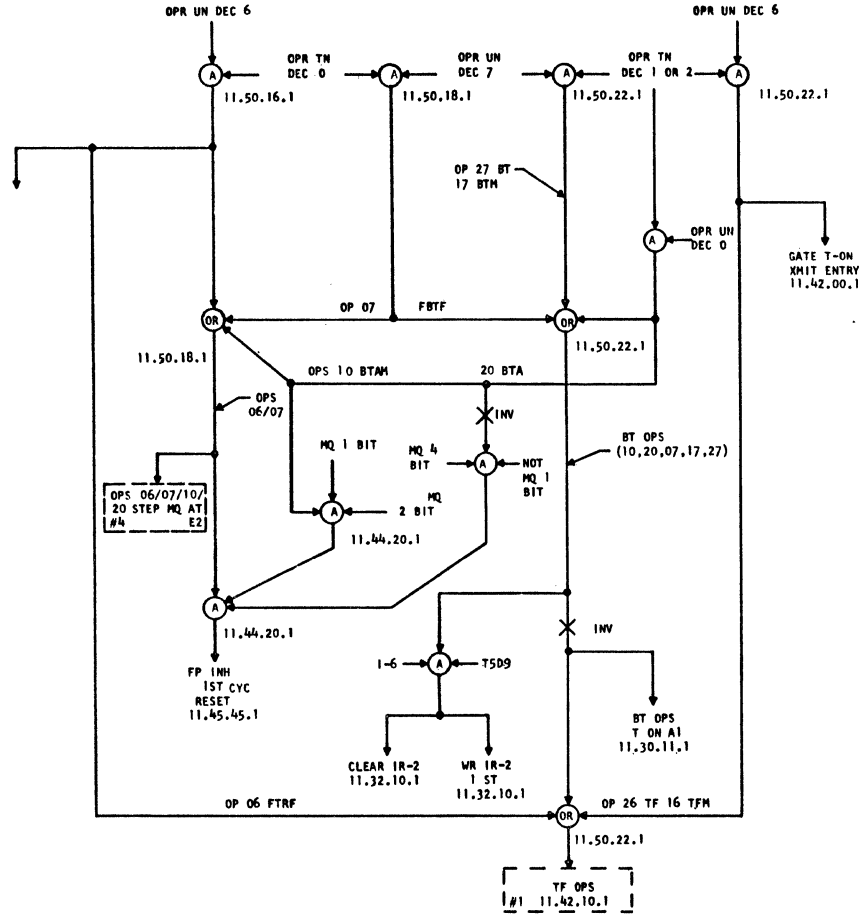


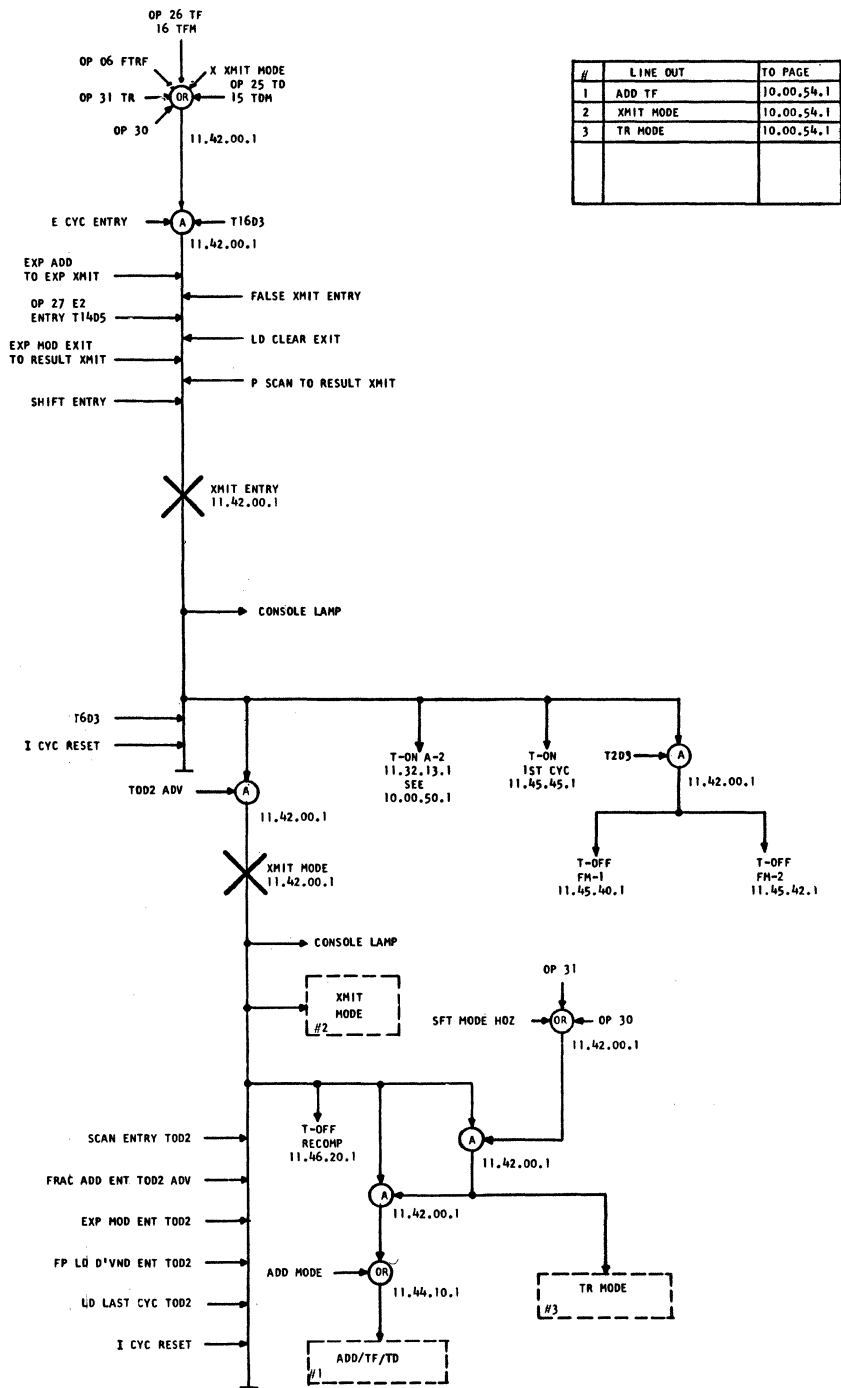
#	LINE IN	FROM PAGE
1	INDEX ENTRY T ON INDEXING	10.00.43.1
#	LINE OUT	TO PAGE
2	GATE XBR TO BIT DR	10.00.42.1
3	GATE XBR TO BIT DR	10.00.36.1
4	INDEXING T-ON I-5	10.00.31.1
5	GATE XBR TO BIT DR	10.01.46.1



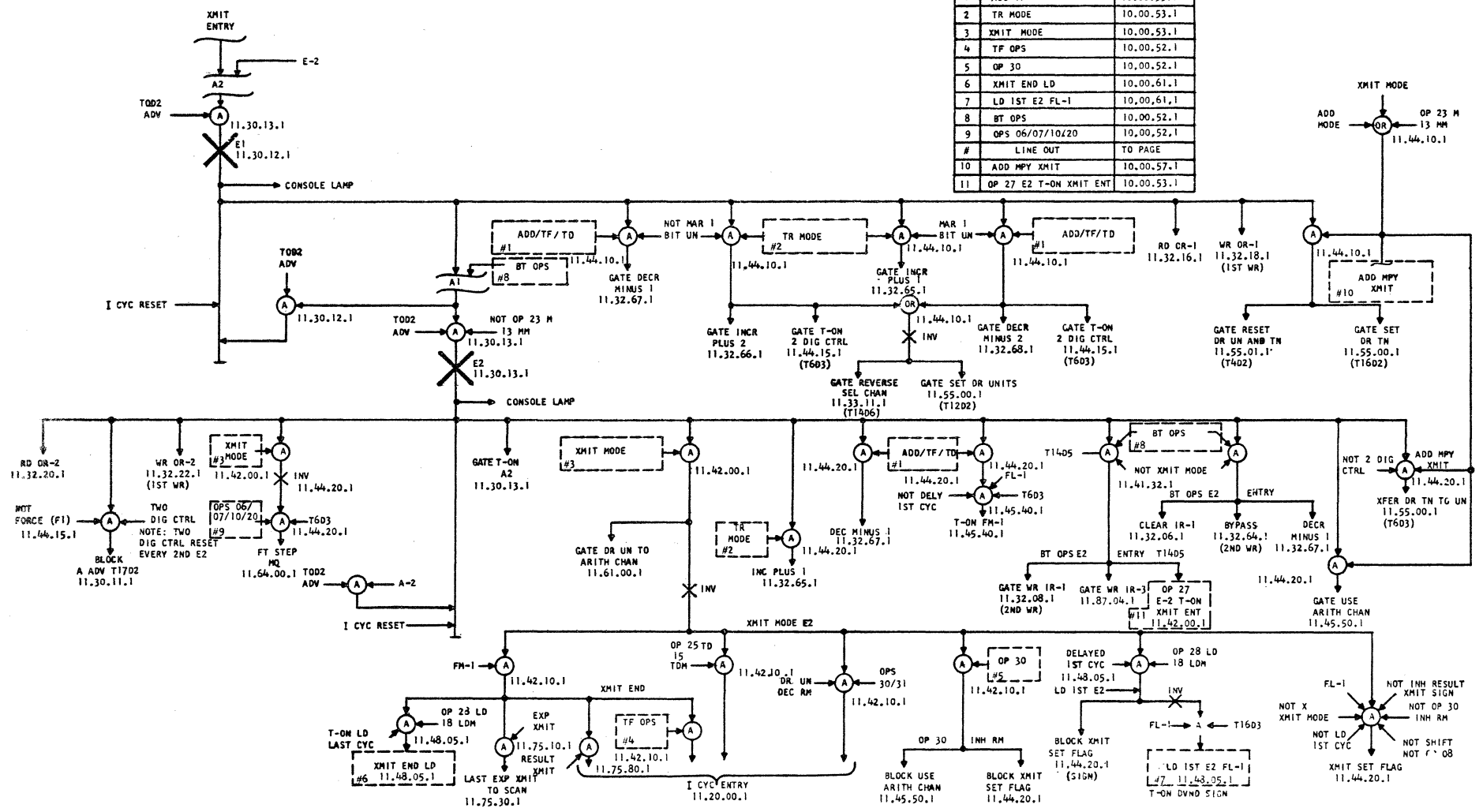




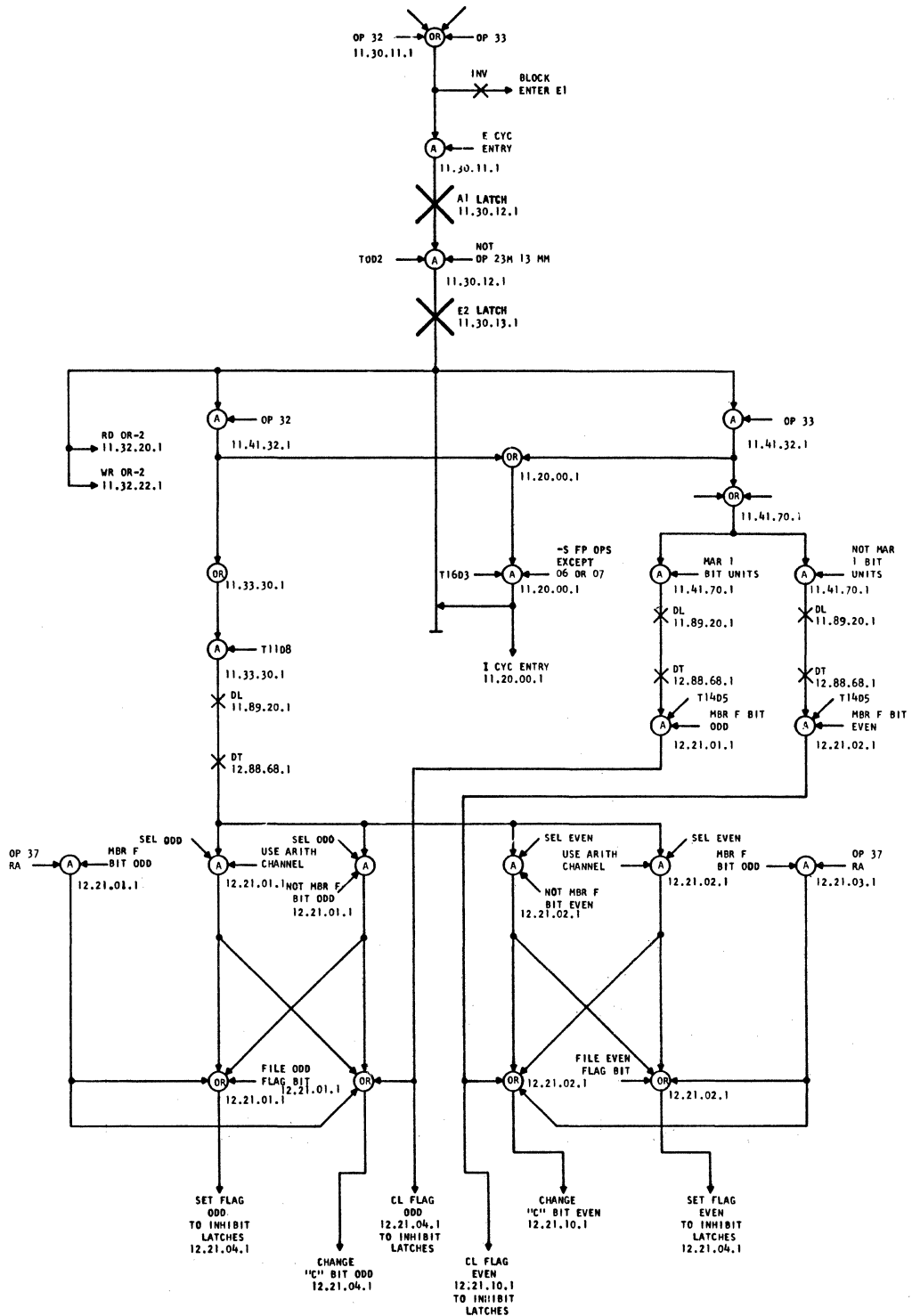


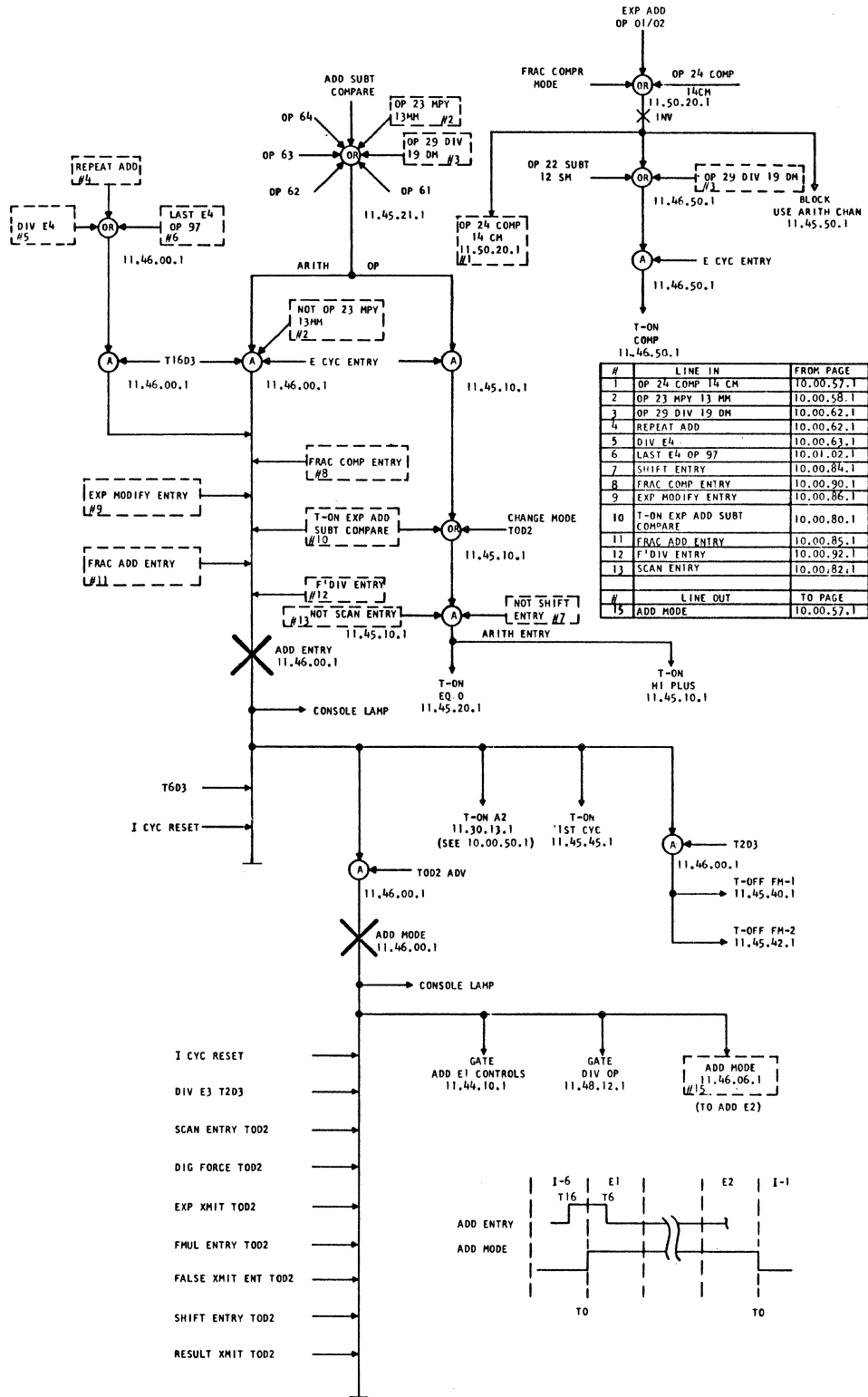


NO.	LINE IN	FROM PAGE
1	ADD TF	10.00.53.1
2	TR MODE	10.00.53.1
3	XMIT MODE	10.00.53.1
4	TF OPS	10.00.52.1
5	OP 30	10.00.52.1
6	XMIT END LD	10.00.61.1
7	LD 1ST E2 FL-1	10.00.61.1
8	BT OPS	10.00.52.1
9	OPS 06/07/10/20	10.00.52.1
#	LINE OUT	TO PAGE
10	ADD MPY XMIT	10.00.57.1
11	OP 27 E2 T-ON XMIT ENT	10.00.53.1

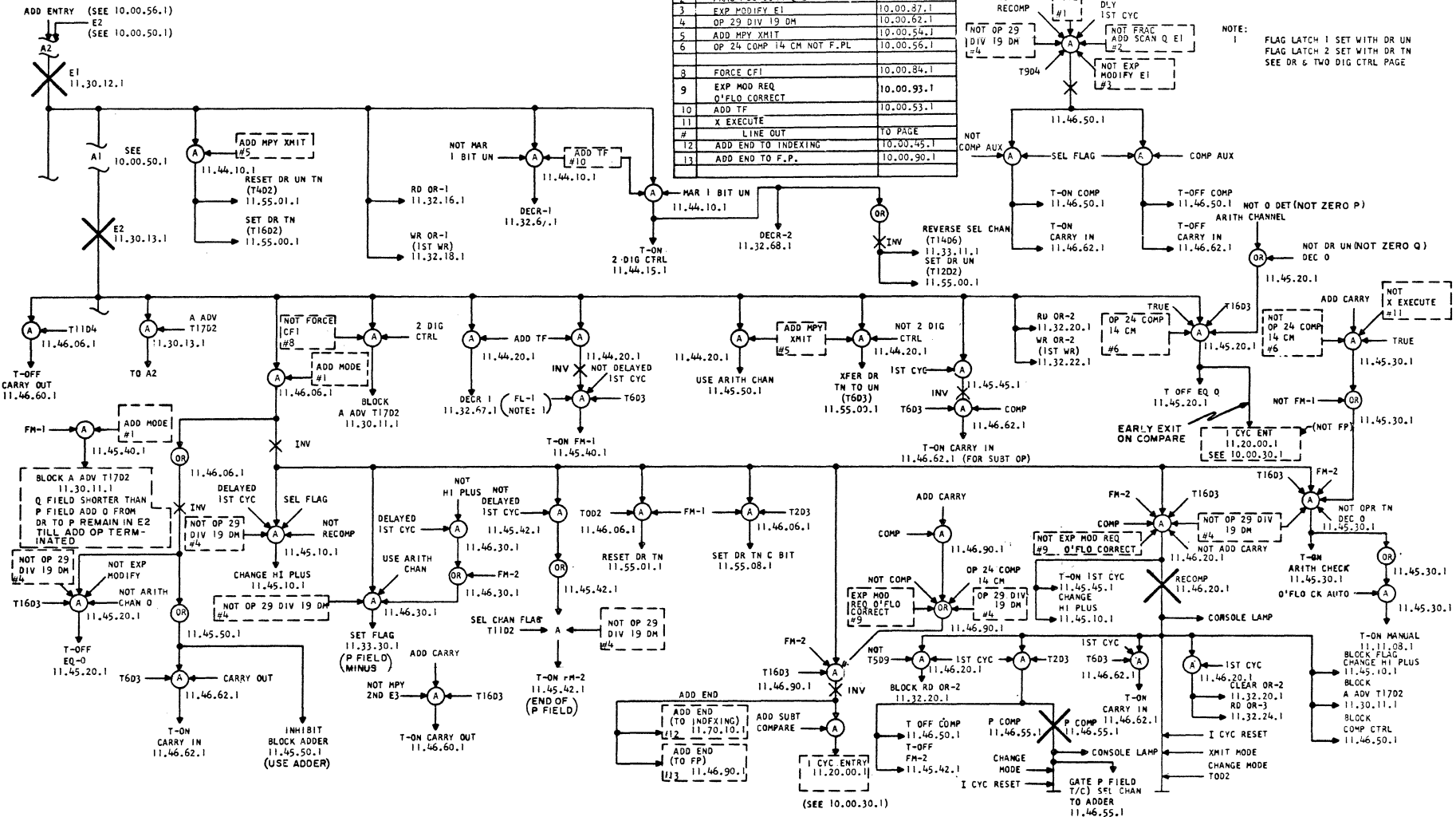


2159118
XMIT E1 & E2
162011
10.00.54.1





#	LINE IN	FROM PAGE
1	ADD MODE	10.00.56.1
2	FRAC ADD SCAN Q EI	10.00.35.1
3	EXP MODIFY EI	10.00.87.1
4	OP 29 DIV 19 DM	10.00.62.1
5	ADD MPY XMIT	10.00.54.1
6	OP 24 COMP 14 CM NOT F.PL	10.00.56.1
8	FORCE CF1	10.00.84.1
9	EXP MOD REQ O'FLO CORRECT	10.00.93.1
10	ADD TF	10.00.53.1
11	X EXECUTE	TO PAGE
#	LINE OUT	TO PAGE
12	ADD END TO INDEXING	10.00.45.1
13	ADD END TO F.P.	10.00.90.1



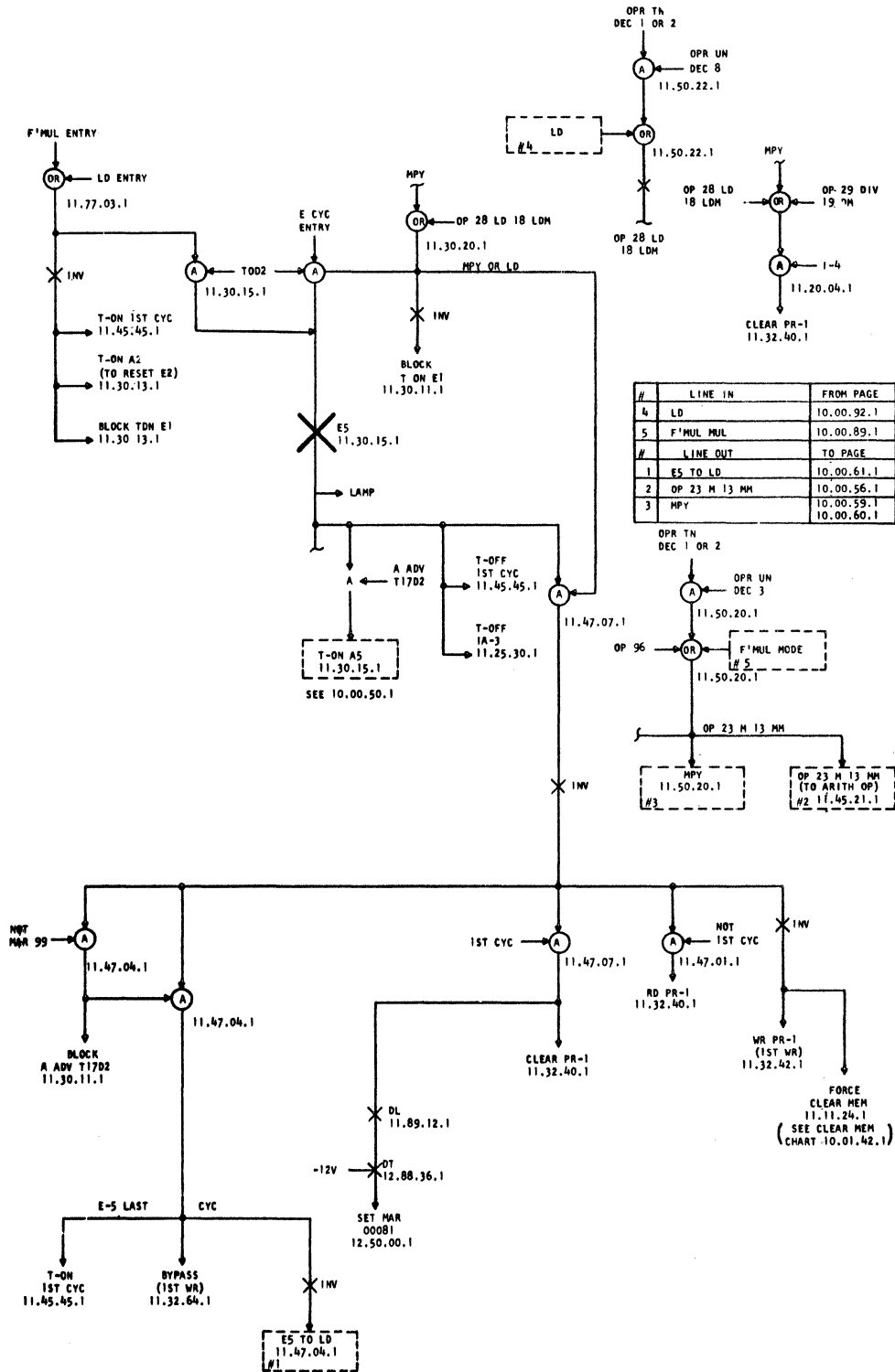
NOTE:
1 FLAG LATCH 1 SET WITH DR UN
FLAG LATCH 2 SET WITH DR TN
SEE DR & TWO DIG CTRL PAGE

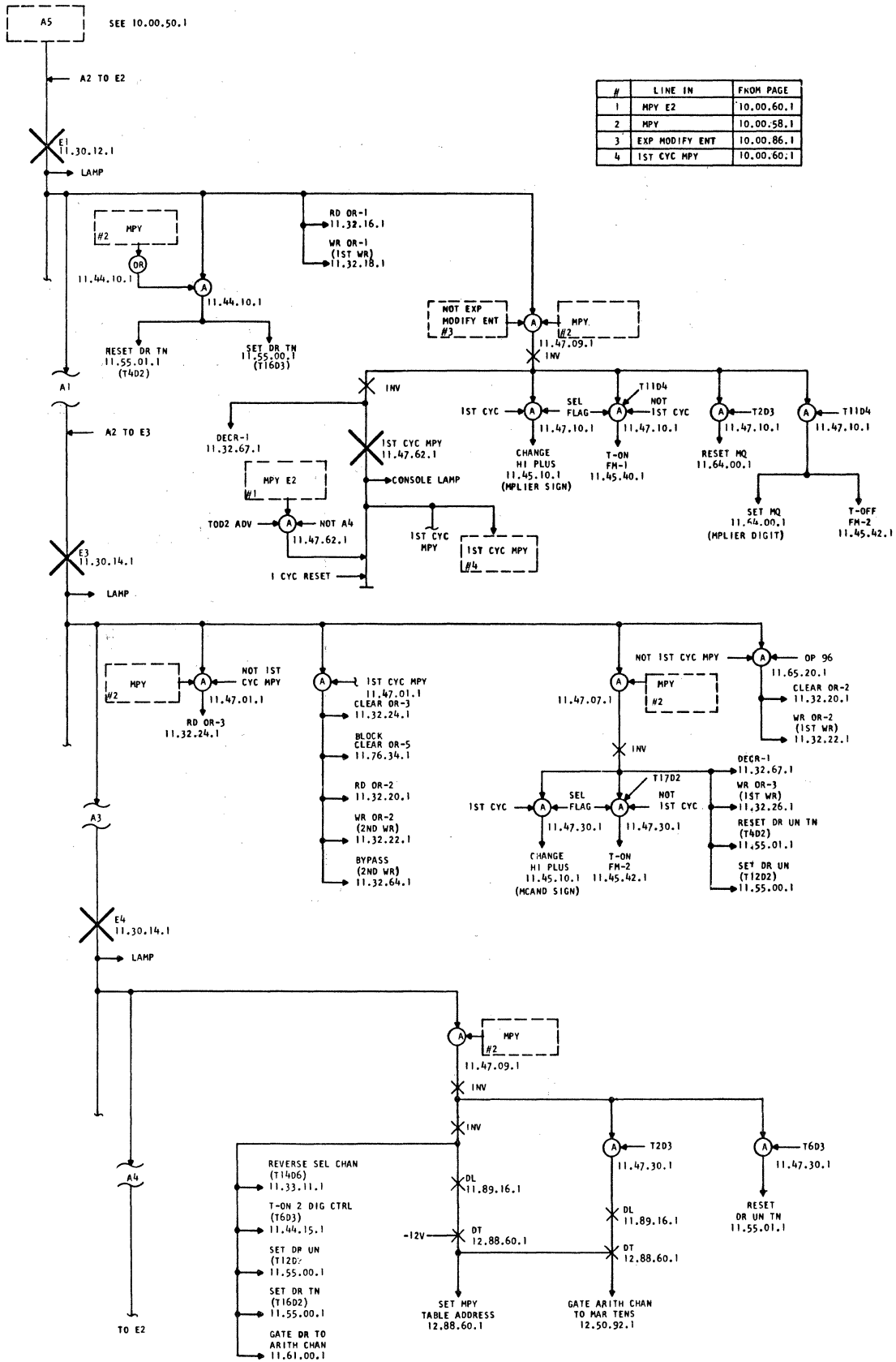
2159121

ADD SUBT COMPARE EI E2

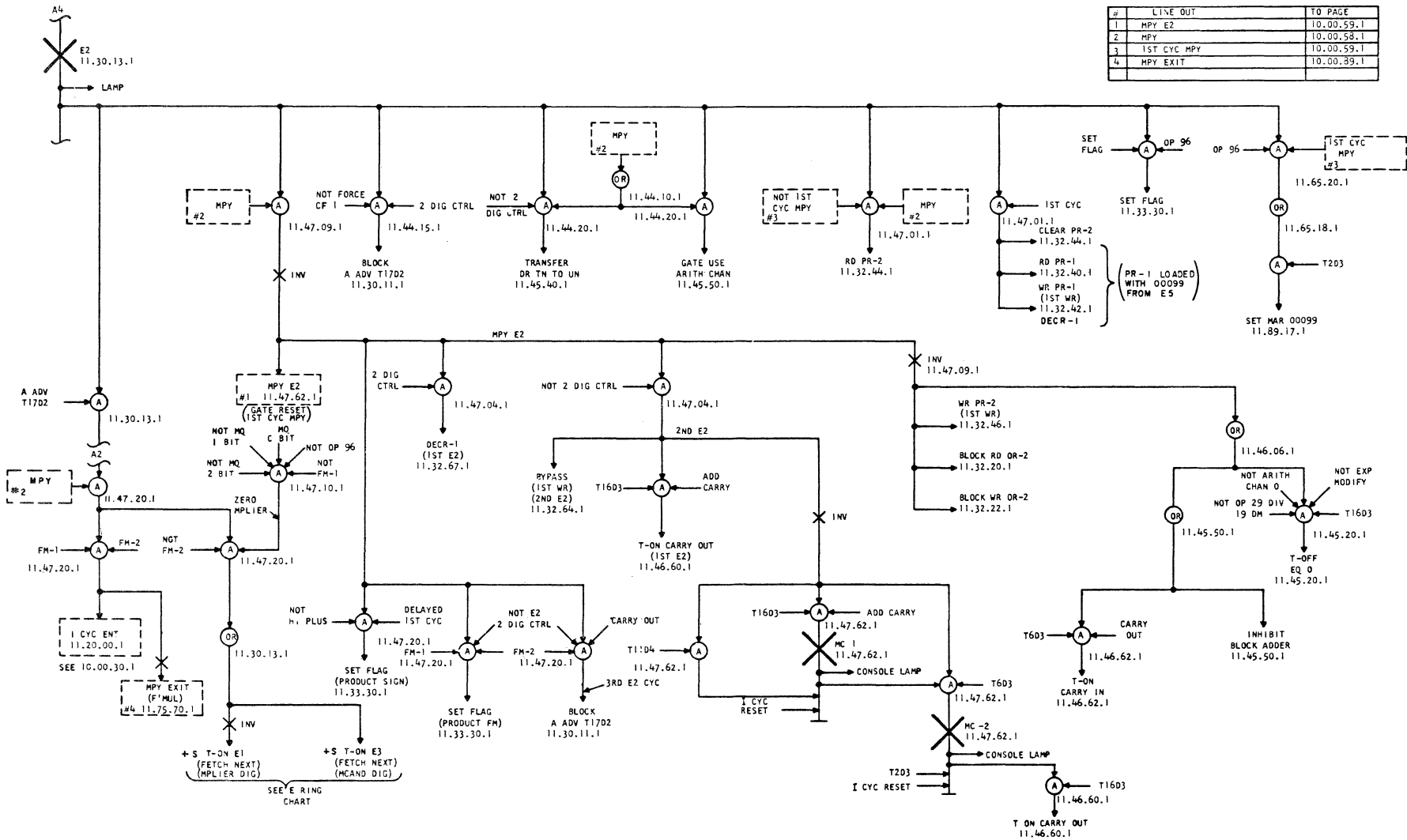
1620 II

10.00.57.1





#	LINE IN	FROM PAGE
1	MPY E2	10.00.60.1
2	MPY	10.00.58.1
3	EXP MODIFY ENT	10.00.86.1
4	1ST CYC MPY	10.00.60.1



#	LINE OUT	TO PAGE
1	MPY E2	10.00.59.1
2	MPY	10.00.58.1
3	1ST CYC MPY	10.00.59.1
4	MPY EXIT	10.00.39.1

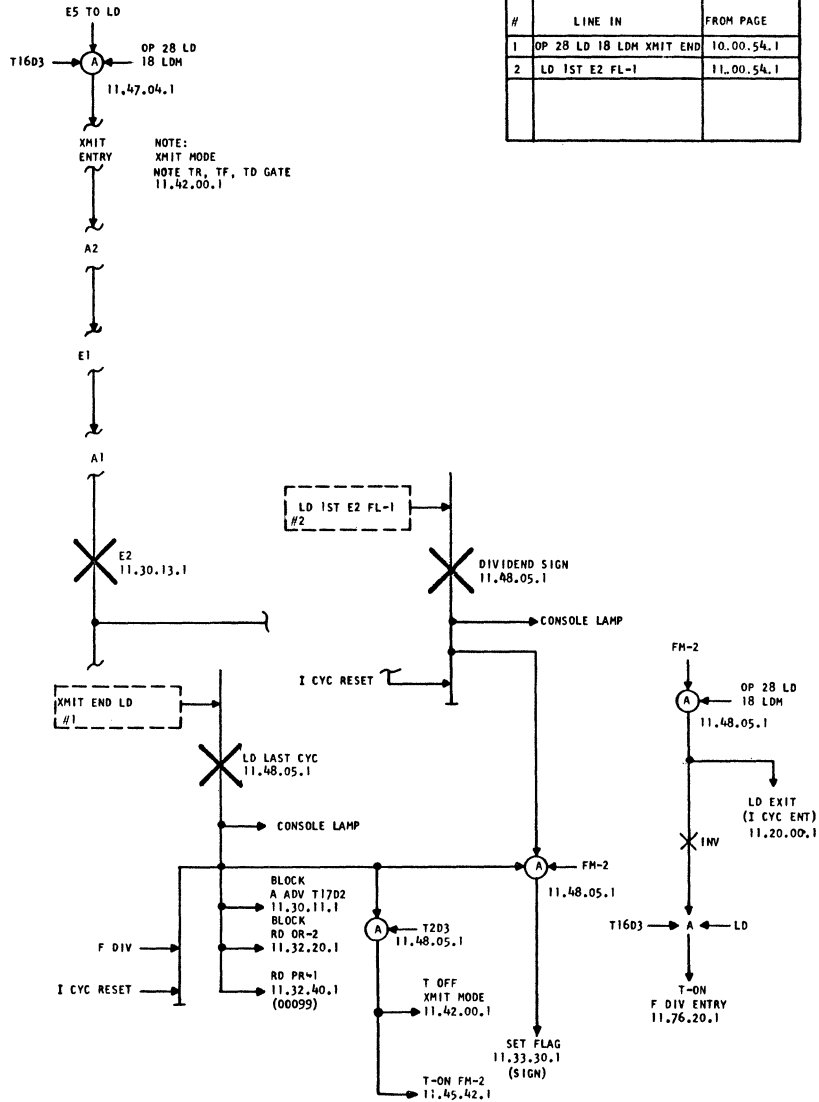
2159124

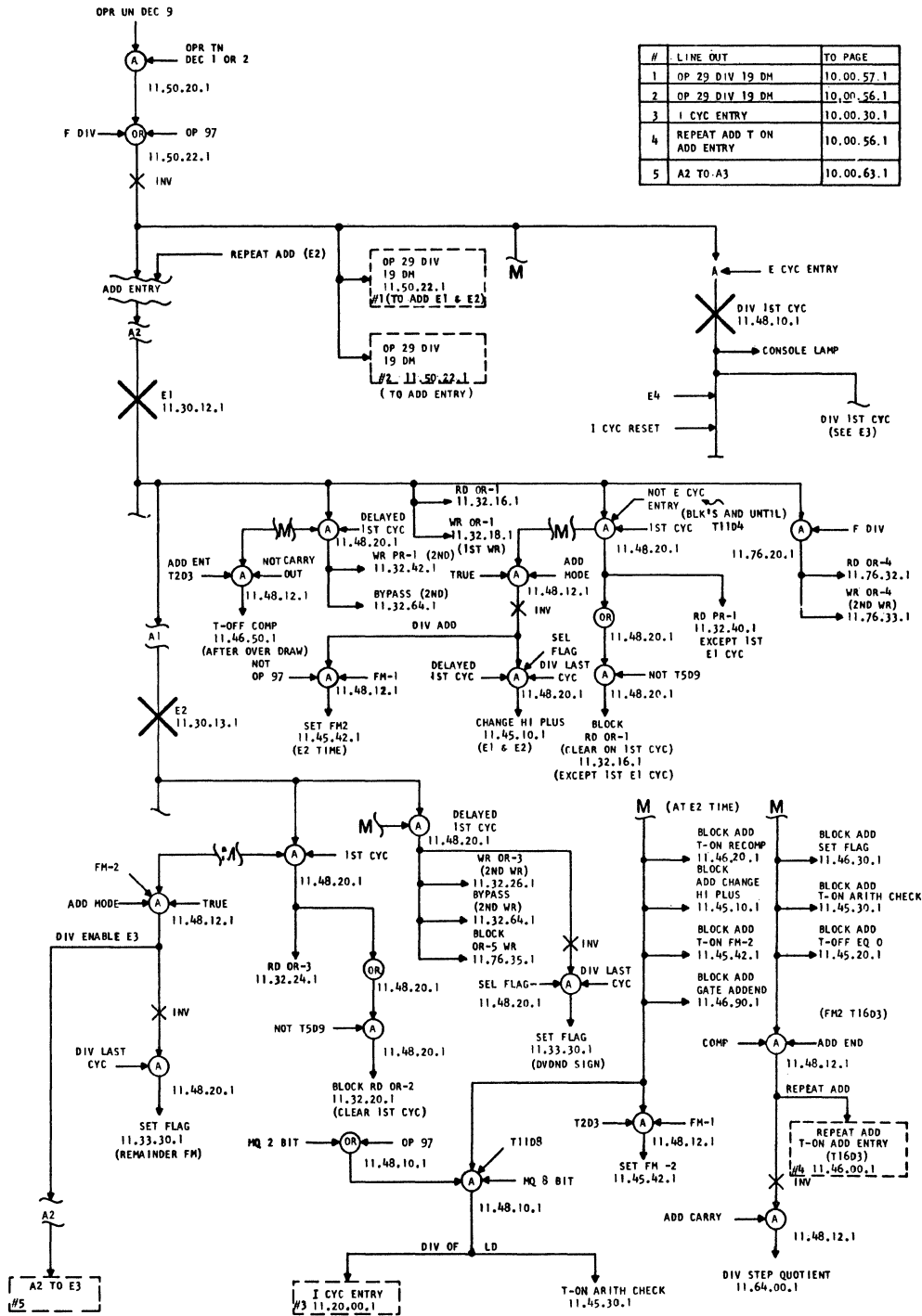
MPY E2

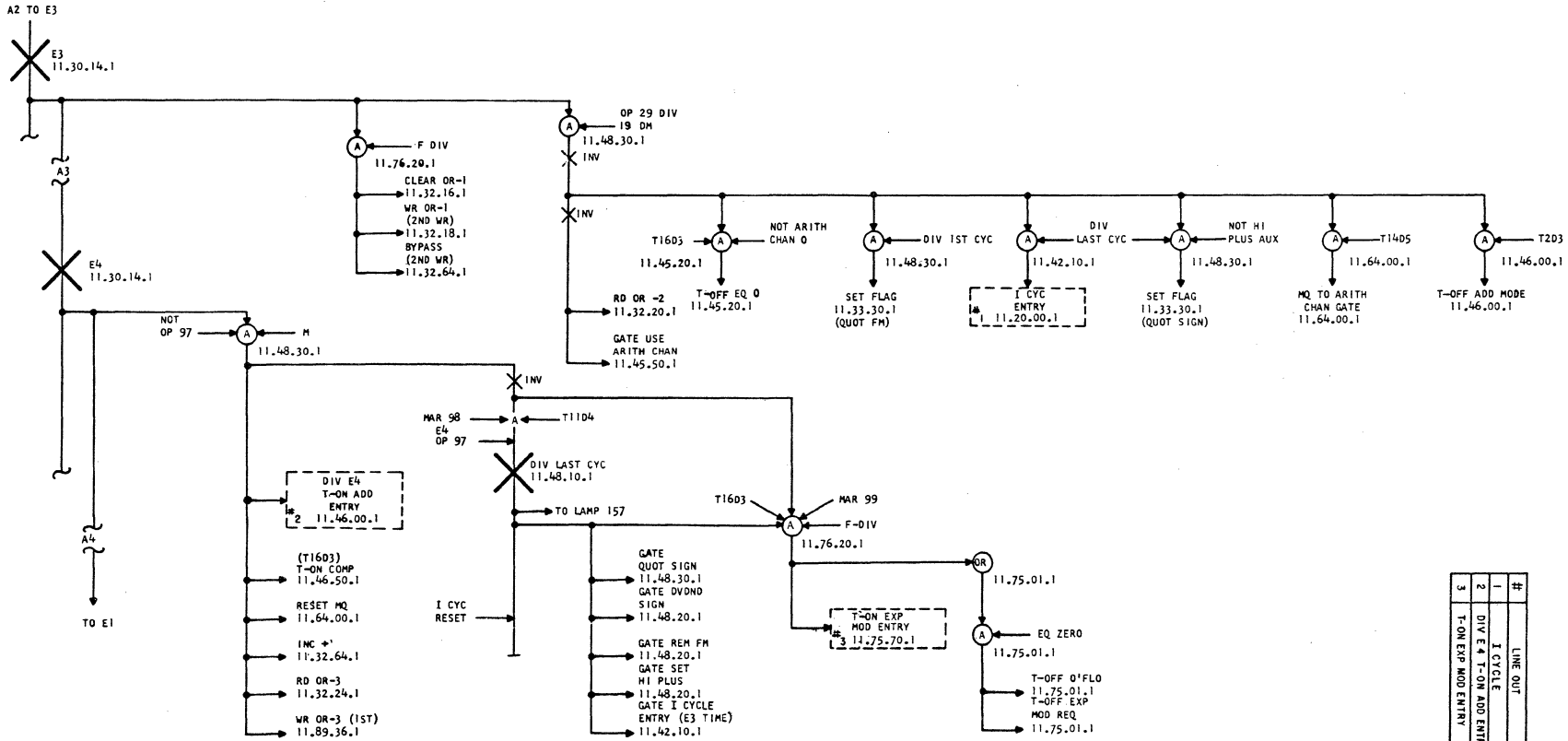
1620 II

10.00.60.1

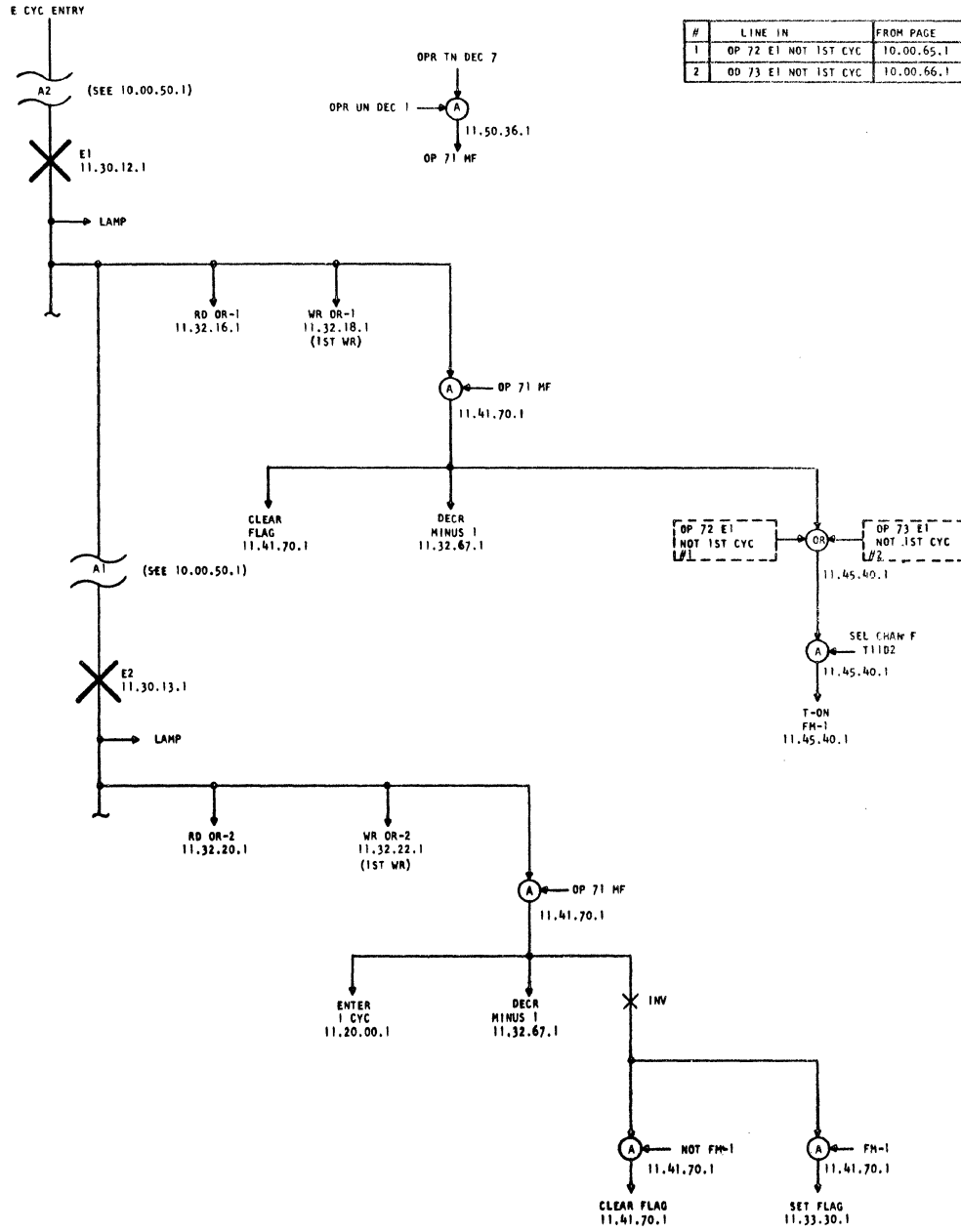
#	LINE IN	FROM PAGE
1	OP 28 LD 18 LDM XMIT END	10.00.54.1
2	LD 1ST E2 FL-1	11.00.54.1



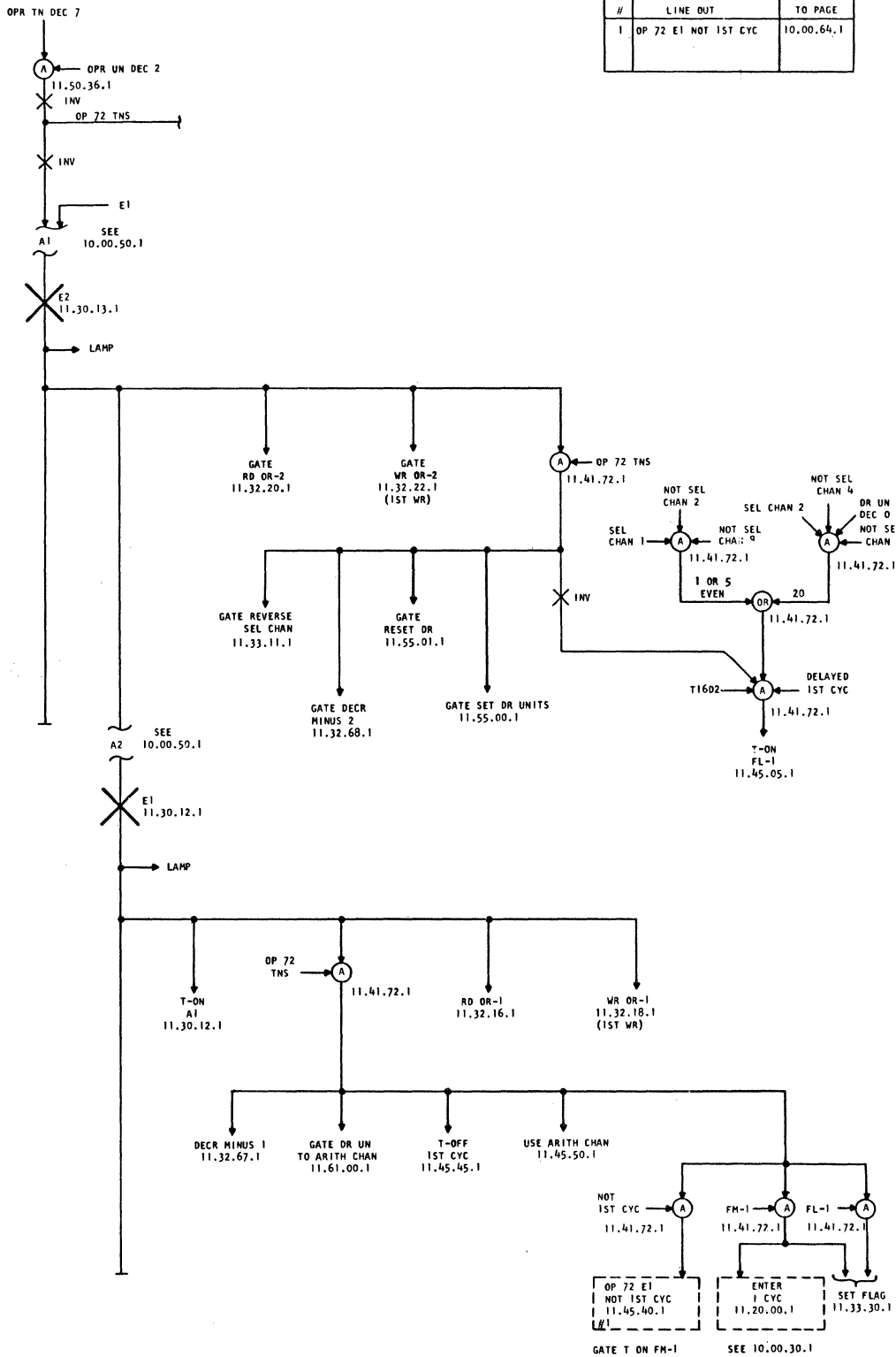


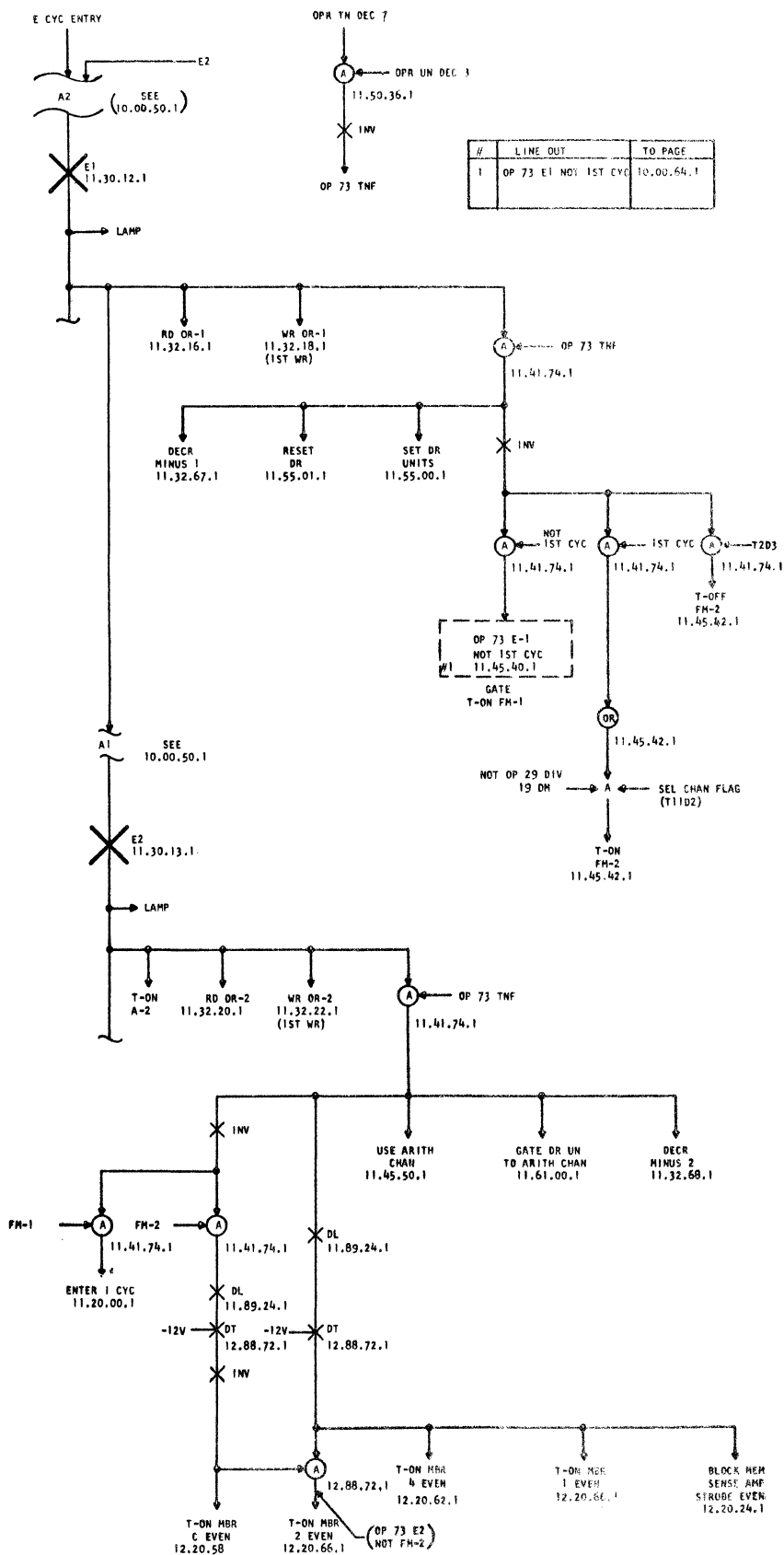


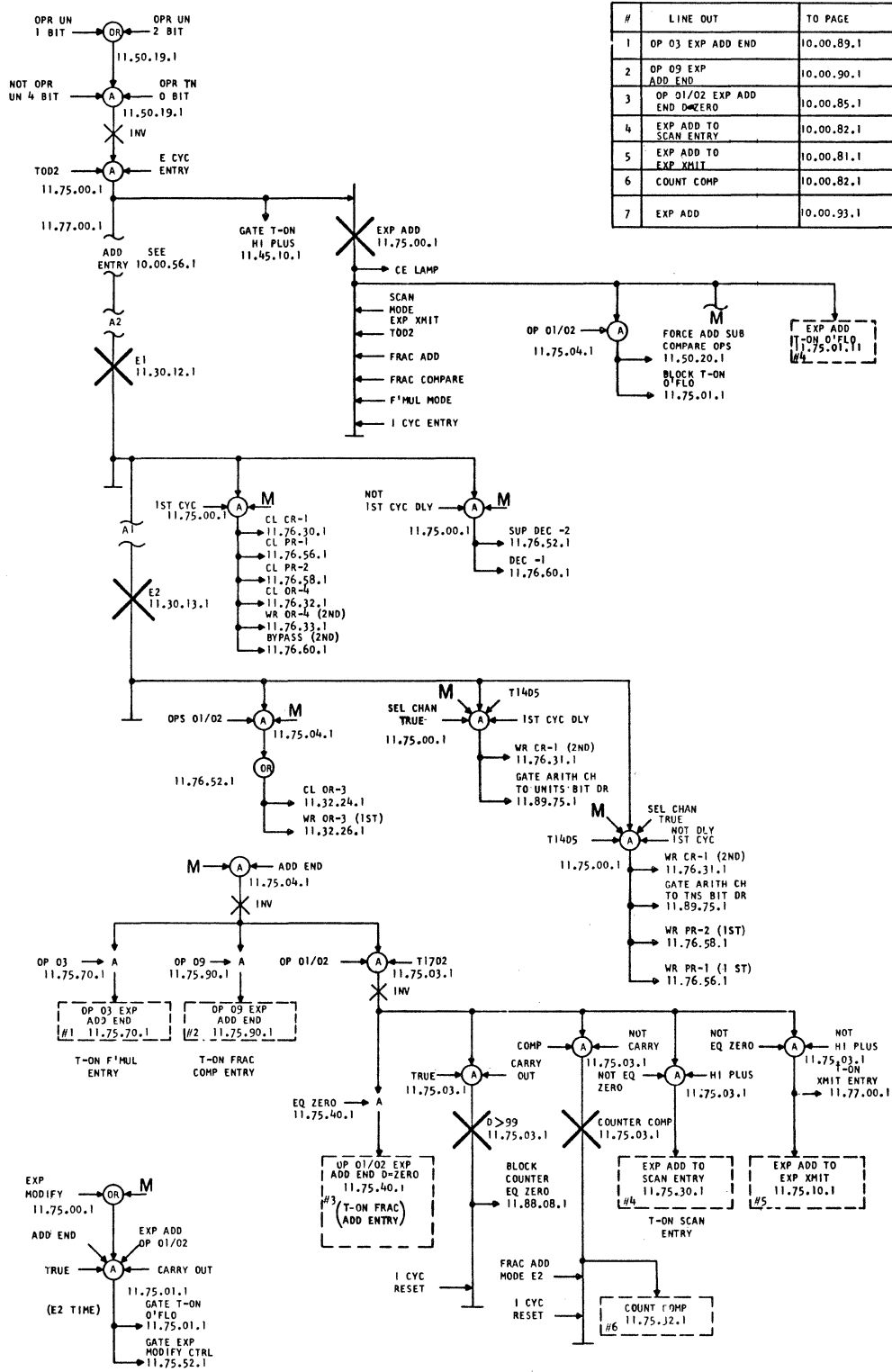
#	LINE OUT	PAGE TO
1	I CYCLE	10.00.30.1
2	DIV E4 T-ON ADD ENTRY	10.00.46.1
3	T-ON EXP MOD ENTRY	10.00.86.1

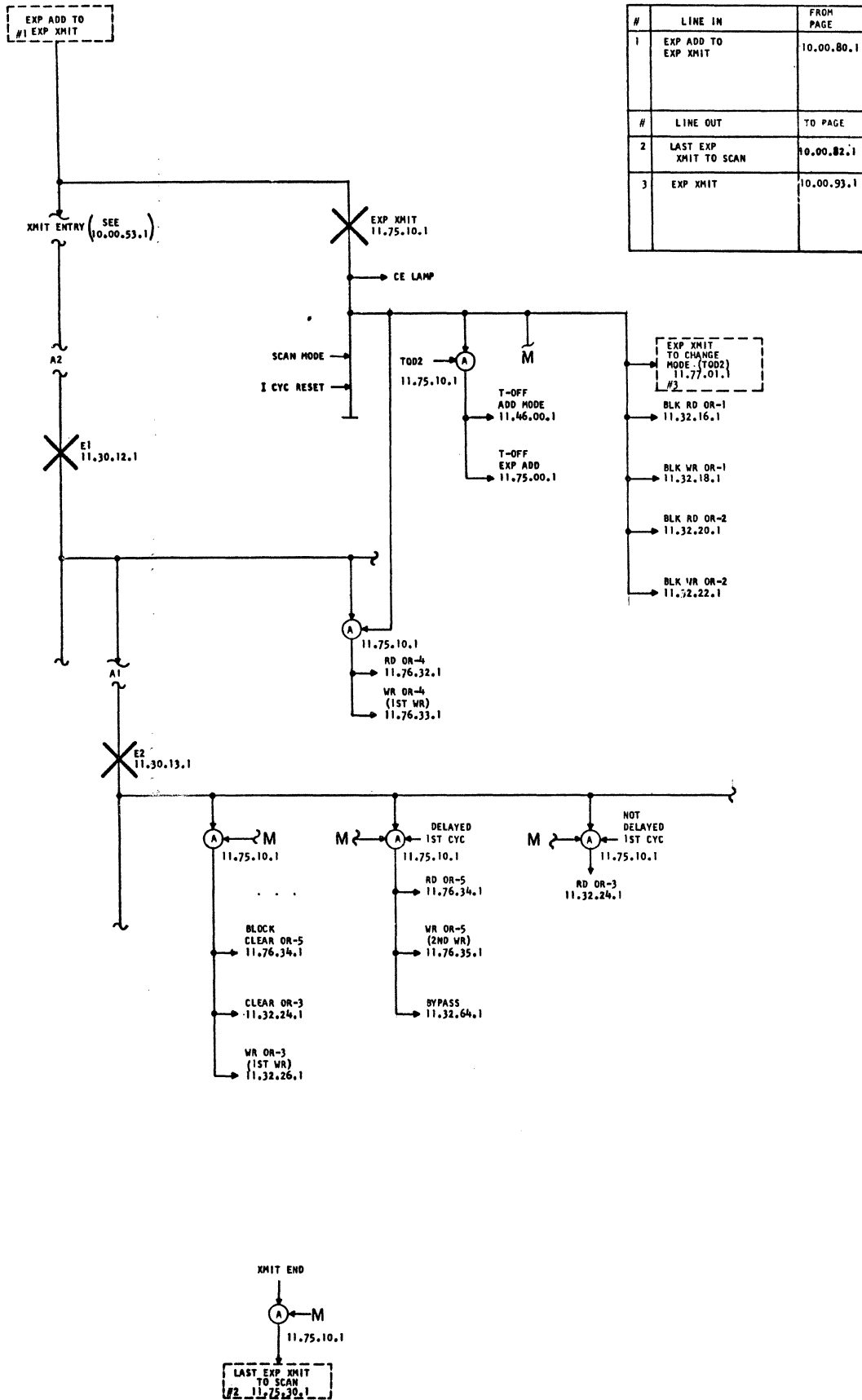


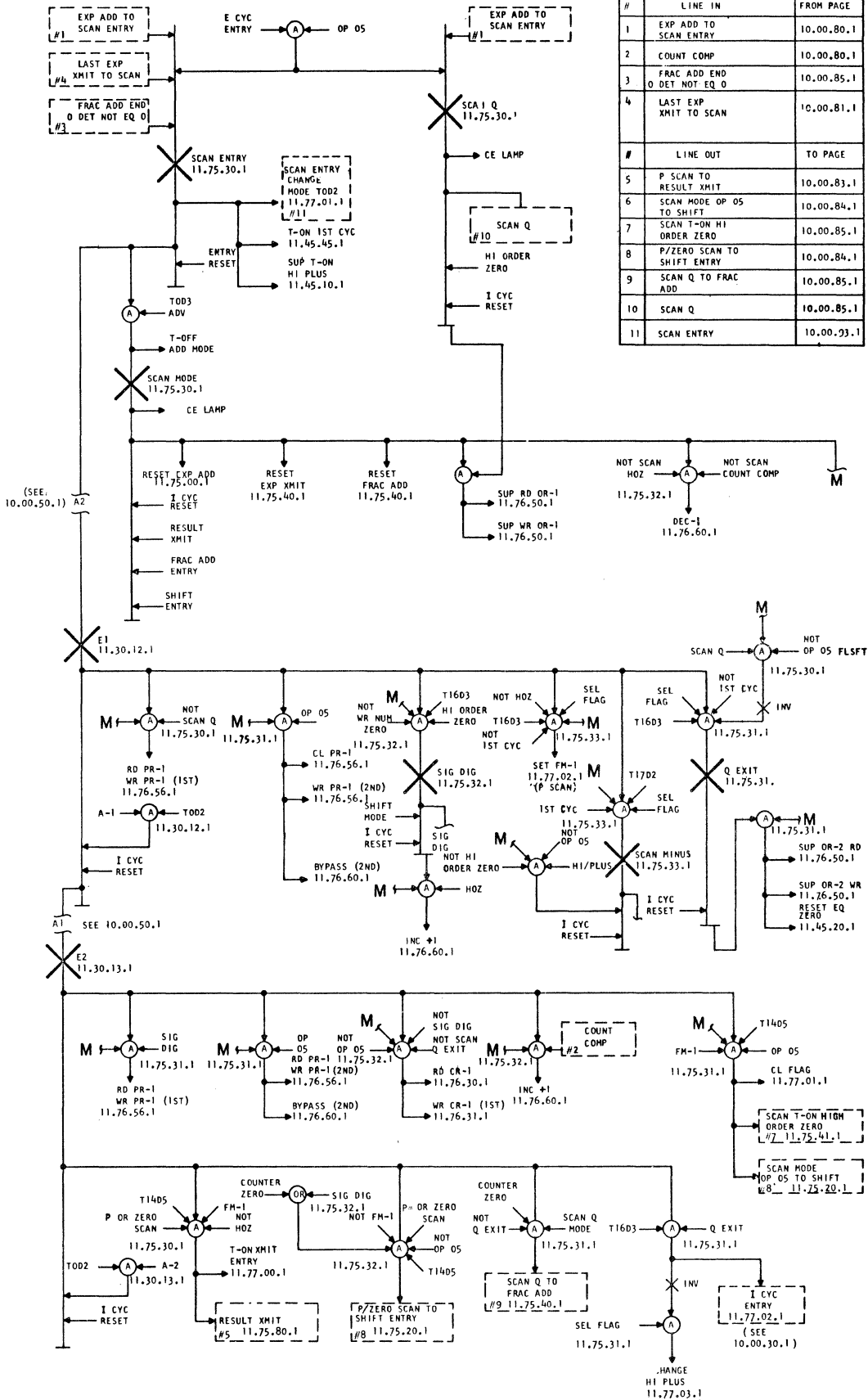
#	LINE OUT	TO PAGE
1	OP 72 E1 NOT 1ST CYC	10.00.64.1





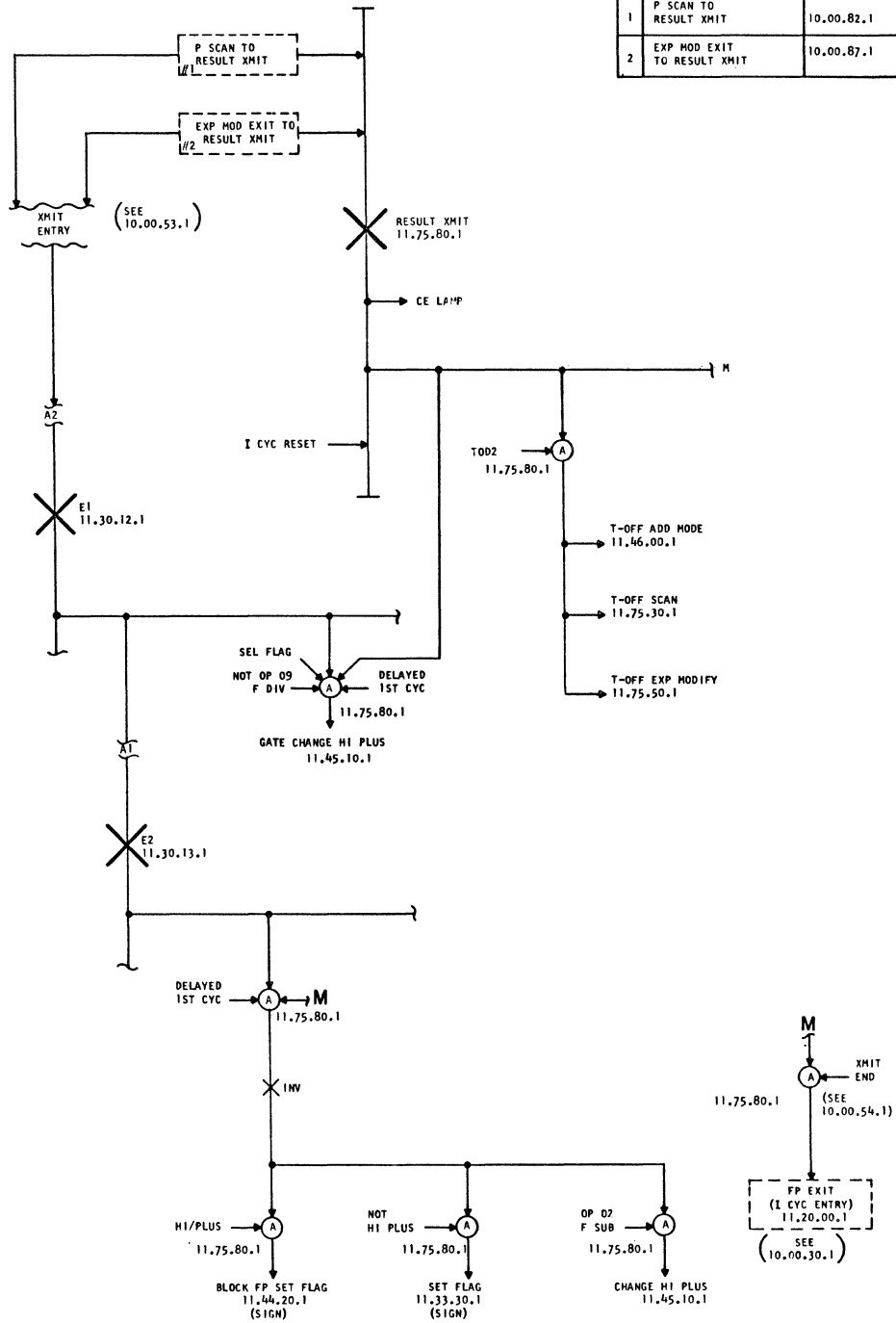


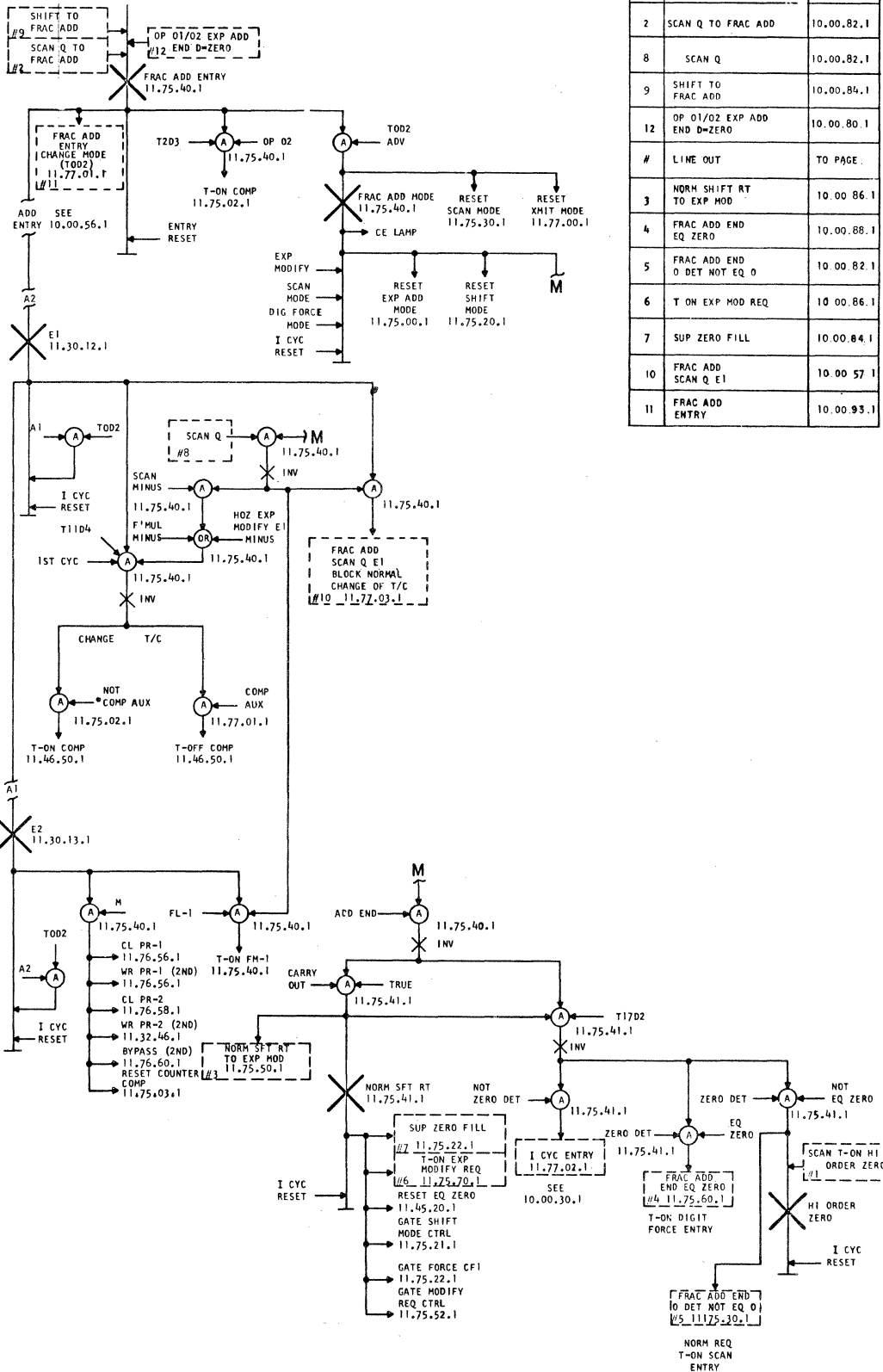




#	LINE IN	FROM PAGE
1	EXP ADD TO SCAN ENTRY	10.00.80.1
2	COUNT COMP	10.00.80.1
3	FRAC ADD END O DET NOT EQ O	10.00.85.1
4	LAST EXP XMIT TO SCAN	10.00.81.1
#	LINE OUT	TO PAGE
5	P SCAN TO RESULT XMIT	10.00.83.1
6	SCAN MODE OP 05 TO SHIFT	10.00.84.1
7	SCAN T-ON HI ORDER ZERO	10.00.85.1
8	P/ZERO SCAN TO SHIFT ENTRY	10.00.84.1
9	SCAN Q TO FRAC ADD	10.00.85.1
10	SCAN Q	10.00.85.1
11	SCAN ENTRY	10.00.93.1

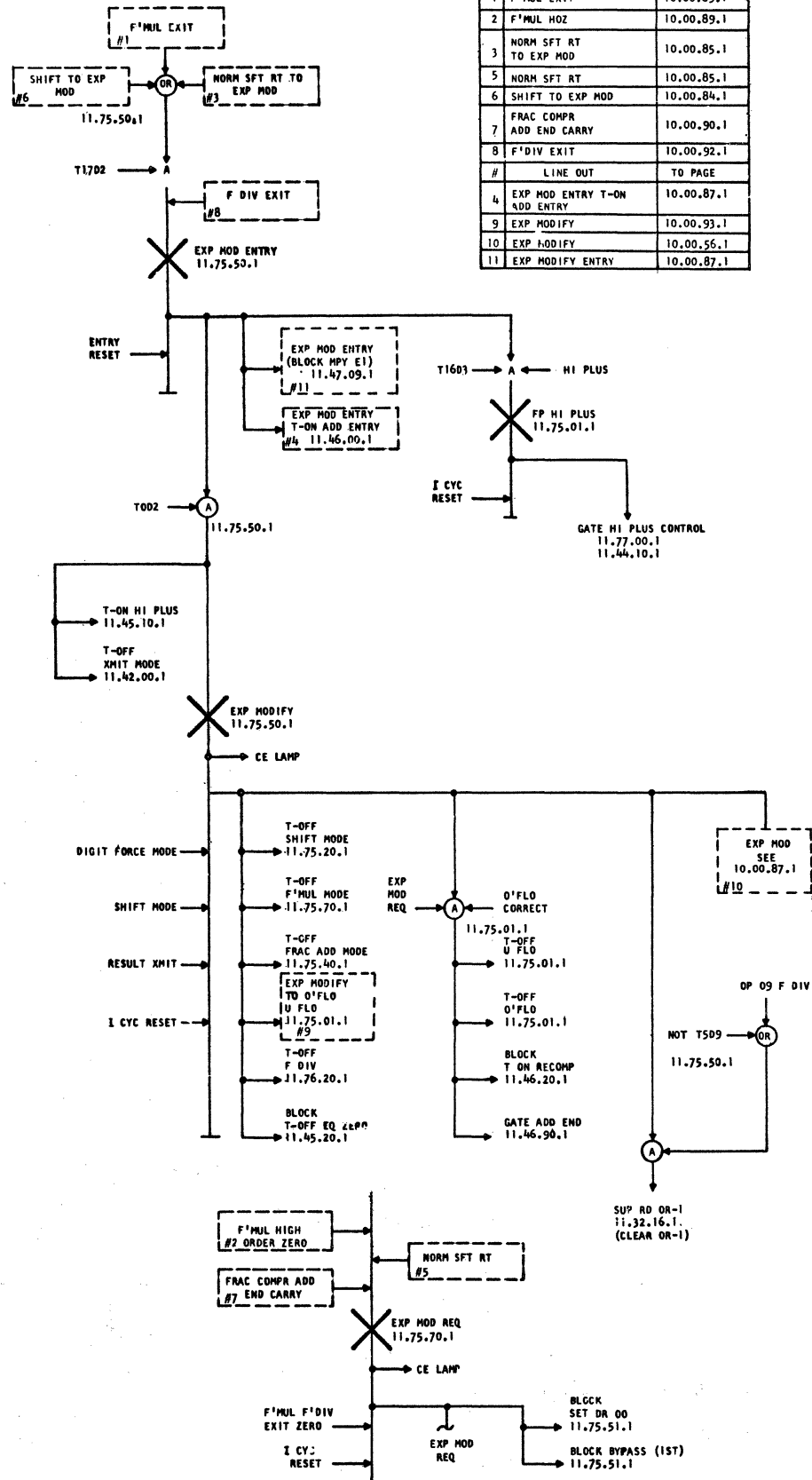
#	LINE IN	FROM PAGE
1	P SCAN TO RESULT XMIT	10.00.82.1
2	EXP MOD EXIT TO RESULT XMIT	10.00.87.1

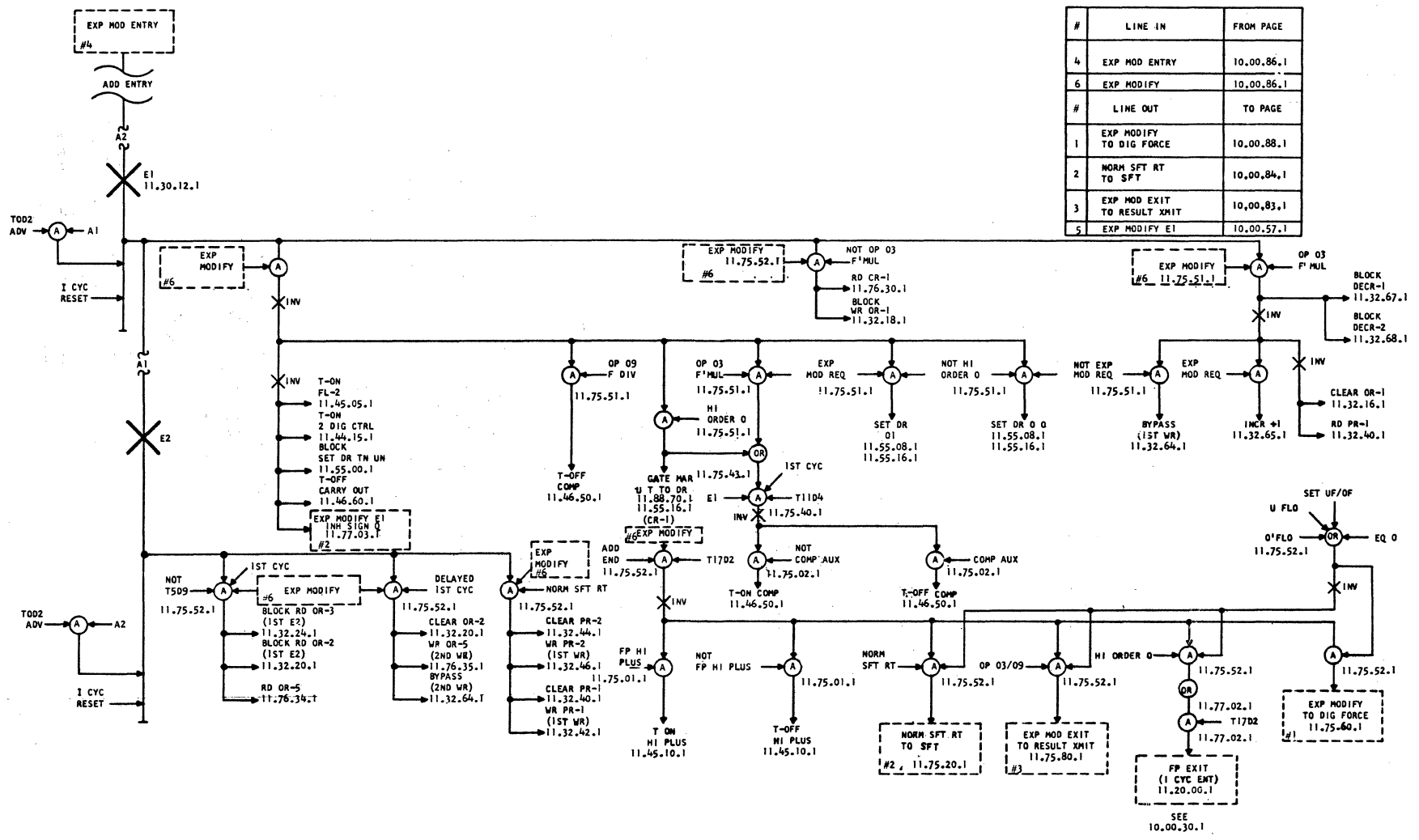




#	LINE IN	FROM PAGE
1	SCAN T ON HI ORDER ZERO	10.00.82.1
2	SCAN Q TO FRAC ADD	10.00.82.1
8	SCAN Q	10.00.82.1
9	SHIFT TO FRAC ADD	10.00.84.1
12	OP 01/02 EXP ADD END D=ZERO	10.00.80.1
#	LINE OUT	TO PAGE
3	NORM SHIFT RT TO EXP MOD	10.00.86.1
4	FRAC ADD END EQ ZERO	10.00.88.1
5	FRAC ADD END 0 DET NOT EQ 0	10.00.82.1
6	T ON EXP MOD REQ	10.00.86.1
7	SUP ZERO FILL	10.00.84.1
10	FRAC ADD SCAN Q E1	10.00.57.1
11	FRAC ADD ENTRY	10.00.93.1

#	LINE IN	FROM PAGE
1	F'MUL EXIT	10.00.89.1
2	F'MUL HDZ	10.00.89.1
3	NORM SFT RT TO EXP MOD	10.00.85.1
5	NORM SFT RT	10.00.85.1
6	SHIFT TO EXP MOD	10.00.84.1
7	FRAC COMPR ADD END CARRY	10.00.90.1
8	F'DIV EXIT	10.00.92.1
#	LINE OUT	TO PAGE
4	EXP MOD ENTRY T-ON ADD ENTRY	10.00.87.1
9	EXP MODIFY	10.00.93.1
10	EXP MODIFY	10.00.56.1
11	EXP MODIFY ENTRY	10.00.87.1





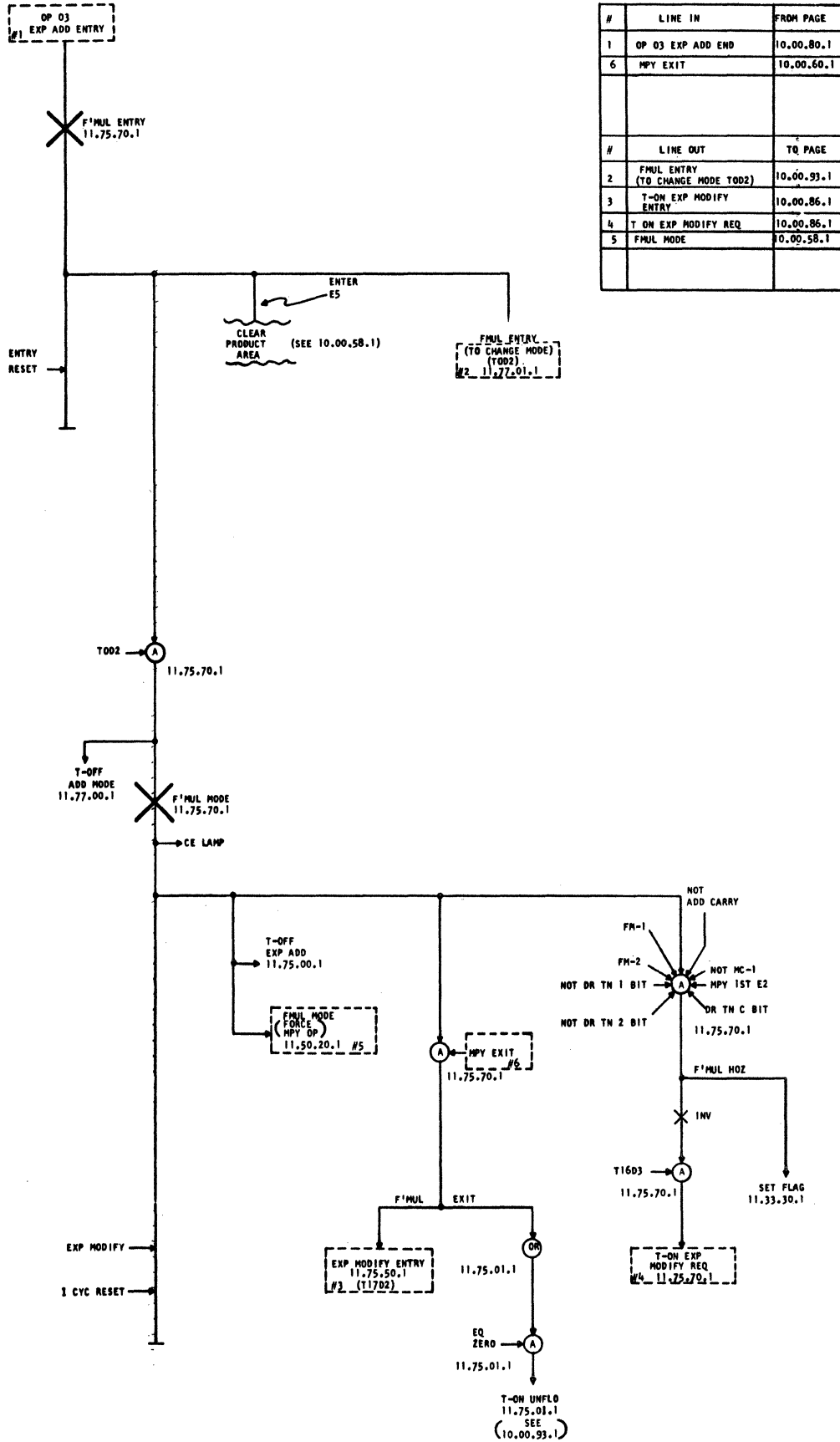
#	LINE IN	FROM PAGE
4	EXP MOD ENTRY	10,00,86.1
6	EXP MODIFY	10,00,86.1
#	LINE OUT	TO PAGE
1	EXP MODIFY TO DIG FORCE	10,00,88.1
2	NORM SFT RT TO SFT	10,00,84.1
3	EXP MOD EXIT TO RESULT XMIT	10,00,83.1
5	EXP MODIFY EI	10,00,57.1

2159140

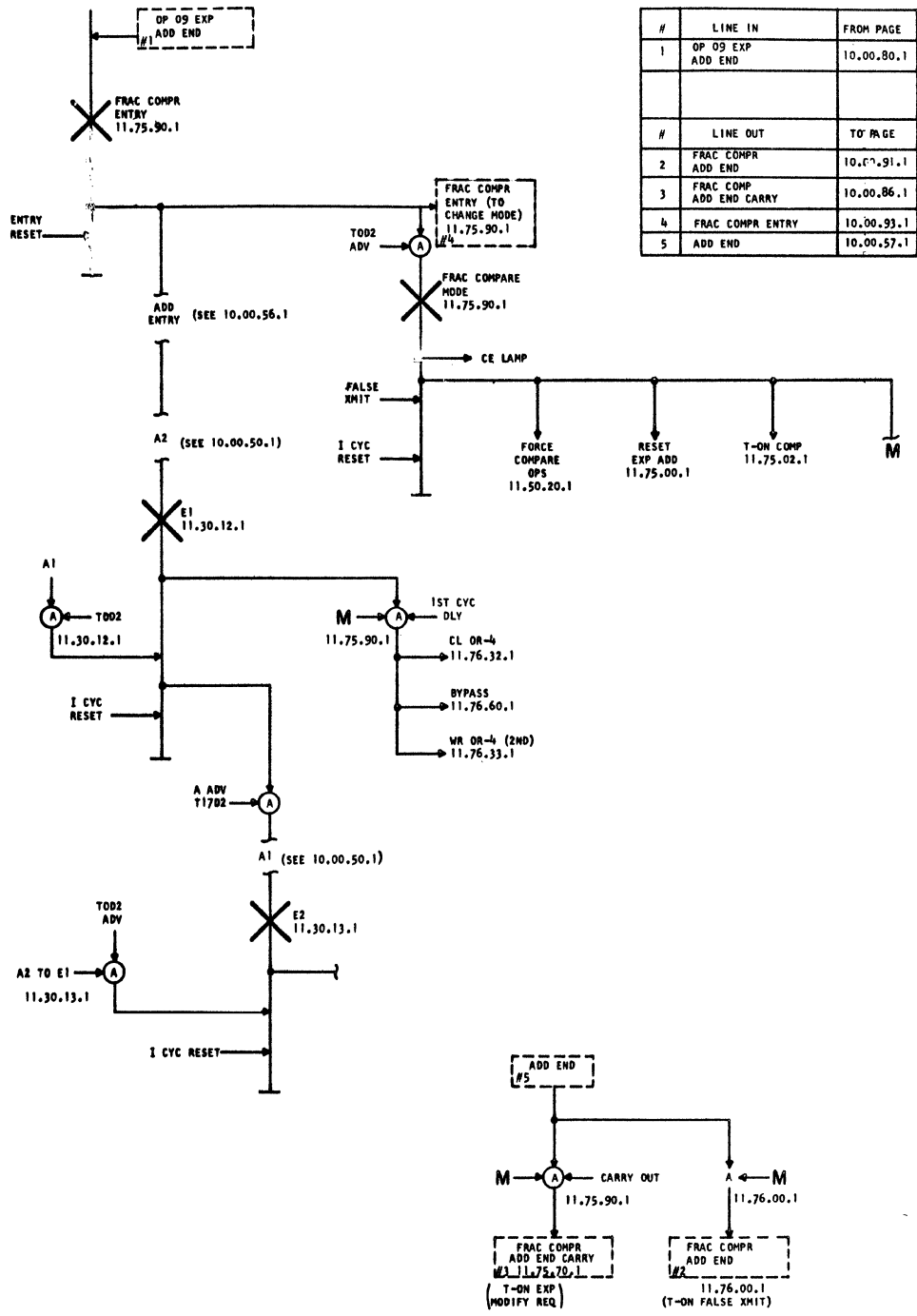
F'MUL

1620 II

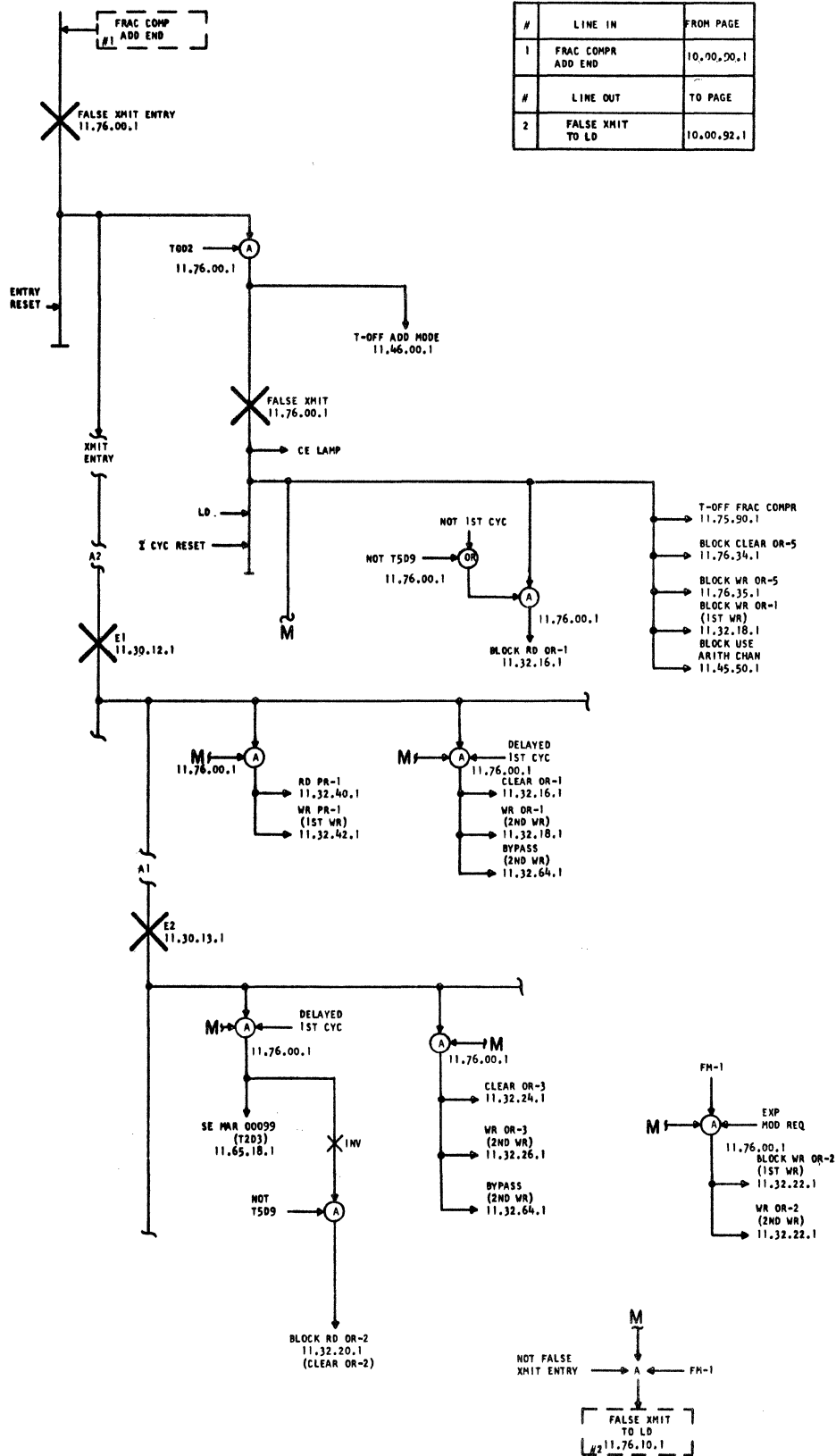
10.00.89.1

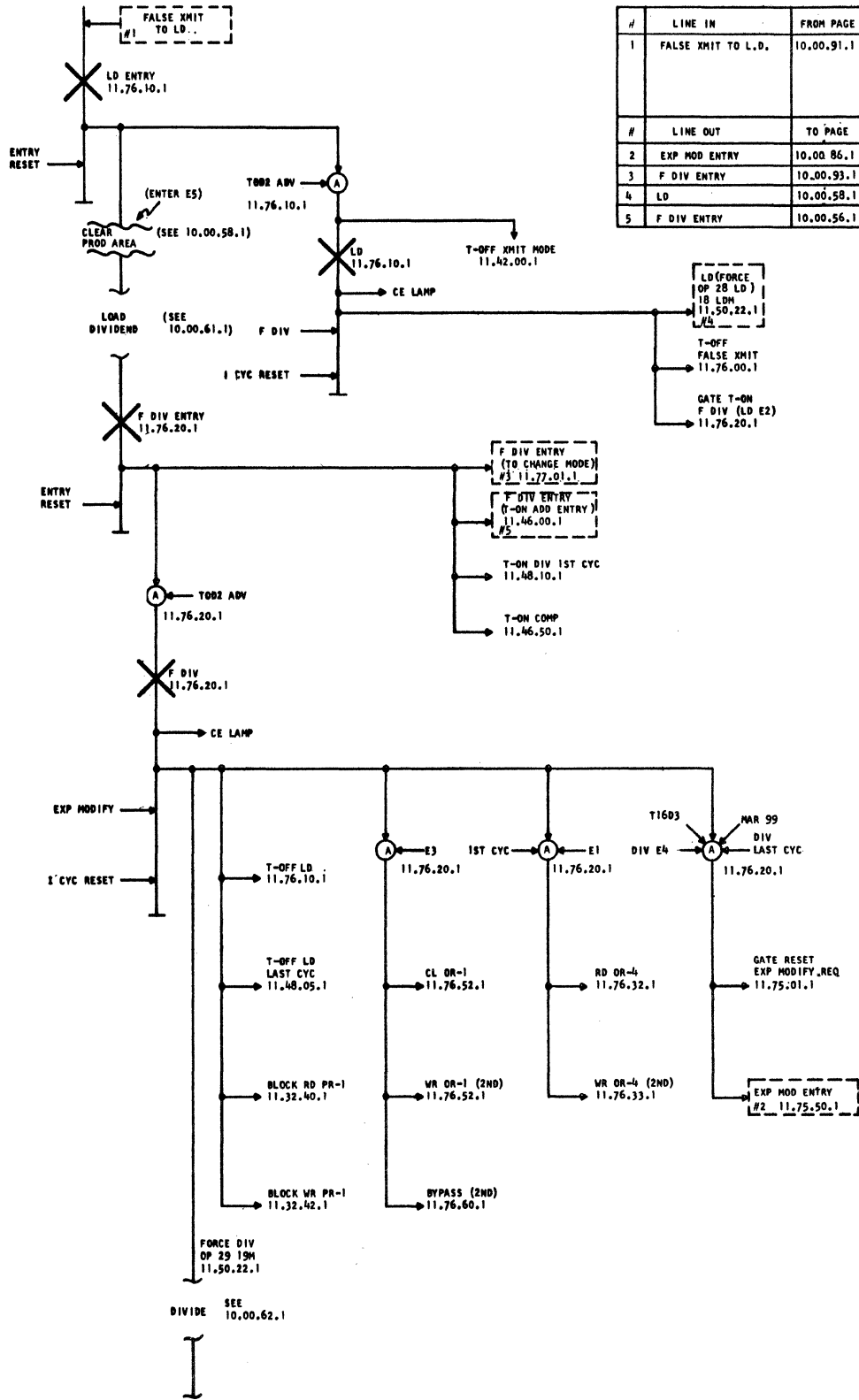


#	LINE IN	FROM PAGE
1	OP 03 EXP ADD END	10.00.80.1
6	MPY EXIT	10.00.60.1
#	LINE OUT	TO PAGE
2	F'MUL ENTRY (TO CHANGE MODE TOD2)	10.00.93.1
3	T-ON EXP MODIFY ENTRY	10.00.86.1
4	T ON EXP MODIFY REQ	10.00.86.1
5	F'MUL MODE	10.00.58.1

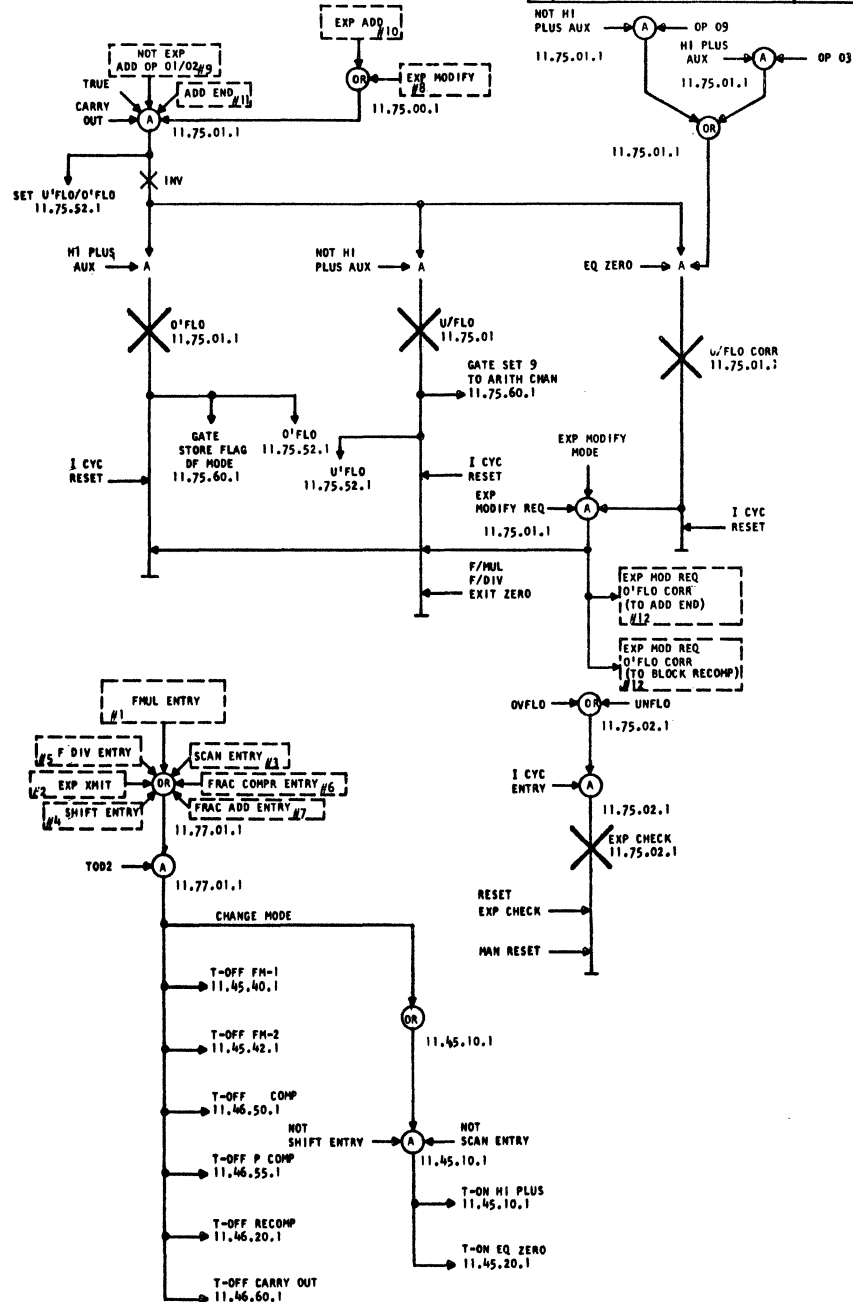


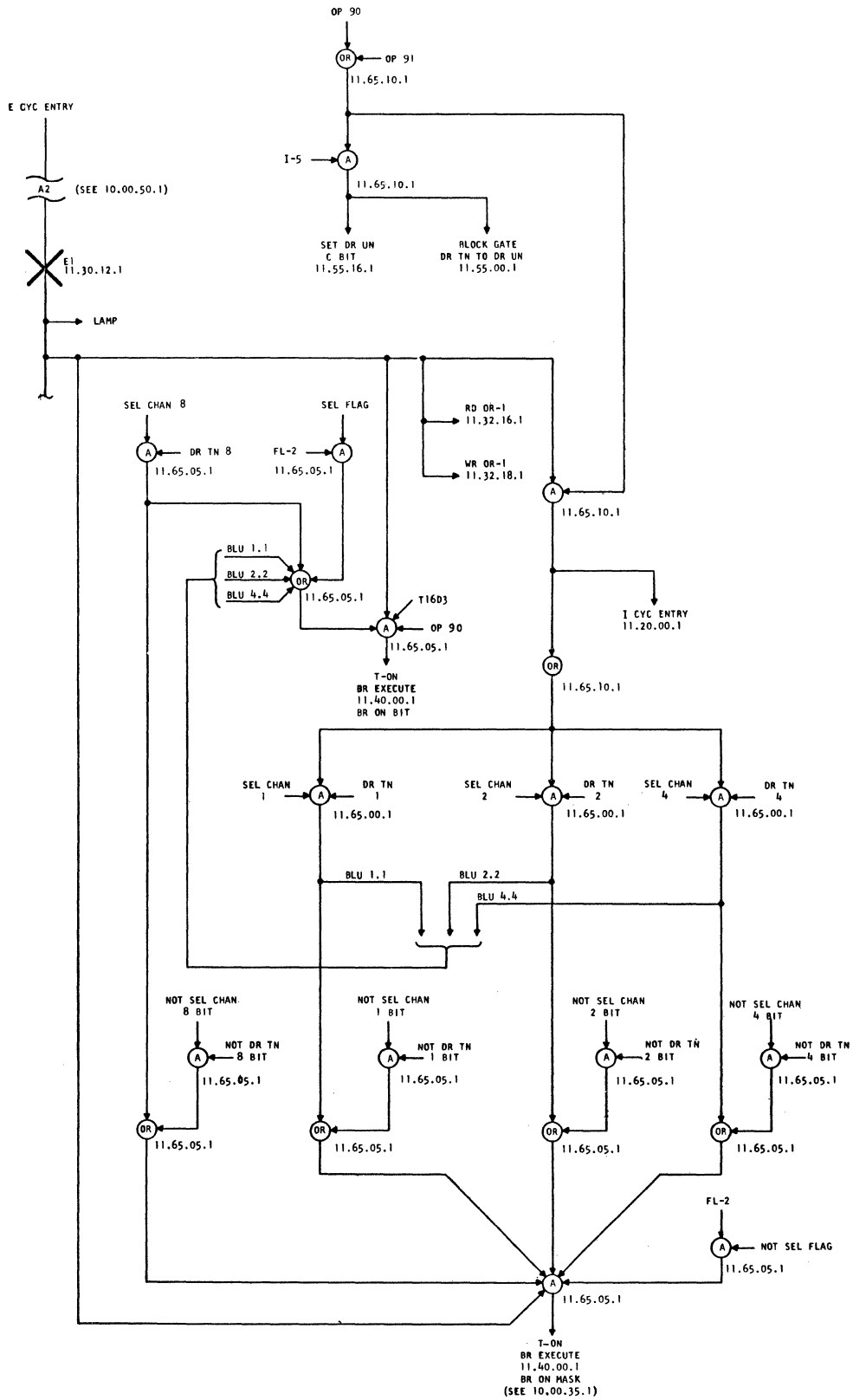
#	LINE IN	FROM PAGE
1	OP 09 EXP ADD END	10.00.80.1
#	LINE OUT	TO PAGE
2	FRAC COMPR ADD END	10.00.91.1
3	FRAC COMPR ADD END CARRY	10.00.86.1
4	FRAC COMPR ENTRY	10.00.93.1
5	ADD END	10.00.57.1

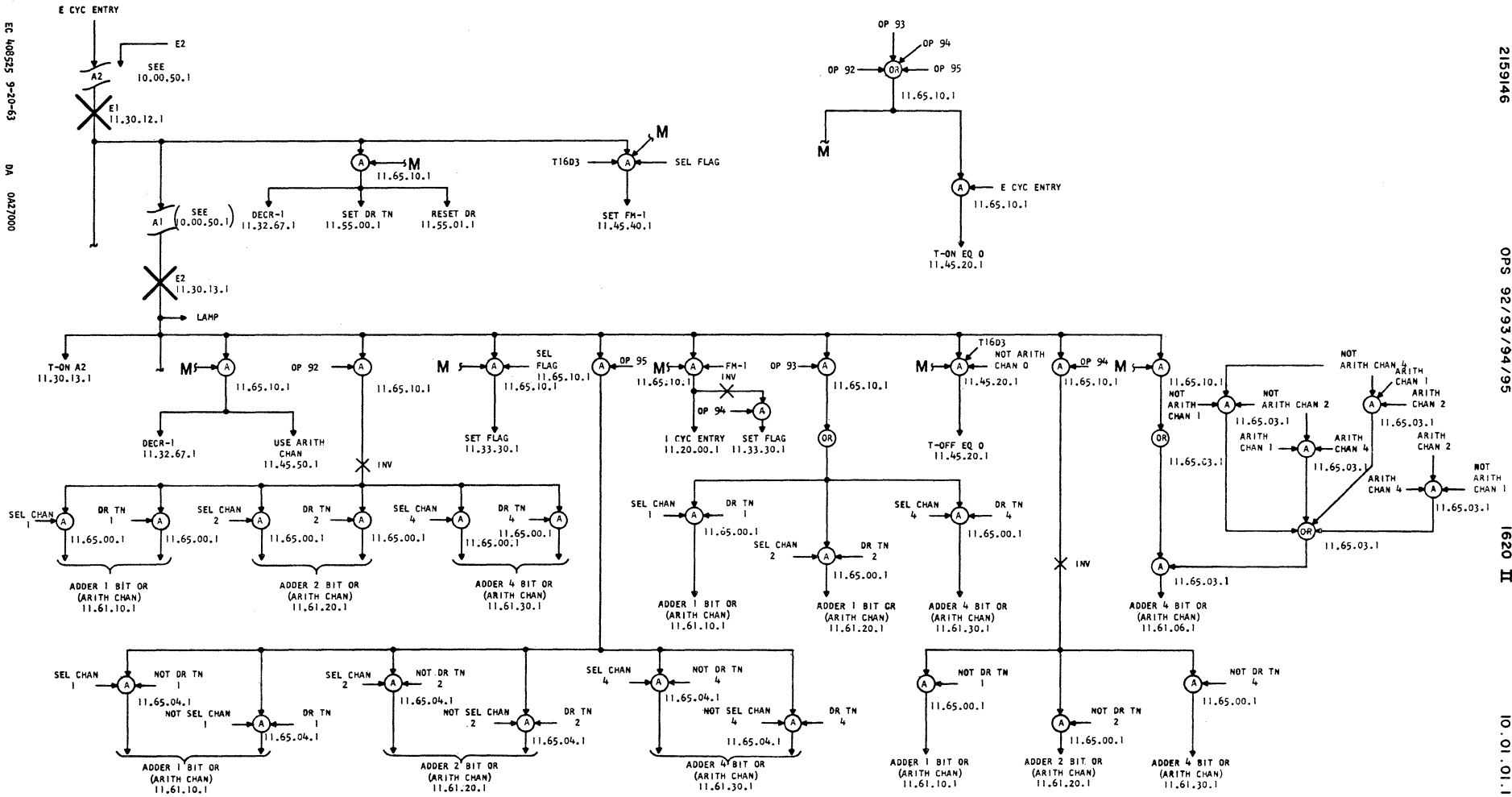


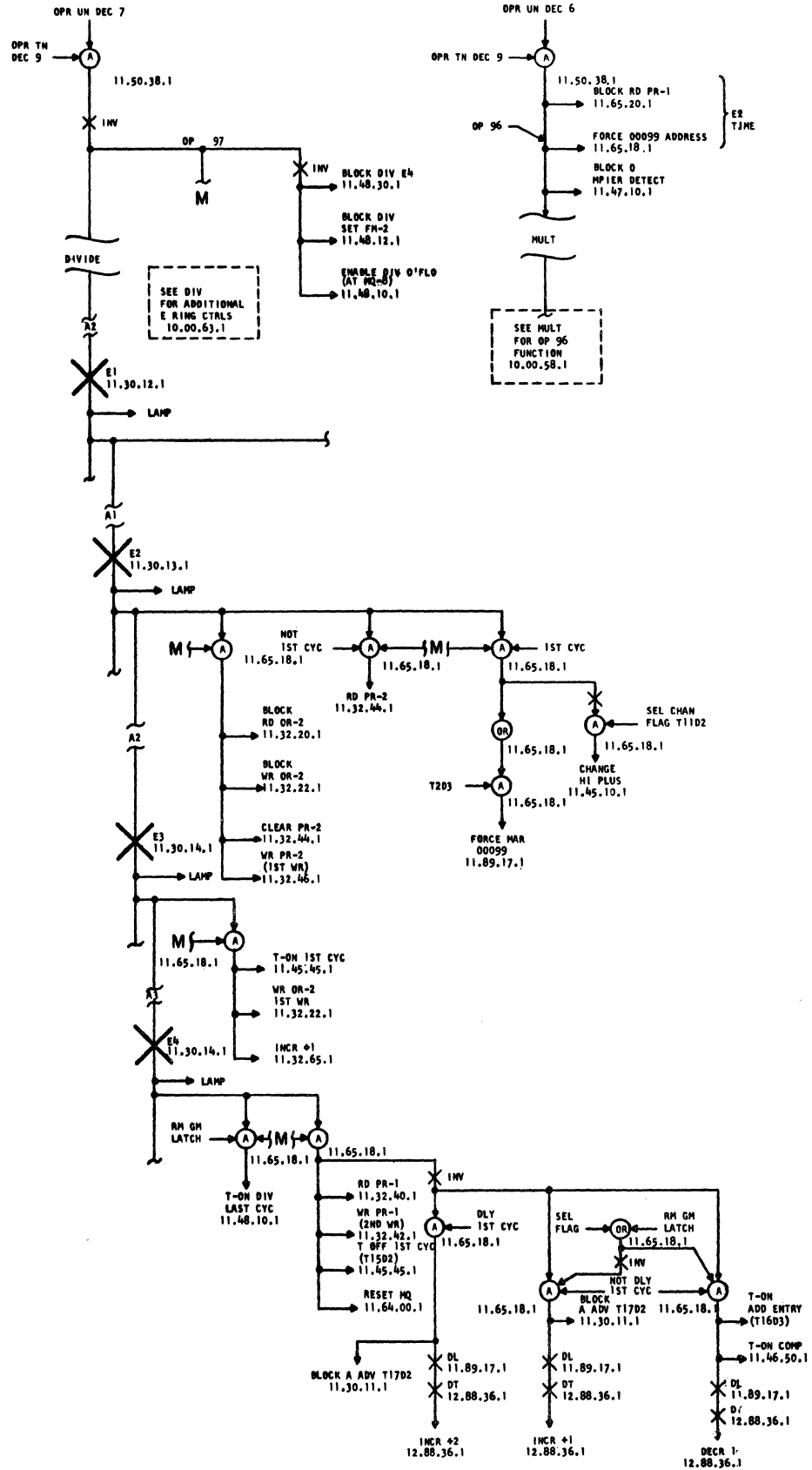


#	LINE IN	FROM PAGE
1	FMUL ENTRY	10.00.89.1
2	EXP XMIT	10.00.81.1
3	SCAN ENTRY	10.00.82.1
4	SHIFT ENTRY	10.00.84.1
5	F DIV ENTRY	10.00.92.1
6	FRAC COMPR ENTRY	10.00.90.1
7	FRAC ADD ENTRY	10.00.85.1
8	EXP MODIFY	10.00.86.1
9	EXP ADD OP 01/02	10.00.80.1
10	EXP ADD	10.00.80.1
11	ADD END	10.00.57.1
#	LINE OUT	TO PAGE
12	EXP MOD REQ O'FLO CORR	10.00.57.1

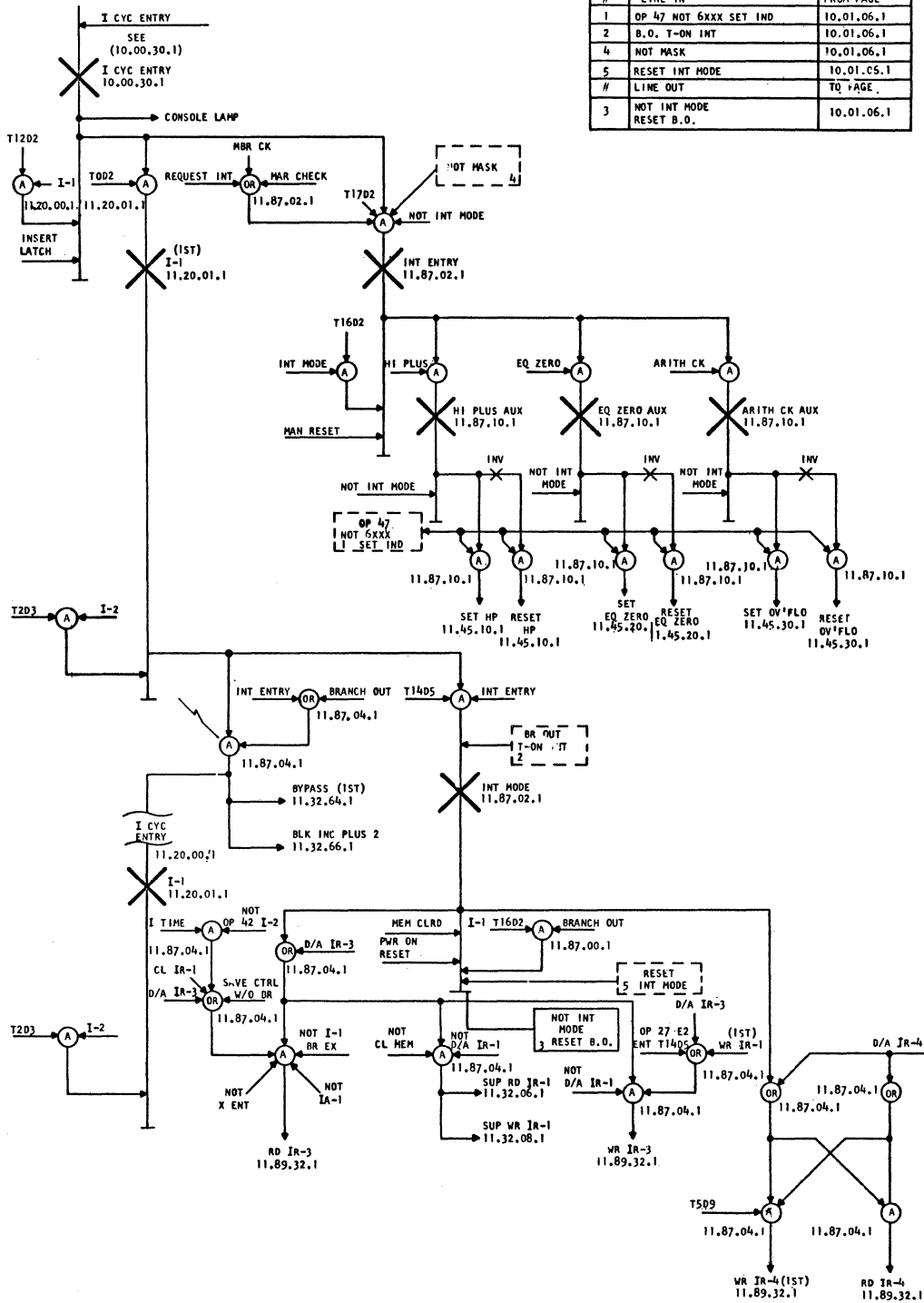


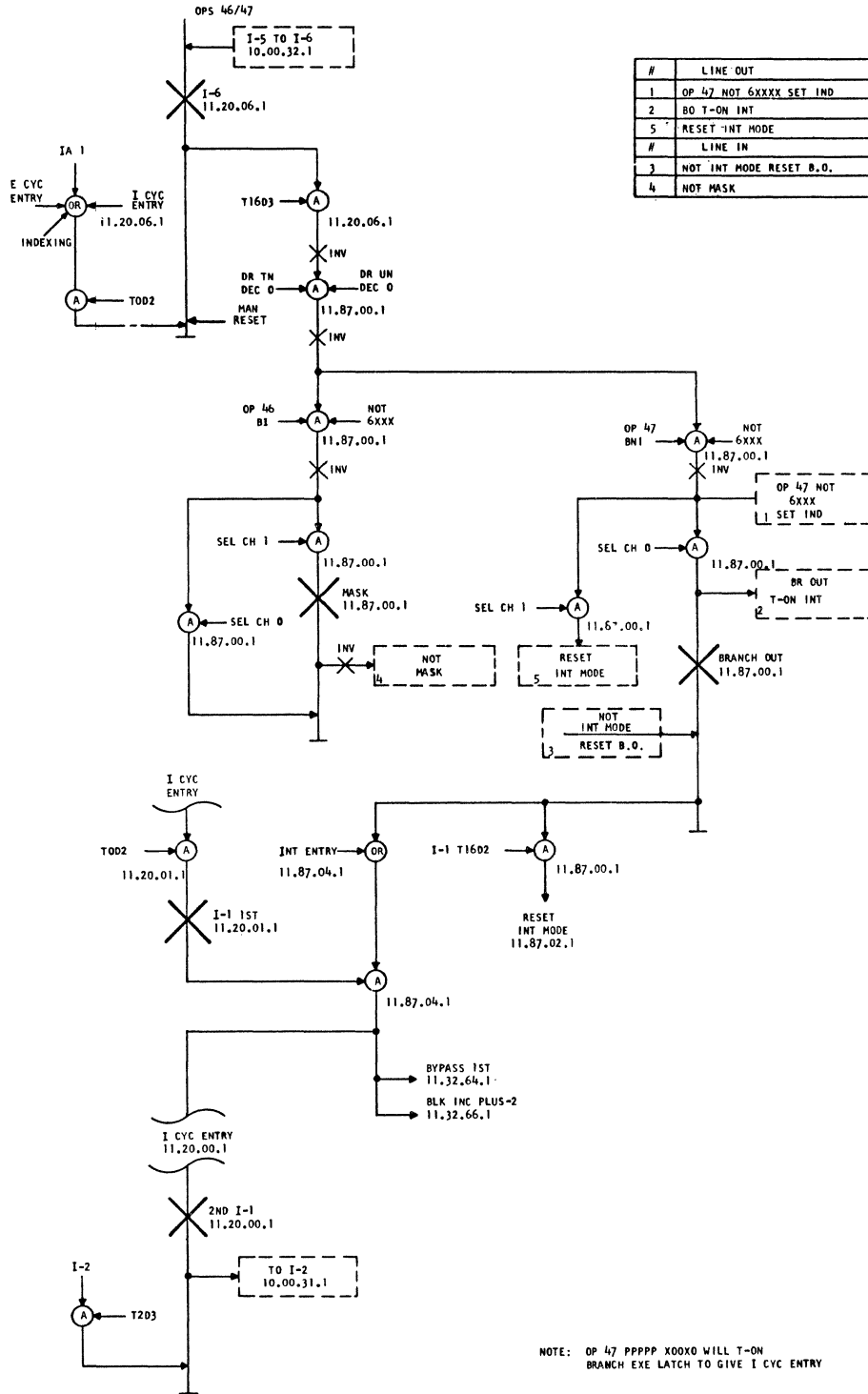






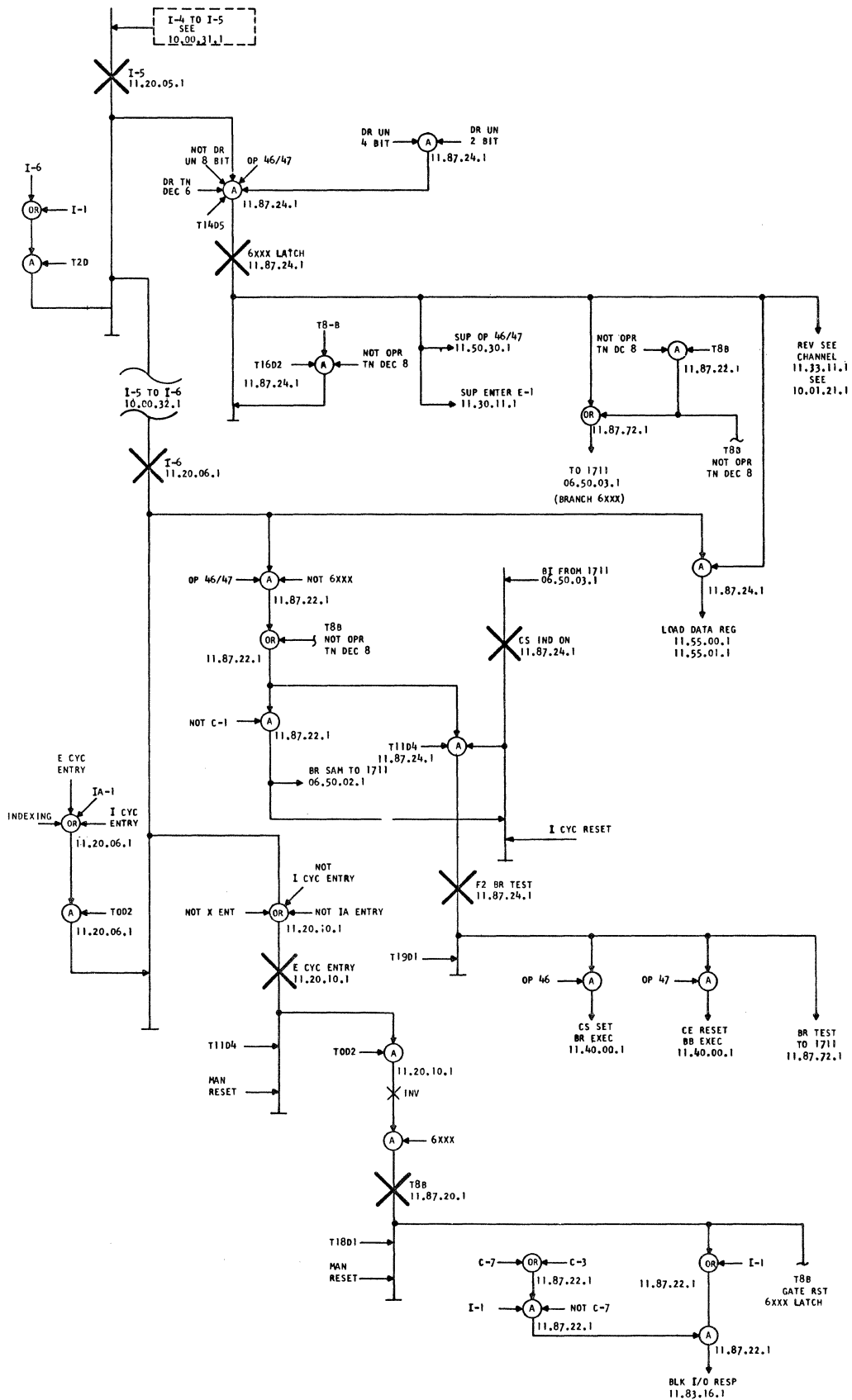
#	LINE IN	FROM PAGE
1	OP 47 NOT 6XXX SET IND	10.01.06.1
2	B.O. T-ON INT	10.01.06.1
4	NOT MASK	10.01.06.1
5	RESET INT MODE	10.01.05.1
#	LINE OUT	TO PAGE
3	NOT INT MODE RESET B.O.	10.01.06.1

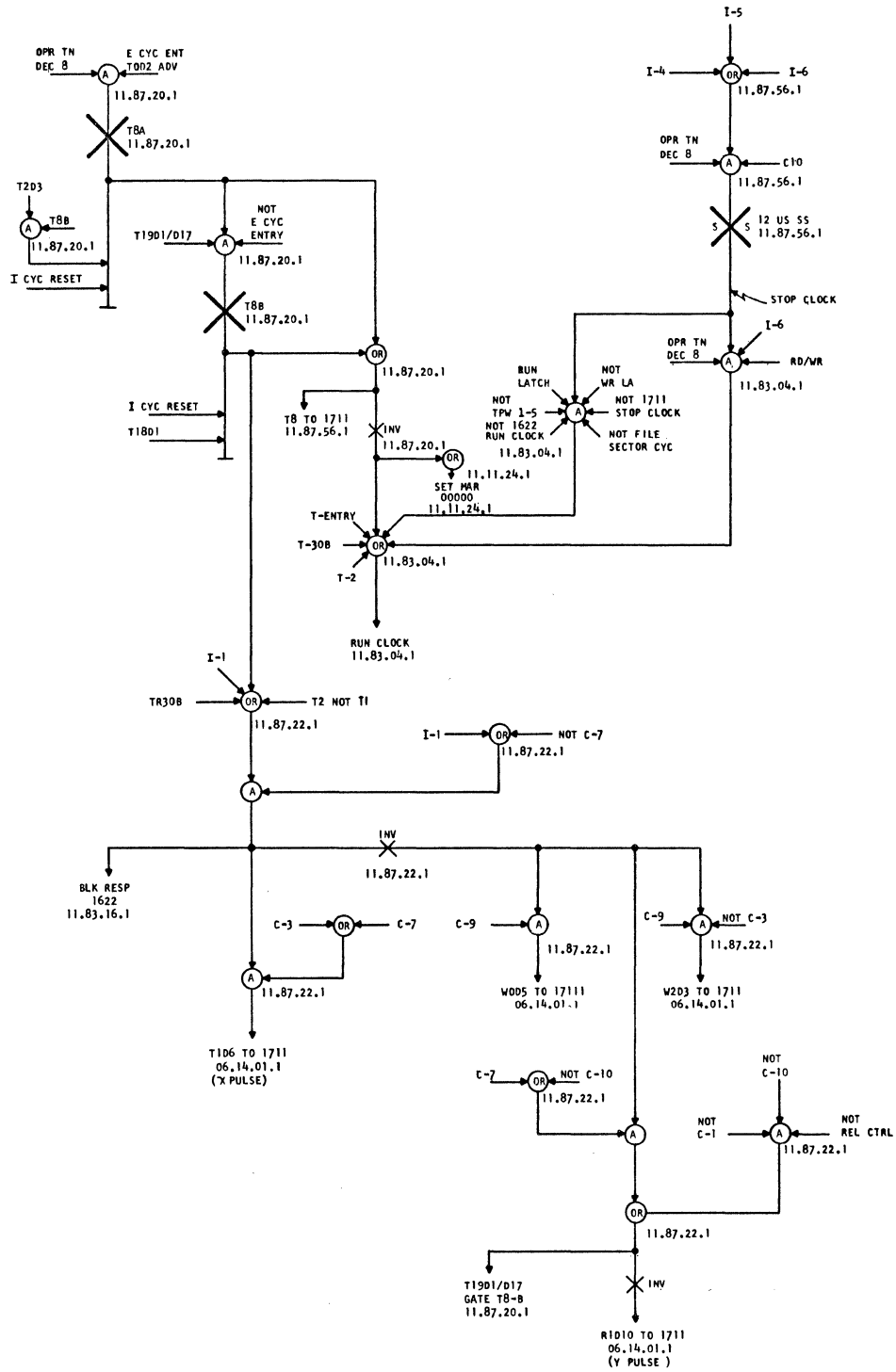


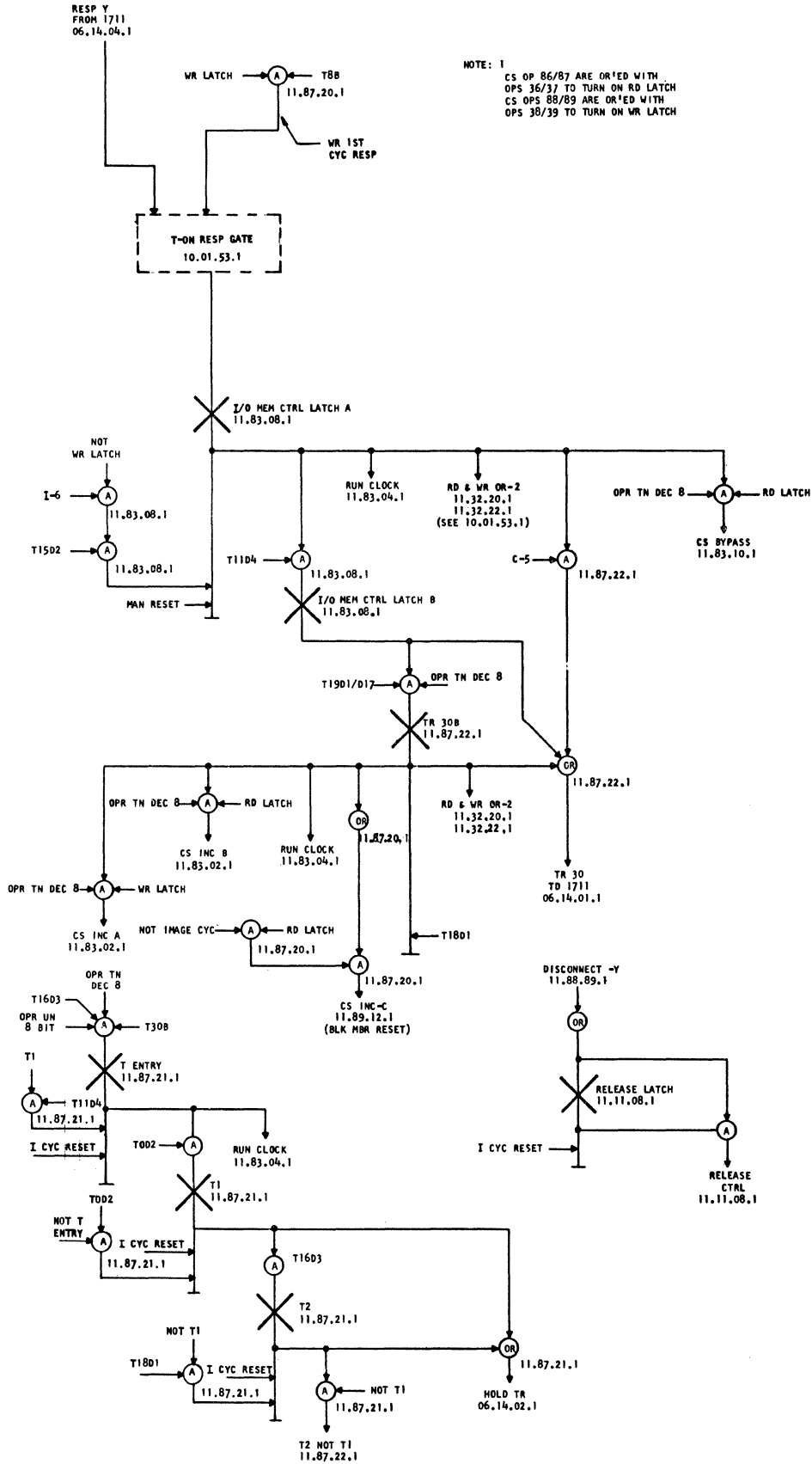


#	LINE OUT	TO PAGE
1	OP 47 NOT 6XXX SET IND	10.01.05.1
2	BO T-ON INT	10.01.05.1
5	RESET INT MODE	10.01.05.1
#	LINE IN	FROM PAGE
3	NOT INT MODE RESET B.O.	10.01.05.1
4	NOT MASK	10.01.05.1

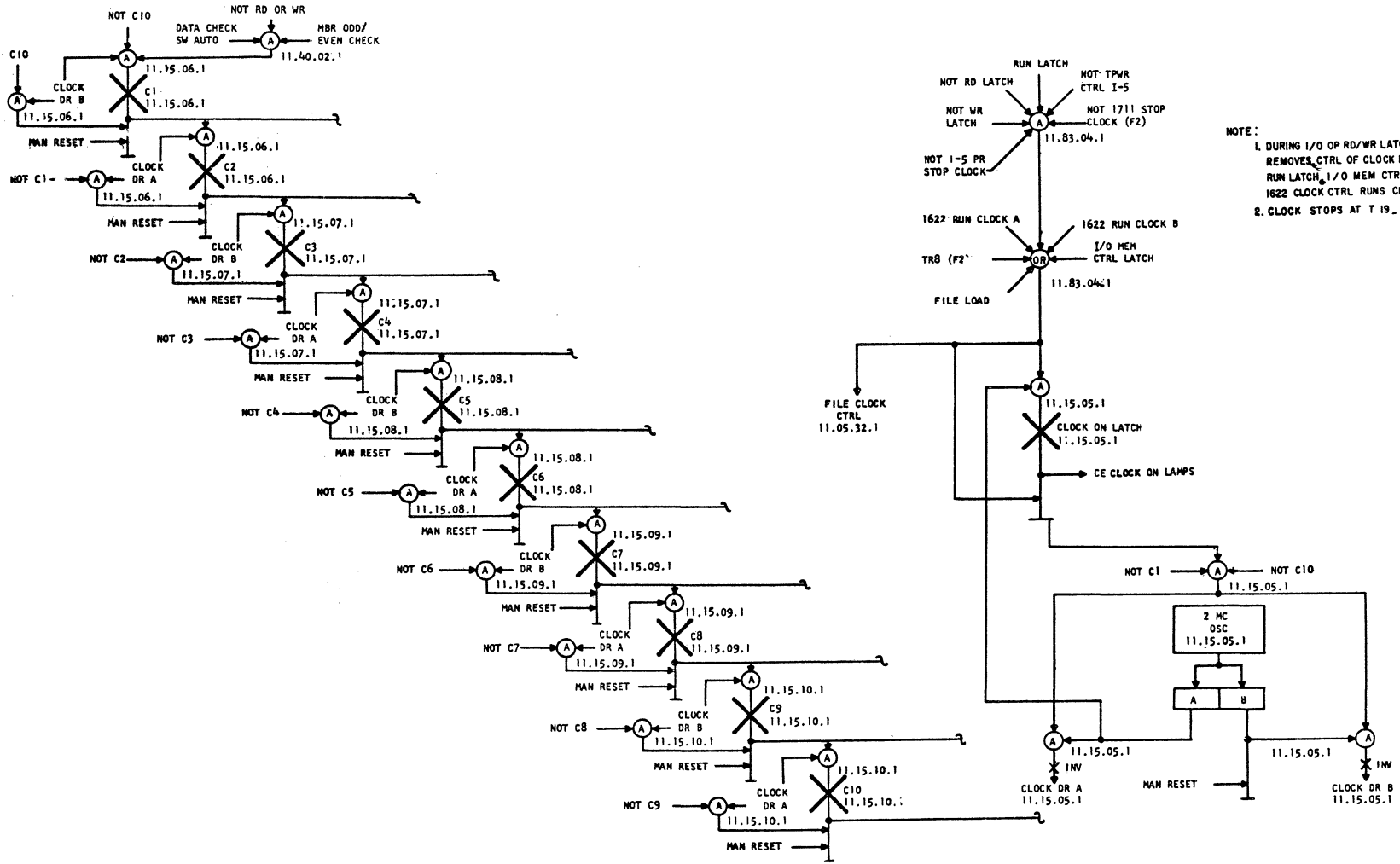
NOTE: OP 47 P P P P X O O X O WILL T-ON BRANCH EXE LATCH TO GIVE I CYC ENTRY



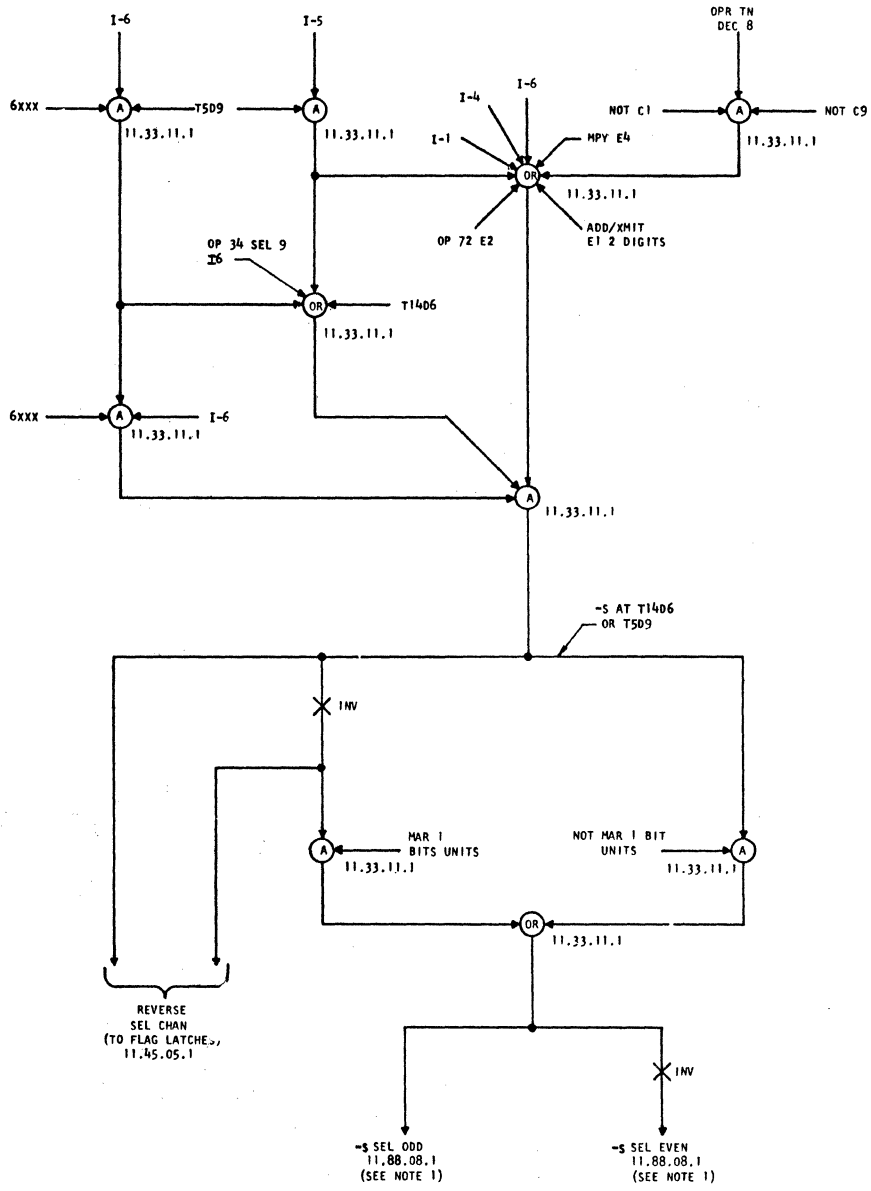




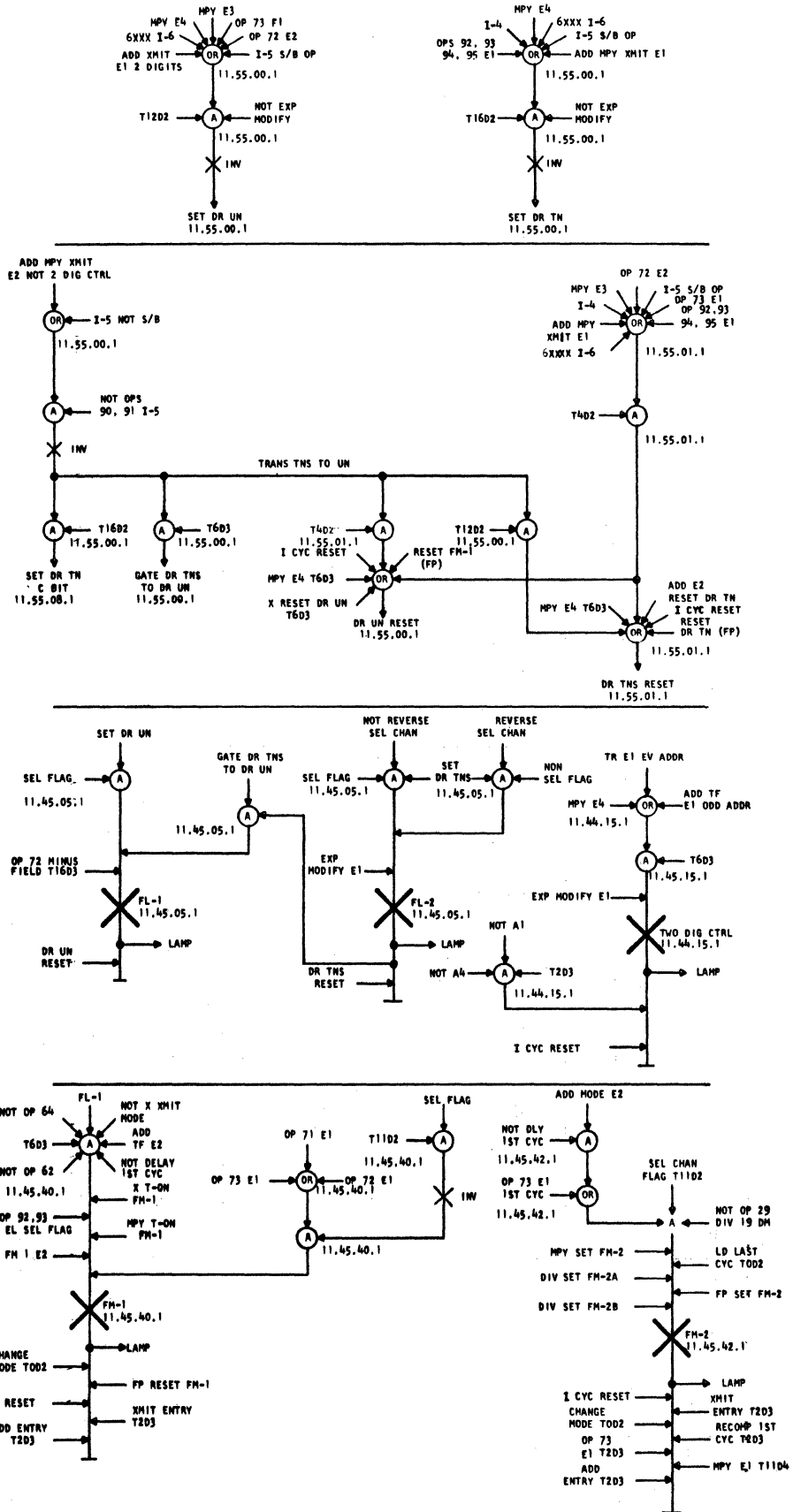
NOTE: 1
 CS OP 86/87 ARE OR'ED WITH
 OPS 36/37 TO TURN ON RD LATCH
 CS OPS 88/89 ARE OR'ED WITH
 OPS 38/39 TO TURN ON WR LATCH

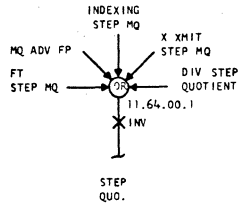


NOTE:
 1. DURING I/O OF RD/WR LATCHES REMOVES CTRL OF CLOCK FROM RUN LATCH. I/O MEM CTRL OR 1622 CLOCK CTRL RUNS CLOCK.
 2. CLOCK STOPS AT T 19.



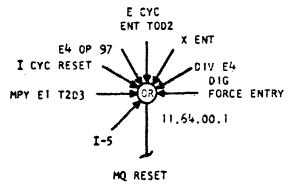
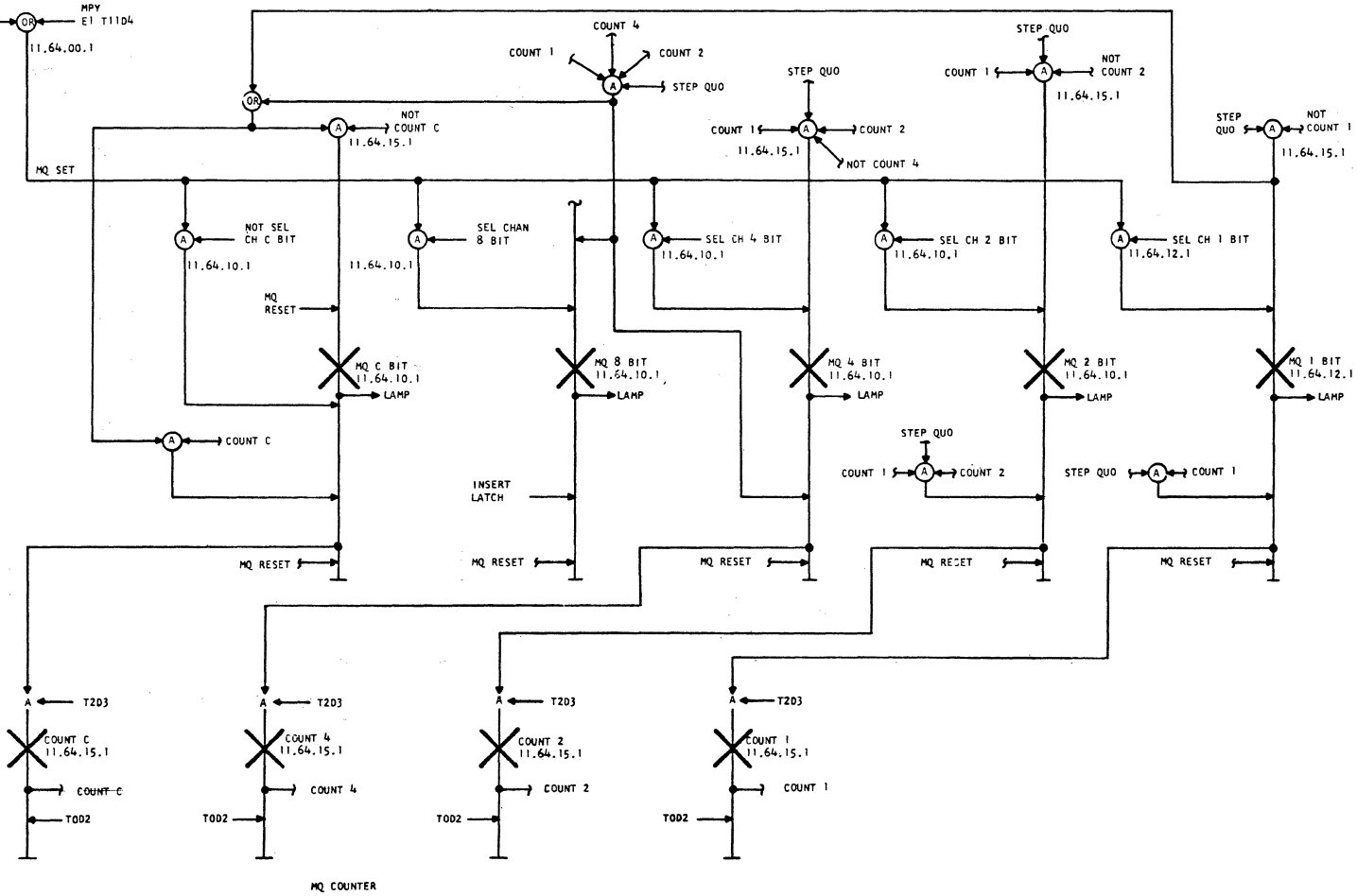
NOTE 1: SELECT FLAG IS ONLY REVERSED AT THE FLAG LATCHES. ALL OTHER SELECT FLAG LINES ARE CONTROLLED BY THE MAR ADDRESS SELECTION.

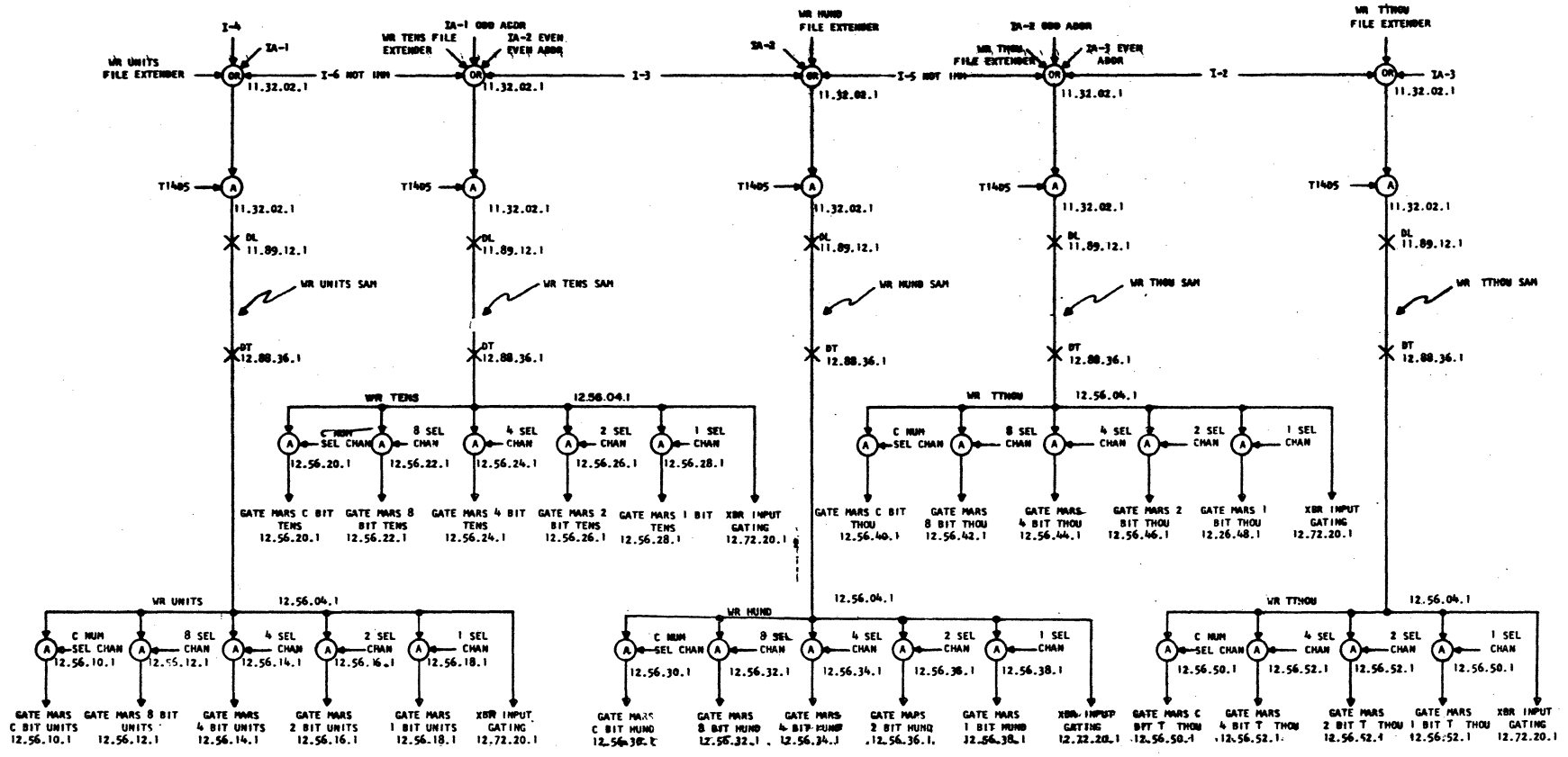


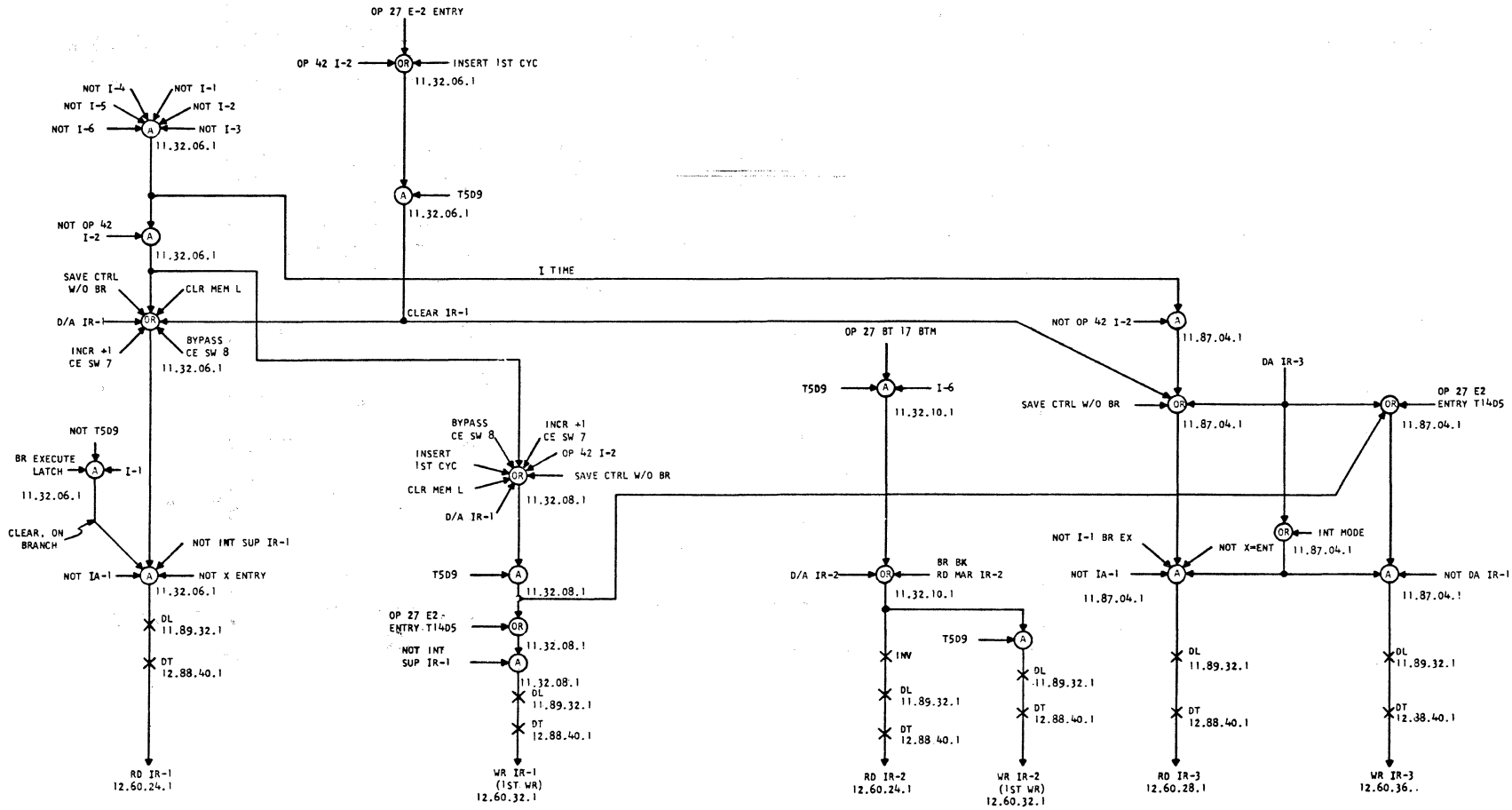


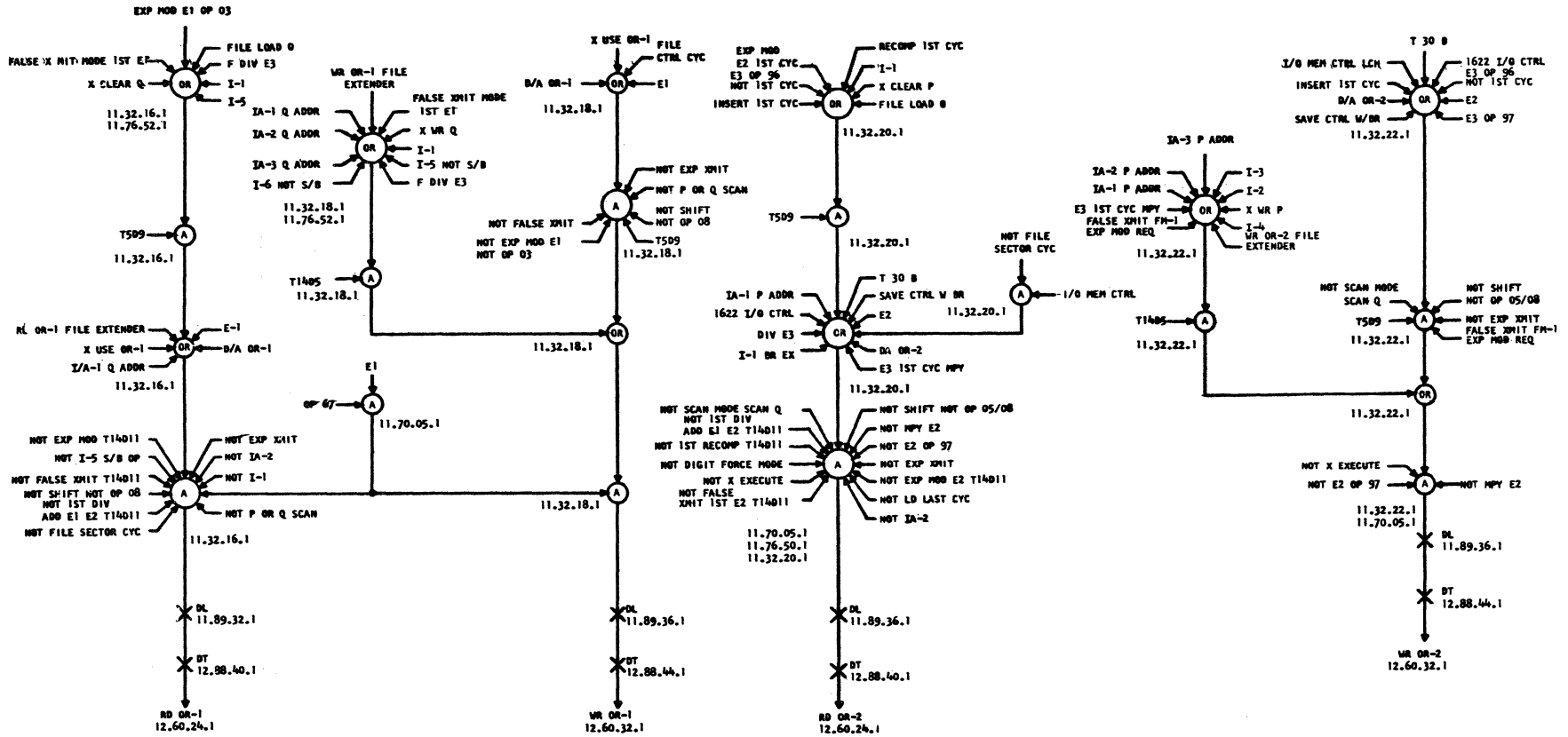
I-6 FILE
OP T1603

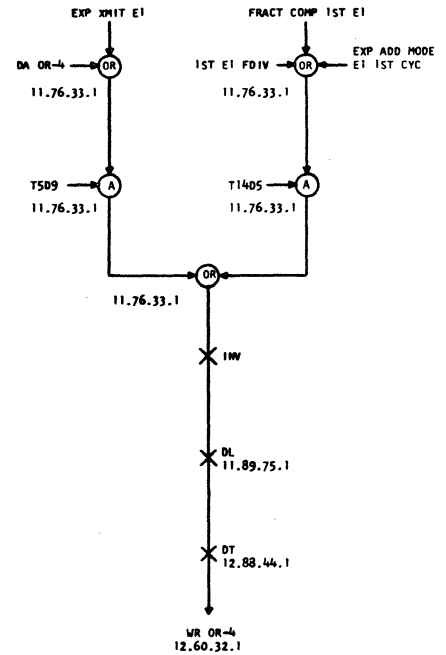
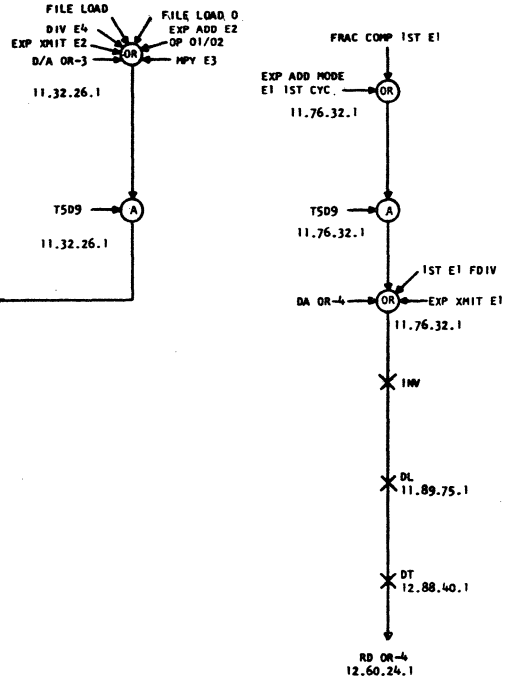
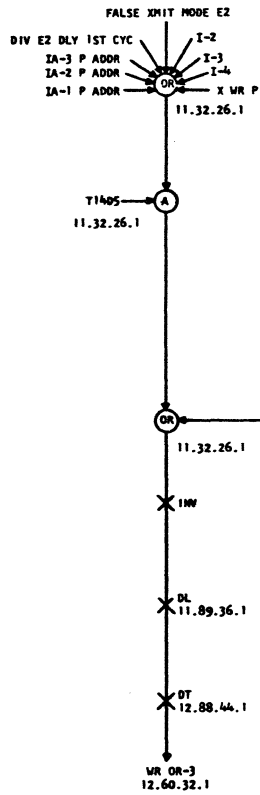
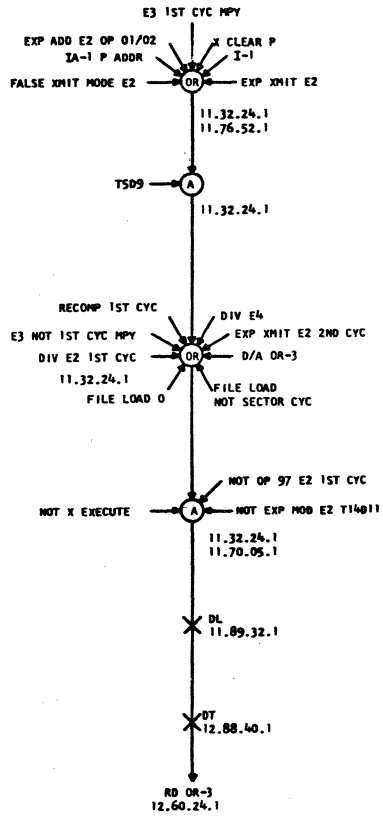
MPY
EI T1104

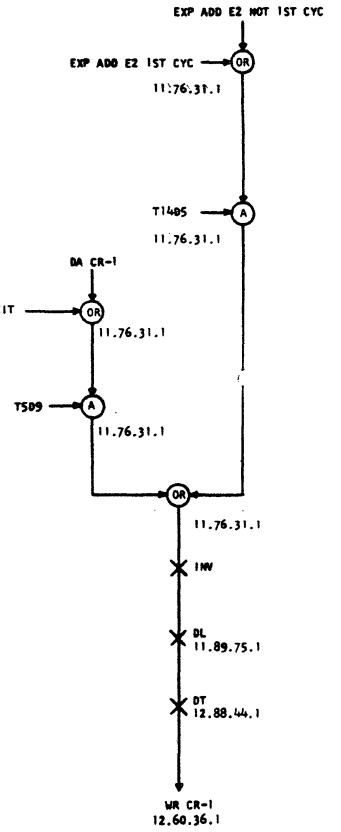
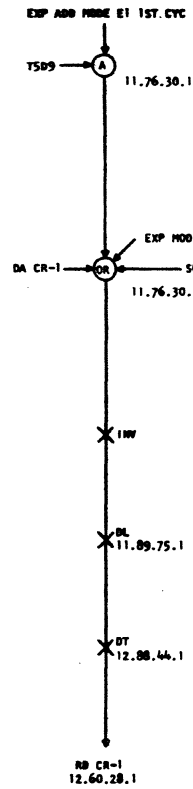
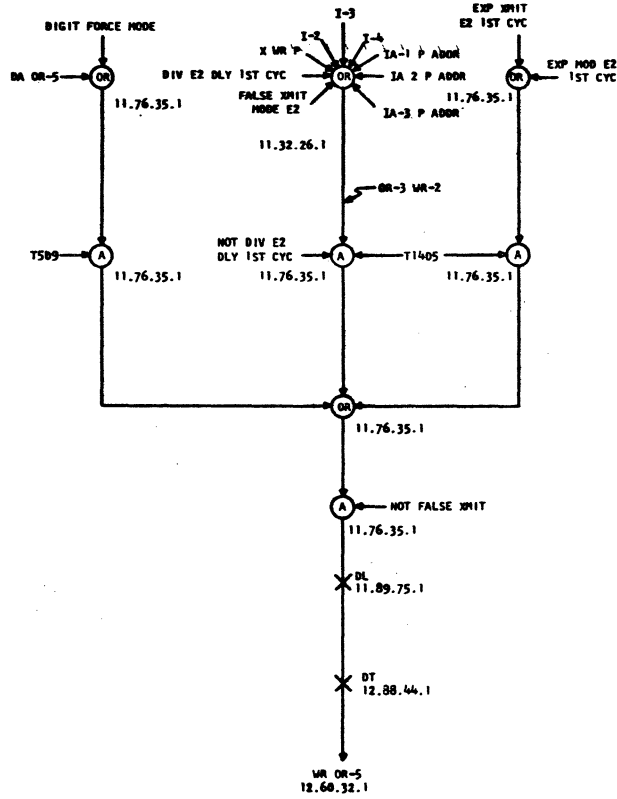
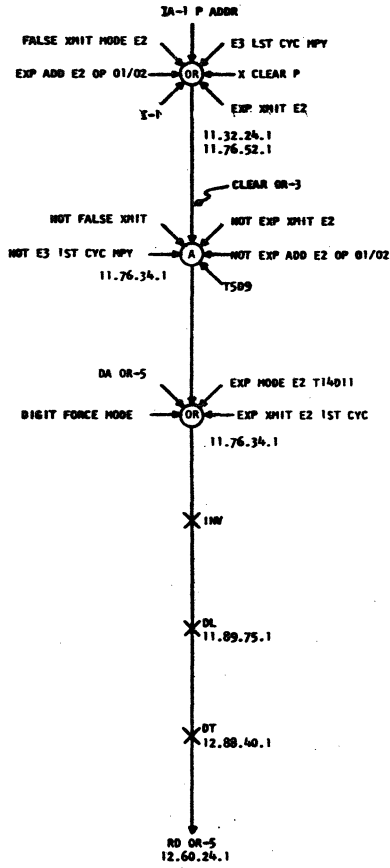


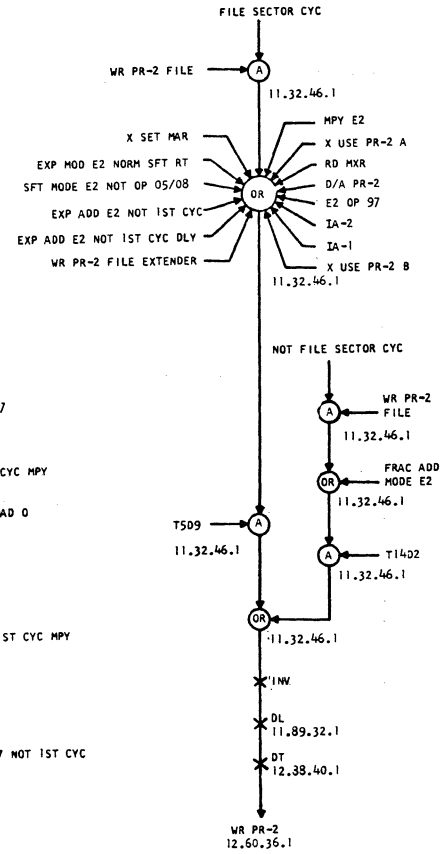
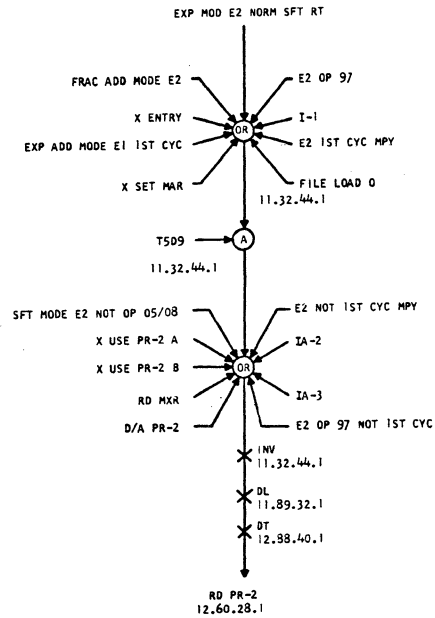
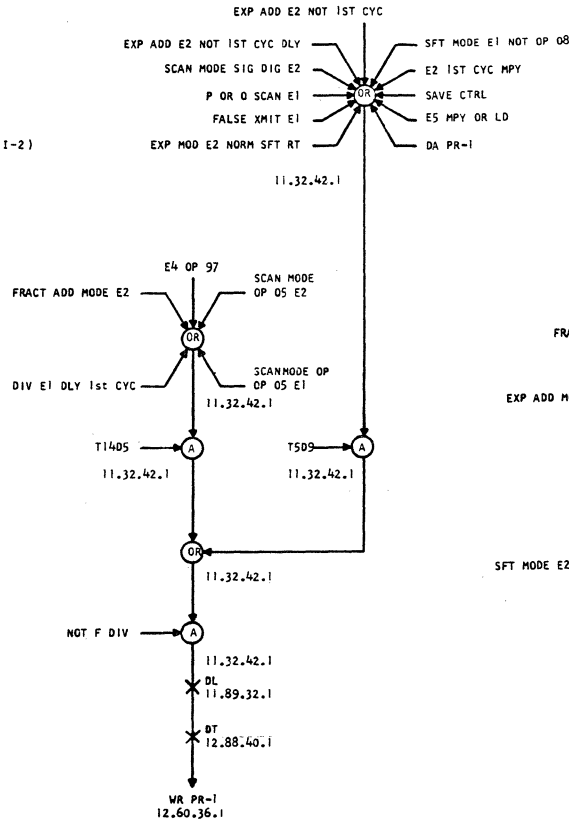
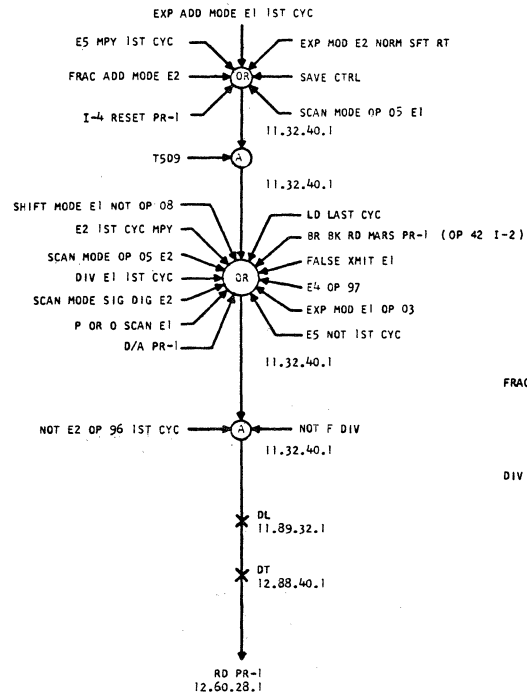


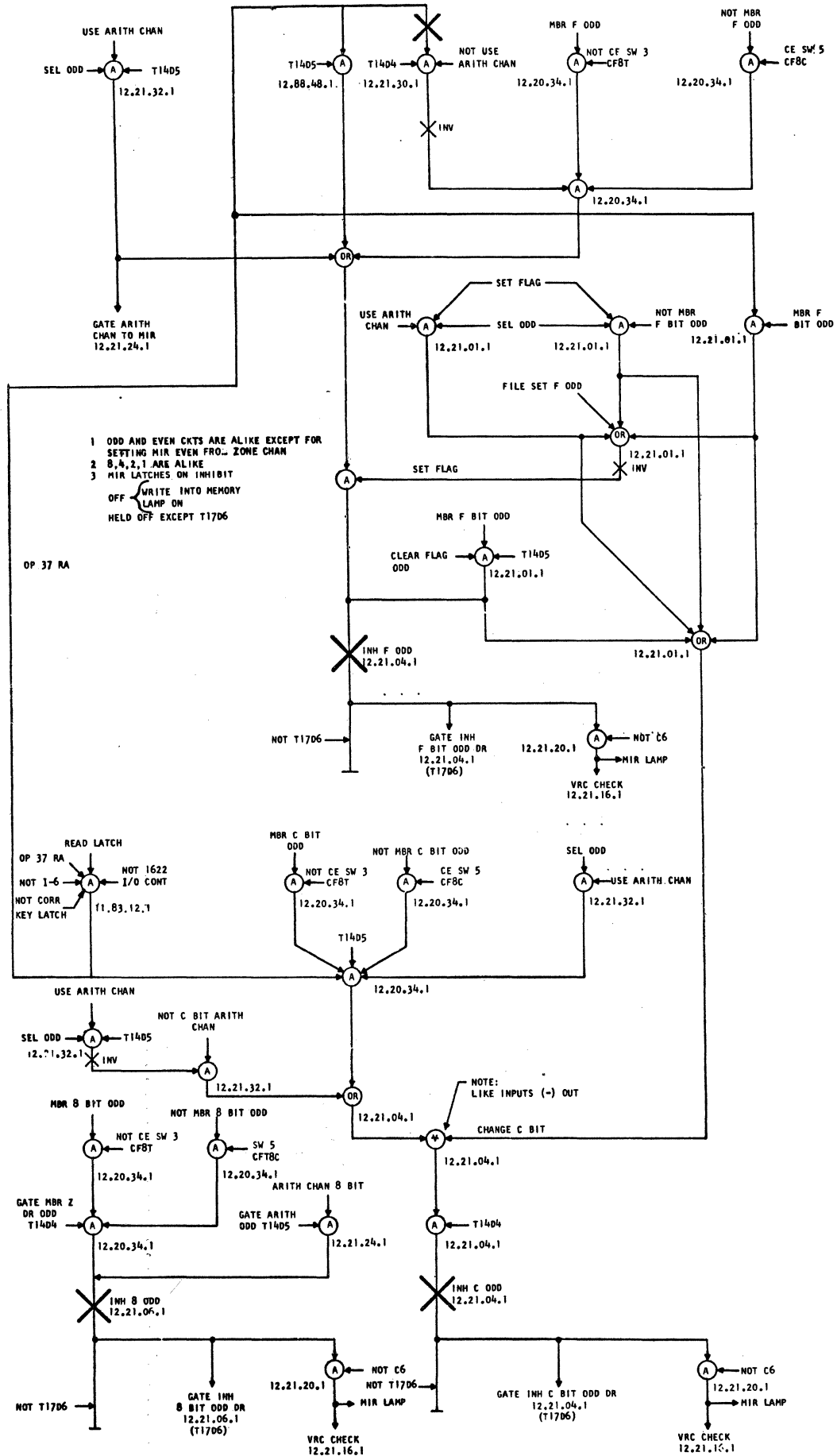


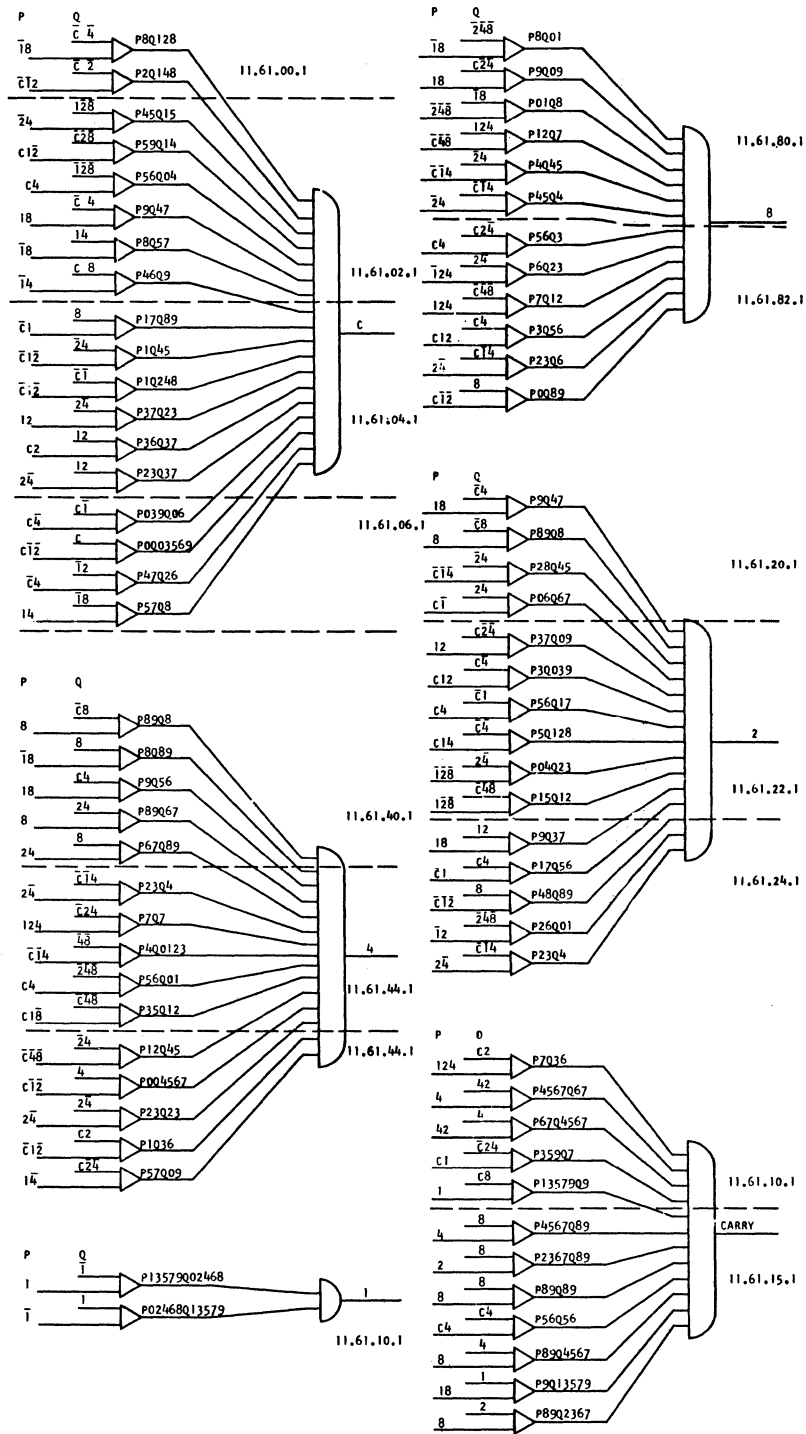


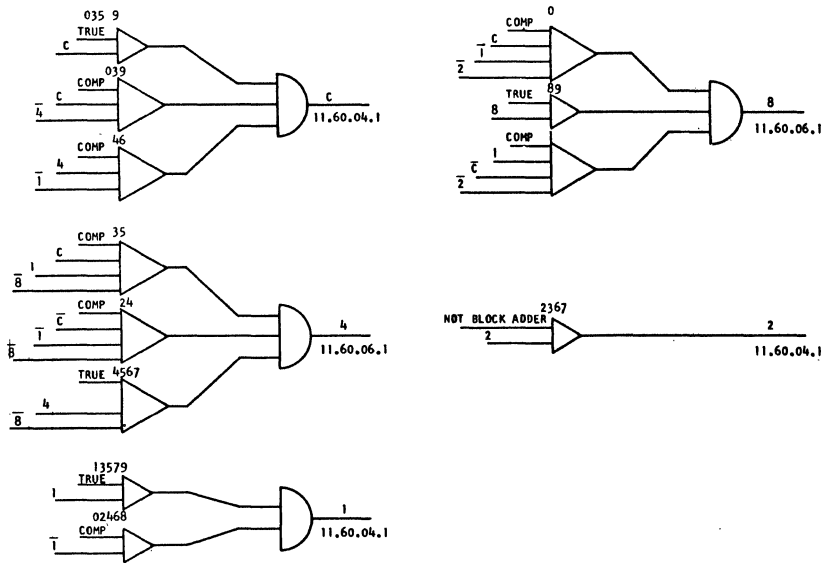




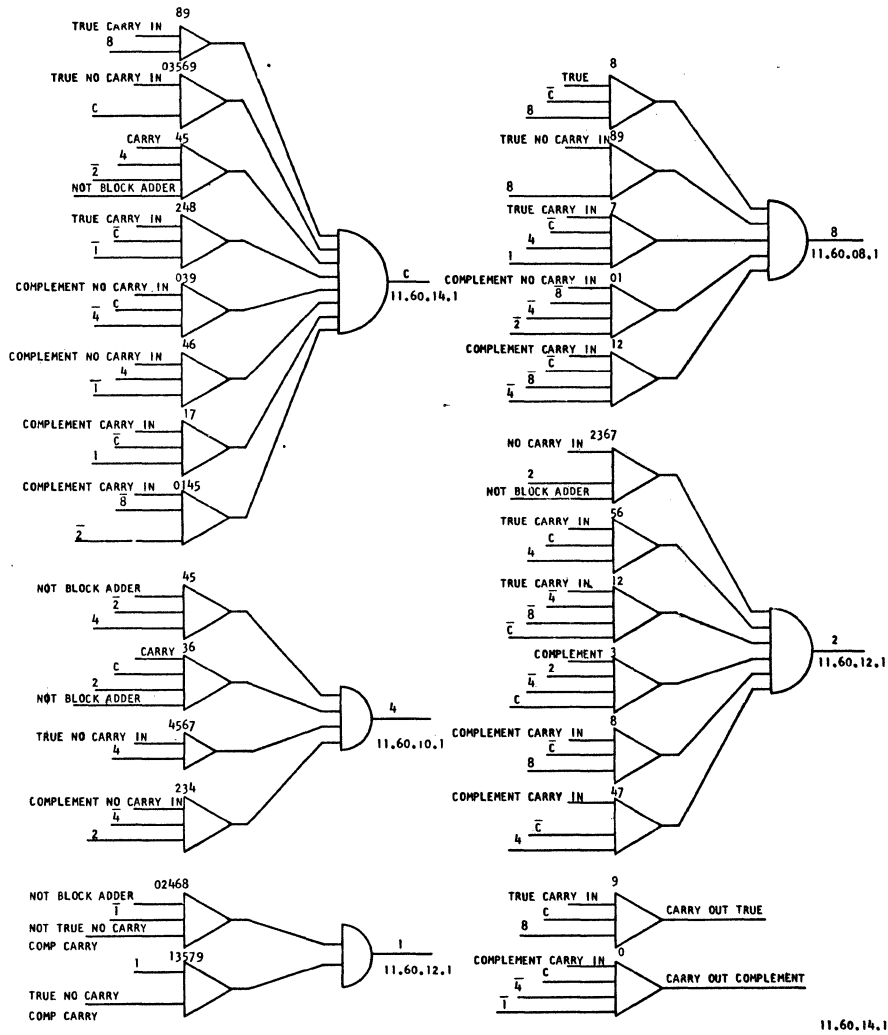




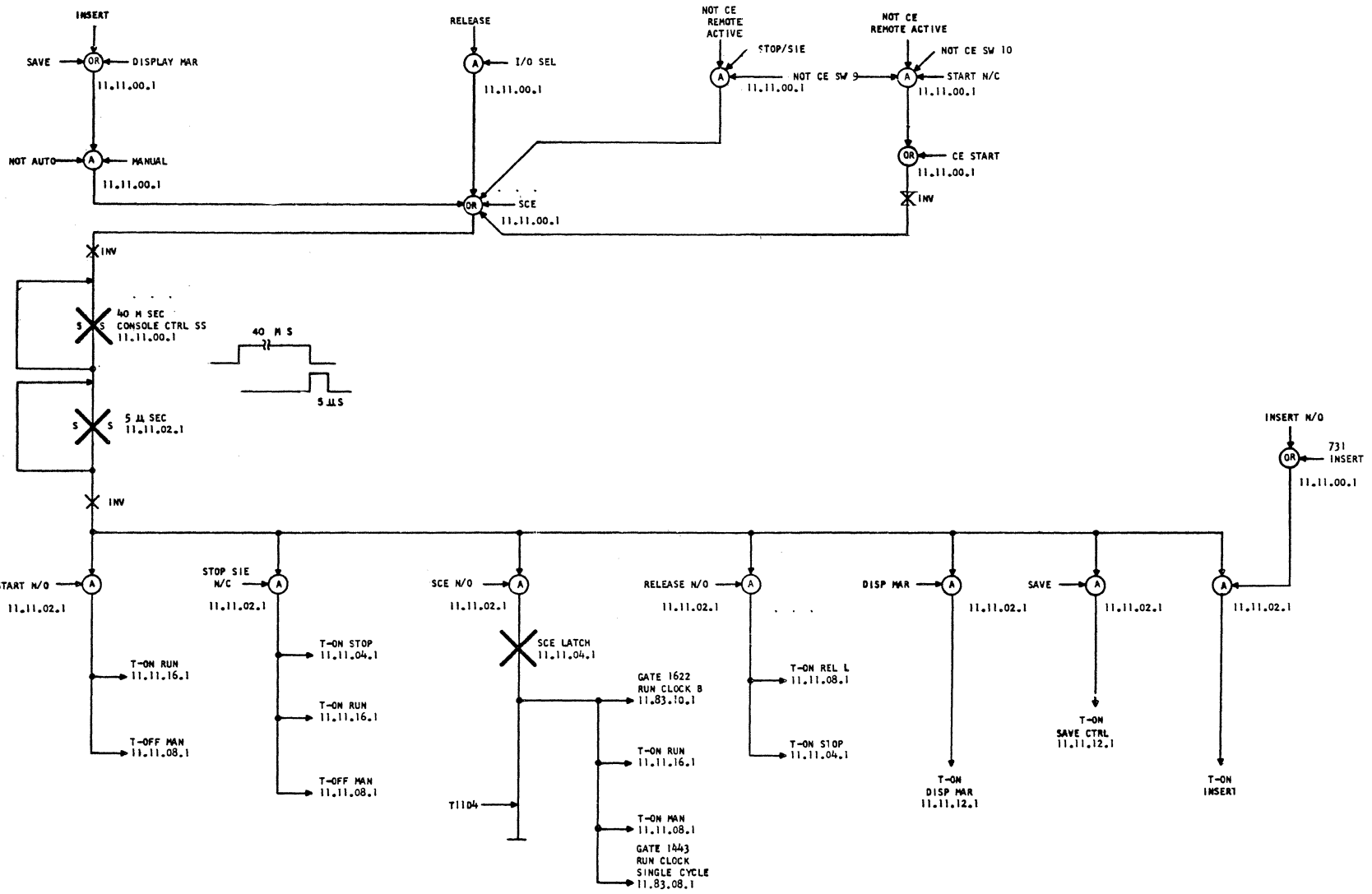


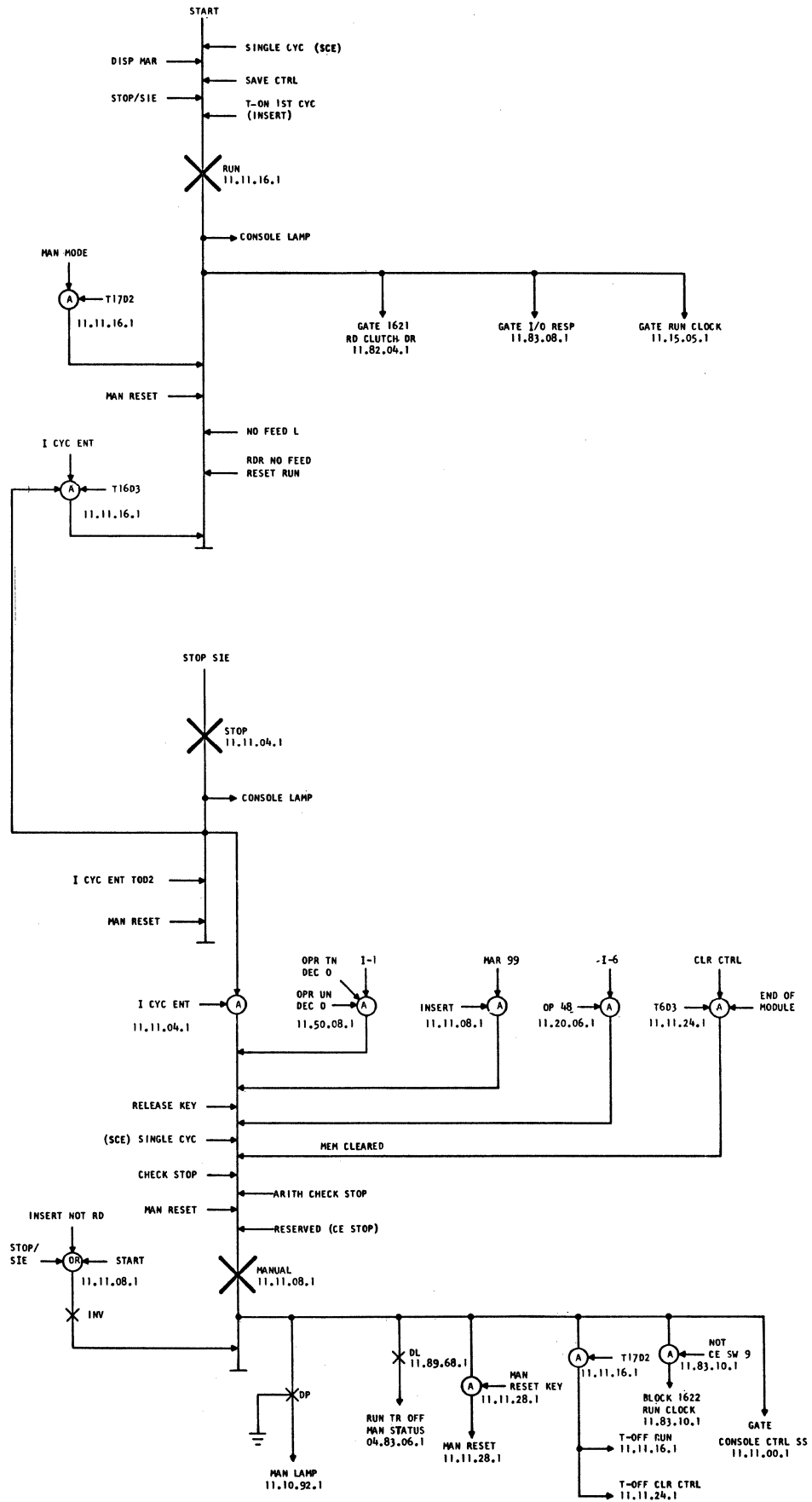


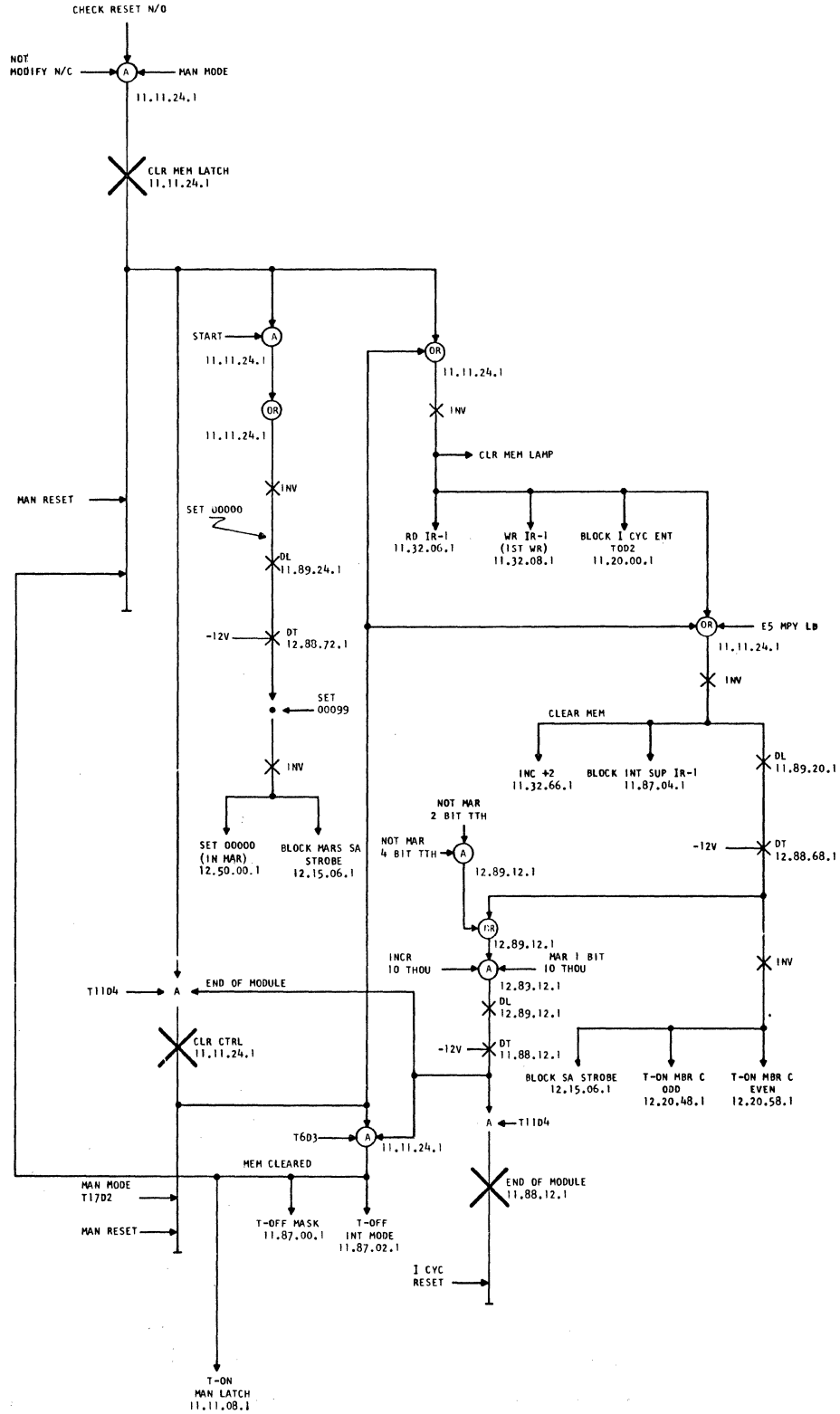
P CHANNEL T/C NETWORK

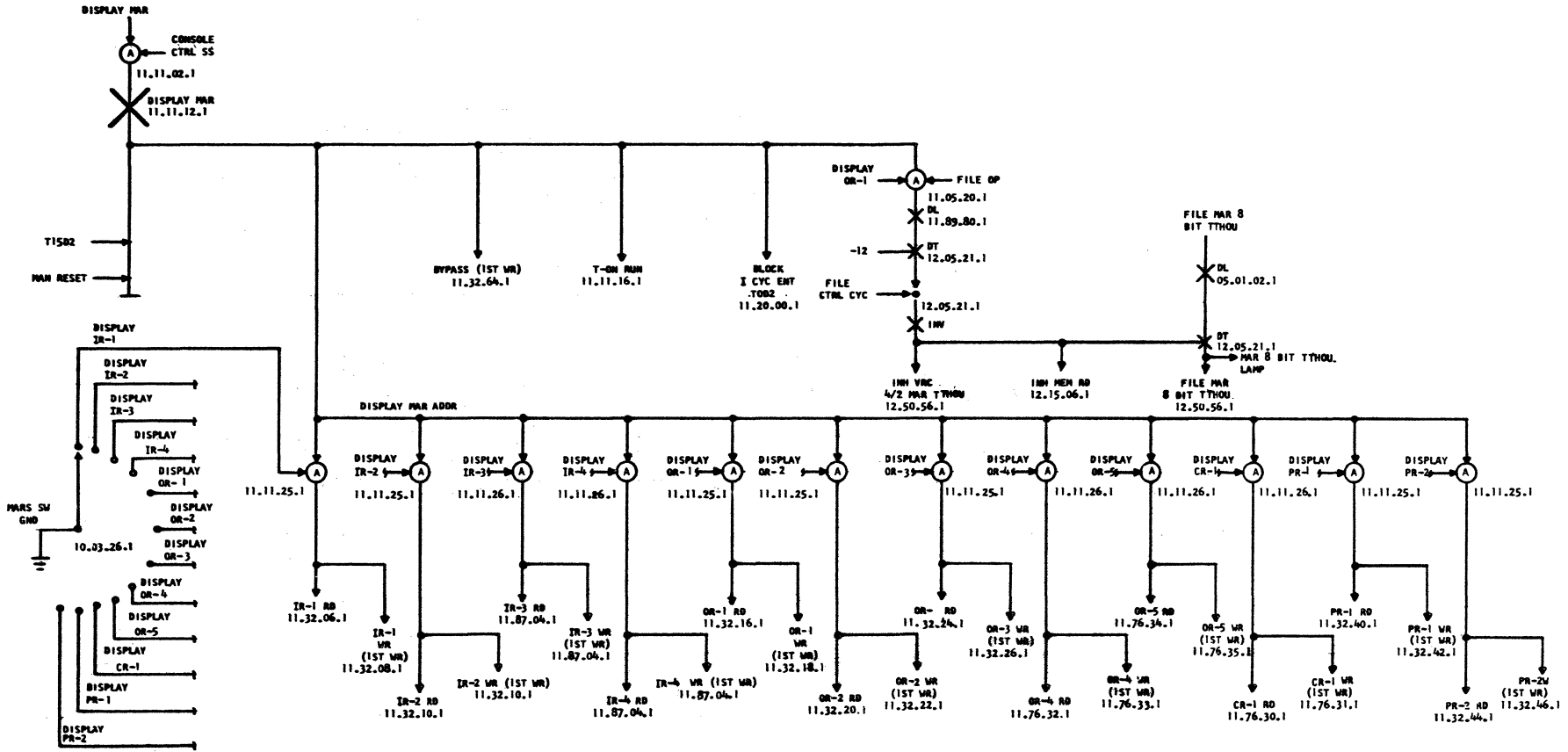


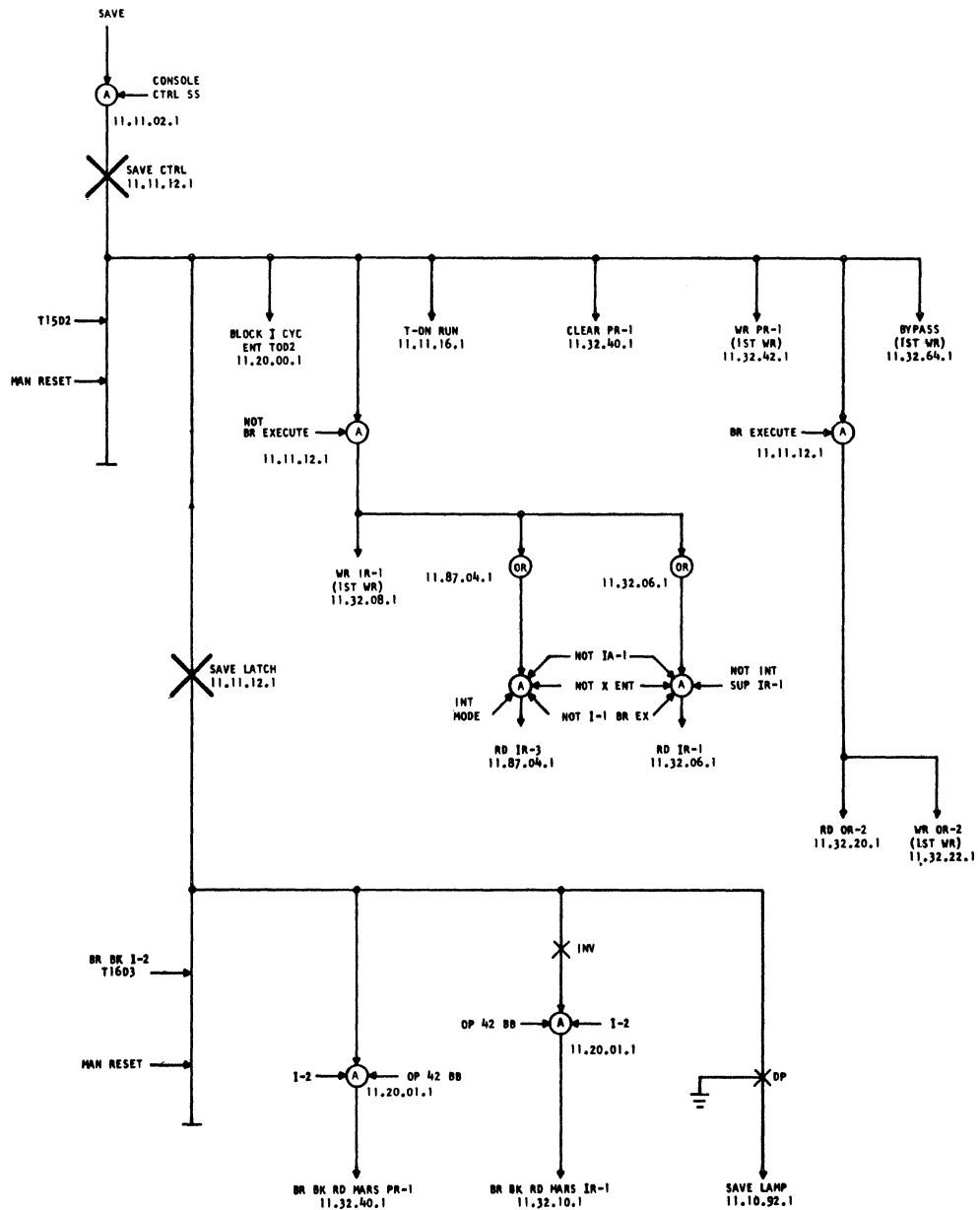
Q CHANNEL T/C NETWORK

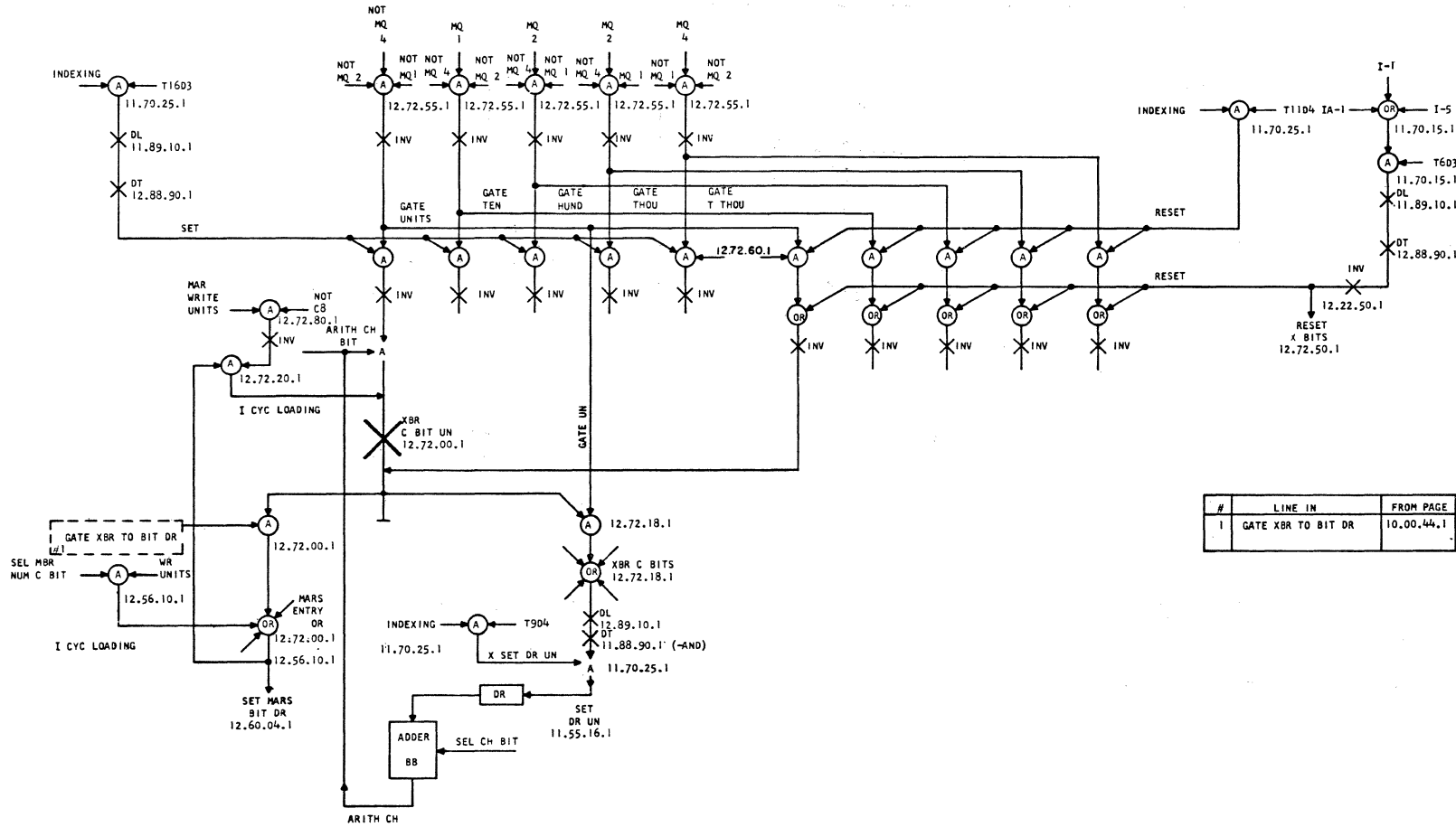




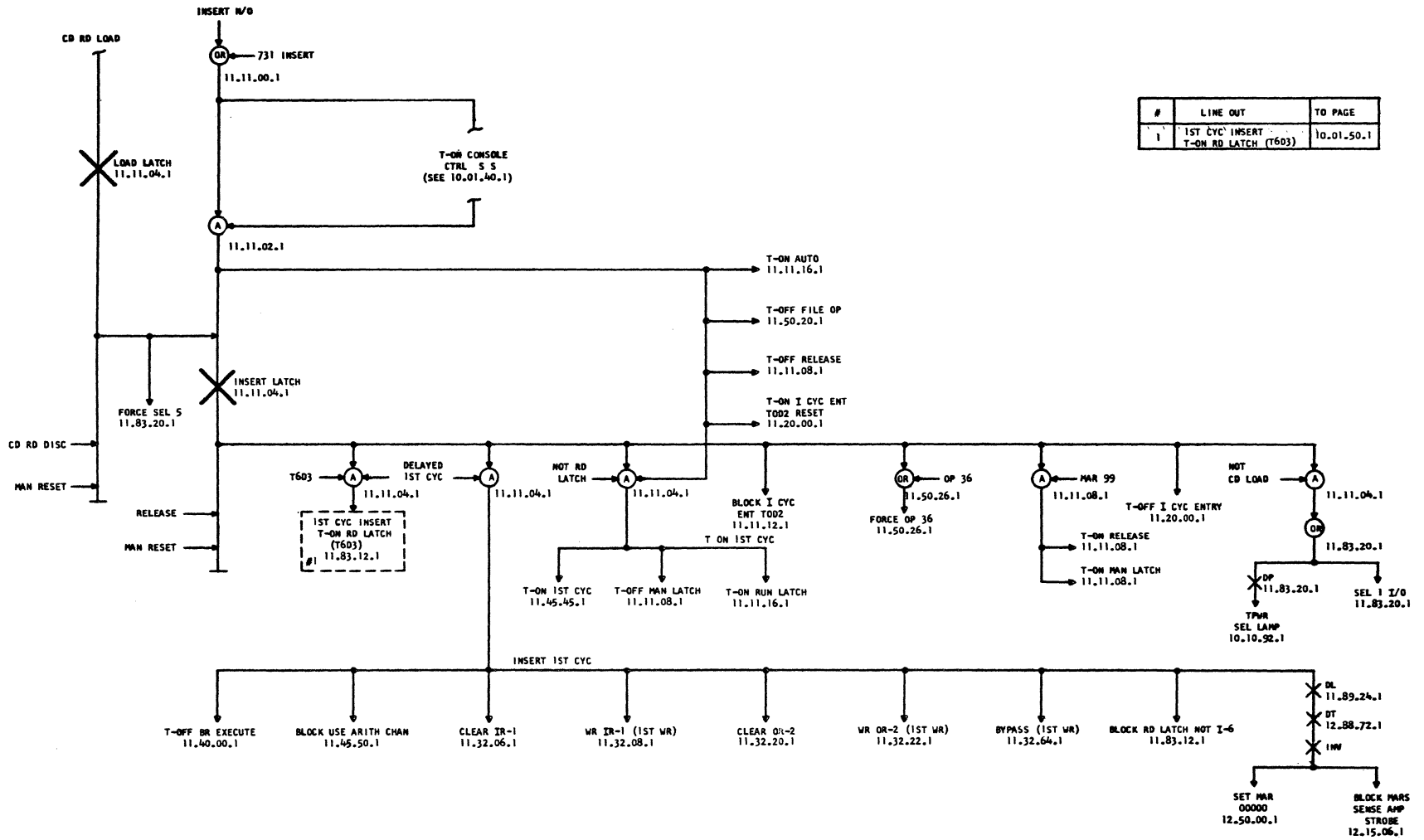


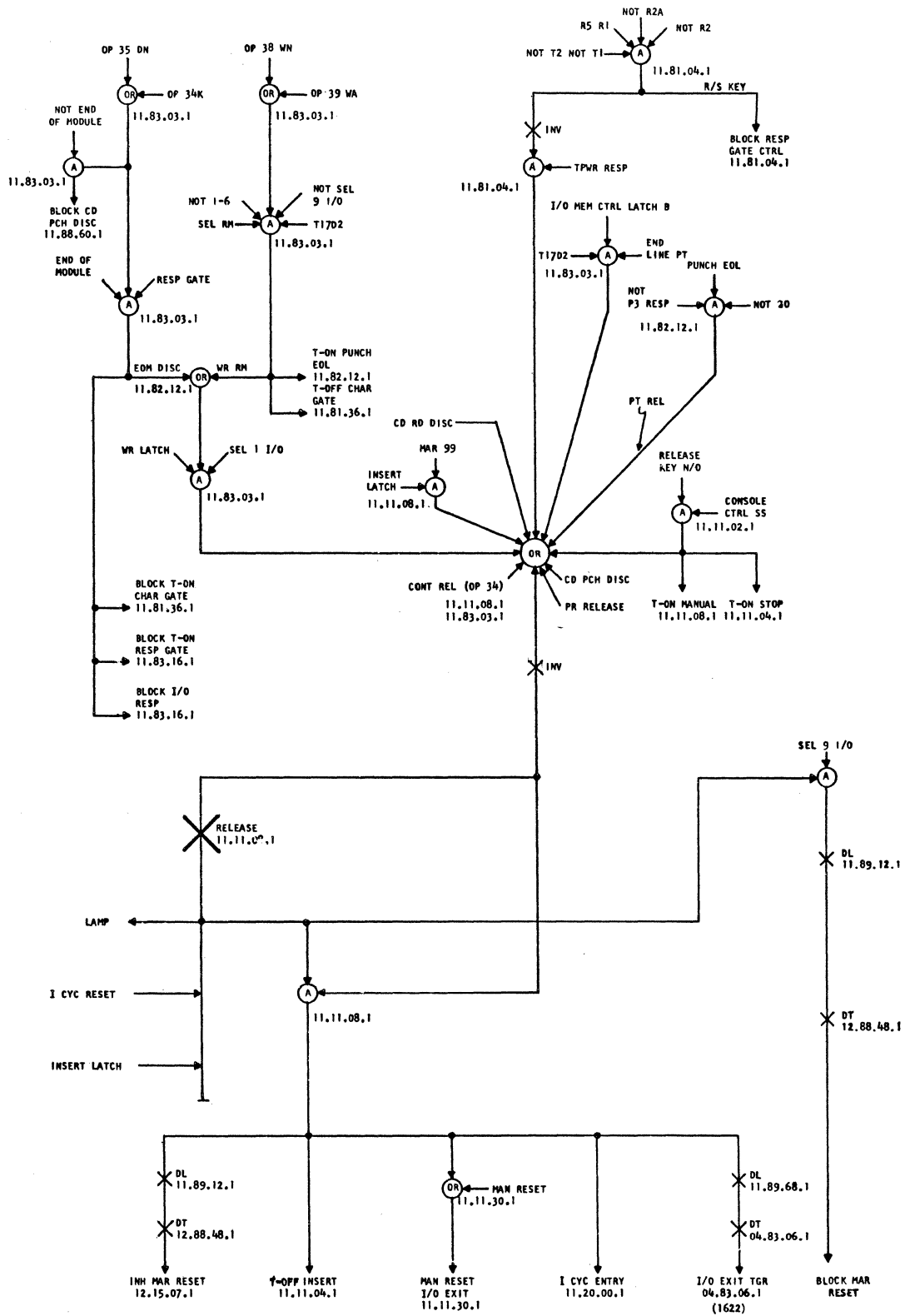




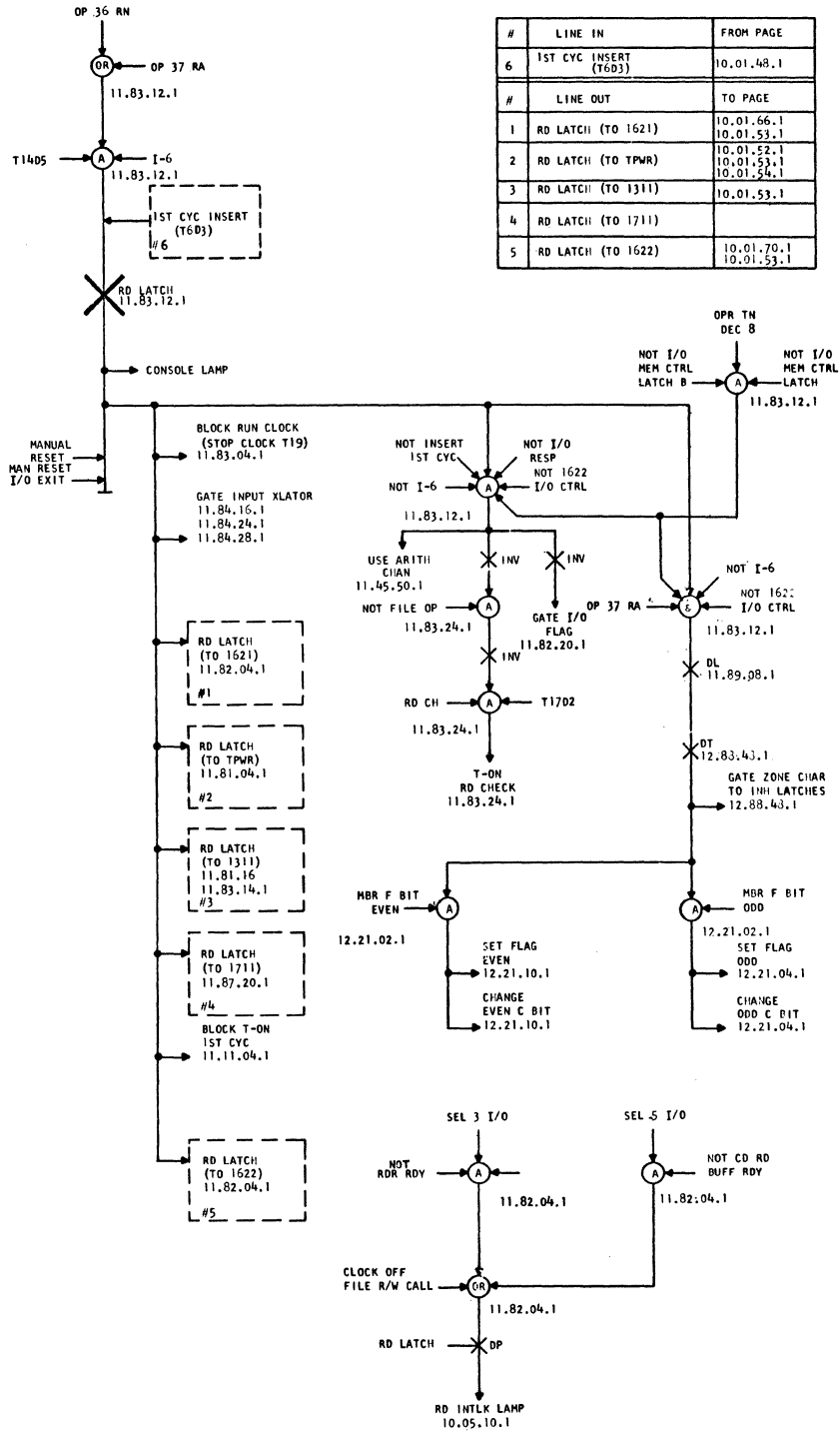


#	LINE IN	FROM PAGE
1	GATE XBR TO BIT DR	10.00.44.1

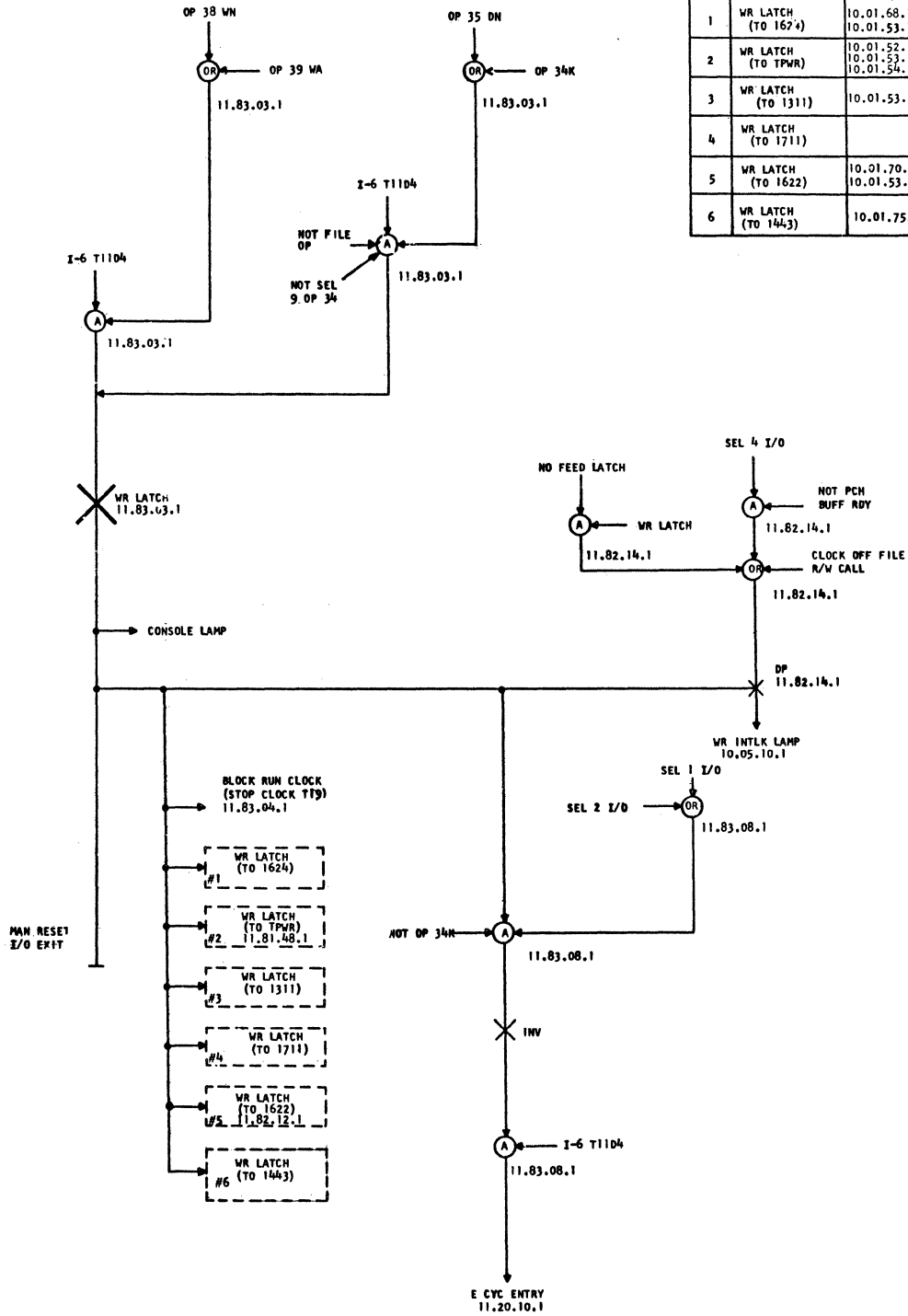




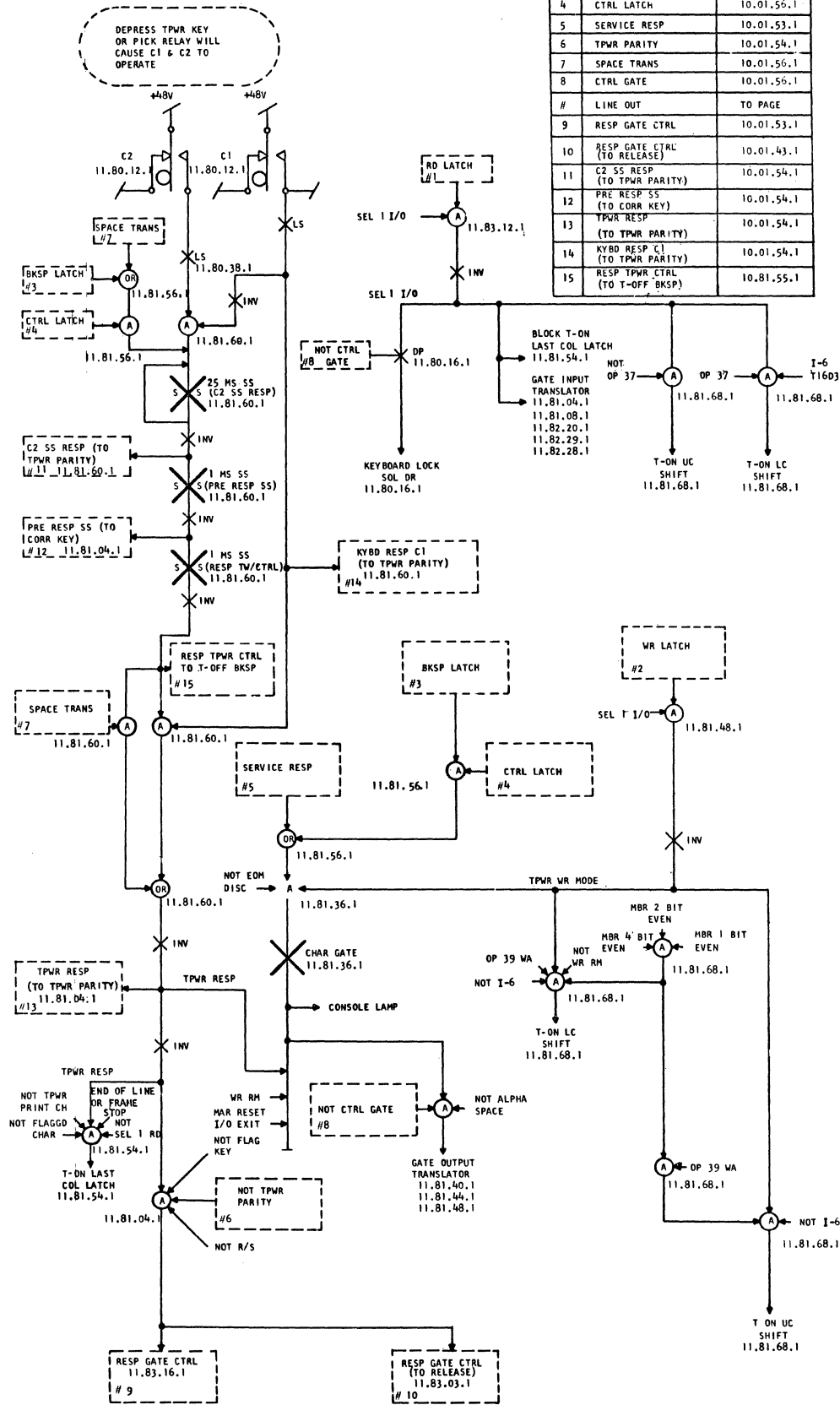
#	LINE IN	FROM PAGE
6	1ST CYC INSERT (T603)	10.01.48.1
#	LINE OUT	TO PAGE
1	RD LATCH (TO 1621)	10.01.66.1 10.01.53.1
2	RD LATCH (TO TPWR)	10.01.52.1 10.01.53.1 10.01.54.1
3	RD LATCH (TO 1311)	10.01.53.1
4	RD LATCH (TO 1711)	
5	RD LATCH (TO 1622)	10.01.70.1 10.01.53.1

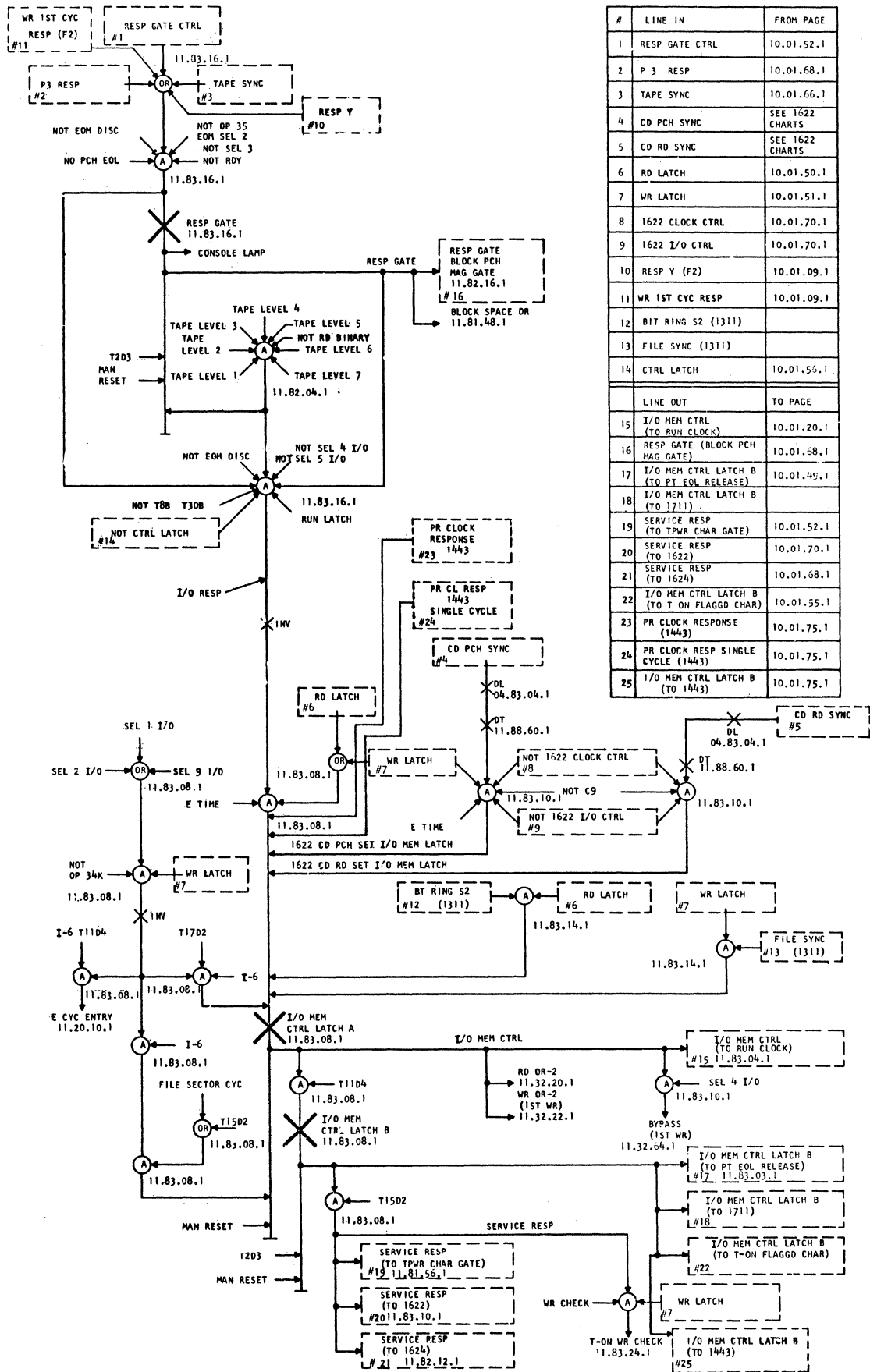


#	LINE OUT	TO PAGE
1	WR LATCH (TO 1624)	10.01.68.1 10.01.53.1
2	WR LATCH (TO TPWR)	10.01.52.1 10.01.53.1 10.01.54.1
3	WR LATCH (TO 1311)	10.01.53.1
4	WR LATCH (TO 1711)	
5	WR LATCH (TO 1622)	10.01.70.1 10.01.53.1
6	WR LATCH (TO 1443)	10.01.75.1

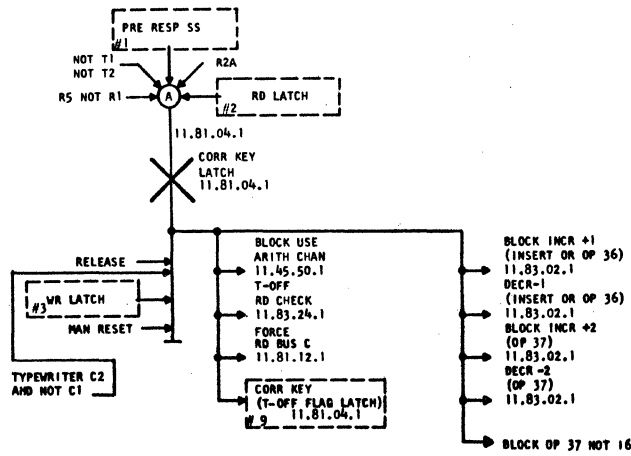


#	LINE IN	FROM PAGE
1	RD LATCH	10.01.50.1
2	WR LATCH	10.01.51.1
3	BKSP LATCH	10.01.55.1
4	CTRL LATCH	10.01.56.1
5	SERVICE RESP	10.01.53.1
6	TPWR PARITY	10.01.54.1
7	SPACE TRANS	10.01.56.1
8	CTRL GATE	10.01.56.1
#	LINE OUT	TO PAGE
9	RESP GATE CTRL	10.01.53.1
10	RESP GATE CTRL (TO RELEASE)	10.01.43.1
11	C2 SS RESP (TO TPWR PARITY)	10.01.54.1
12	PRE RESP SS (TO CORR KEY)	10.01.54.1
13	TPWR RESP (TO TPWR PARITY)	10.01.54.1
14	KYBD RESP C1 (TO TPWR PARITY)	10.01.54.1
15	RESP TPWR CTRL (TO T-OFF BKSP)	10.81.55.1



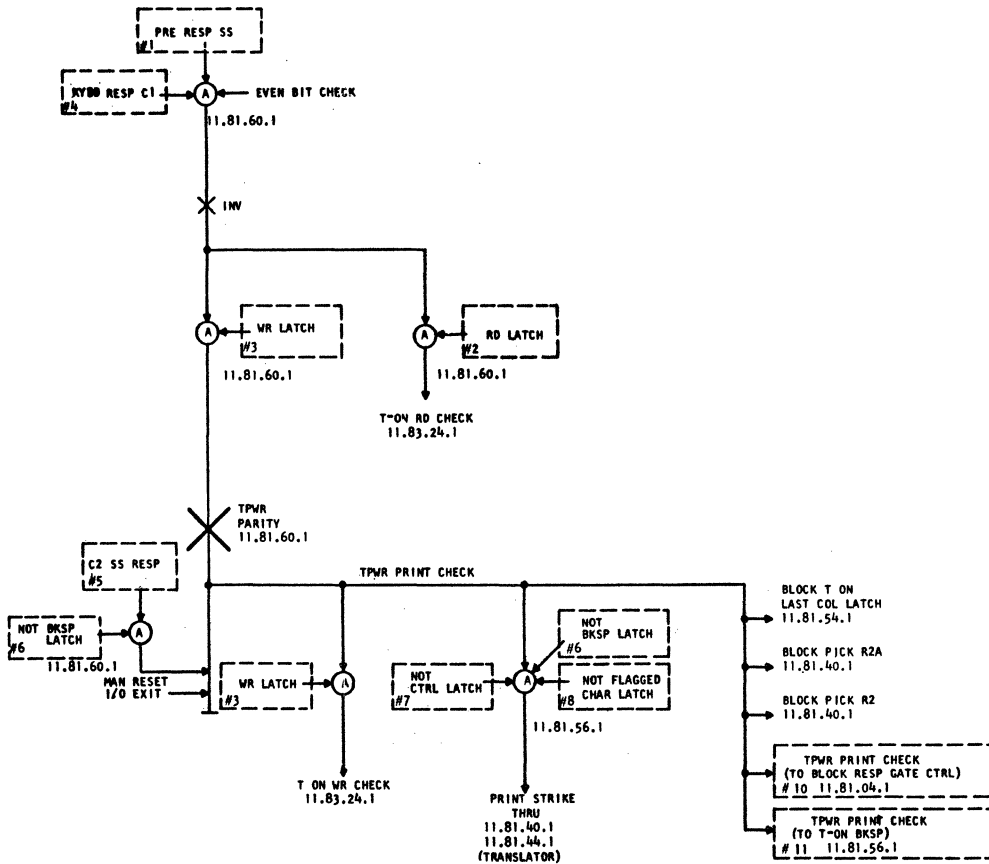


#	LINE IN	FROM PAGE
1	RESP GATE CTRL	10.01.52.1
2	P 3 RESP	10.01.68.1
3	TAPE SYNC	10.01.66.1
4	CD PCH SYNC	SEE 1622 CHARTS
5	CD RD SYNC	SEE 1622 CHARTS
6	RD LATCH	10.01.50.1
7	WR LATCH	10.01.51.1
8	1622 CLOCK CTRL	10.01.70.1
9	1622 I/O CTRL	10.01.70.1
10	RESP Y (F2)	10.01.09.1
11	WR 1ST CYC RESP	10.01.09.1
12	BIT RING S2 (1311)	
13	FILE SYNC (1311)	
14	CTRL LATCH	10.01.56.1
	LINE OUT	TO PAGE
15	I/O MEM CTRL (TO RUN CLOCK)	10.01.20.1
16	RESP GATE (BLOCK PCH MAG GATE)	10.01.68.1
17	I/O MEM CTRL LATCH B (TO PT EOL RELEASE)	10.01.45.1
18	I/O MEM CTRL LATCH B (TO 1711)	
19	SERVICE RESP (TO TPWR CHAR GATE)	10.01.52.1
20	SERVICE RESP (TO 1622)	10.01.70.1
21	SERVICE RESP (TO 1624)	10.01.68.1
22	I/O MEM CTRL LATCH B (TO T-ON FLAGGD CHAR)	10.01.55.1
23	PR CLOCK RESPONSE (1443)	10.01.75.1
24	PR CLOCK RESP SINGLE CYCLE (1443)	10.01.75.1
25	I/O MEM CTRL LATCH B (TO 1443)	10.01.75.1

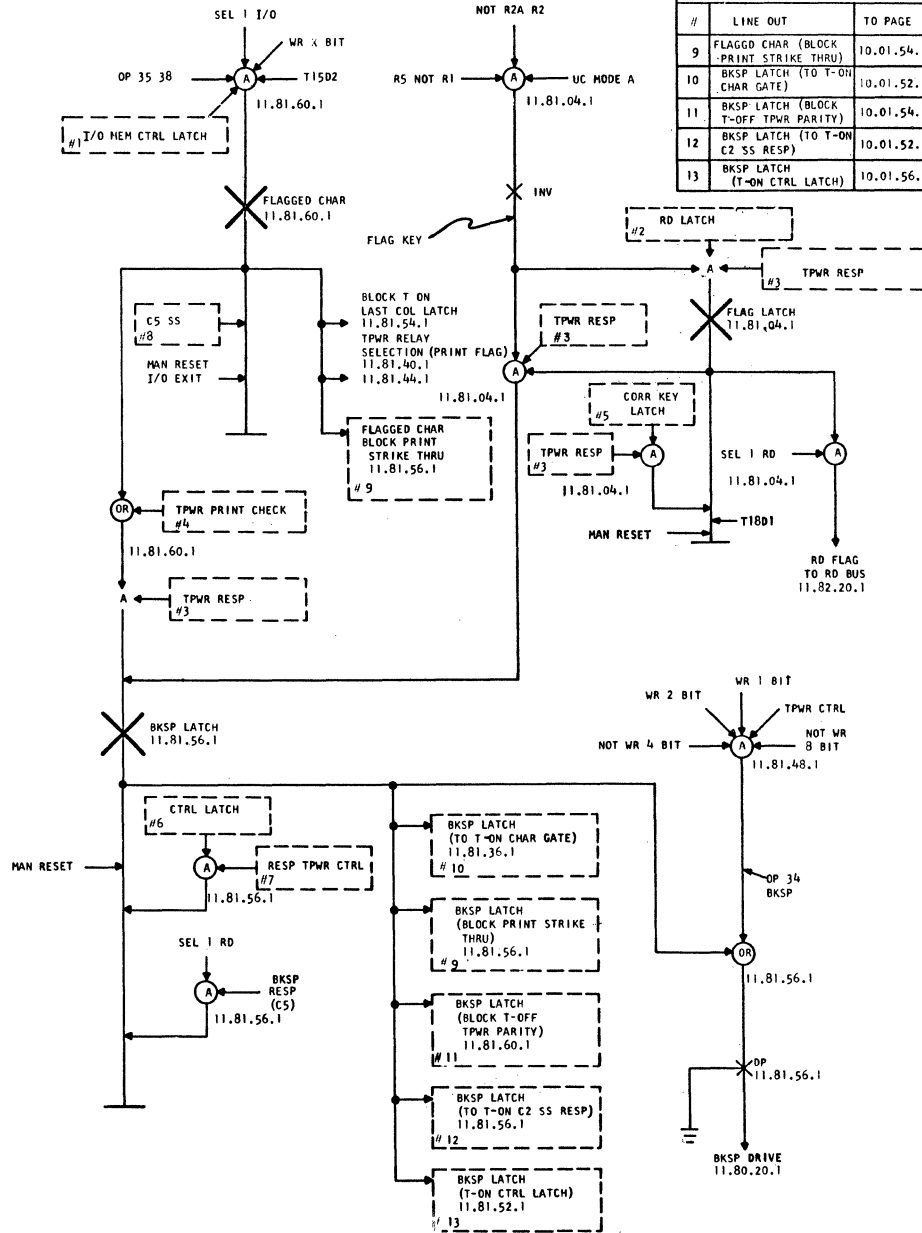


#	LINE IN	FROM PAGE
1	PRE RESP SS	10.01.52.1
2	RD LATCH	10.01.50.1
3	WR LATCH	10.01.51.1
4	KYBD RESP C1	10.01.52.1
5	C2 SS RESP	10.01.52.1
6	BKSP LATCH	10.01.55.1
7	CTRL LATCH	10.01.56.1
8	FLAGGED CHAR LATCH	10.01.55.1
	LINE OUT	TO PAGE
9	CORR KEY (T-OFF FLAG LATCH)	10.01.55.1
10	TPWR PRINT CHECK (TO BLOCK RESP GATE CTRL)	10.01.52.1
11	TPWR PRINT CHECK (TO T-ON BKSP)	10.01.05.1

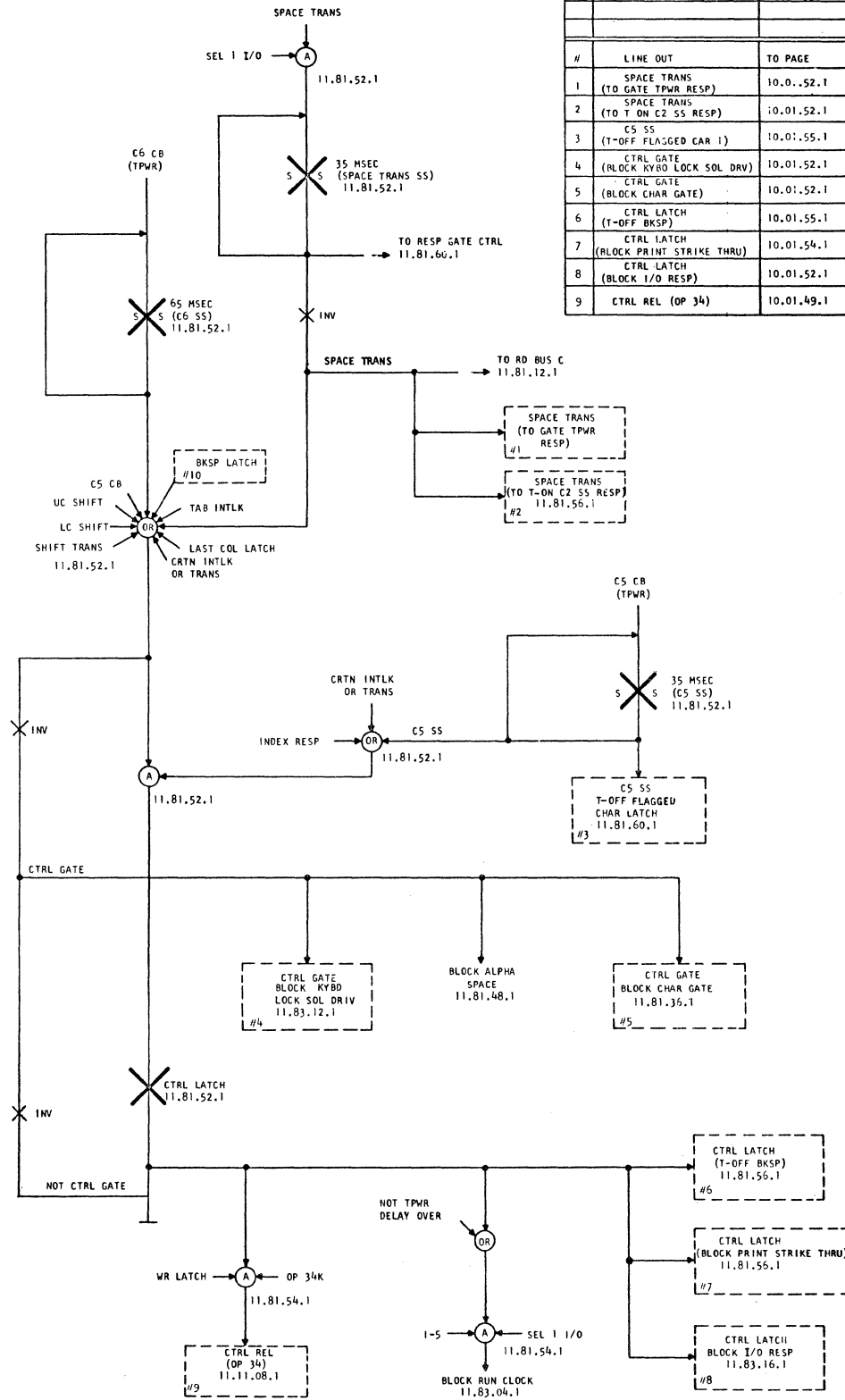
TPWR PARITY



#	LINE IN	FROM PAGE
1	I/O MEM CTRL LATCH B	10.01.53.1
2	RD LATCH	10.01.50.1
3	TPWR RESP	10.01.52.1
4	TPWR PRINT CHECK	10.01.54.1
5	CORR KEY LATCH	10.01.54.1
6	CTRL LATCH	10.01.53.1
7	RESP TPWR CTRL	10.01.52.1
8	C5 SS	10.01.56.1
#	LINE OUT	TO PAGE
9	FLAGGED CHAR (BLOCK PRINT STRIKE THRU)	10.01.54.1
10	BKSP LATCH (TO T-ON CHAR GATE)	10.01.52.1
11	BKSP LATCH (BLOCK T-OFF TPWR PARITY)	10.01.54.1
12	BKSP LATCH (TO T-ON C2 SS RESP)	10.01.52.1
13	BKSP LATCH (T-ON CTRL LATCH)	10.01.56.1



#	LINE IN	FROM PAGE
10	BKSP LATCH	10.01.55.1
#	LINE OUT	TO PAGE
1	SPACE TRANS (TO GATE TPWR RESP)	10.01.52.1
2	SPACE TRANS (TO T ON C2 S5 RESP)	10.01.52.1
3	C5 S5 (T-OFF FLAGGED CAR 1)	10.01.55.1
4	CTRL GATE (BLOCK KYBD LOCK SOL DRV)	10.01.52.1
5	CTRL GATE (BLOCK CHAR GATE)	10.01.52.1
6	CTRL LATCH (T-OFF BKSP)	10.01.55.1
7	CTRL LATCH (BLOCK PRINT STRIKE THRU)	10.01.54.1
8	CTRL LATCH (BLOCK I/O RESP)	10.01.52.1
9	CTRL REL (OP 34)	10.01.49.1



TILT CONTACTS TRANSFERRED

0	0
1	0
0	1
1	1

T1 T2

UPPER CASE

⊗	R	S	—	⊗	⊗	@	⊗	⊗	0	#	⊗	—	R	⊗	⊗	⊗	@	+	(⊗	#	=	0	
⊗	⊗	⊗	⊗	⊗	#	⊗	⊗	1	⊗	⊗	8	/	T	V	X	Y	#	S	U	W	Z	,	1	
4	6	⊗	⊗	⊗	⊗	.	5	7	3	⊗	⊗	J	L	N	P	Q	*	K	M	O	R	\$	2	
⊗	⊗	⊗	⊗	⊗	⊗	⊗	⊗	⊗	⊗	⊗	2	9	A	C	E	G	H)	B	D	F	I	.	3

-5 -4 -3 -2 -1 0 +1 +2 +3 +4 +5 -5 -4 -3 -2 -1 0 +1 +2 +3 +4 +5

LOWER CASE

TILT LATCHES SELECTED

1	1
0	1
1	0
0	0

T1 T2

ROTATE MOTION

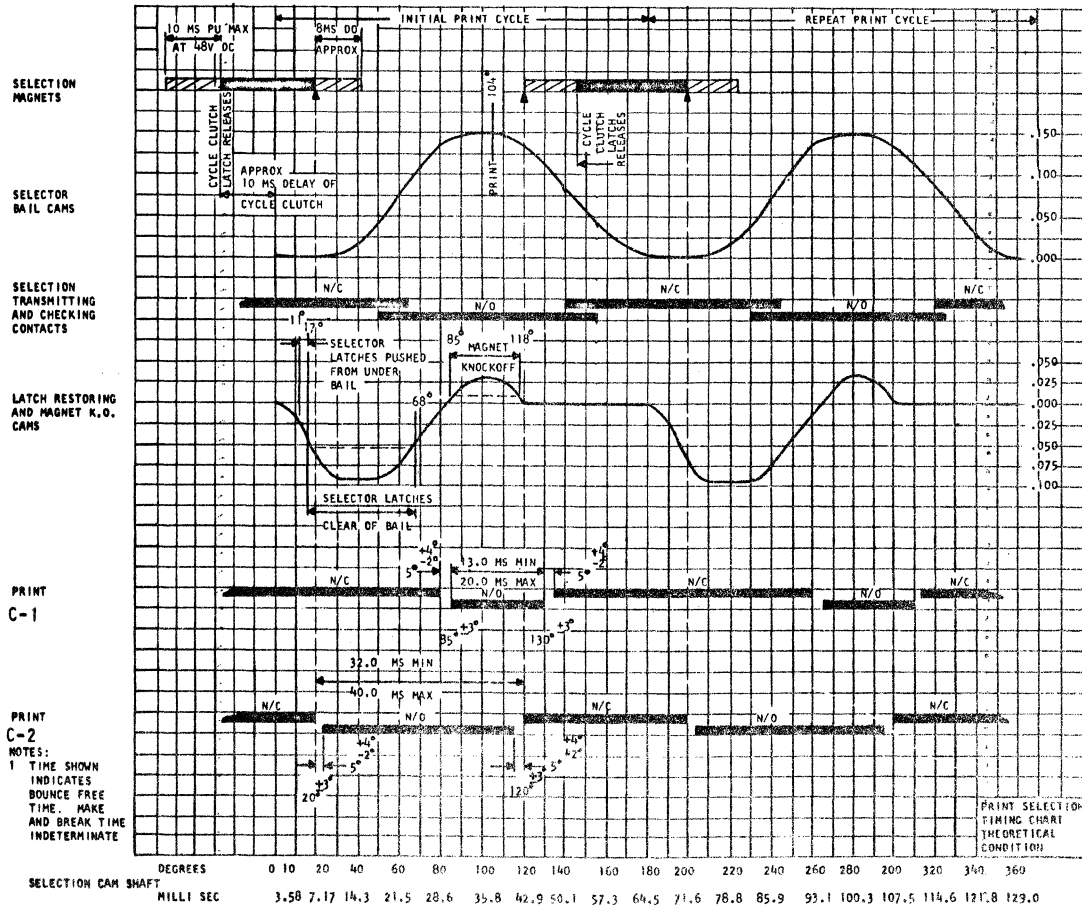
ROTATE LATCHES SELECTED

2A	1	1	1	1	0	0	1	1	1	0	0	1	1	1	1	0	0	1	1	1	0	0	1	1	0	0
2	1	1	0	0	0	0	1	0	0	0	0	1	1	0	0	0	0	1	0	0	0	1	0	0	0	0
1	1	0	1	0	1	0	0	1	0	1	0	1	0	1	0	1	0	0	1	0	1	0	1	0	1	0
-5	1	1	1	1	1	1	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0

ROTATE CONTACT TRANSFERRED

2A	0	0	0	0	1	1	0	0	0	1	1	0	0	0	0	1	1	0	0	0	1	1	0	0	1	1
2	0	0	1	1	1	1	0	1	1	1	1	0	0	1	1	1	1	0	1	1	1	1	1	1	1	1
1	0	1	0	1	0	1	1	0	1	0	1	0	1	0	1	0	1	1	0	1	0	1	0	1	0	1
-5	1	1	1	1	1	1	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0

TYPE ELEMENT 731 C1 C2 TIMING



NOTE 1: TIME SHOWN INDICATES BOUNCE FREE TIME, MAKE AND BREAK TIME INDETERMINATE

UPPER CASE SHIFT MAGNET

UPPER CASE SHIFT CAM

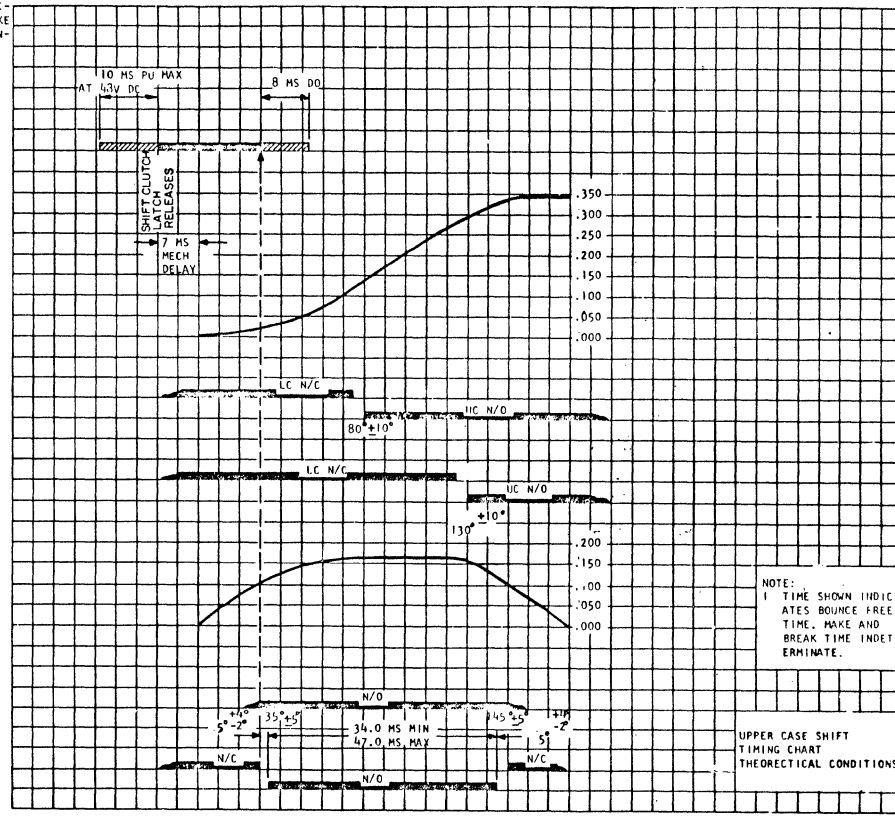
#1 MODE CONTACTS

#2 MODE CONTACTS

UPPER CASE CONTACT ACTUATOR

UPPER CASE SHIFT CONTACTS (SEE NOTE 1)

UPPER CASE SHIFT FEEDBACK C-3



NOTE: TIME SHOWN INDICATES BOUNCE FREE TIME, MAKE AND BREAK TIME INDETERMINATE.

UPPER CASE SHIFT TIMING CHART THEORETICAL CONDITIONS

OPERATIONAL CAM SHAFT	DEGREES	MILLI SEC
	0	3.58
	10	7.17
	20	14.3
	30	21.5
	40	28.6
	50	35.8
	60	42.9
	70	50.1
	80	57.3
	90	64.5

LOWER CASE SHIFT MAGNET

LOWER CASE SHIFT CAM

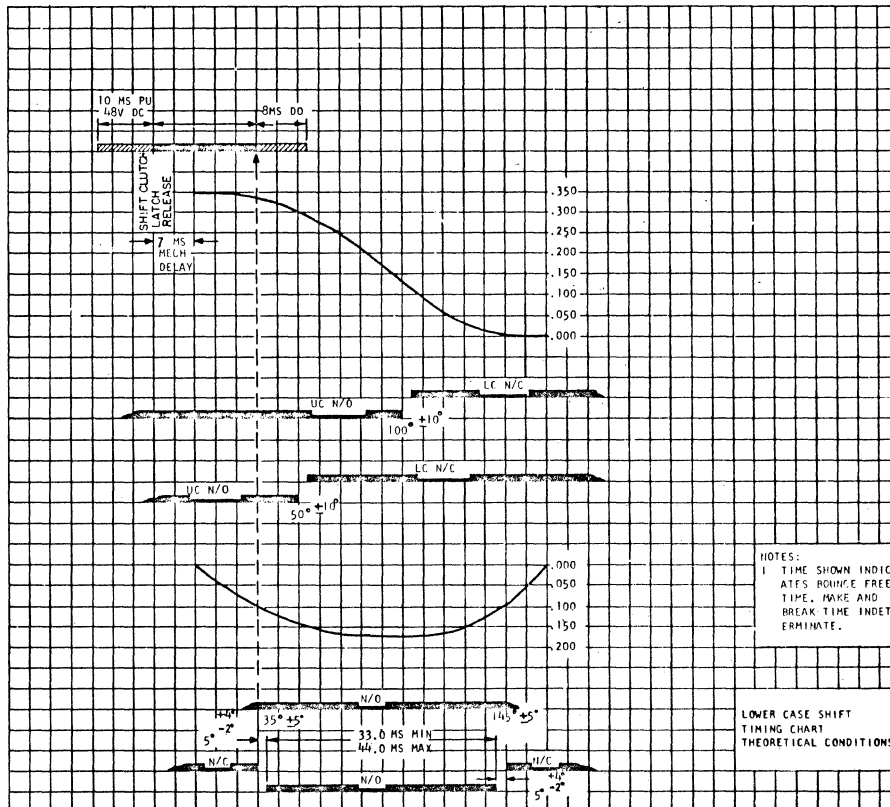
#1 MODE CONTACTS

#2 MODE CONTACTS

LOWER CASE CONTACT ACTUATOR

LOWER CASE SHIFT CONTACTS (SEE NOTE 1)

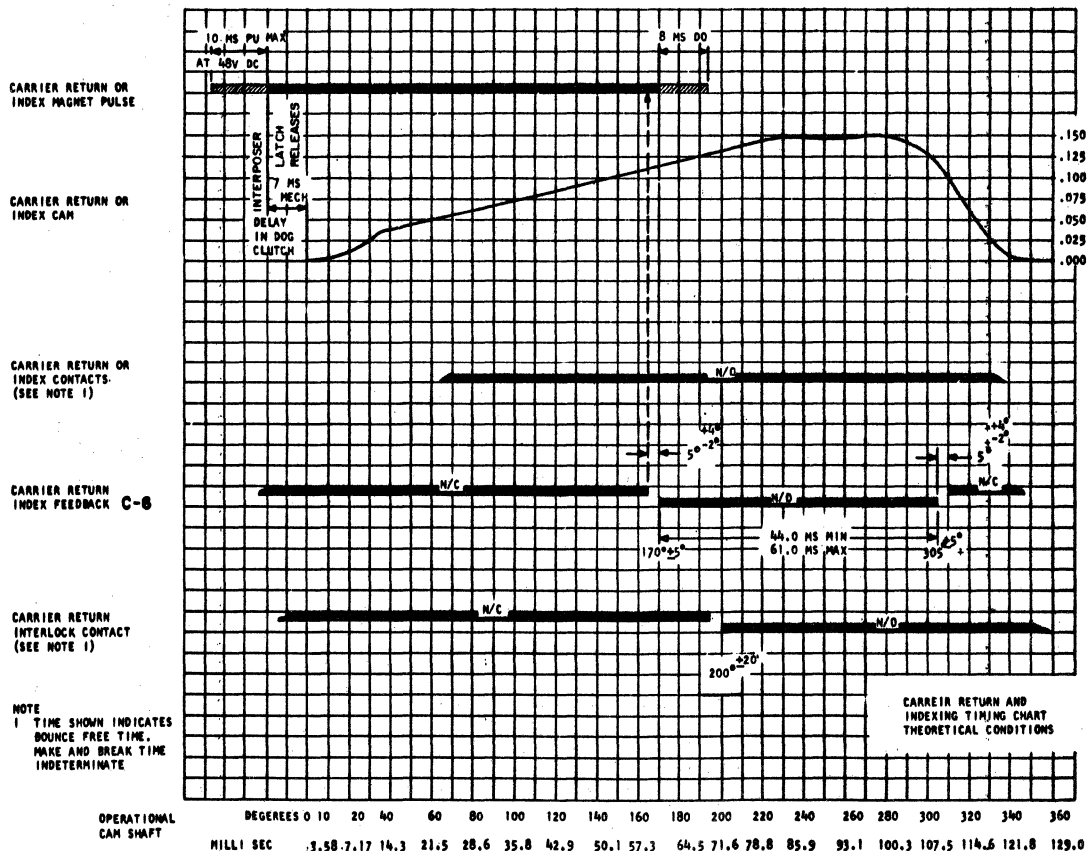
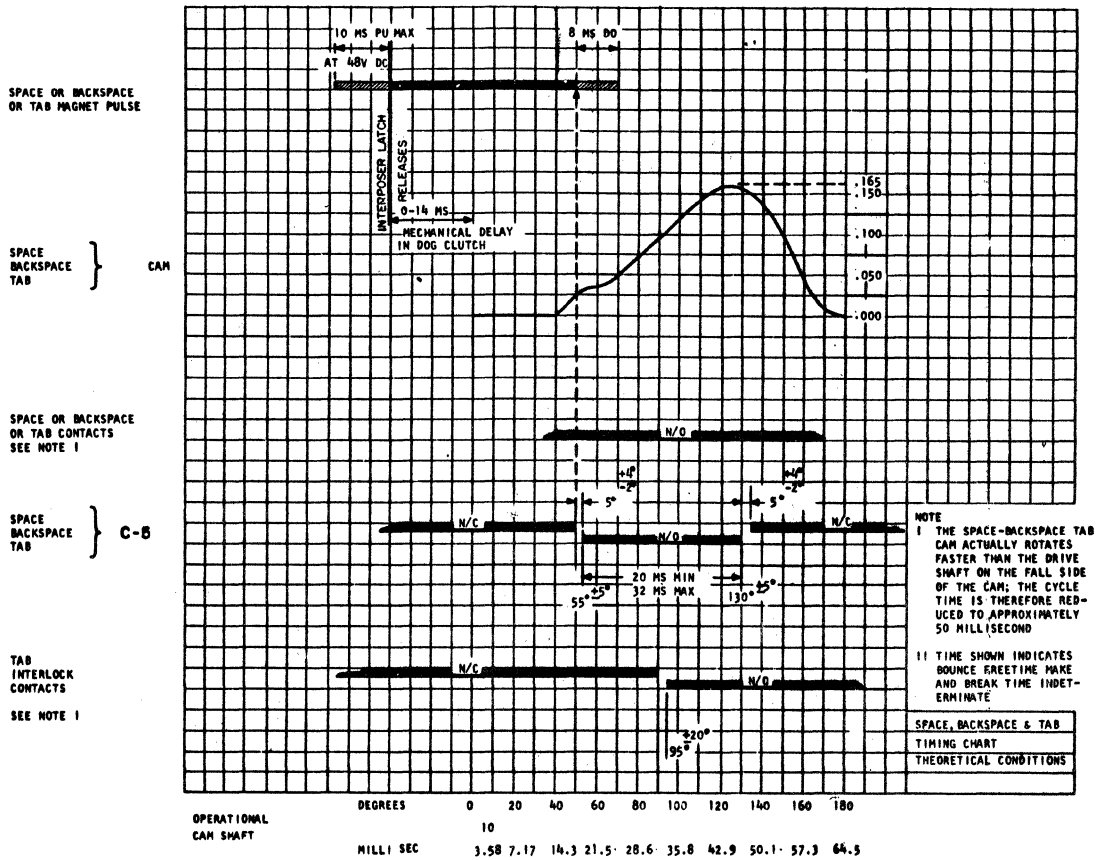
LOWER CASE SHIFT FEEDBACK C-4

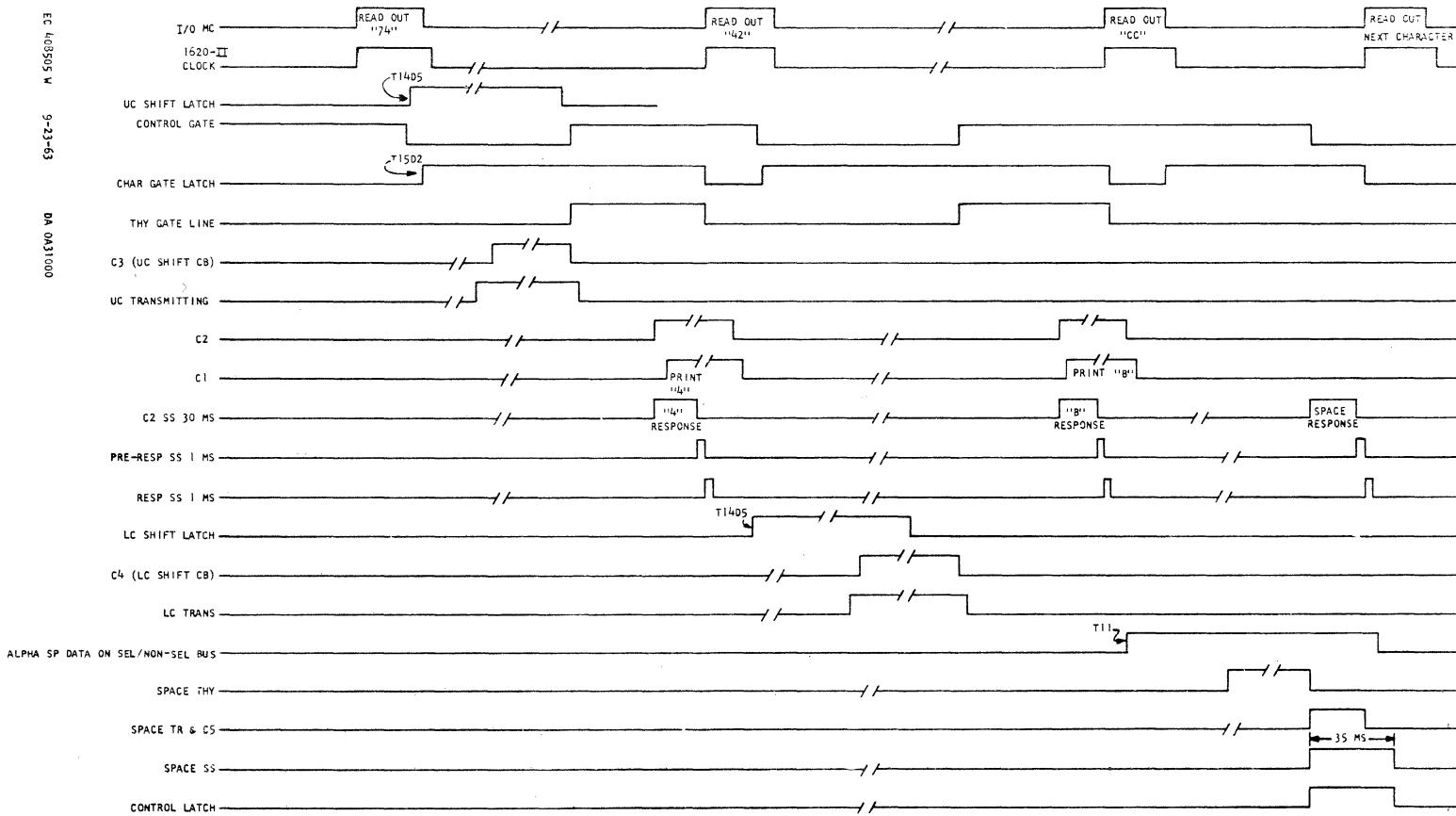


NOTES: TIME SHOWN INDICATES BOUNCE FREE TIME, MAKE AND BREAK TIME INDETERMINATE.

LOWER CASE SHIFT TIMING CHART THEORETICAL CONDITIONS

OPERATIONAL CAM SHAFT	DEGREES	MILLI SEC
	0	3.58
	10	7.17
	20	14.3
	30	21.5
	40	28.6
	50	35.8
	60	42.9
	70	50.1
	80	57.3
	90	64.5





OPERATION: WRITE ALPHA. INITIALLY IN LOWER CASE MODE. SHIFT TO UPPER CASE PRINT '4' SHIFT TO LOWER CASE PRINT 'B' ALPHA SPACE AND READ OUT NEXT CHARACTER FROM MEMORY

1. READ OUT UPPER CASE MODE CHARACTER
2. INITIATE SHIFT TO UPPER CASE
3. WAIT FOR TYPE ELEMENT TO SHIFT
4. PRINT '4' AS PER MBR DATA

1. READ OUT LOWER CASE MODE CHARACTER
2. INITIATE SHIFT TO LOWER CASE
3. WAIT FOR TYPE ELEMENT TO SHIFT
4. PRINT 'B' AS PER MBR DATA

1. READ OUT ALPHA SPACE DATA
2. BLOCK OUTPUT OF CHAR GATE LATCH TO KEEP FROM GATING SELECTION THYRATONS WITH ALPHA DATA
3. ENERGIZE SPACE SOLENOID
4. INITIATE RESPONSE WITH SPACE TRANSMITTING CONTRACTS TO CPU
5. RESPONSE STARTS CPU CLOCK TO READ OUT NEXT MEMORY POSITION

OPERATION: PRINT INVALID
PARITY FLAGGED CHARACTER

2159823

731 FUNCTIONAL TIMING CHART

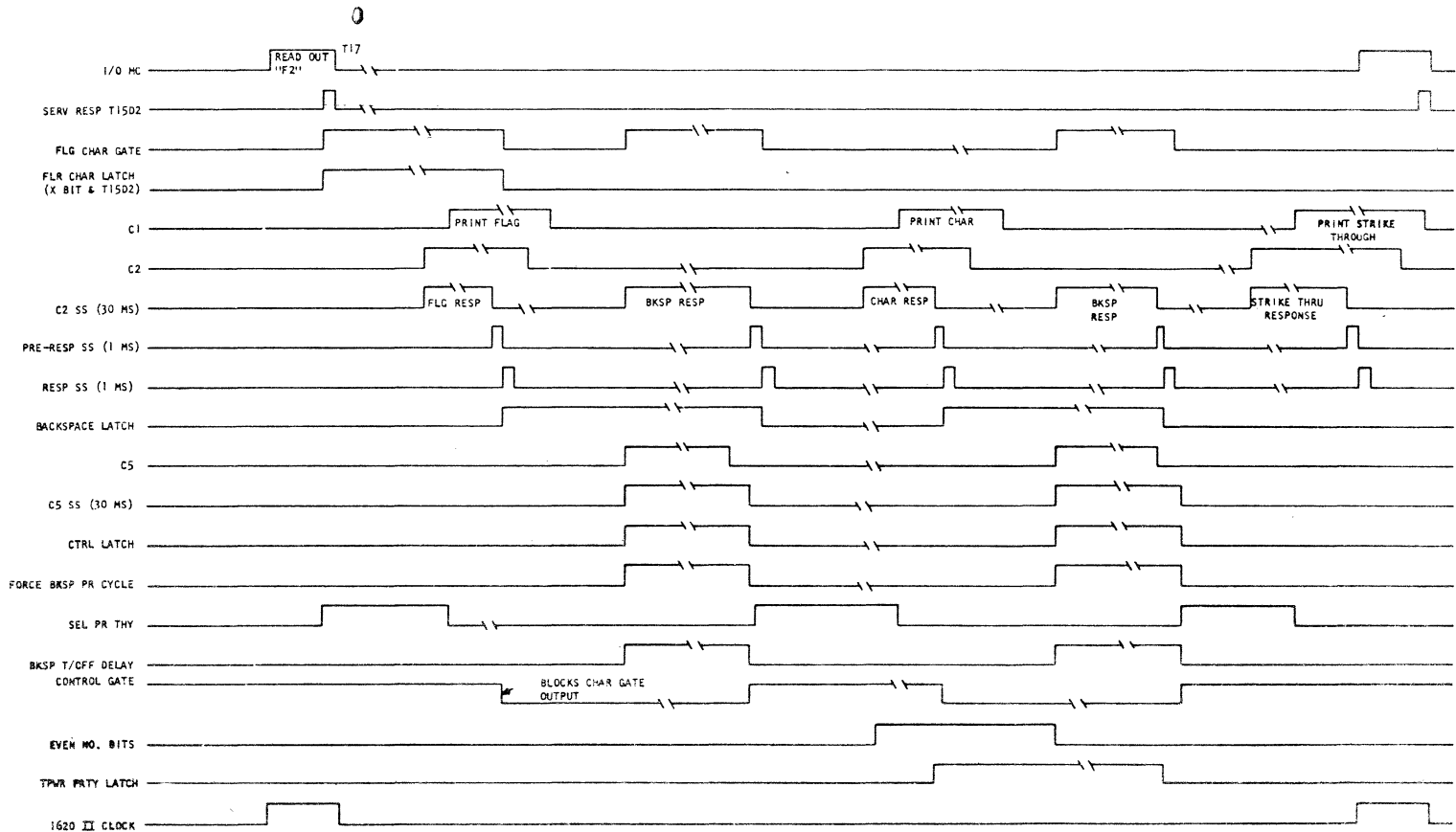
1620 II

100158.2

EC 4408905 W

9-23-63

DA 0A31000

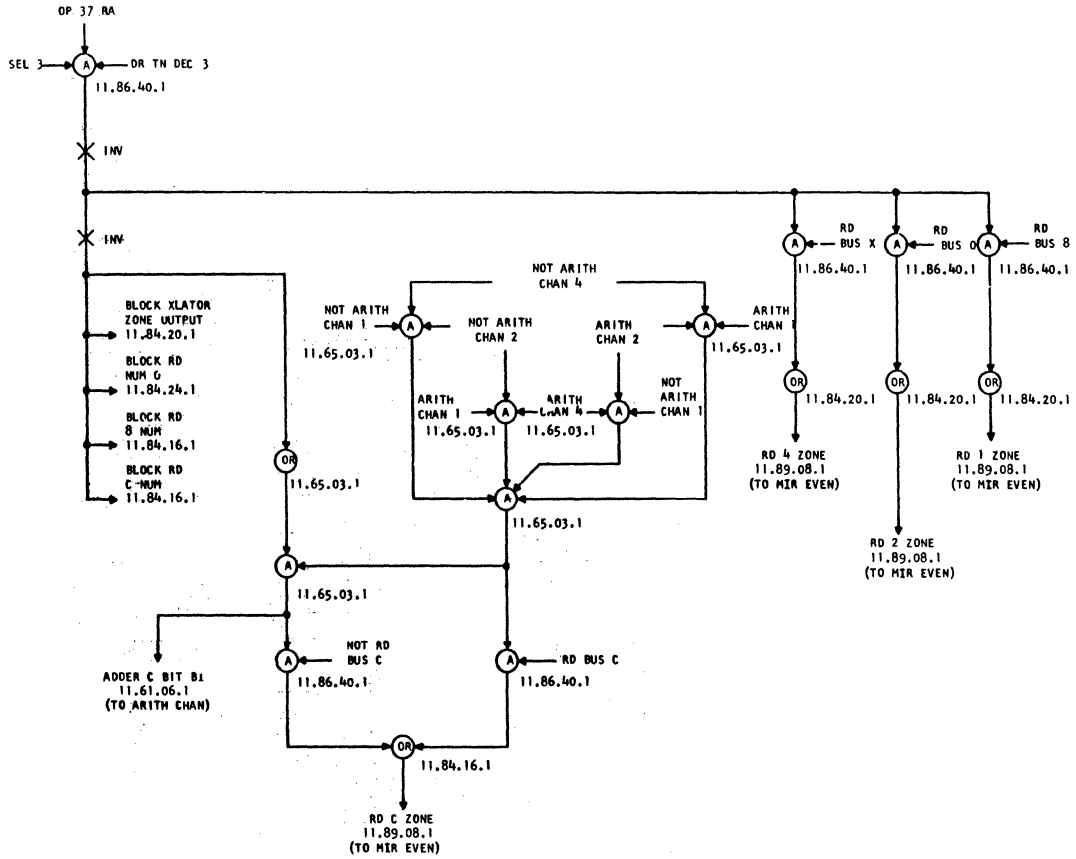


1. RECOGNIZE "X" BIT FOR FLAG PRINTING. PRINT FLAG
2. INITIATE BACKSPACE OPERATION. BLOCK RESPONSE TO CPU.

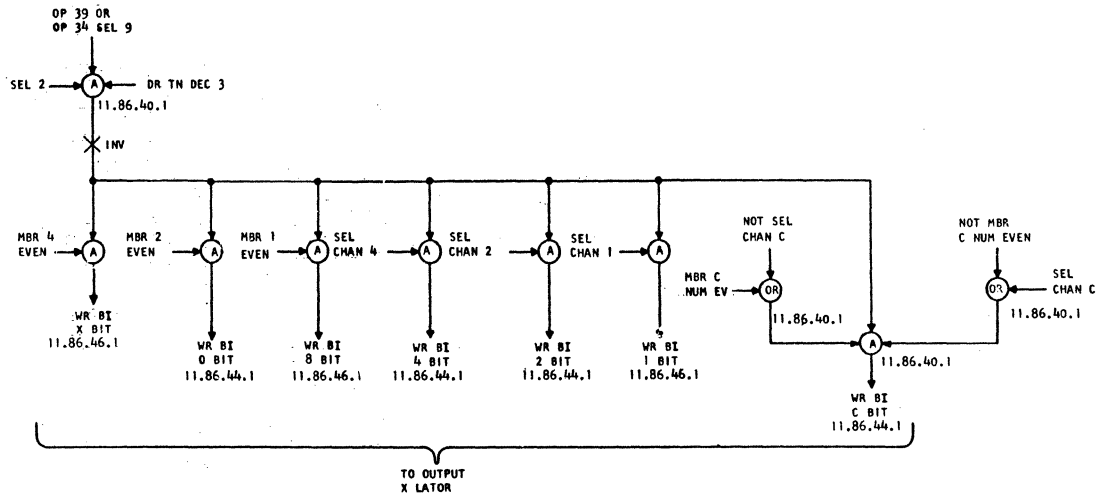
1. WAIT FOR BACKSPACE TO FINISH.
2. TURN ON CHAR GATE TO PRINT CHARACTER UNDER FLAG.
3. DURING CHARACTER PRINTING CHECK PARITY
4. IF BAD PARITY; BLOCK RESPONSE TO CPU INITIATE BACKSPACE.

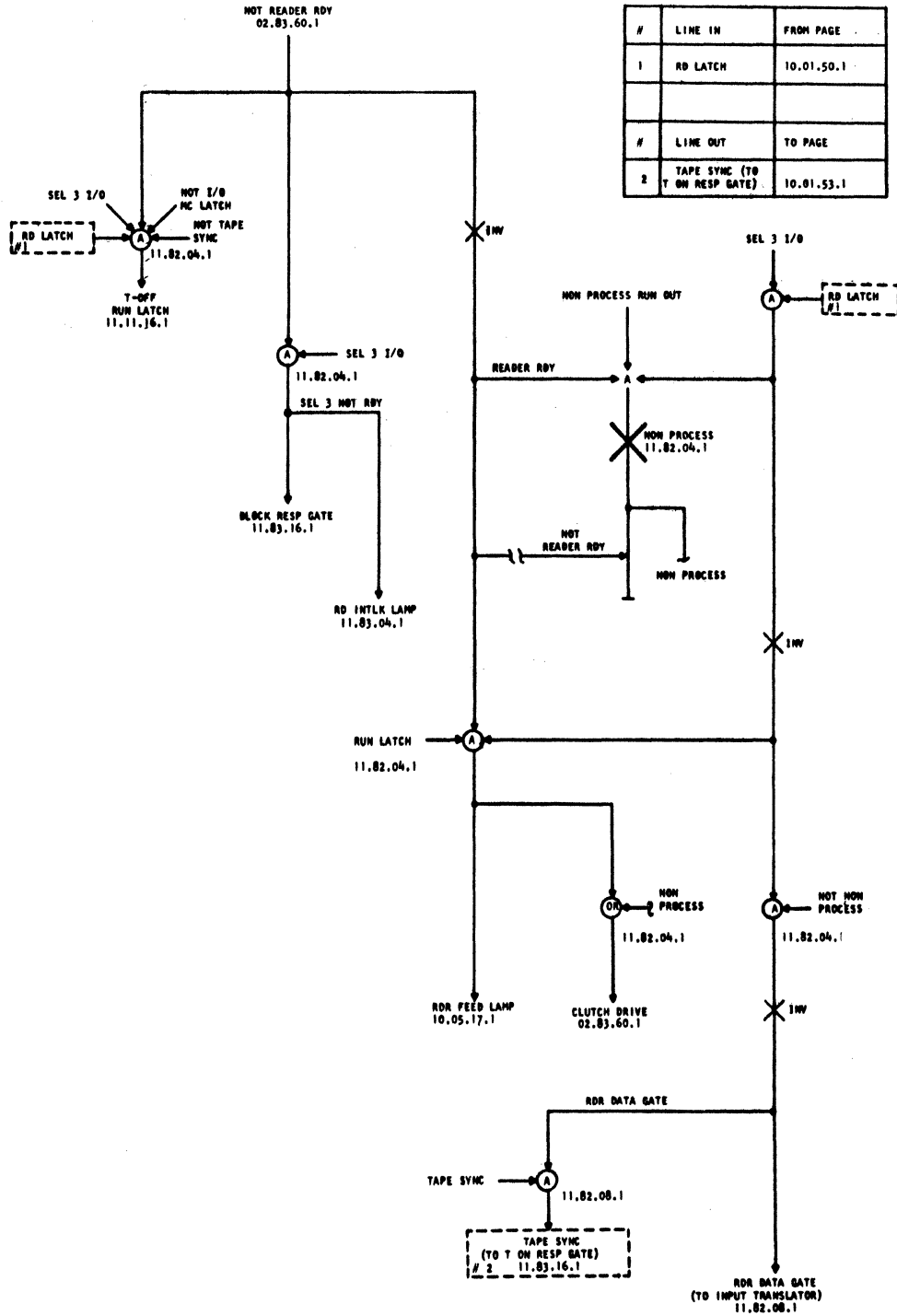
1. WAIT FOR BACKSPACE TO FINISH
2. TURN ON CHAR GATE TO PRINT STRIKE THROUGH
3. FORCE STRIKE THROUGH PRINTING
4. SHIP RESPONSE TO CPU TO BRING OUT NEXT CHARACTER TO BE PRINTED.

RD BINARY
37 XXXXX 03300

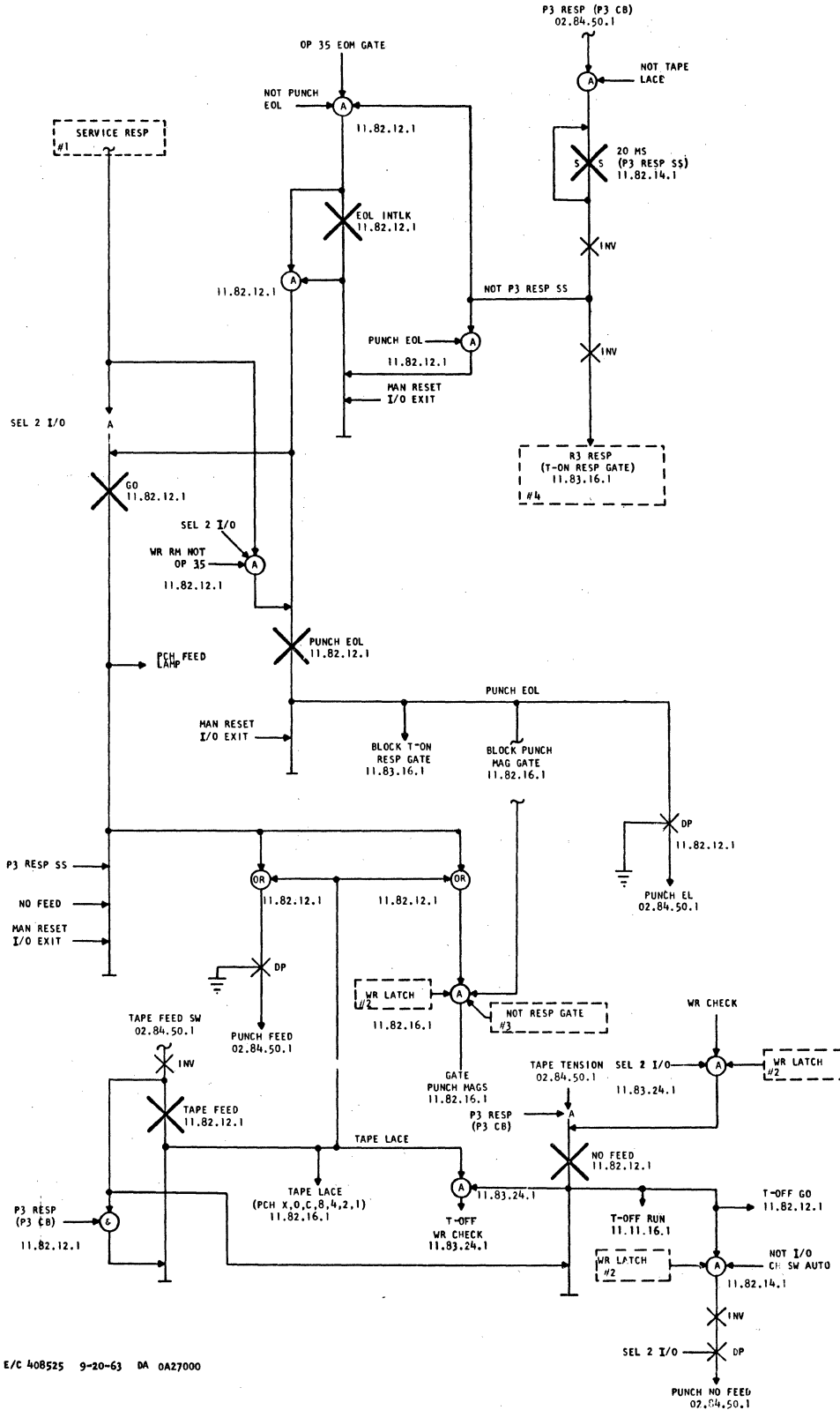


WR BINARY
39 XXXXX 03200

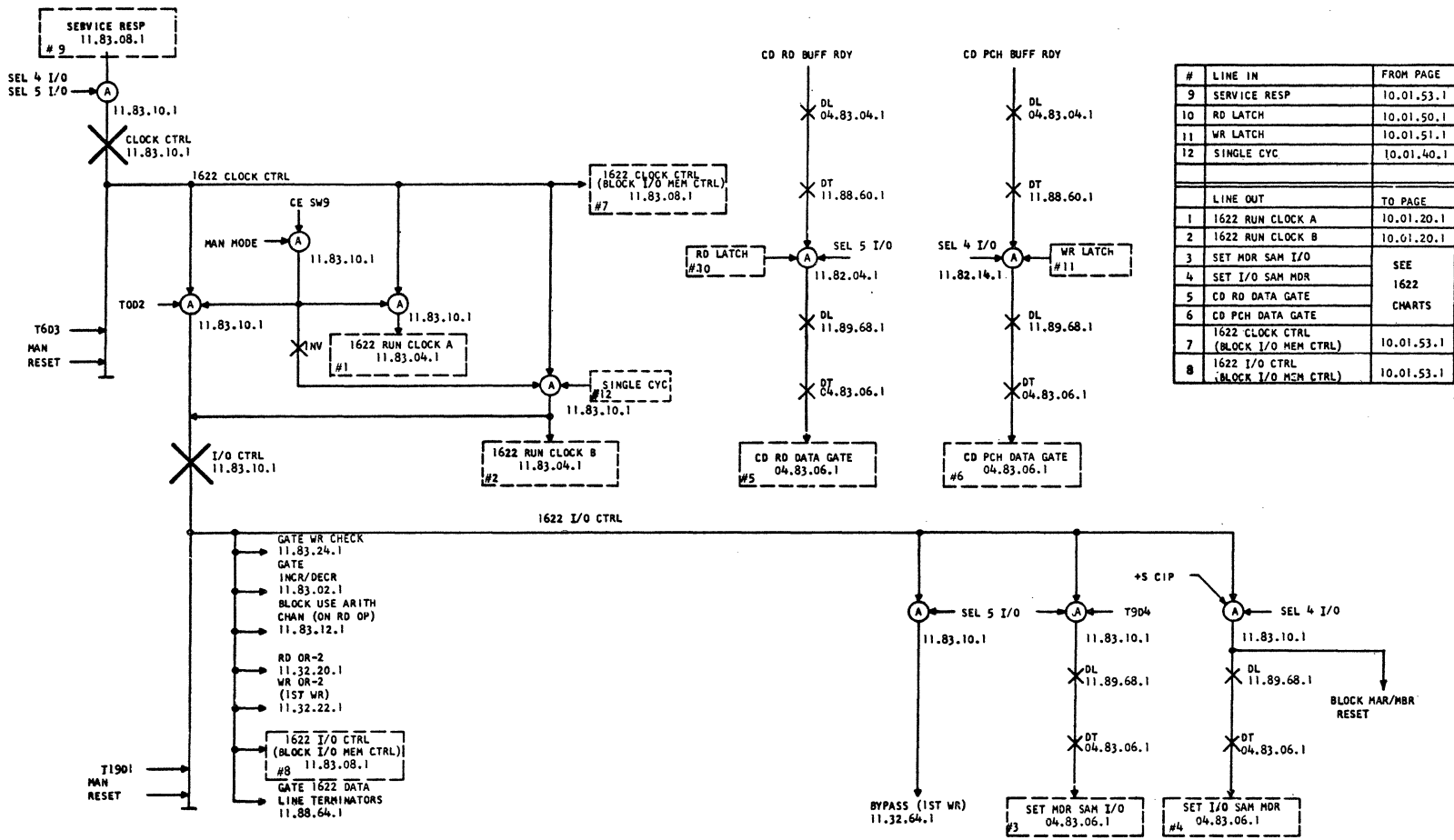




#	LINE IN	FROM PAGE
1	SERVICE RESP	10.01.53.1
2	WR LATCH	10.01.51.1
3	RESP GATE	10.01.53.1
#	LINE OUT	TO PAGE
4	P3 RESP (T-ON RESP GATE)	10.01.53.1

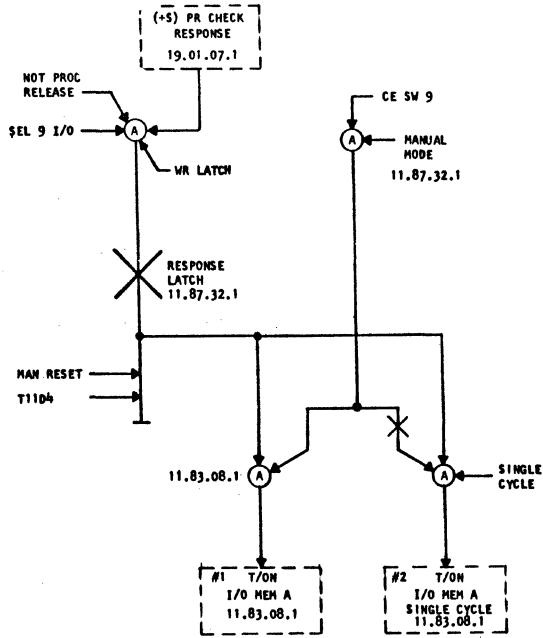


E/C 408525 9-20-63 DA 0A27000



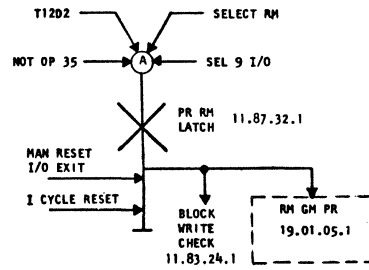
#	LINE IN	FROM PAGE
9	SERVICE RESP	10.01.53.1
10	RD LATCH	10.01.50.1
11	WR LATCH	10.01.51.1
12	SINGLE CYC	10.01.40.1
LINE OUT	TO PAGE	
1	1622 RUN CLOCK A	10.01.20.1
2	1622 RUN CLOCK B	10.01.20.1
3	SET MDR SAM I/O	SEE 1622 CHARTS
4	SET I/O SAM MDR	
5	CD RD DATA GATE	
6	CD PCH DATA GATE	
7	1622 CLOCK CTRL (BLOCK I/O MEM CTRL)	10.01.53.1
8	1622 I/O CTRL (BLOCK I/O MEM CTRL)	10.01.53.1

CLOCK CONTROL

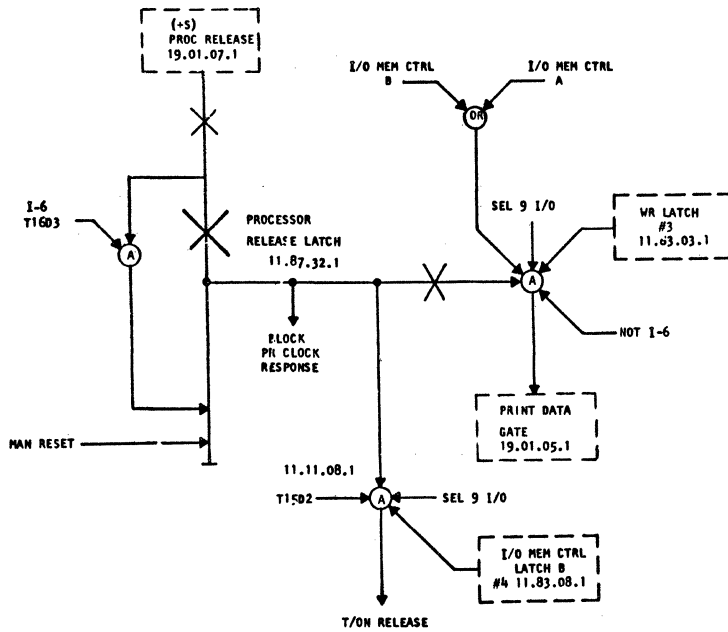


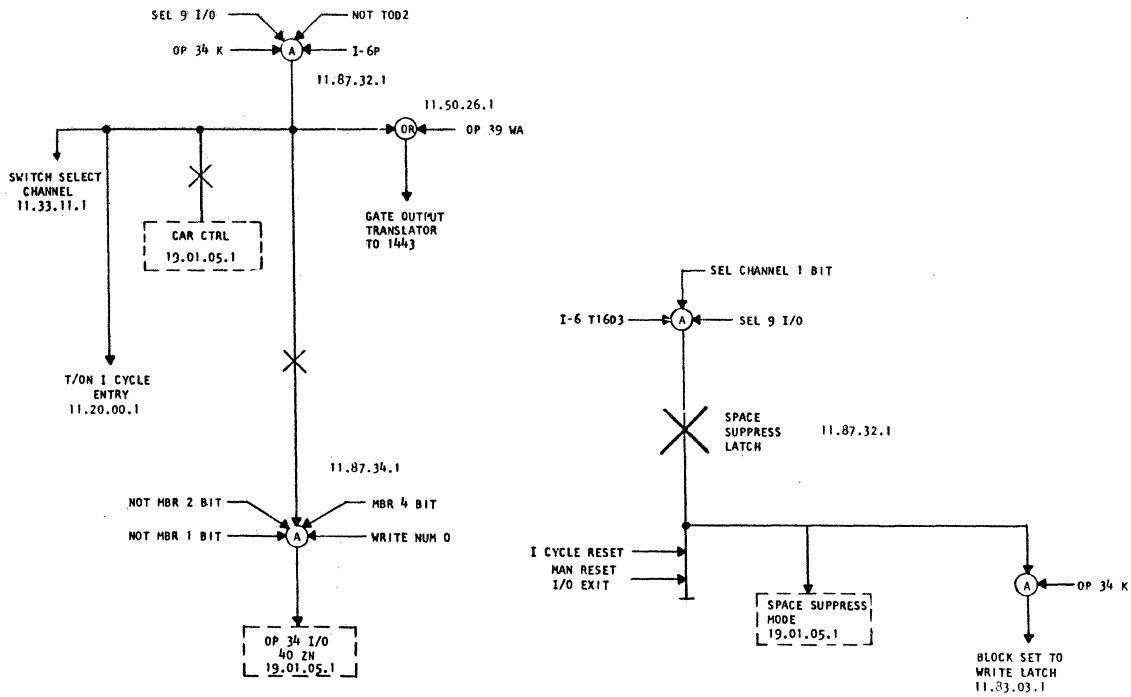
#	LINE IN	FROM PAGE
1	T/ON I/O MEM A	10.01.53.1
2	T/ON I/O MEM A SINGLE CYCLE	10.01.53.1
3	WRITE LATCH	10.01.51.1
4	I/O MEM CTRL LATCH B	10.01.53.1

END DATA TRANSFER



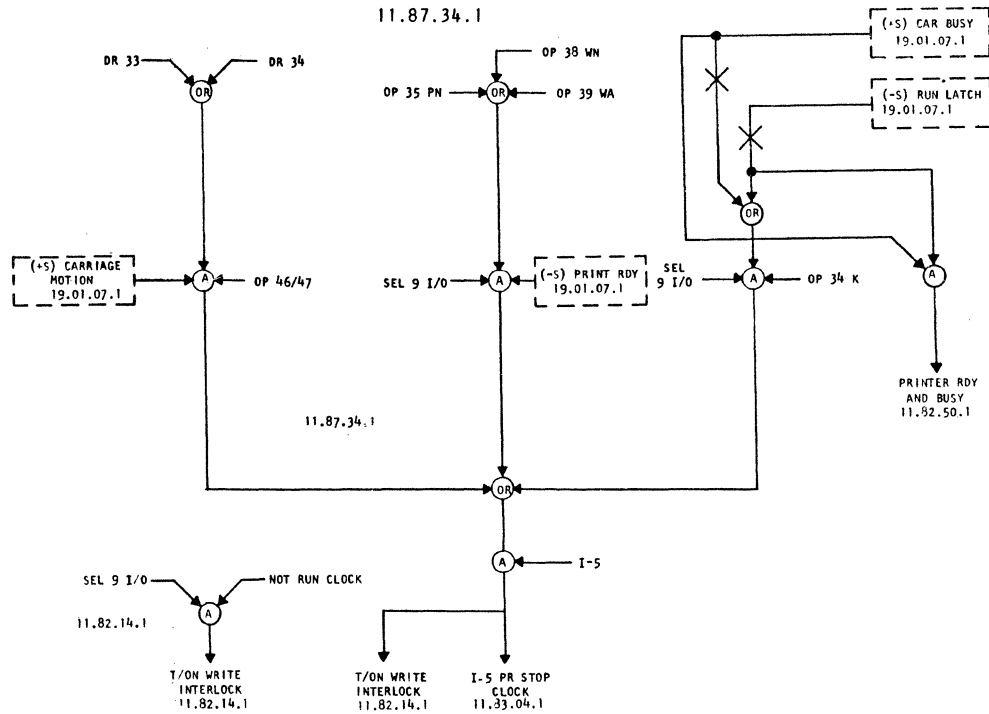
1443 RELEASE

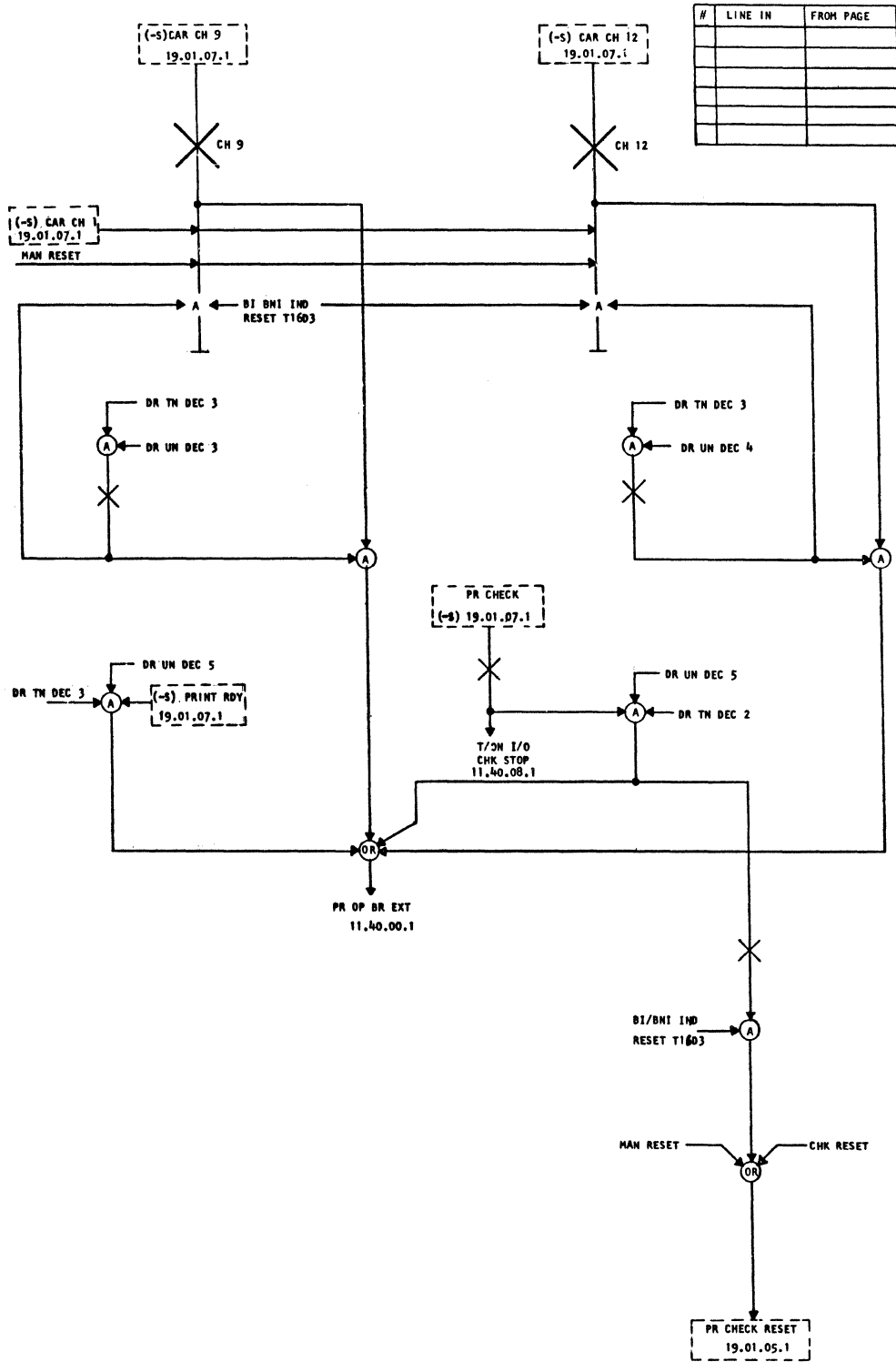




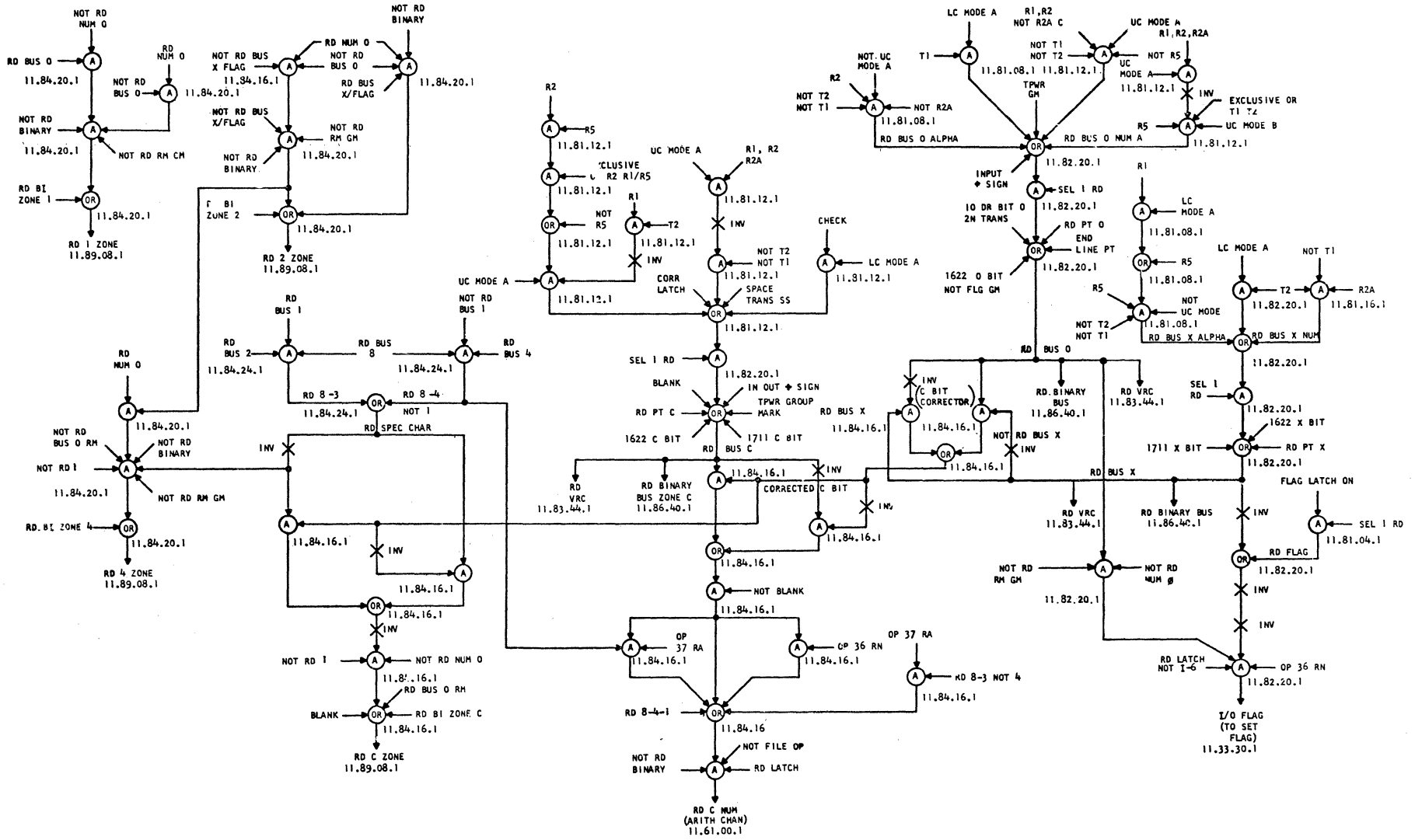
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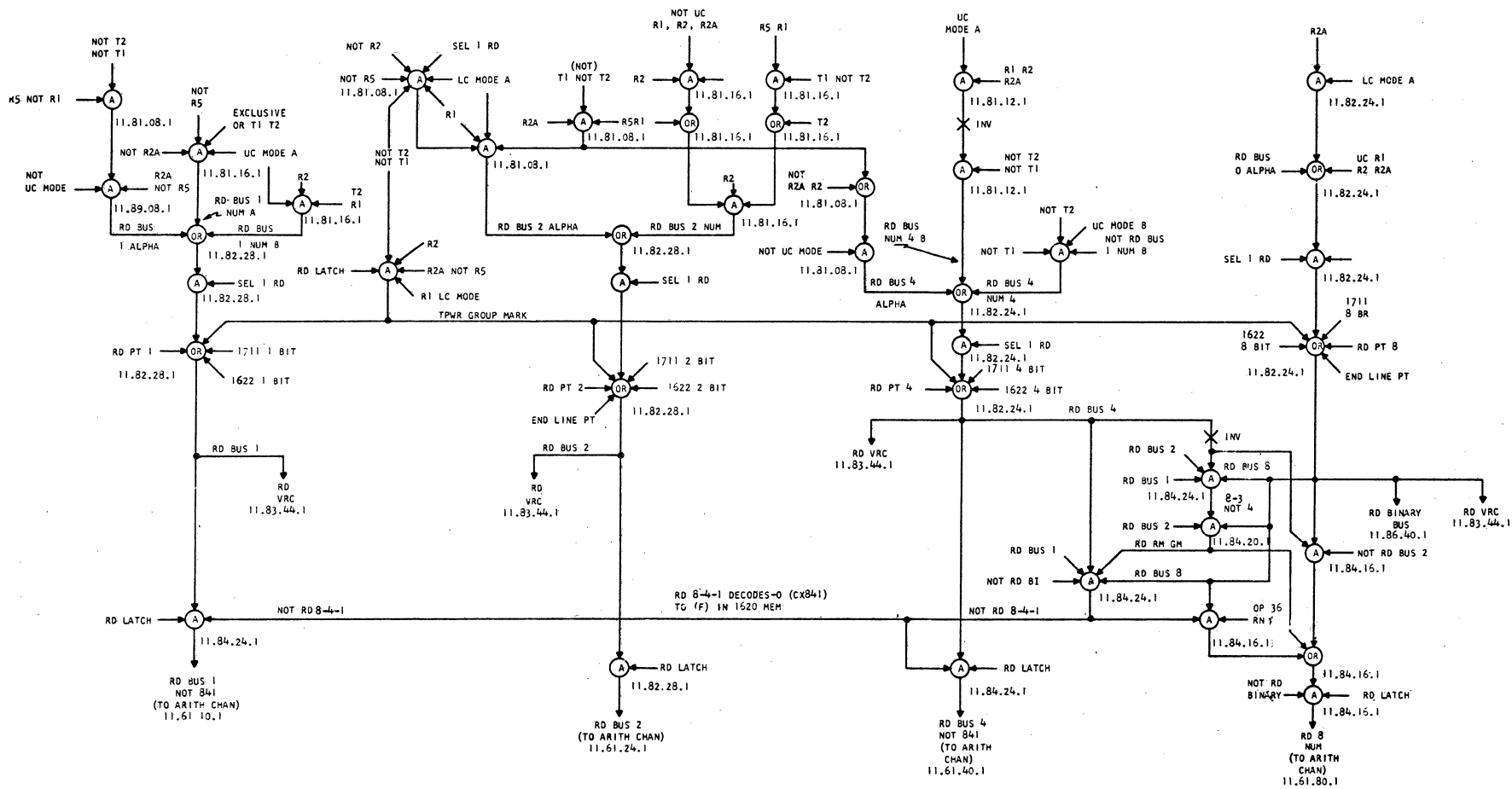
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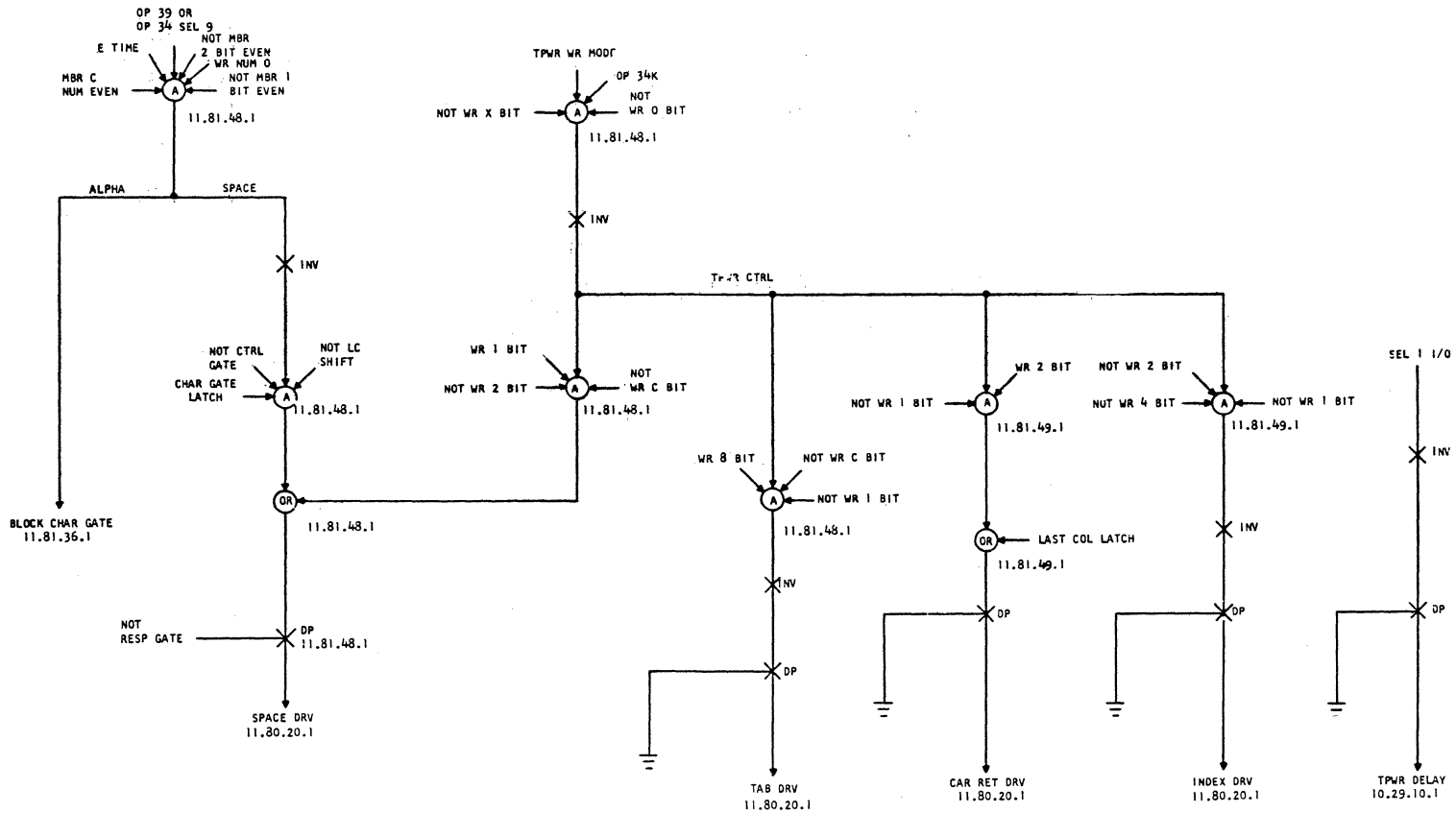


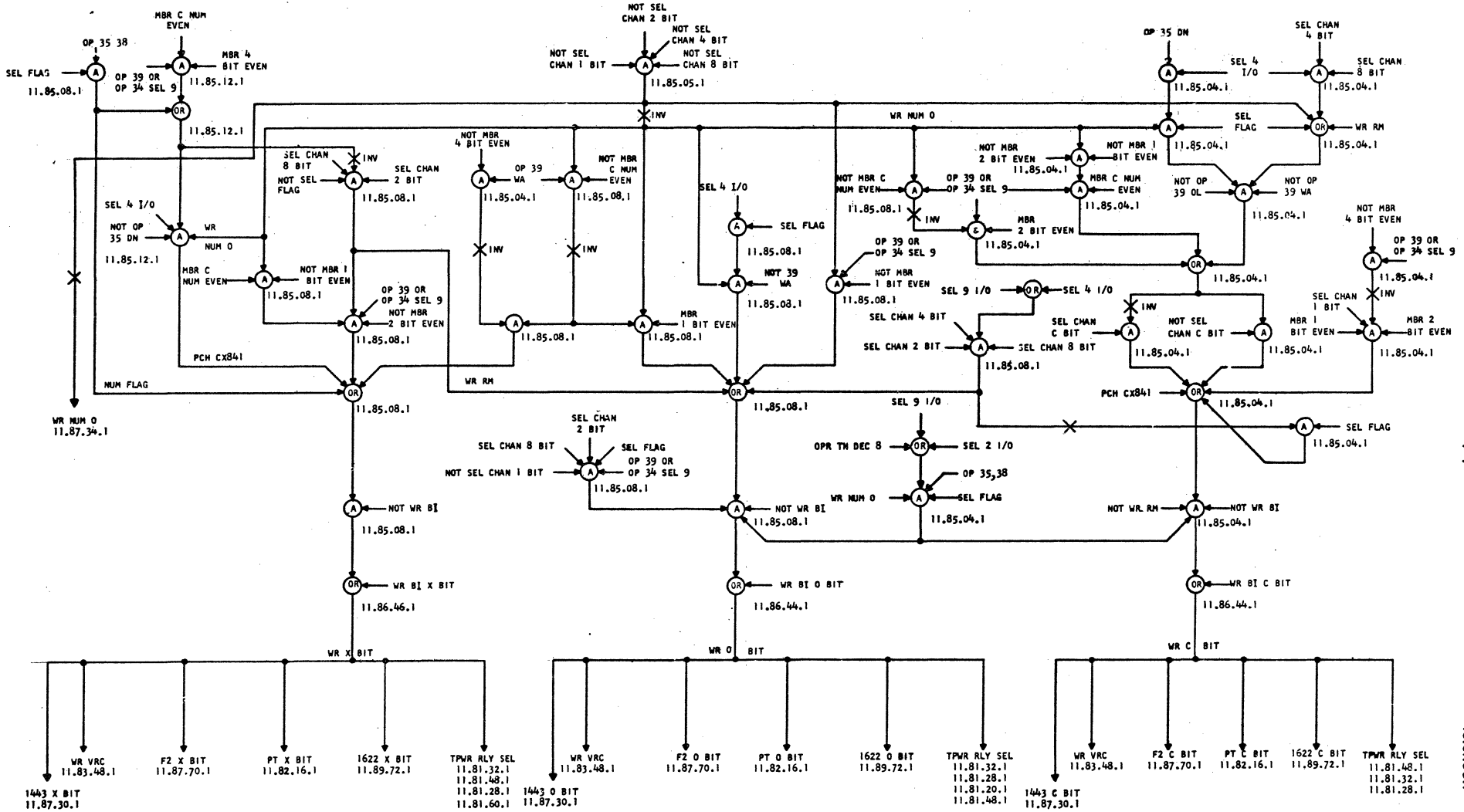


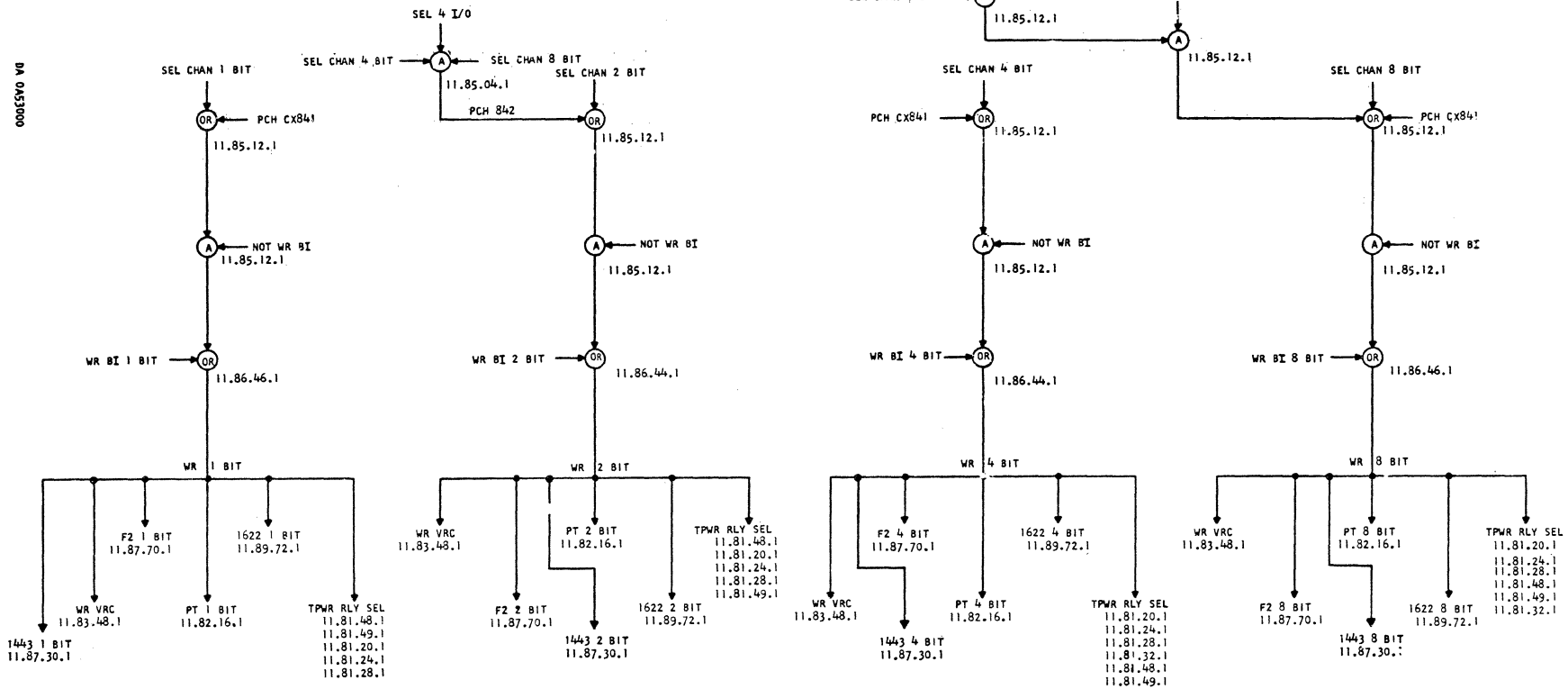
#	LINE IN	FROM PAGE



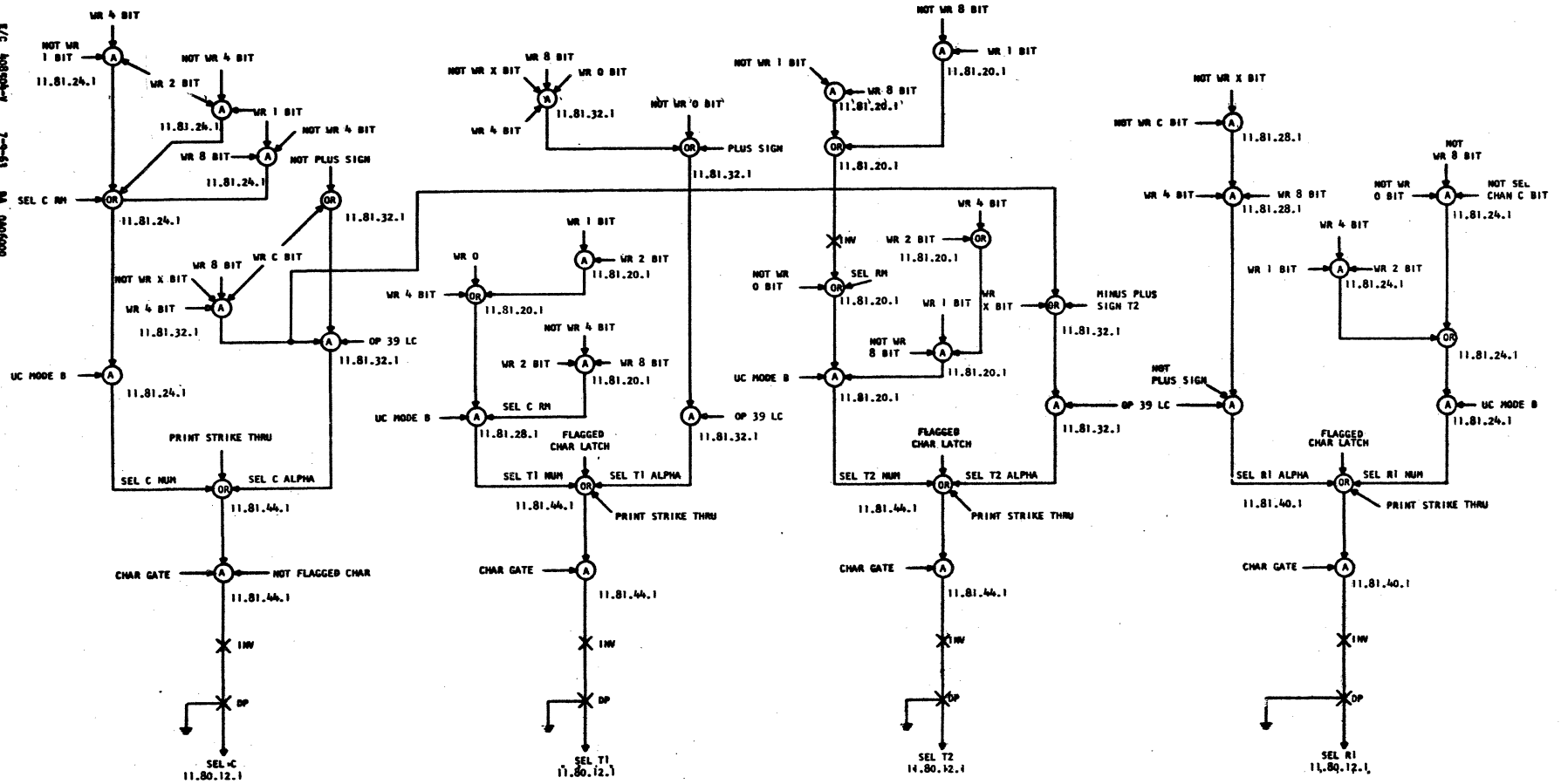








2/2 108584-7 7-9-63 BA 0406000

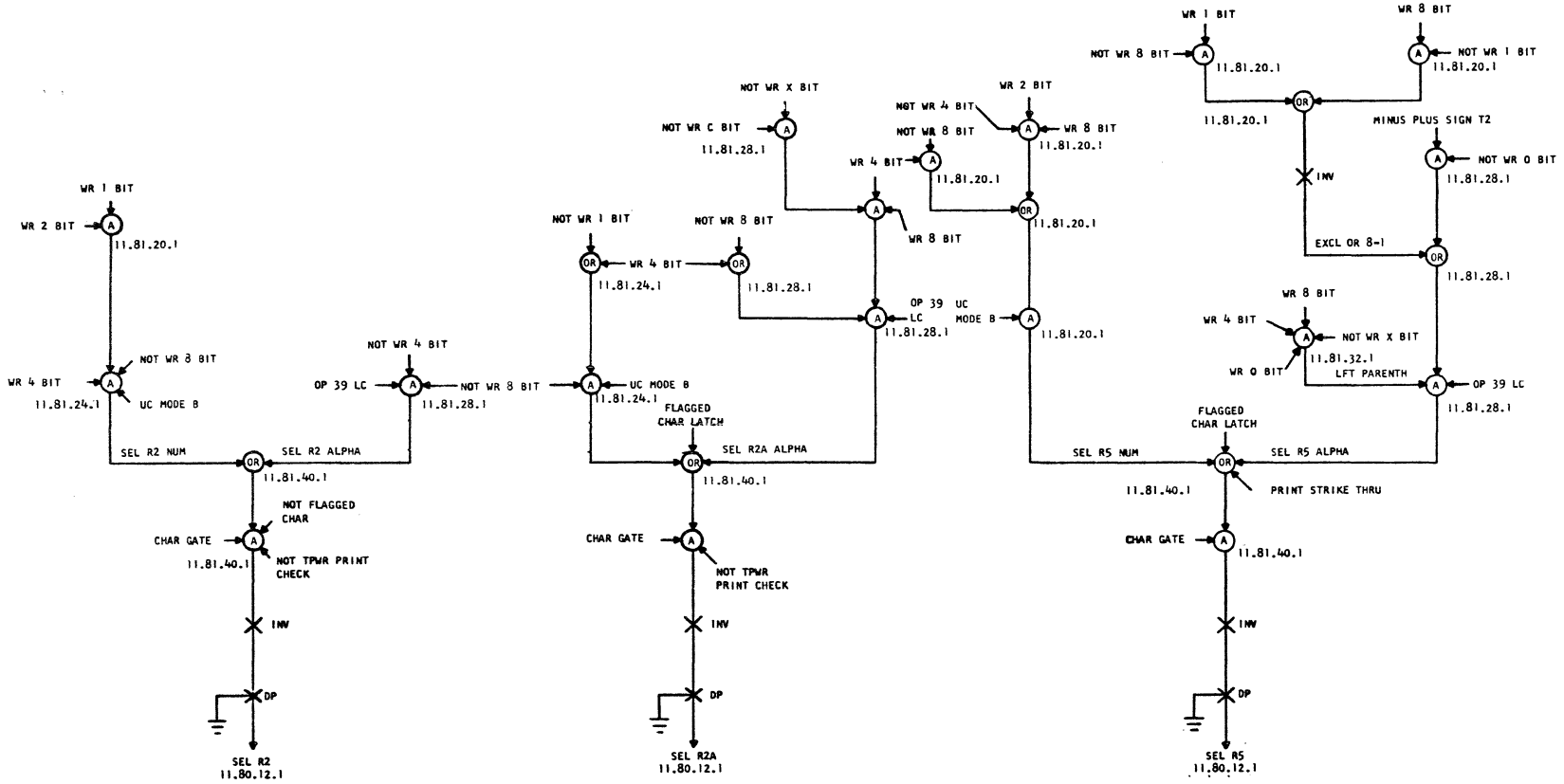


2139183

TPWR SEL RELAYS
C,T1,T2,R1

1620 II

NO. 01.97.1

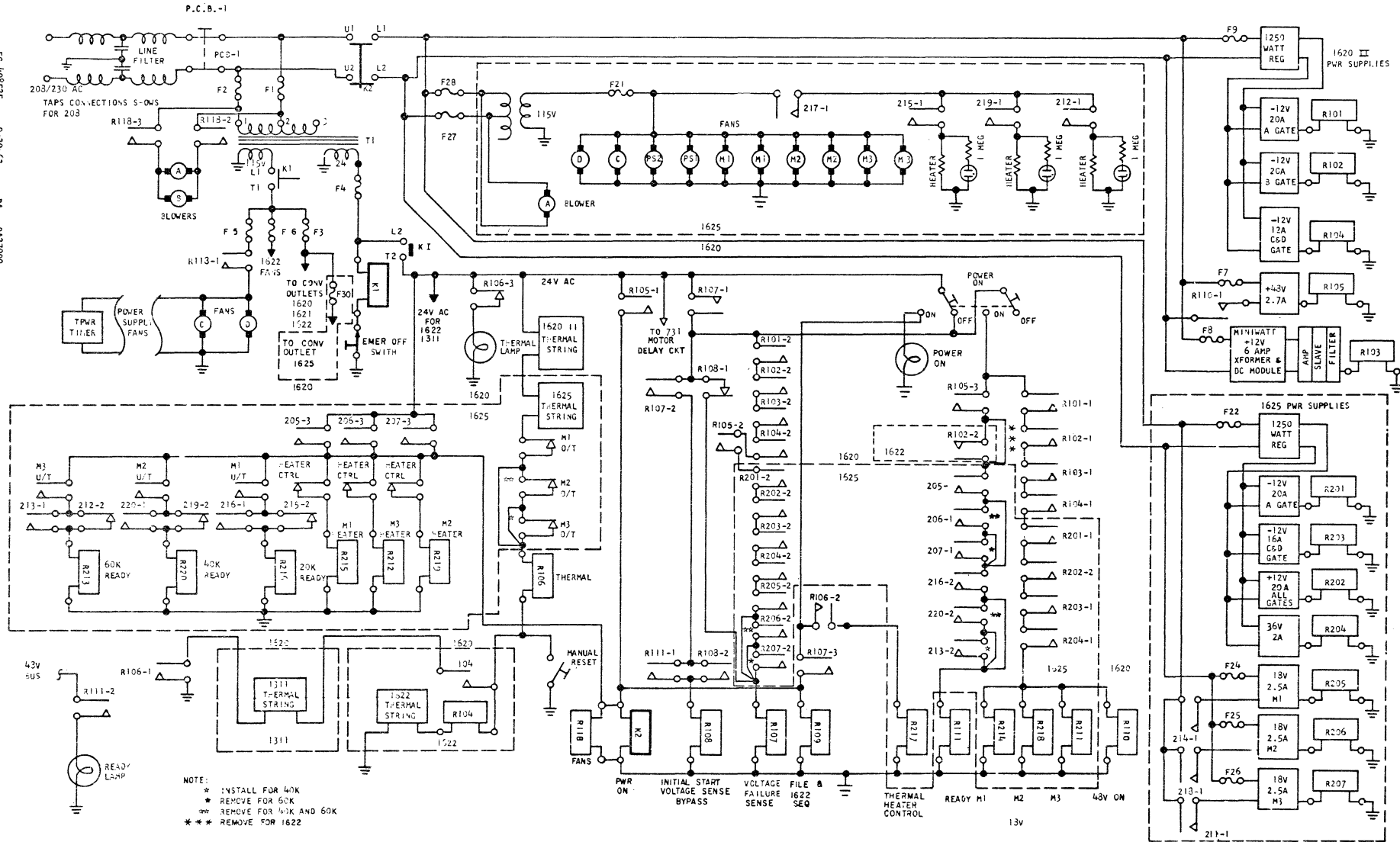


2159184

TPWR SEL RELAY
R2, R2A, R5

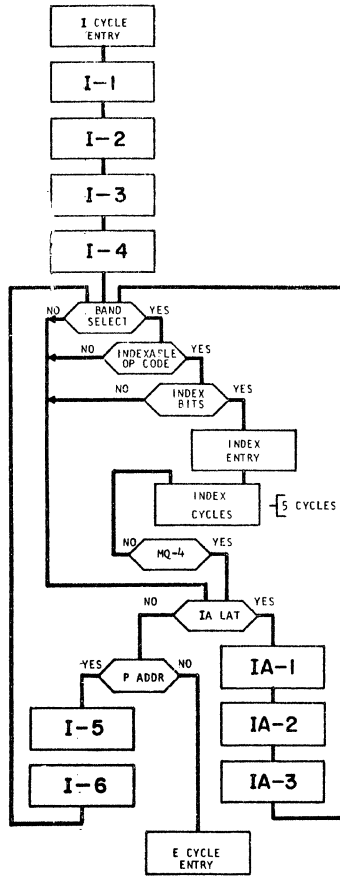
1620 II

1001981

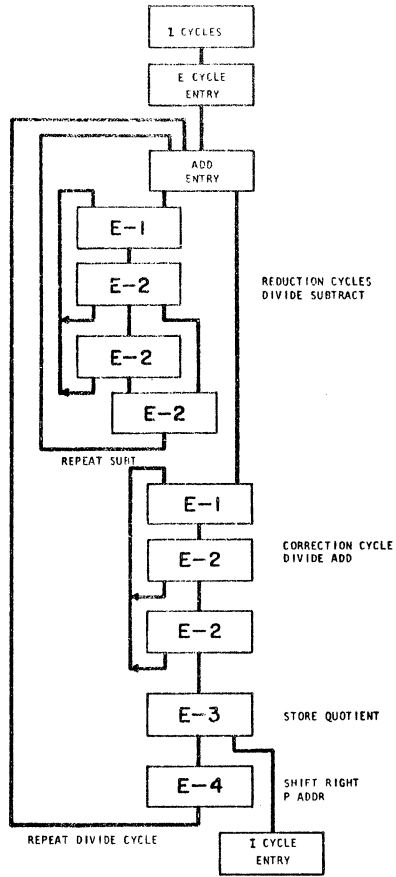


<u>LOGIC TITLE OR FUNCTION</u>	<u>LOGIC PAGE NUMBER</u>	<u>LOGIC TITLE OR FUNCTION</u>	<u>LOGIC PAGE NUMBER</u>
ADD ENTRY ADD MODE	10.00.56.1	O'FLO/U'FLO O'FLO CORRECT CHANGE MODE	10.00.93.1
ADD SUBT COMPARE E1 E2	10.00.57.1	OP 33 CLEAR FLAG OP 32 SET FLAG	10.00.55.1
ADD, SUBTRACT, COMPARE	10.00.12.1	OP 34 TPWR CTRL	10.01.56.1
BASIC I AND E CYCLE SEQUENCES	10.00.02.1	OP 34 TRANSLATOR TPWR CTRL	10.01.94.1
BINARY OPERATIONS	10.00.16.1	OP 71 MF	10.00.64.1
BRANCH AND LOAD OR STORE INDEX REG	10.00.06.1	OP 72 TNS	10.00.65.1
BRANCH AND MODIFY INDEX REGISTER	10.00.05.1	OP 73 TNF	10.00.66.1
BRANCH I CYCLES	10.00.33.1	OP 96 OTD CONVERT OP 97 DTO CONVERT	10.01.02.1
BRANCH I CYCLES 1-6	10.00.34.1	OP 90/91	10.01.00.1
BRANCH ON SINGLE CHARACTER	10.00.07.1	OPS 06 F TF 07 F BT 26TF 16TFM 27BT	10.00.52.1
CLOCK	10.01.20.1	OPS 43BD, 45BNR, 55BNG	10.00.51.1
CONSOLE CTRL SS	10.01.40.1	OPS 92/93/94/95	10.01.01.1
CORRECTION KEY TPWR PARITY	10.01.54.1	OUTPUT TRANSLATOR X, 0, C	10.01.95.1
DATA FLOW	10.00.00.1	OUTPUT TRANSLATOR 1, 2, 4, 8	10.01.96.1
DECIMAL TO OCTAL CONVERSION	10.00.17.1	PAPER TAPE PUNCH - 1624	10.00.23.1
DIGIT FORCE	10.00.88.1	PAPER TAPE READER - 1621	10.00.22.1
DISPLAY MAR	10.01.43.1	POWER SEQUENCE	10.04.49.1
DIV E-1 AND E-2	10.00.62.1	PRINT ELEMENT CHAR ARRT C1, C2 TIMINGS	10.01.57.1
DIV E-3 AND E-4	10.00.63.1	PROPAGATION OF CARRY IN MPY PARTIAL	10.00.13.2
DIVIDE	10.00.15.1	PUNCH - 1622	10.00.21.1
DIVIDE	10.00.15.2	RD LATCH	10.01.50.1
E RING	10.00.50.1	RD WR BINARY PT	10.01.64.1
EXP ADD SUB COMP	10.00.80.1	READ TYPEWRITER	10.00.18.1
EXP MODIFY E-1 & E-2	10.00.87.1	READER - 1622	10.00.20.1
EXP MODIFY ENTRY	10.00.86.1	RESP GATE I/O MEM CTRL A AND B	10.01.53.1
EXP XMIT	10.00.81.1	RESULT XMIT	10.00.83.1
F'MUL	10.00.89.1	RUN STOP MANUAL	10.01.41.1
FALSE XMIT	10.00.91.1	R/S, RELEASE	10.01.49.1
FP LD AND FDIV	10.00.92.1	SAVE	10.01.44.1
FRACTION ADD	10.00.85.1	SCAN	10.00.82.1
FRACTION COMPARE	10.00.90.1	SELECT CHANNEL GENERATION	10.01.21.1
I CYC MARS CTRL	10.01.24.1	SHIFT AND ZERO FILL	10.00.84.1
I CYCLES	10.00.03.1	TPWR SEL RELAY R2, R2A, R5	10.01.98.1
I CYCLES 1-2 - 1-5	10.00.31.1	TPWR SEL RELAYS C, T1, T2, R1	10.01.97.1
I CYCLES 1-6	10.00.32.1	TRANSMIT DIGIT AND FLAG OPS	10.00.10.1
I CYCLES I ENTRY 1-1	10.00.30.1	TRANSMIT NUMERIC STRIP AND FILL	10.00.11.1
INDEX EXECUTE TRANSMIT AND ADD OPS	10.00.45.1	TRANSMIT RECORD	10.00.09.1
INDEX SELECTION AND ENTRY	10.00.43.1	TYPEWRITER	10.01.52.1
INDEXING AND INDIRECT ADDRESSING OPS	10.00.04.1	WR LATCH	10.01.51.1
INDEXING CYCLES	10.00.44.1	WRITE TYPEWRITER	10.00.19.1
INDIRECT ADDRESSING	10.00.36.1	XBR 1-1 TO 1-4 LOADING	10.00.40.1
INDIRECT ADDRESSING	10.00.37.1	XBR 1-5 TO 1-6 LOADING	10.00.41.1
INPUT TRANSLATOR 1, 2, 4, 8	10.01.91.1	XBR IA LOADING	10.00.42.1
INPUT TRANSLATOR X, 0, C	10.01.90.1	XBR SET AND RESET WITH LOADING	10.01.46.1
INSERT	10.01.48.1	XMIT E-1 AND E-2	10.00.54.1
L.D. E-1 AND E-2	10.00.61.1	XMIT ENTRY AND MODE	10.00.53.1
LOAD DIVIDEND	10.00.14.1	1621	10.01.66.1
MACHINE TIMING CYCLE	10.00.01.1	1622	10.01.70.1
MARS RD/WR. IR-1 IR-2 IR-3	10.01.25.1	1624	10.01.68.1
MARS RD/WR. OR-1, OR-2	10.01.26.1	2 DIG CTRL, FL-1, FL-2, DR CTRL	10.01.22.1
MARS RD/WR. OR-3, OR-4	10.01.27.1	731 LOWER/UPPER CASE SHIFT TIMING	10.01.57.2
MARS RD/WR. OR-5, CR-1	10.01.28.1	731 OPR TIMING SP/BKSP/TAB/CARR RET/I	10.01.57.3
MARS RD/WR. PR-1, PR-2	10.01.29.1		
MEM INHIBIT REG (MIR) ODD	10.01.30.1		
MPY E1/E3/E4	10.00.59.1		
MPY E2	10.00.60.1		
MPY/LD CLEAR PRODUCT AREA-E5	10.00.58.1		
MQ REG & MQ COUNTER	10.01.23.1		
MULTIPLY AND OCTAL TO DECIMAL CONV	10.00.13.1		

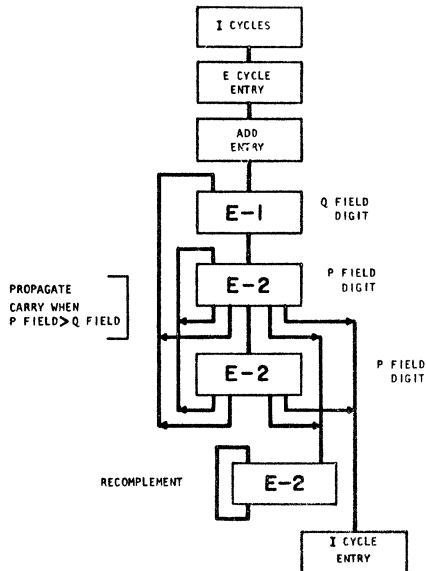
I CYCLES - INDEXING - IA



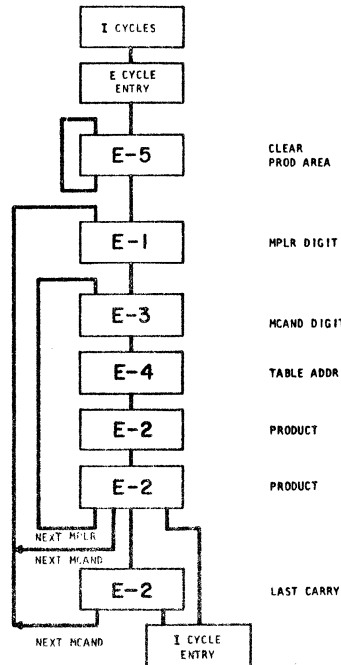
DIVIDE

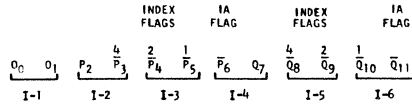


ADD - SUBTRACT - COMPARE



MULTIPLY



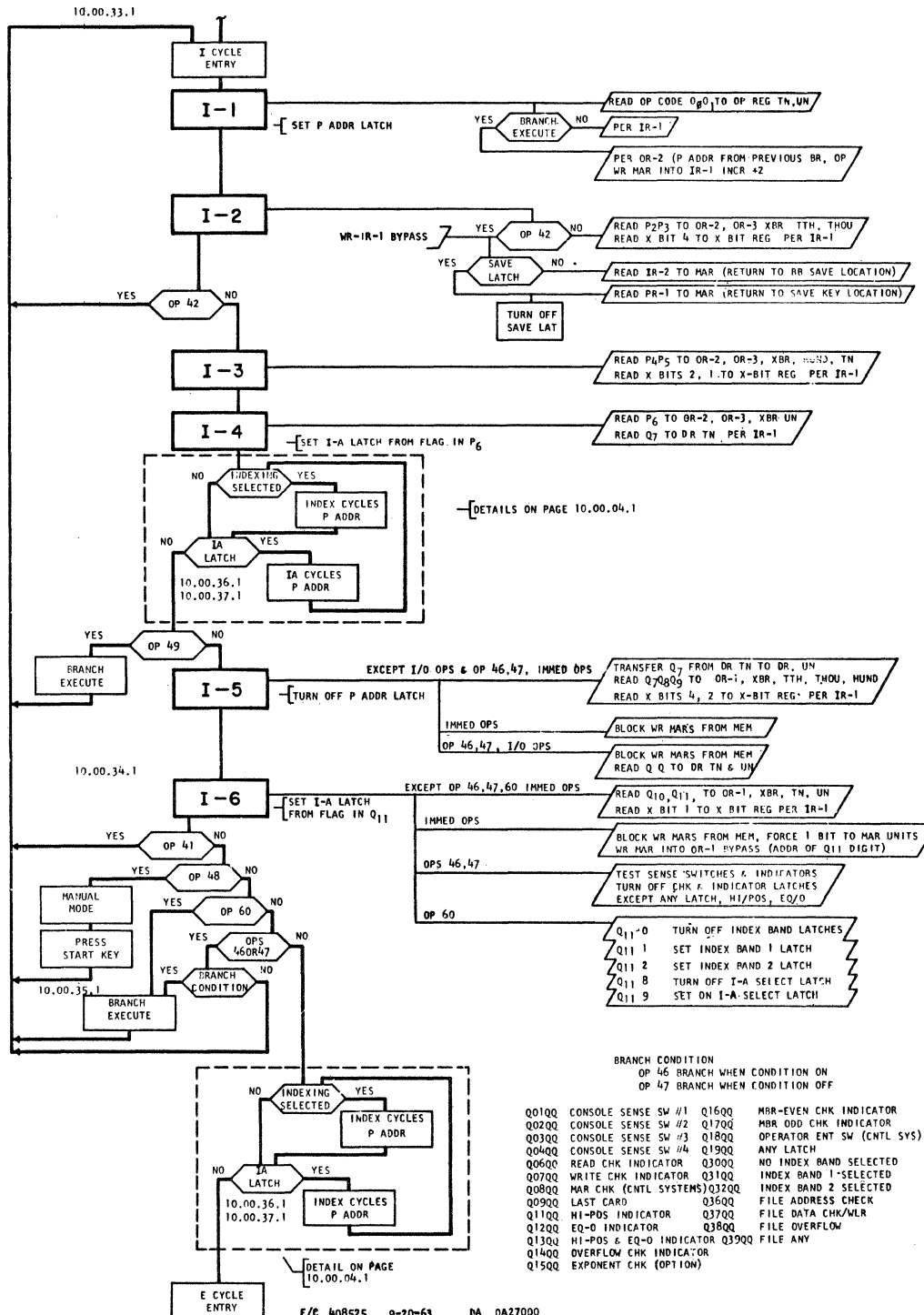


PURPOSE: STORE INSTR CODE, P & Q ADDR, AND FLAGS TO RESPECTIVE REGISTERS
 EXECUTE THOSE OP CODES NOT REQUIRING E CYCLE*
 PERFORM INDEXING AND INDIRECT ADDRESSING

OP CODES EXECUTED ON I CYCLES

NON-BRANCH CODES	NOP	41	0 0 0 0 0	0 0 0 0 0	P & Q READ OUT BUT NOT USED
	H	48	0 0 0 0 0	0 0 0 0 0	P & Q READ OUT BUT NOT USED
UNCONDITIONAL BRANCH	BB	42	X X X X X	X X X X X	P & Q NOT READ OUT
	B	49	P P P P P	X X X X X	Q NOT READ OUT
	BS	60	P P P P P	0 0 0 0 1	
CONDITIONAL BRANCH	BI	46	P P P P P	X Q8 Q9 X X	Q8 Q9 SENSE CODES 01-19
	BNI	47	P P P P P	X Q8 Q9 X X	Q8 Q9 SENSE CODES 01-19

FUNCTION CHART	PAGE REF
I CYCLES I ENTRY I-1	10.00.30.1
I CYCLES I-2 → I-5	10.00.31.1
I CYCLES I-6	10.00.32.1
I-15 BRANCH	10.00.33.1
16 BRANCH	10.00.34.1
BRANCH EXEC	10.00.35.1
INDIRECT ADDR	10.00.36.1
	10.00.37.1
I-14 XFR LOAD	10.00.40.1
I-16 XFR LOAD	10.00.41.1
XBR SET & RESET	10.01.30.1



06 TFL TRANSMIT FLOATING
 26 TF TRANSMIT FIELD
 16 TFM TRANSMIT FIELD IMMED

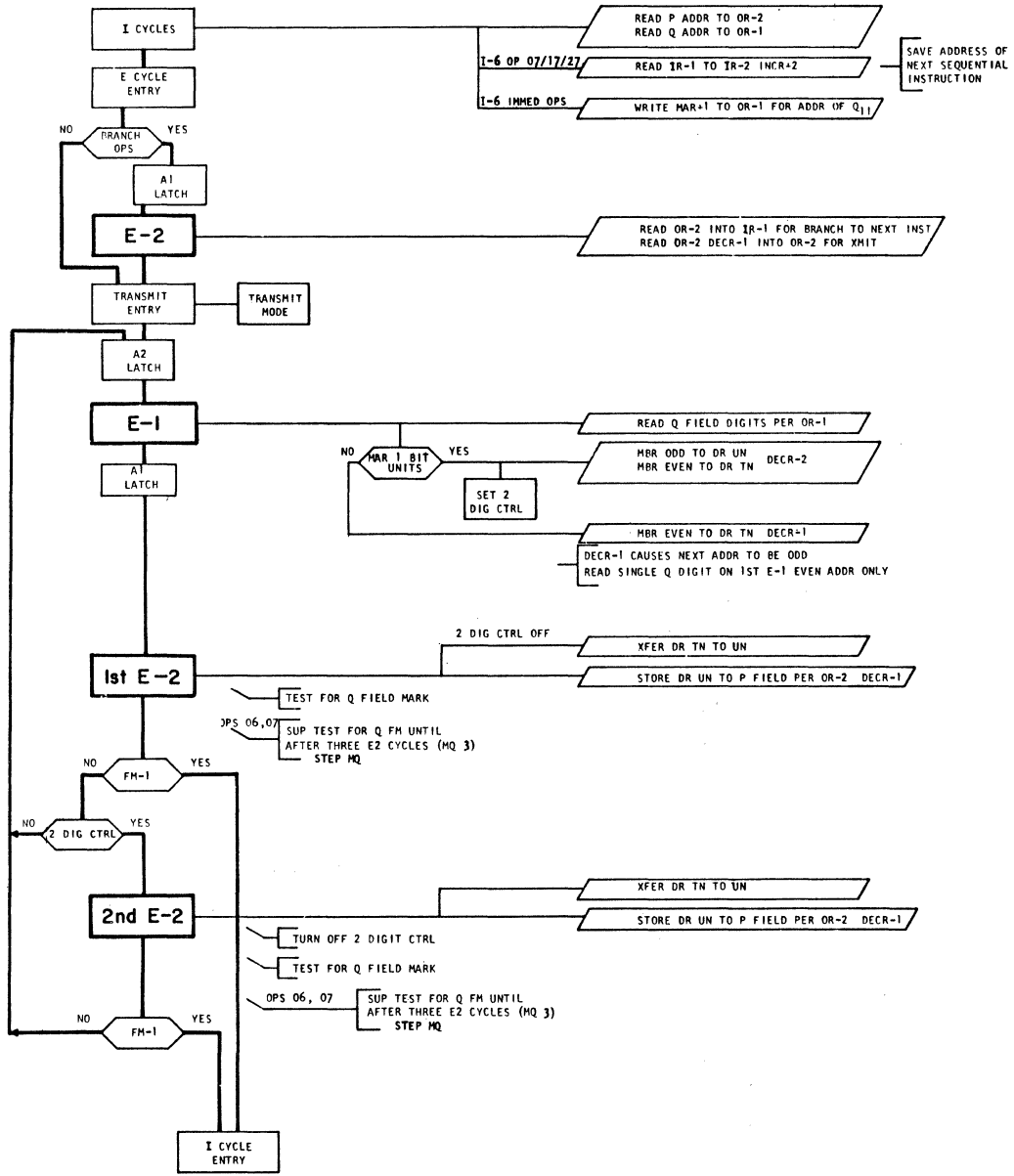
07 BTFL BRANCH AND TRANSMIT FLOATING
 27 BT BRANCH AND TRANSMIT
 17 BTM BRANCH AND TRANSMIT IMMED

INSTRUCTION: 0φ1 P P P P P Q Q Q Q Q P AND Q ADDRESS LOW ORDER POSITIONS
 OPERATION TERMINATED BY Q FIELD MARK

PURPOSE OF TRANSMIT: TRANSMIT Q FIELD TO P FIELD

PURPOSE OF BRANCH AND TRANSMIT: TRANSMIT FIELD AT Q ADDR TO FIELD AT P ADDR MINUS 1
 SAVE ADDRESS OF NEXT SEQUENTIAL INSTRUCTION
 BRANCH UNCONDITIONALLY TO LOCATION P

FUNCTION CHART	PAGE REF
XMIT OPS	10.00.52.1
XMIT ENTRY & MODE	10.00.53.1
XMIT E1, E2	10.00.54.1
DR & 2 DIGIT CTRL	10.01.22.1



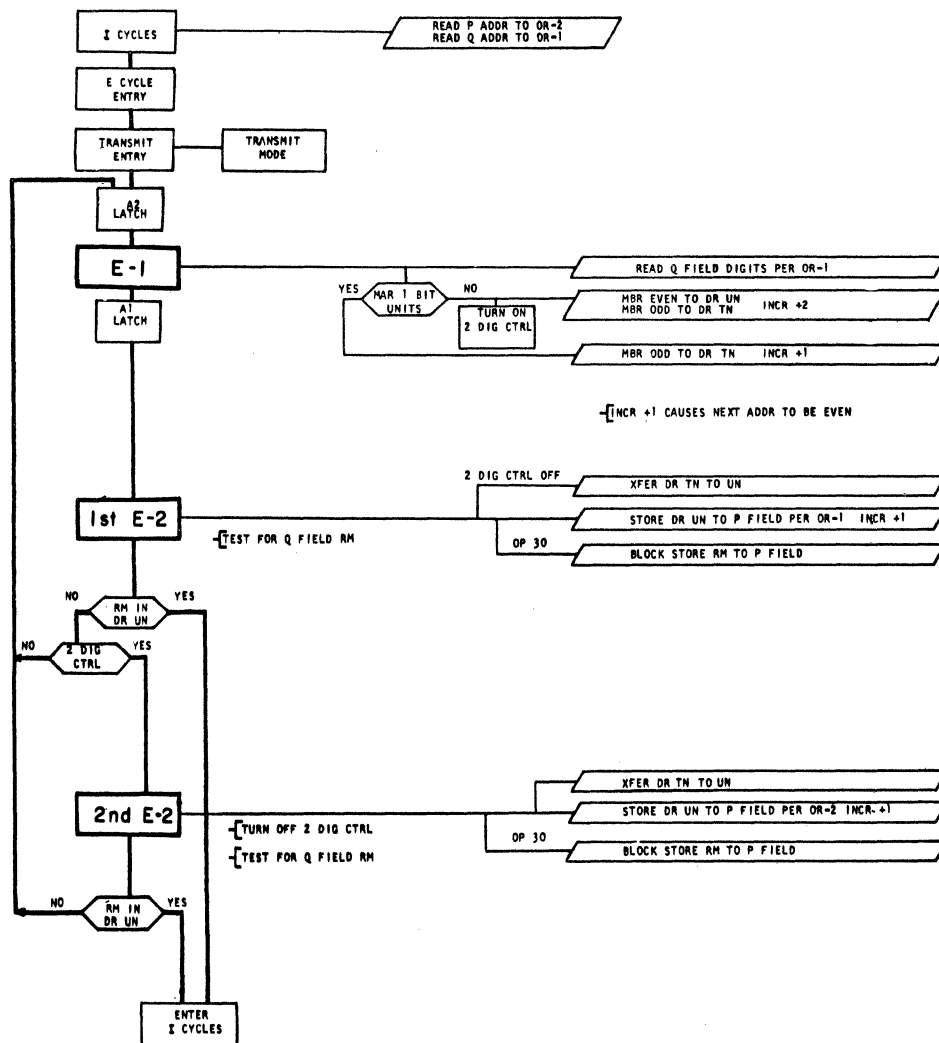
3I TR TRANSMIT RECORD
30 TRNM TRANSMIT RECORD NO RECORD MARK

INSTRUCTION: 0₀0₁ P P P P P Q Q Q Q Q

P AND Q ADDRESS HIGH ORDER POSITIONS
OPERATION TERMINATED BY Q FIELD RECORD MARK

PURPOSE: TRANSMIT RECORD AT Q ADDR TO P ADDR
OMIT RECORD MARK IN P FIELD ON OP 30

FUNCTION CHART	PAGE REF
XMIT OPS	10.00.52.1
XMIT ENTRY & MODE	10.00.53.1
XMIT E1, E2	10.00.54.1
DR & 2 DIGIT CTRL	10.01.22.1

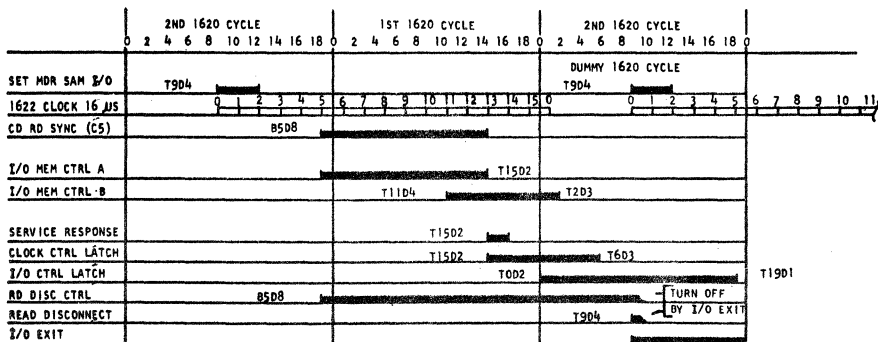
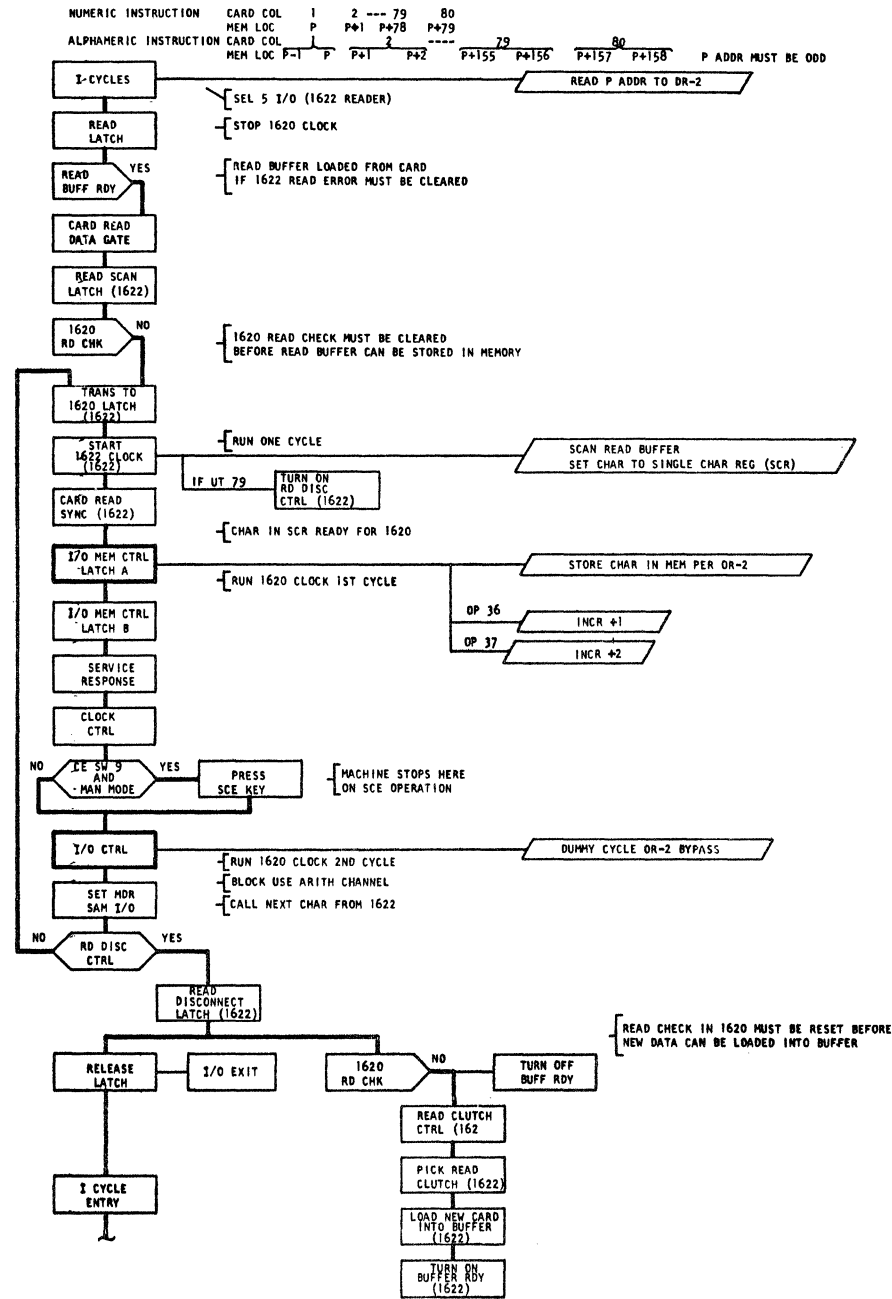


**36 RN READ NUMERICALLY
37 RA REA ALPHAMERICALLY**

FUNCTION CHART	PAGE REF
RELEASE	10.01.49.1
READ LATCH	10.01.50.1
RESP GATE, I/O MEM CTRL	10.01.53.1
1622 CARD READER	10.01.70.1

INSTRUCTION: 0₀0₁ 0₂ 0₃ 0₄ 0₅ 0₆ 0₇ 0₈ 0₉ 1₀ 1₁ 0₅ SELCTS 1622 READER

PURPOSE: READ 80 CHAR FROM A CARD
STORE IN MEMORY STARTING FROM P ADDR THRU SUCCESSIVELY HIGHER NUMBERED LOCATIONS



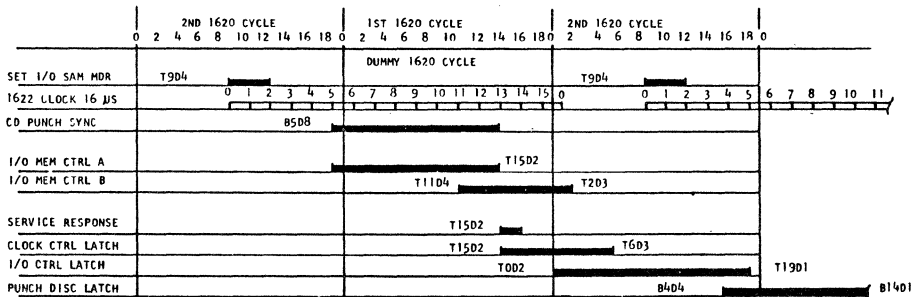
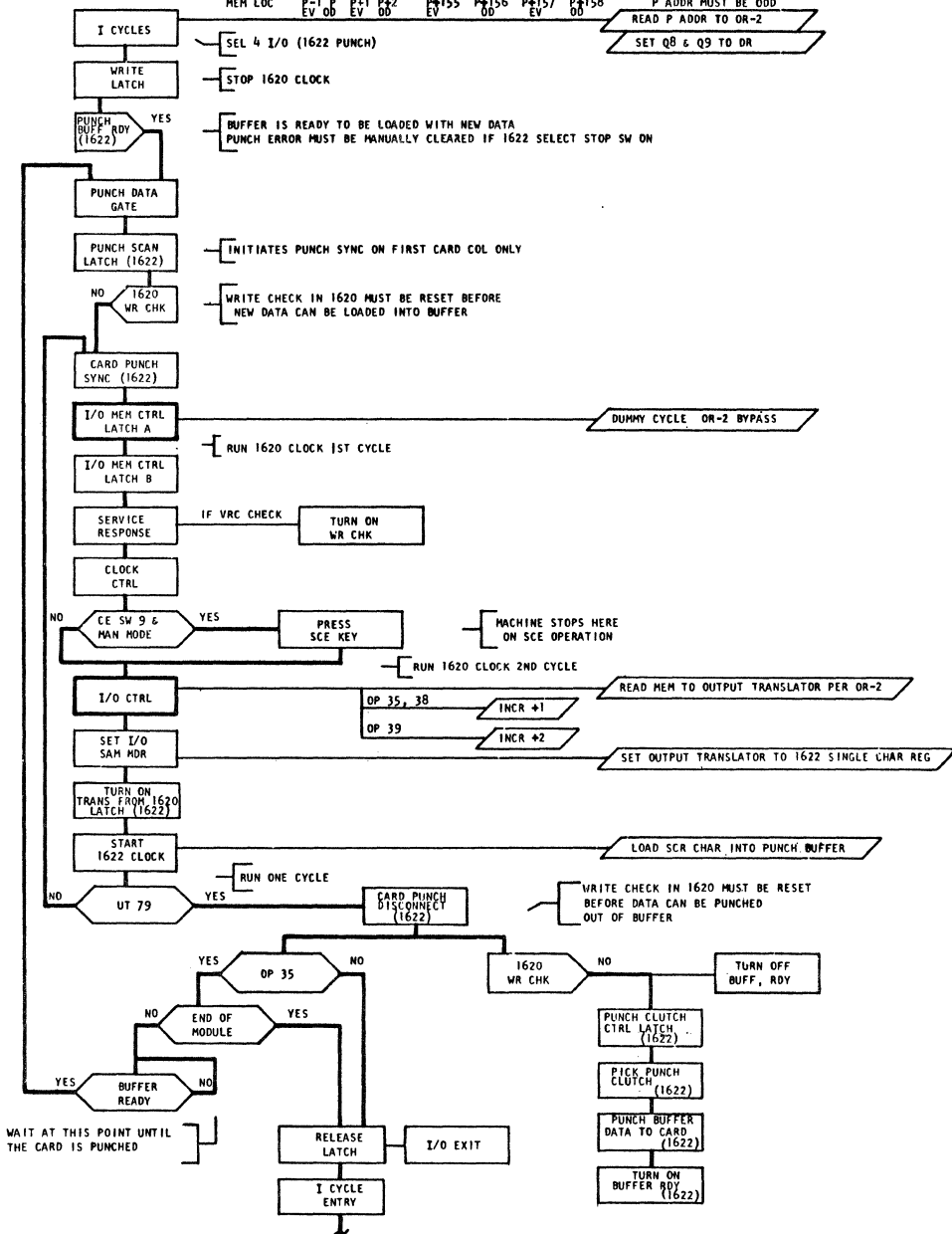
35 DN DUMP NUMERICALLY
38 WN WRITE NUMERICALLY
39 WA WRITE ALPHAMERICALLY

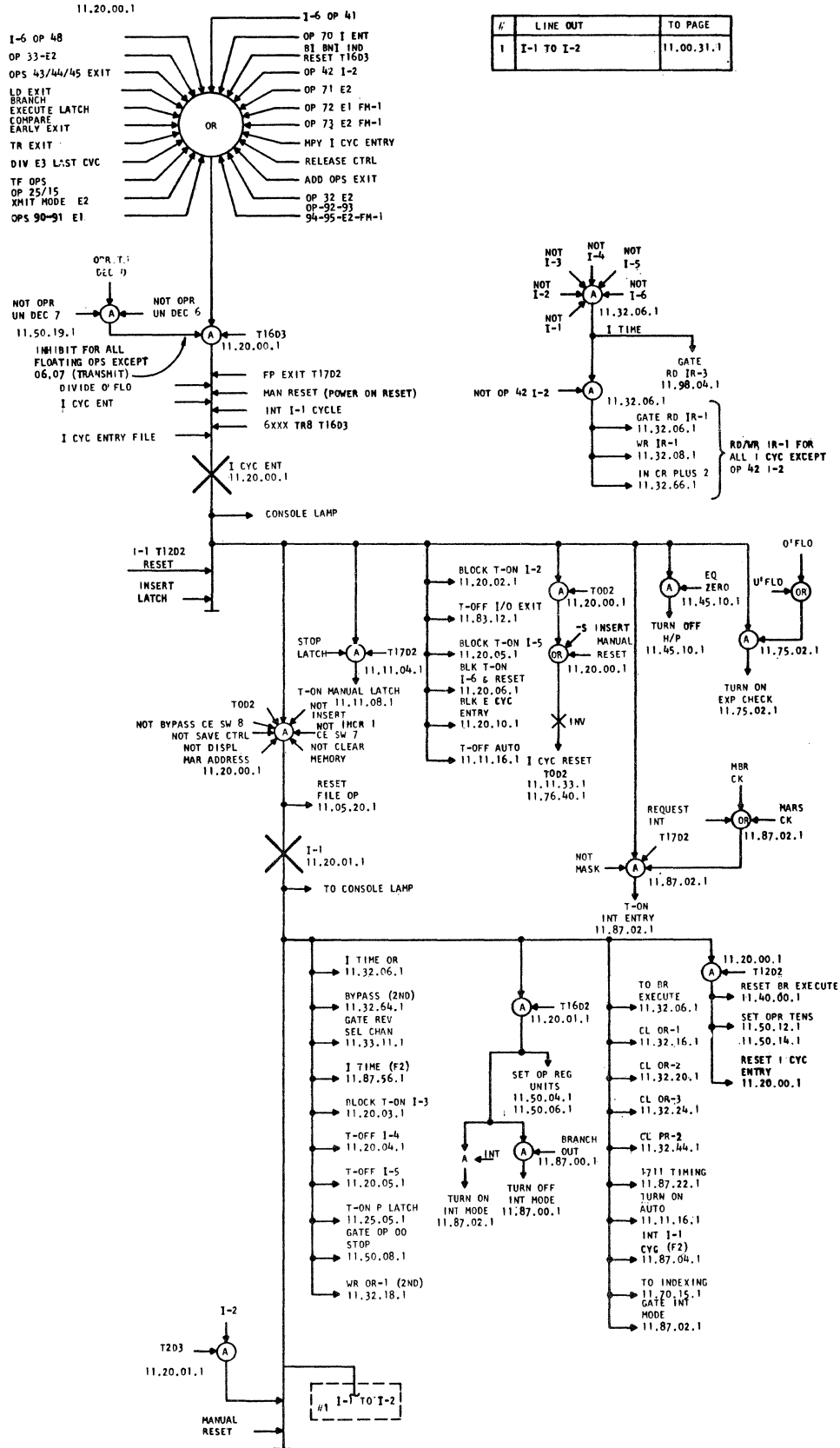
FUNCTION CHART	PAGE REF
RELEASE	10.01.49.1
WRITE LATCH	10.01.51.1
RESP. GATE, I/O MEM CTRL	10.01.53.1
1622 CARD PUNCH	10.01.70.1

INSTRUCTION: 0 0 1 P P P P P P Q Q Q Q Q Q 04 SELECTS 1622 PUNCH

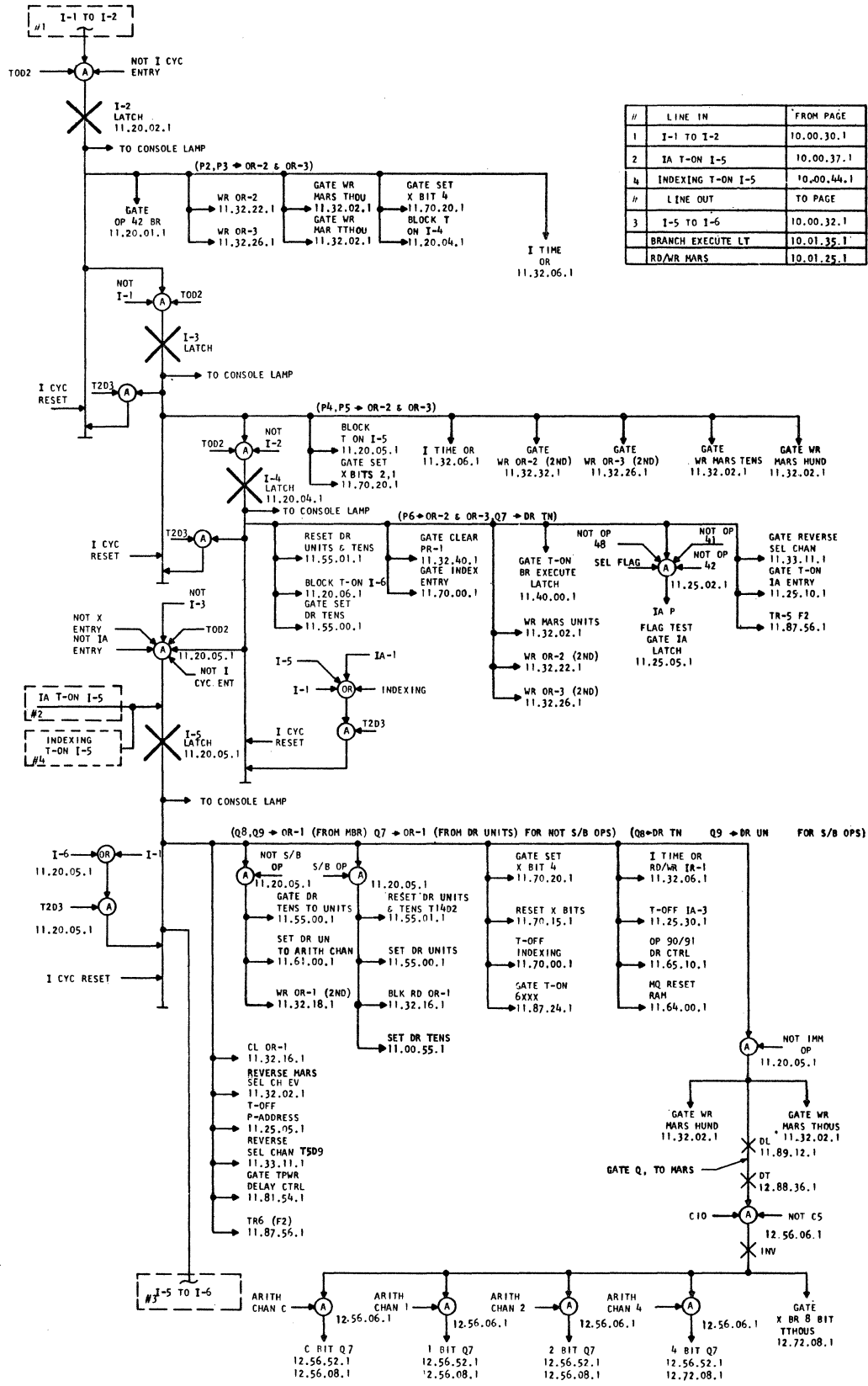
PURPOSE: PUNCH 80 CHAR INTO CARD STARTING FROM P ADDR THRU SUCCESSIVELY HIGHER NUMBERED LOCATIONS IN MEMORY

NUMERIC INSTRUCTION: CARD COL 1 2 ----- 79 80 MEM ADDR=P+CC-1
 MEM LOC P P+1 P+78 P+79
 ALPHAMERIC INSTRUCTION: CARD COL 1 2 ----- 79 80 MEM ADDR=P+2(CC-1)
 MEM LOC P+1 P+2 P+155 P+156 P+157 P+158 P ADDR MUST BE ODD
 EV OD EV OD EV OD EV OD

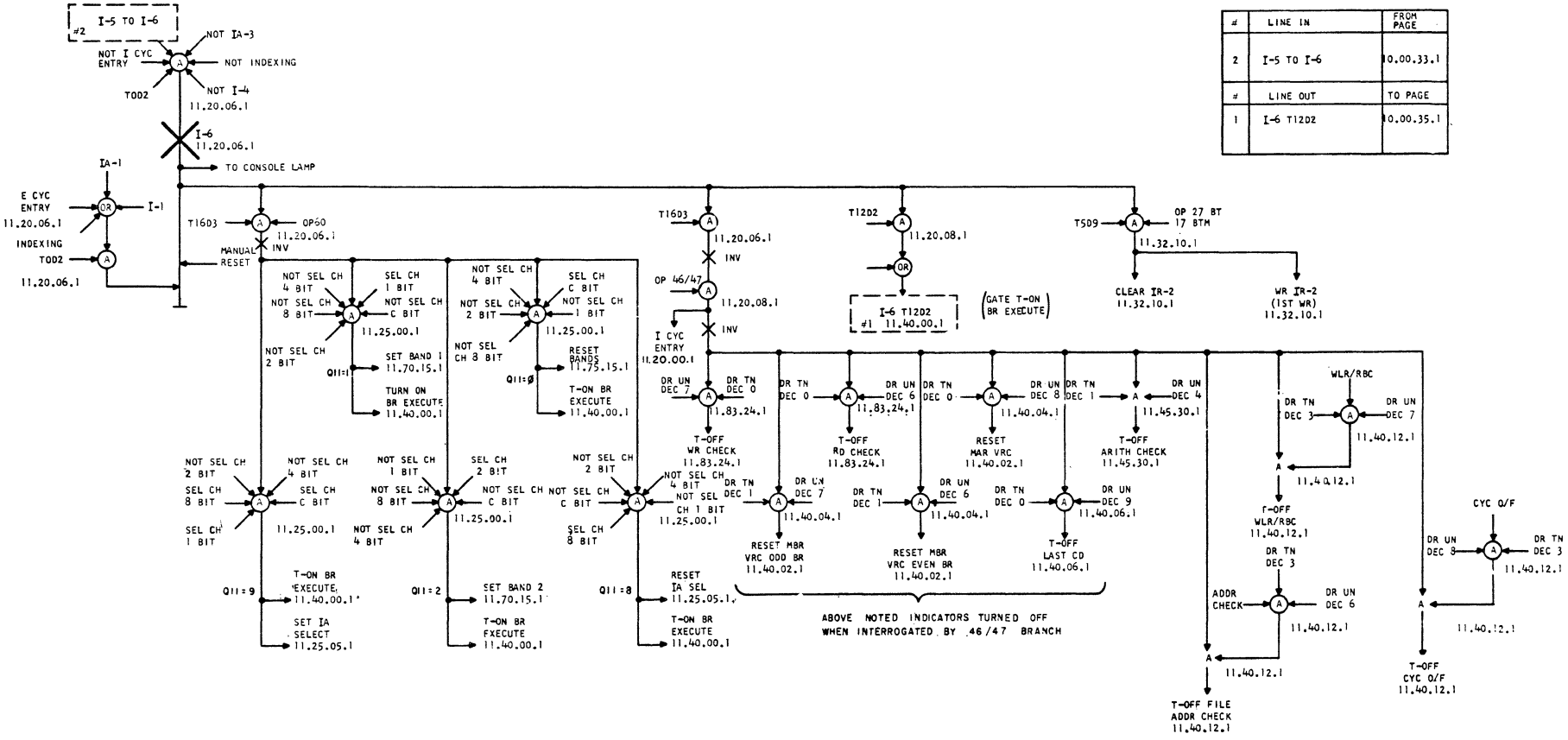




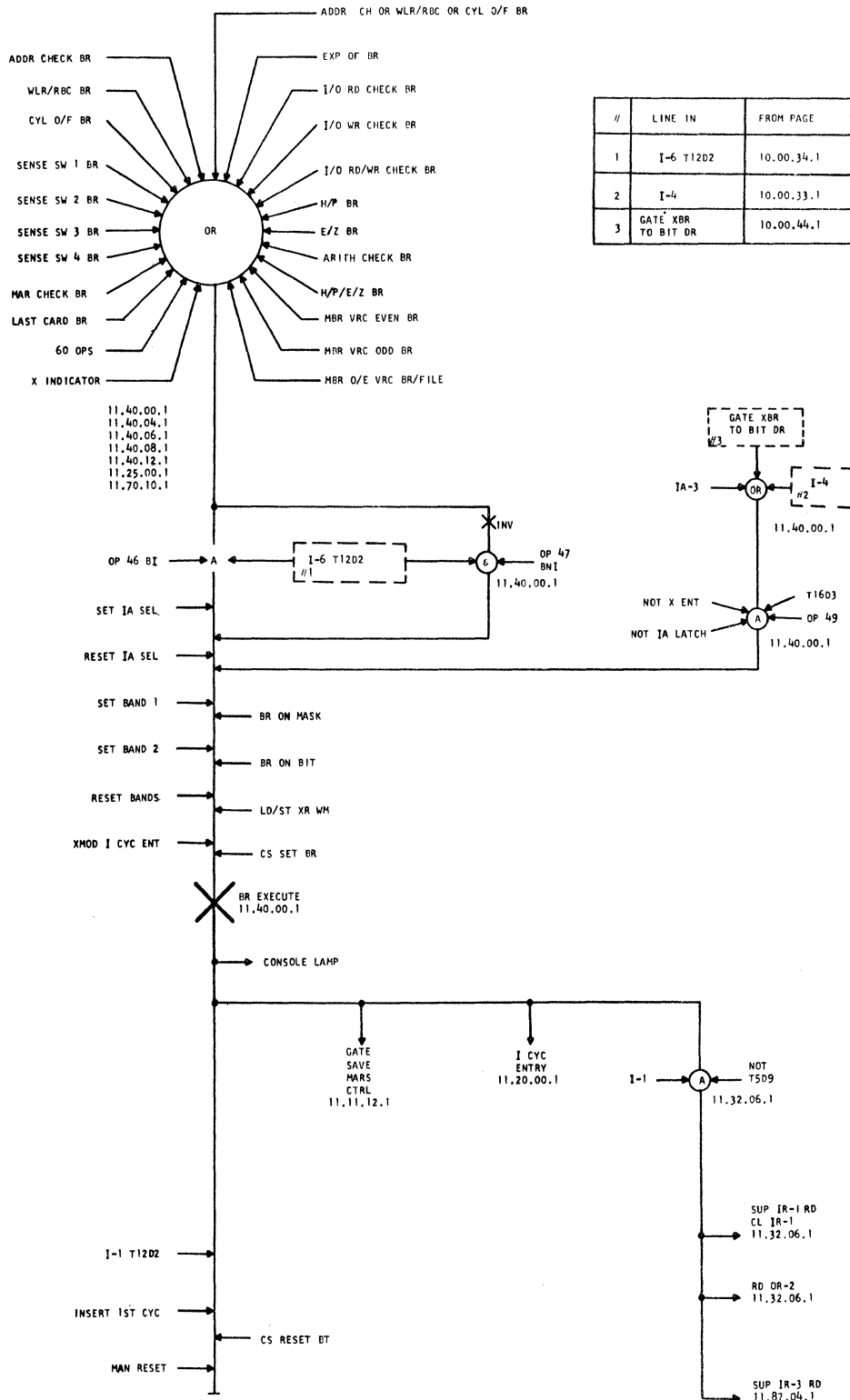
#	LINE OUT	TO PAGE
1	I-1 TO I-2	11.00.31.1

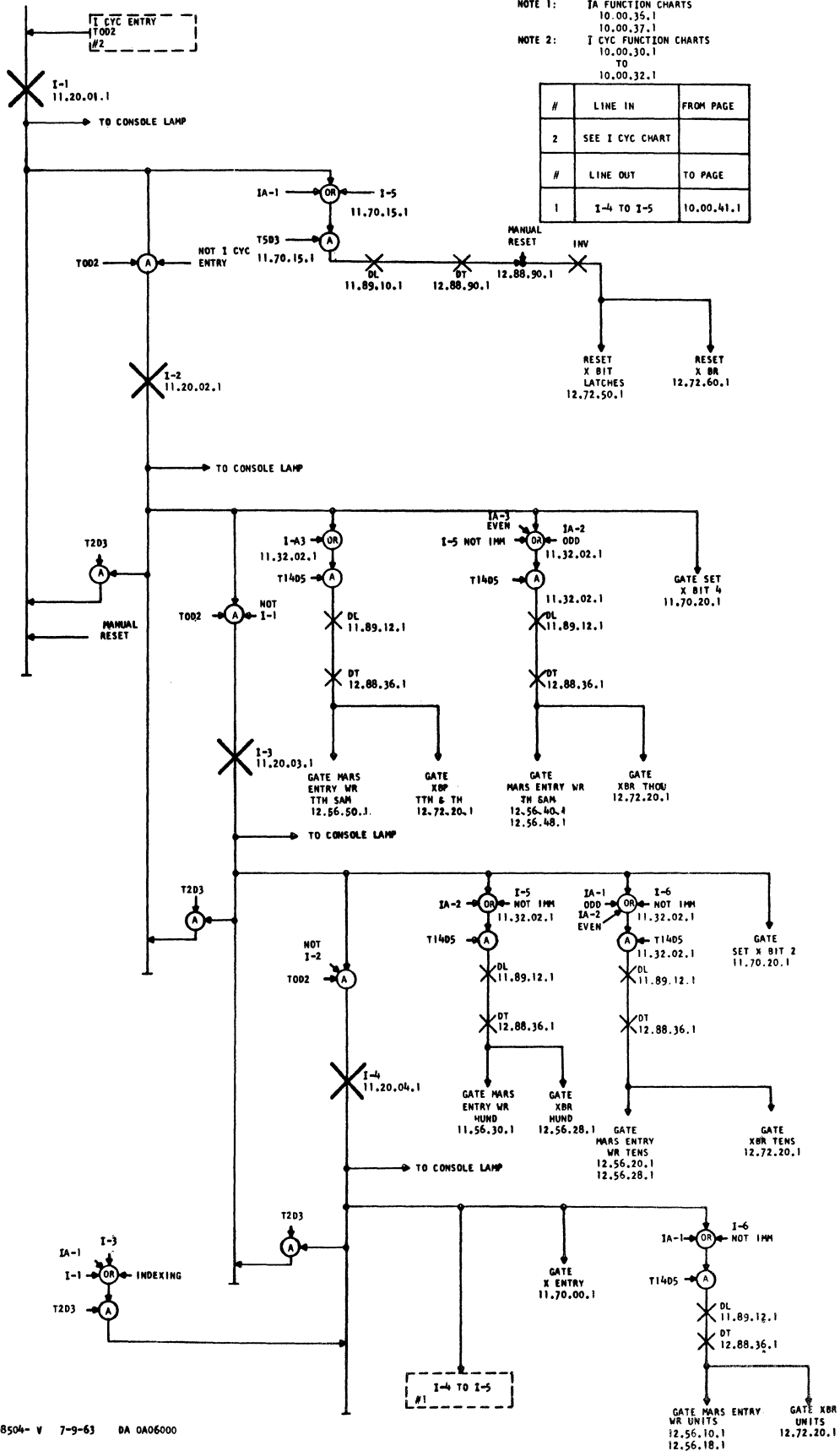


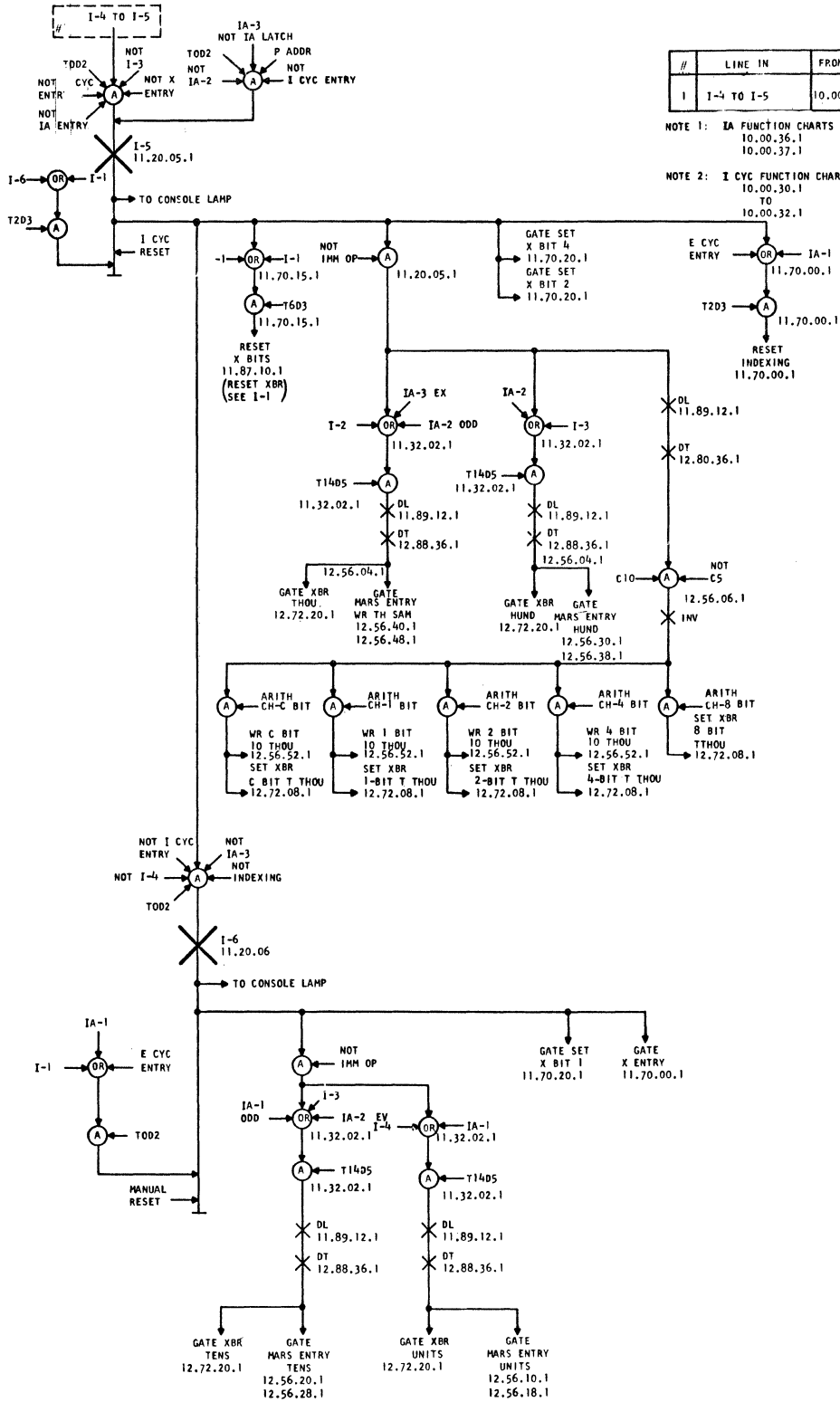
#	LINE IN	FROM PAGE
1	I-1 TO I-2	10.00.30.1
2	IA T-ON I-5	10.00.37.1
4	INDEXING T-ON I-5	10.00.44.1
#	LINE OUT	TO PAGE
3	I-5 TO I-6	10.00.32.1
	BRANCH EXECUTE LT	10.01.35.1
	RD/WR MARS	10.01.25.1

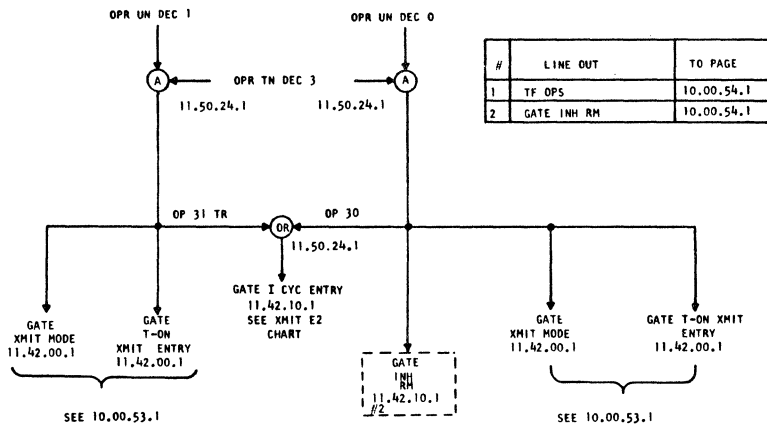
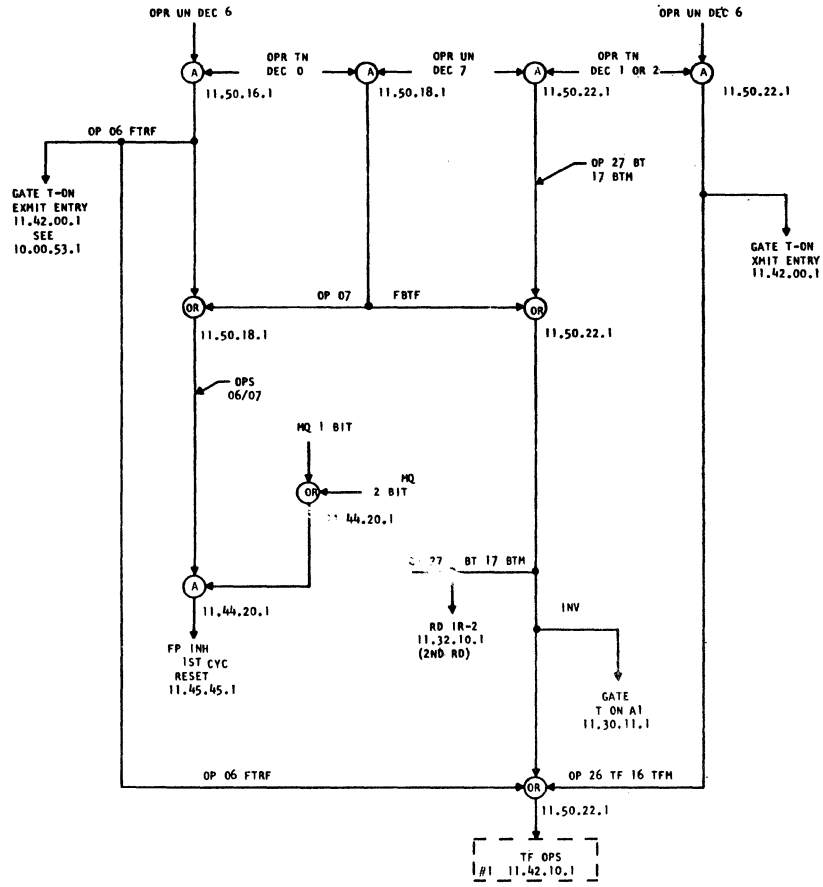


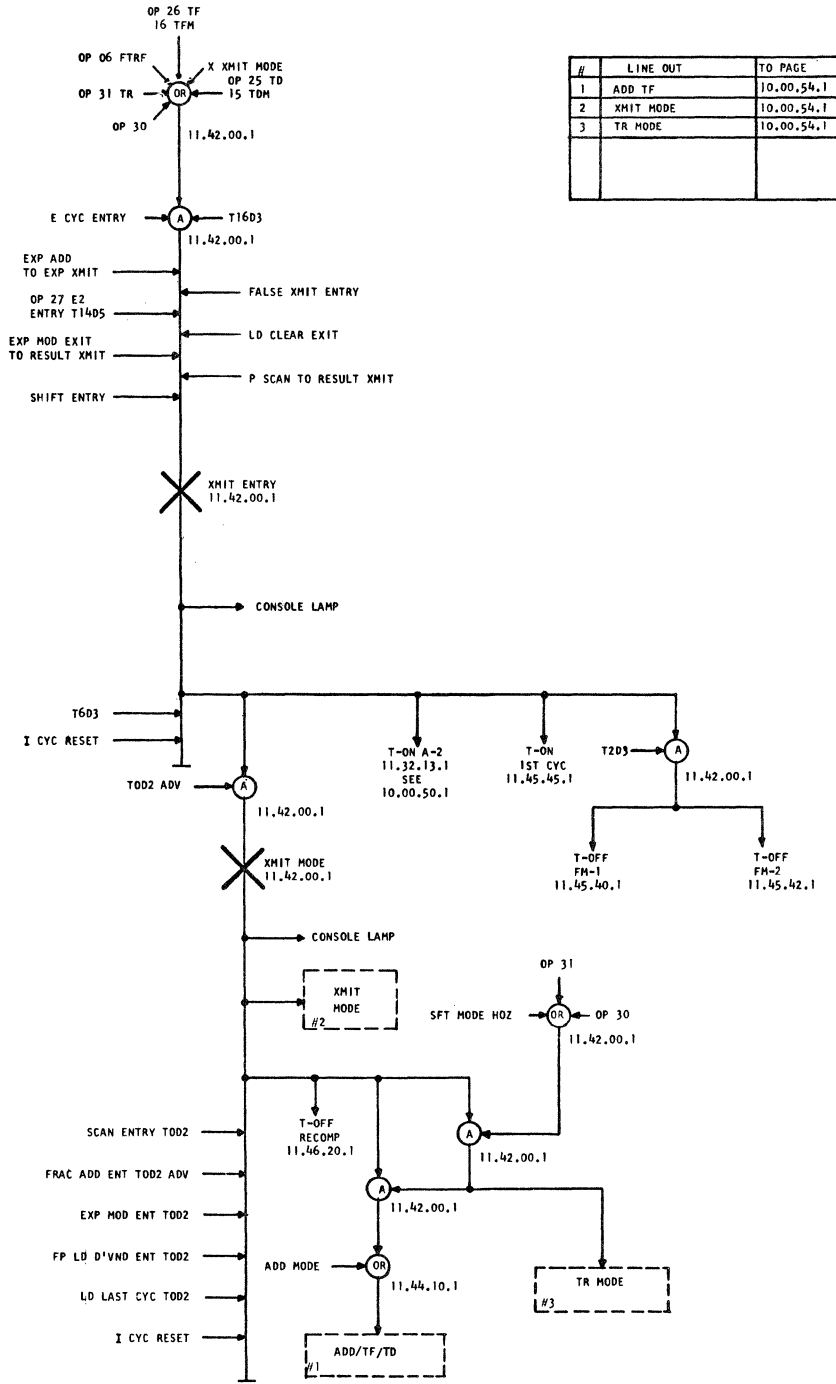
#	LINE IN	FROM PAGE
2	I-5 TO I-6	0,00,33,1
#	LINE OUT	TO PAGE
1	I-6 T1202	0,00,35,1

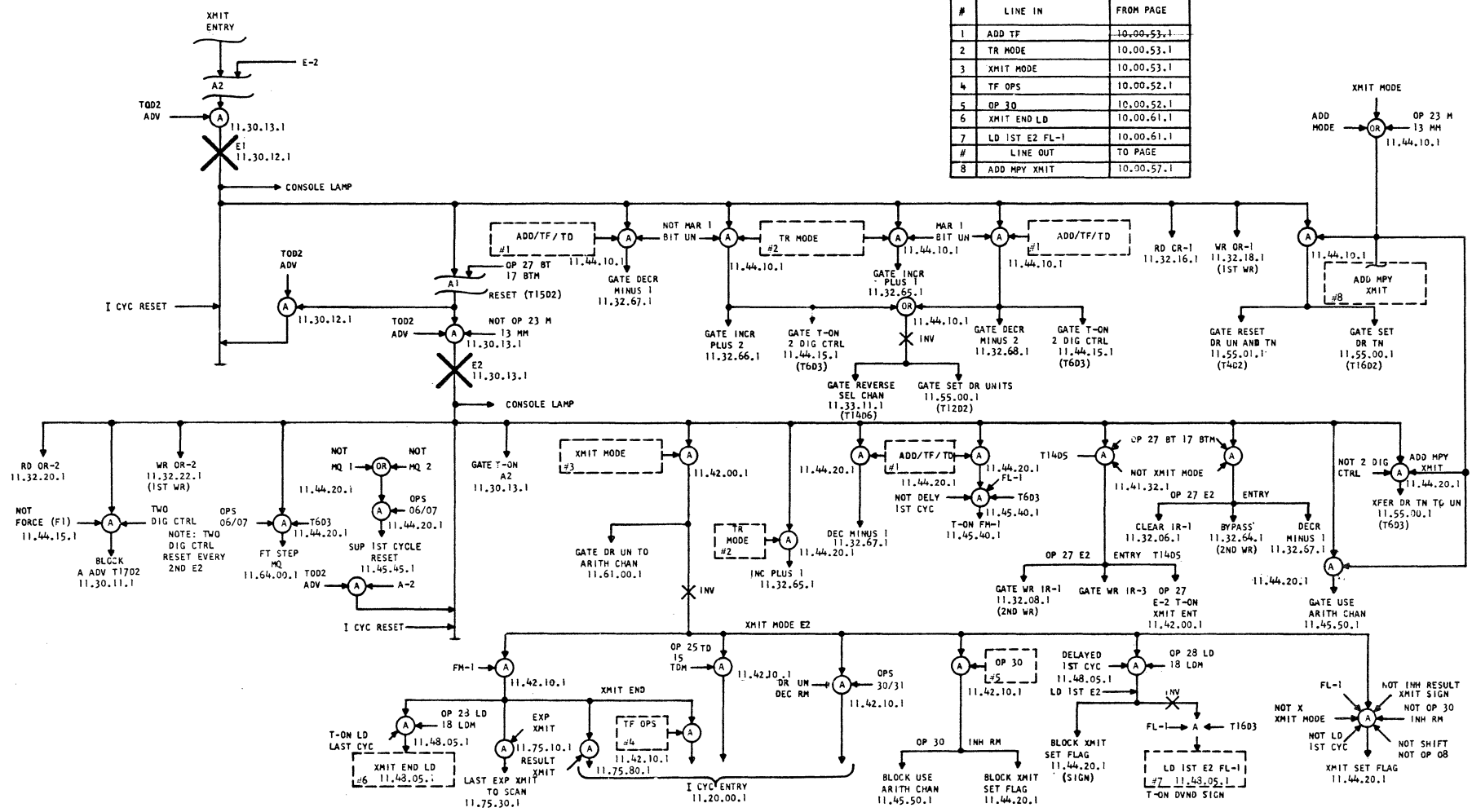




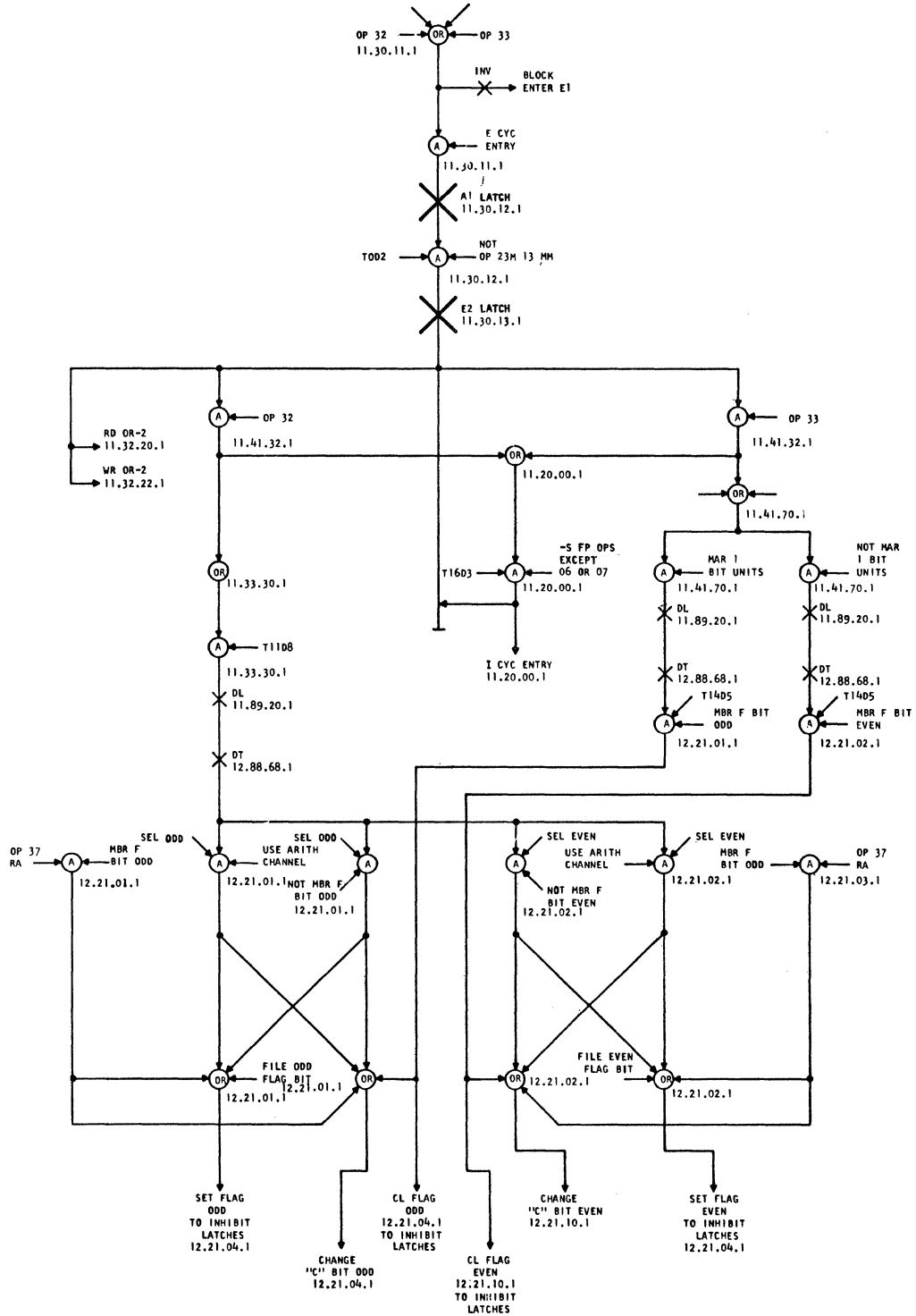


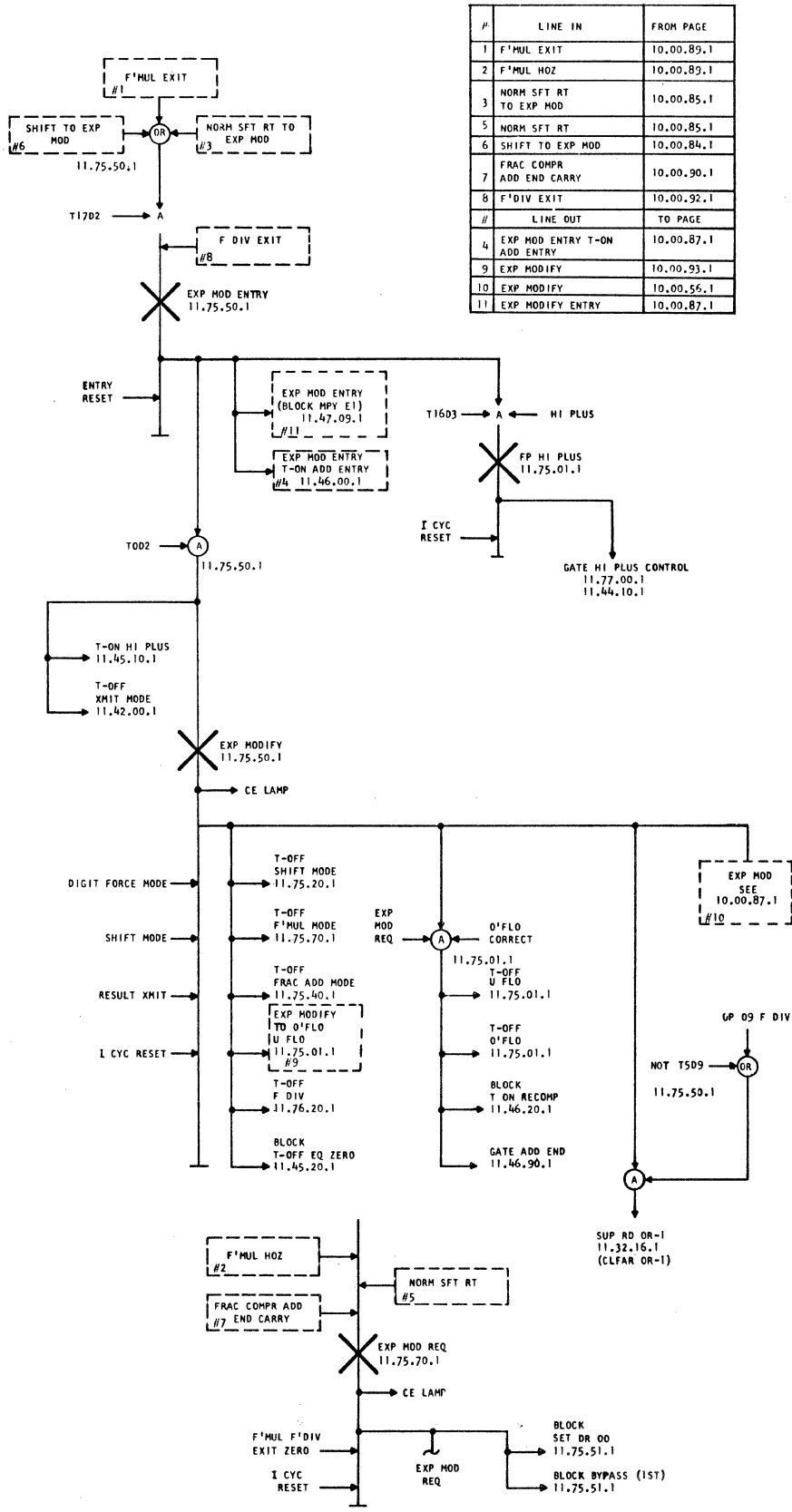




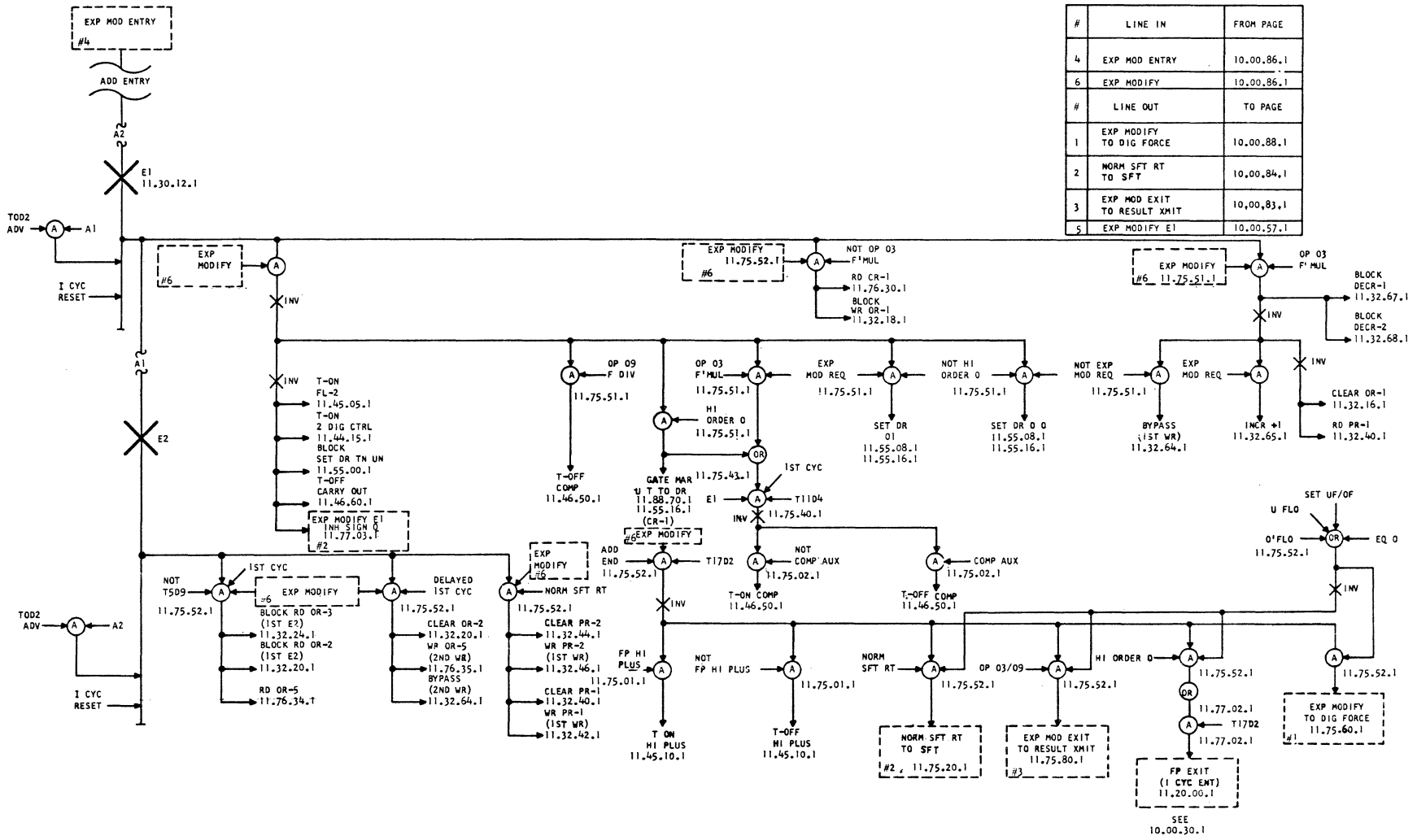


#	LINE IN	FROM PAGE
1	ADD TF	10.00.53.1
2	TR MODE	10.00.53.1
3	XMIT MODE	10.00.53.1
4	TF OPS	10.00.52.1
5	OP 30	10.00.52.1
6	XMIT END LD	10.00.61.1
7	LD 1ST E2 FL-1	10.00.61.1
#	LINE OUT	TO PAGE
8	ADD MPY XMIT	10.00.57.1





#	LINE IN	FROM PAGE
1	F'MUL EXIT	10.00.89.1
2	F'MUL HOZ	10.00.89.1
3	NORM SFT RT TO EXP MOD	10.00.85.1
5	NORM SFT RT	10.00.85.1
6	SHIFT TO EXP MOD	10.00.84.1
7	FRAC COMPR ADD END CARRY	10.00.90.1
8	F'DIV EXIT	10.00.92.1
#	LINE OUT	TO PAGE
4	EXP MOD ENTRY T-ON ADD ENTRY	10.00.87.1
9	EXP MODIFY	10.00.93.1
10	EXP MODIFY	10.00.56.1
11	EXP MODIFY ENTRY	10.00.87.1



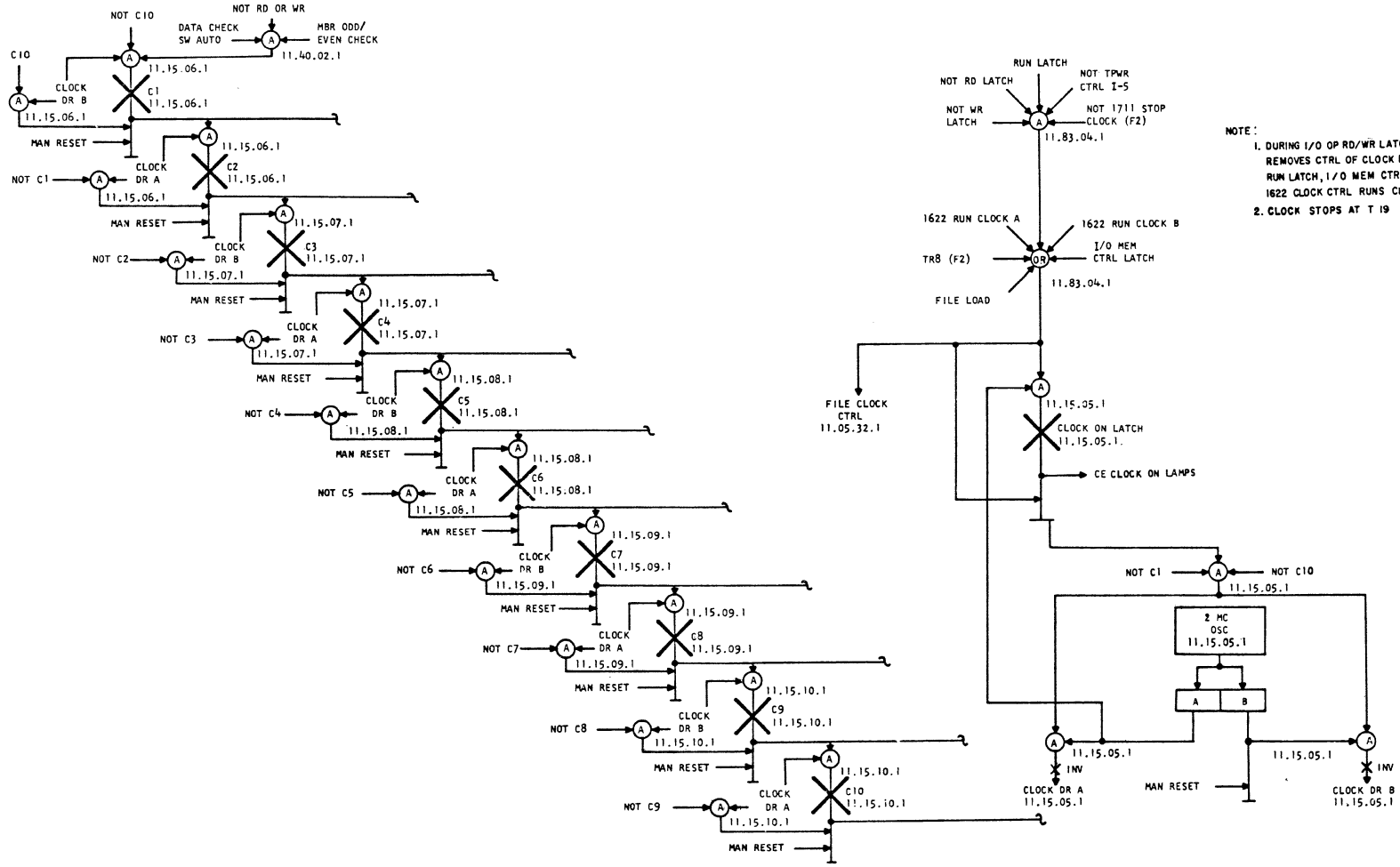
#	LINE IN	FROM PAGE
4	EXP MOD ENTRY	10.00.86.1
6	EXP MODIFY	10.00.86.1
#	LINE OUT	TO PAGE
1	EXP MODIFY TO DIG FORCE	10.00.88.1
2	NORM SFT RT TO SFT	10.00.84.1
3	EXP MOD EXIT TO RESULT XMIT	10.00.83.1
5	EXP MODIFY E1	10.00.57.1

2159138

EXP MODIFY E-1 & E-2

1620 II

1000.87.1



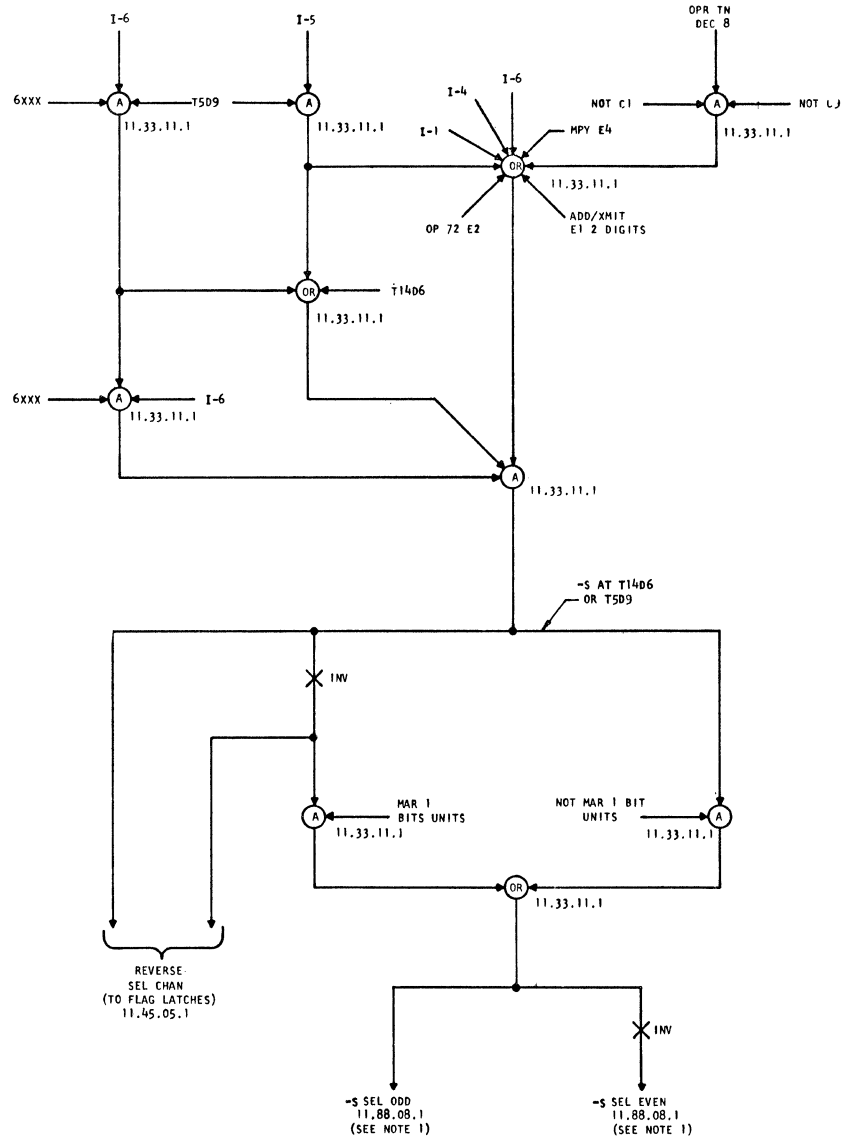
NOTE:
 1. DURING I/O OP RD/WR LATCHES REMOVES CTRL OF CLOCK FROM RUN LATCH, I/O MEM CTRL OR 1622 CLOCK CTRL RUNS CLOCK
 2. CLOCK STOPS AT T 19

2159148

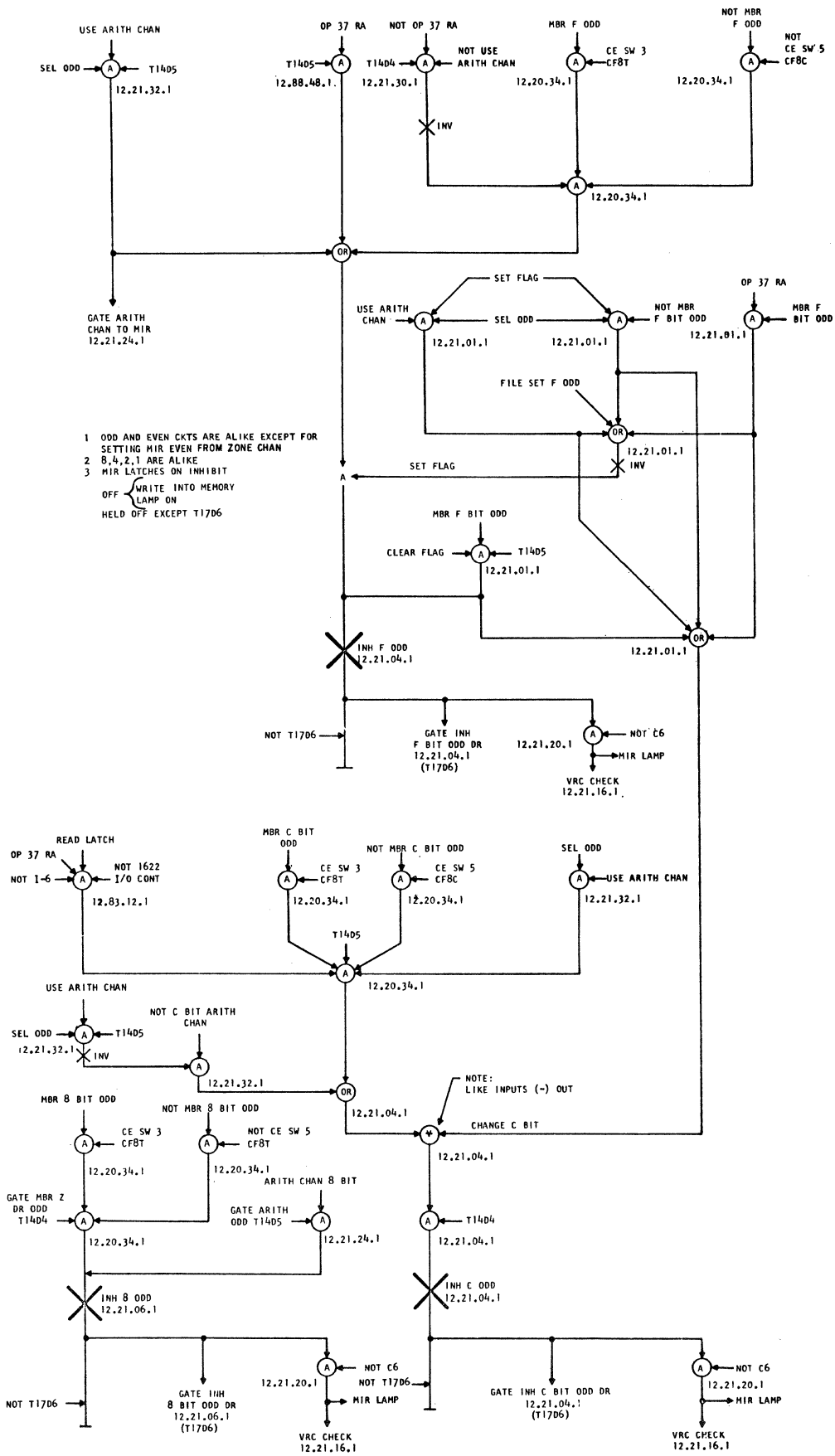
CLOCK

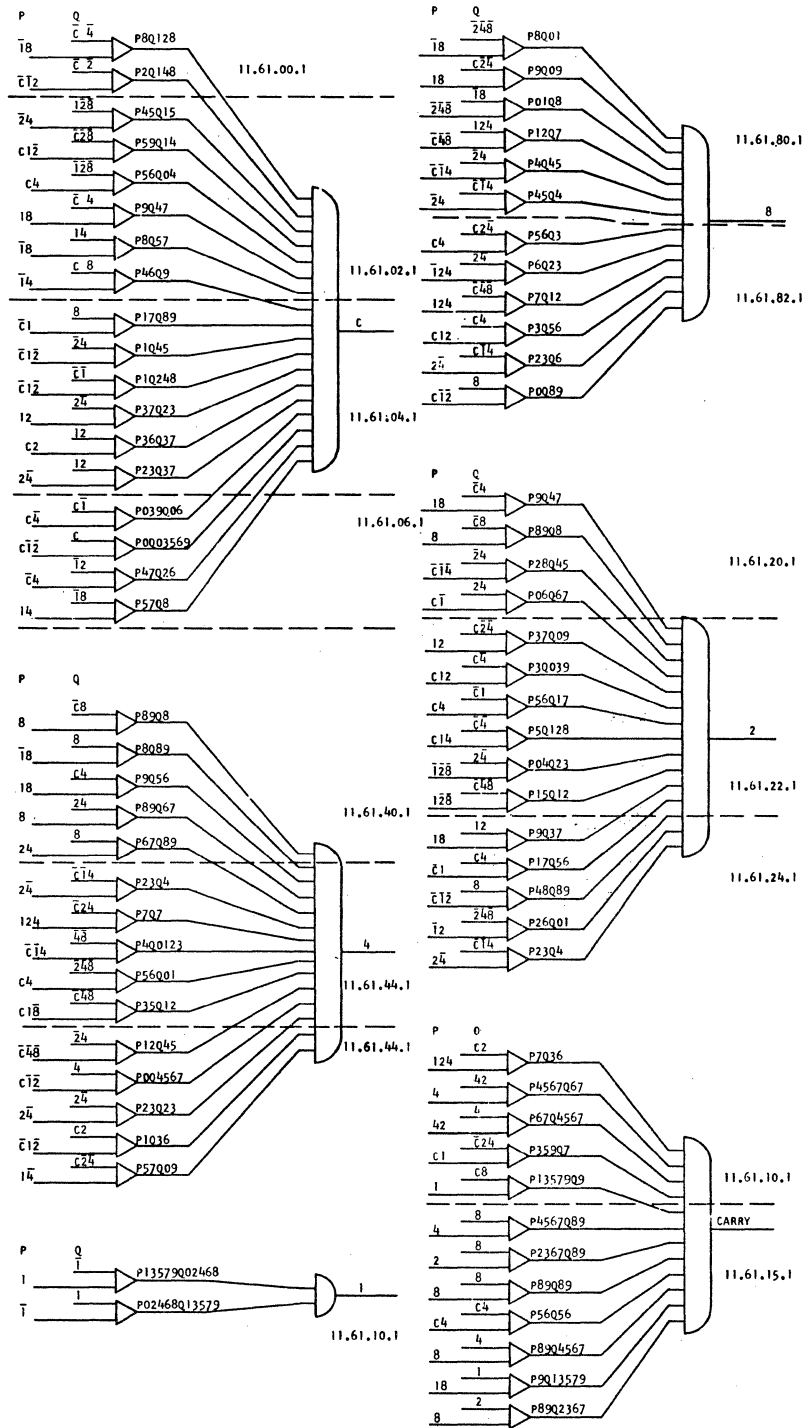
1620 II

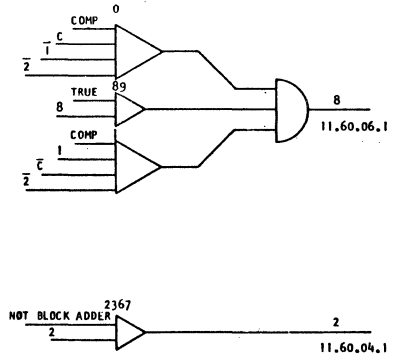
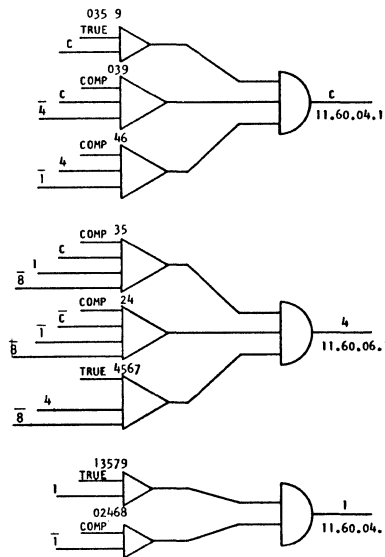
10.01.20.1



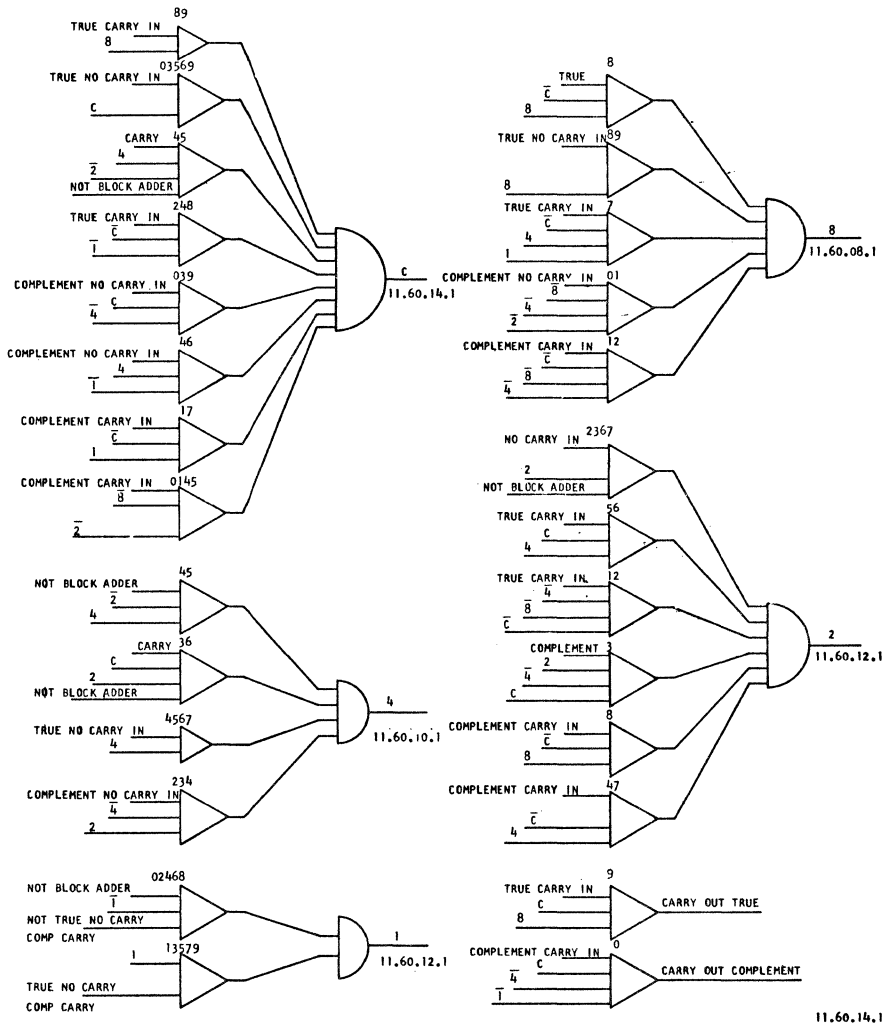
NOTE 1: SELECT FLAG IS ONLY REVERSED AT THE FLAG LATCHES. ALL OTHER SELECT FLAG LINES ARE CONTROLLED BY THE MAR ADDRESS SELECTION.



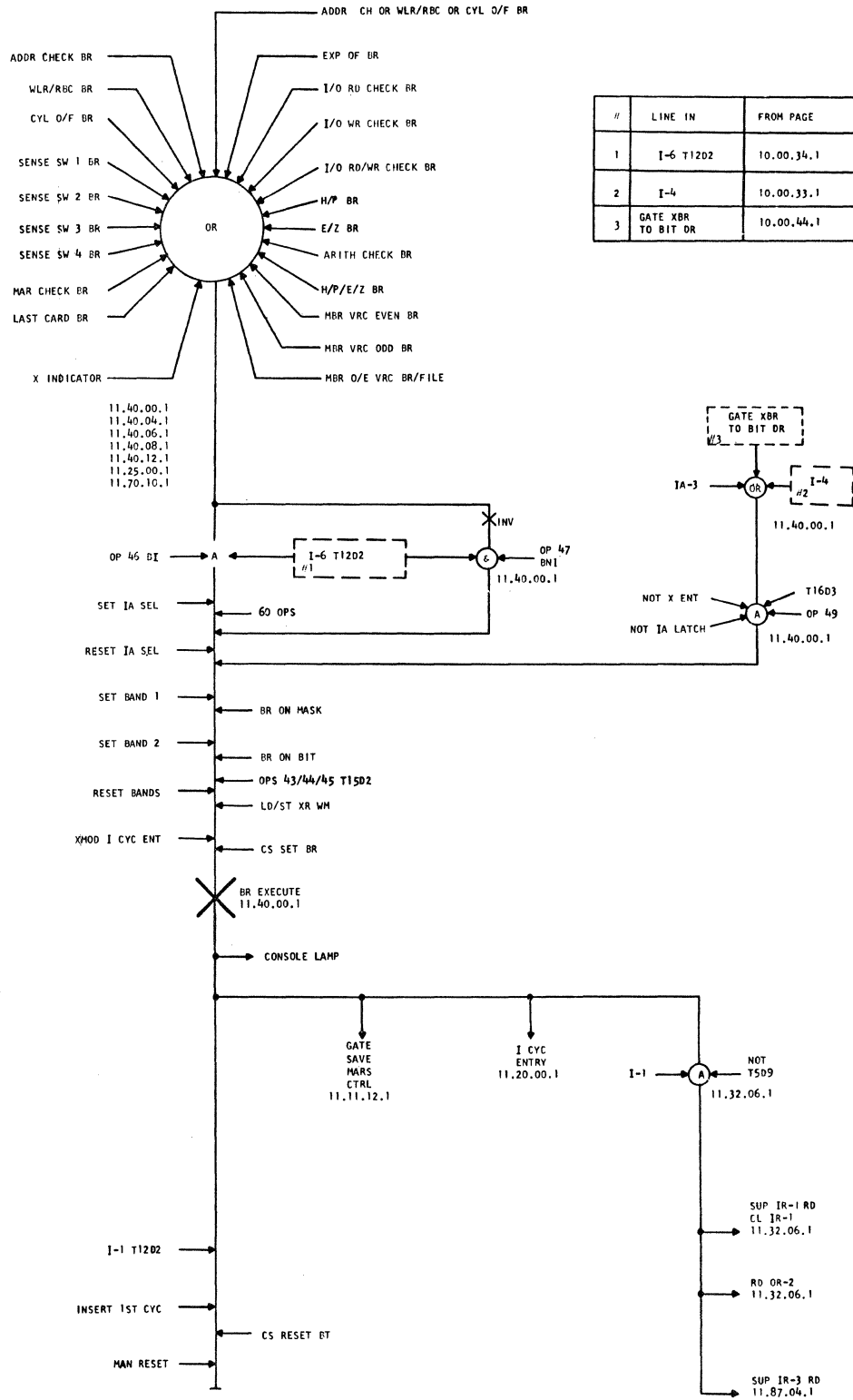


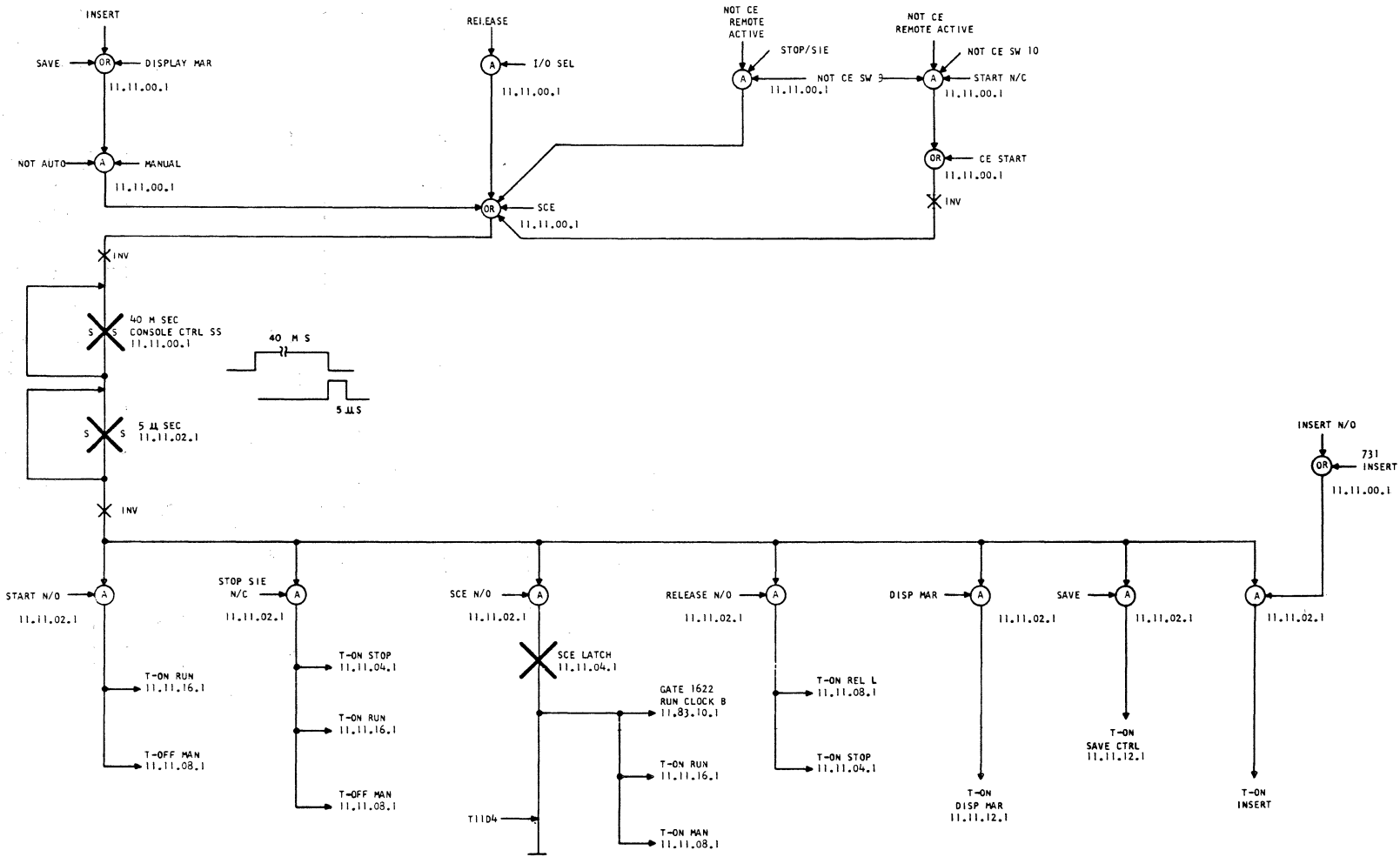


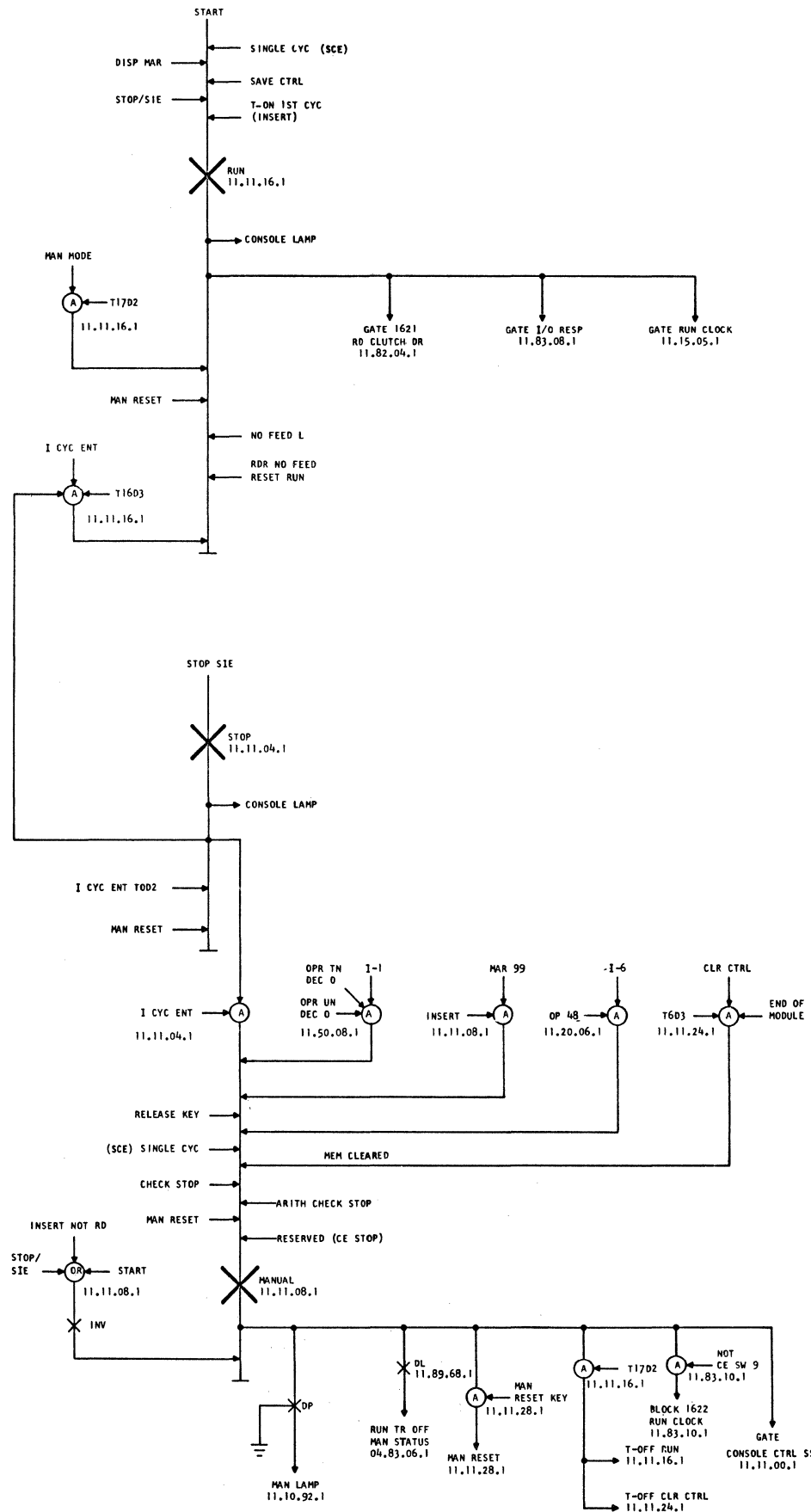
P CHANNEL T/C NETWORK

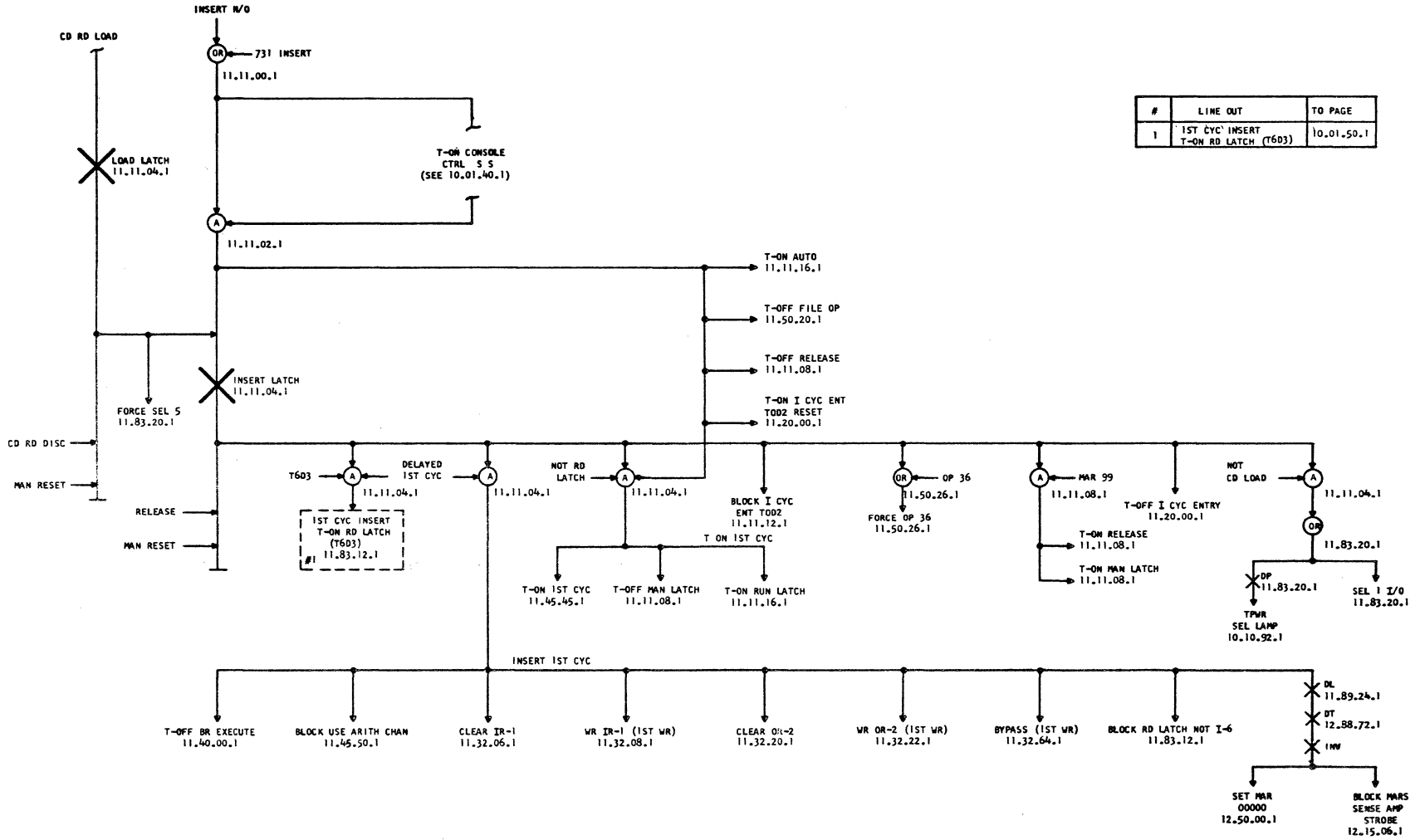


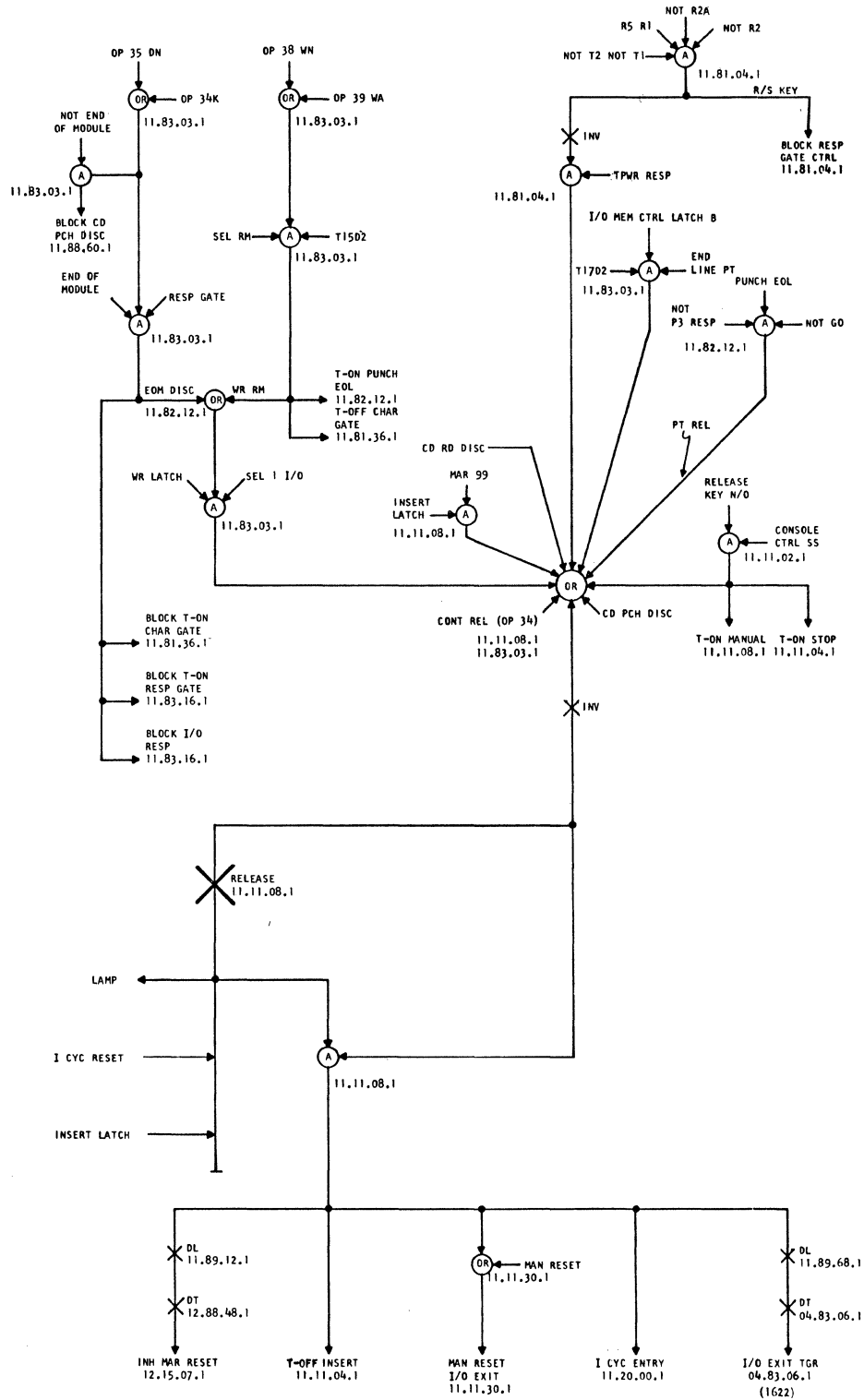
Q CHANNEL T/C NETWORK



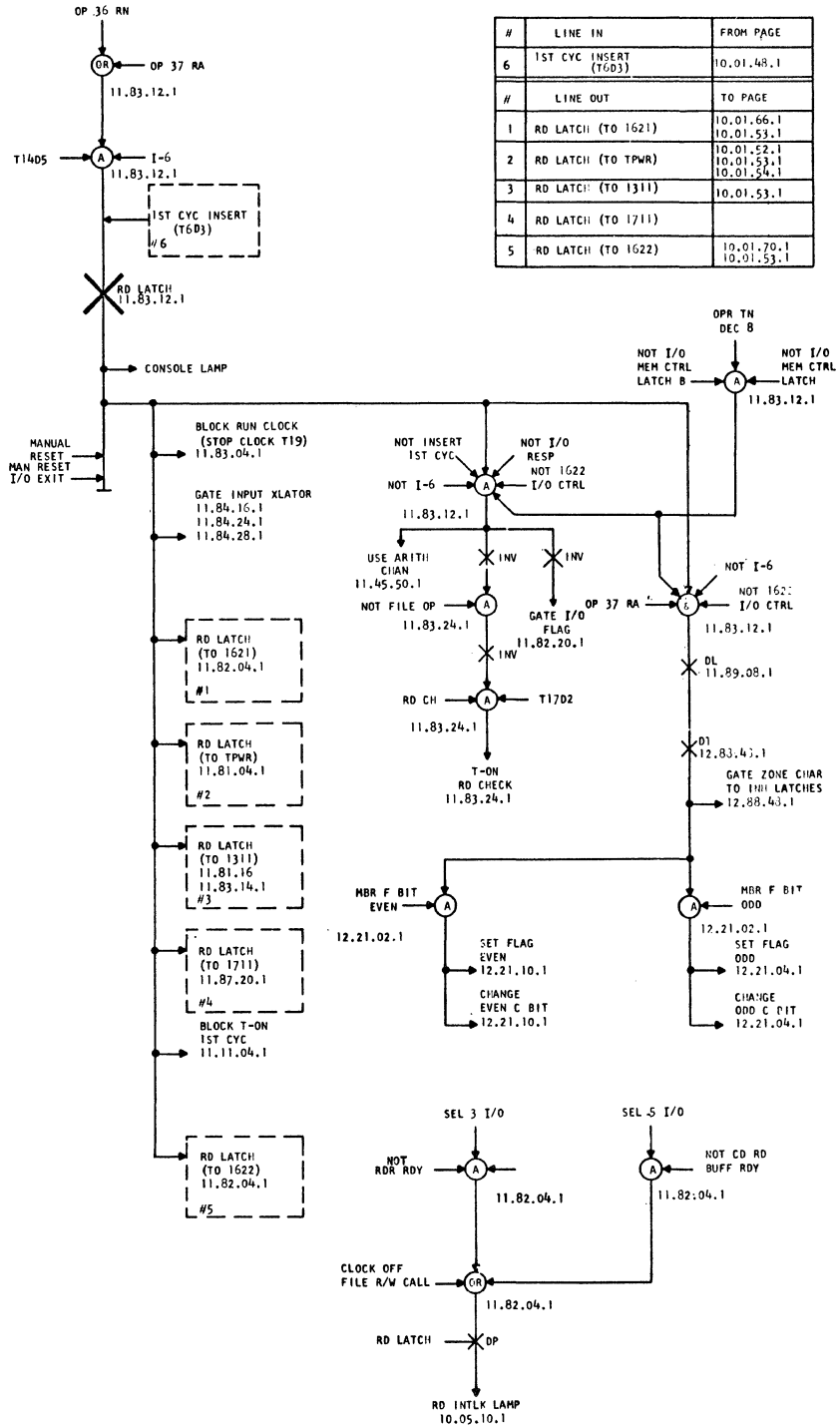




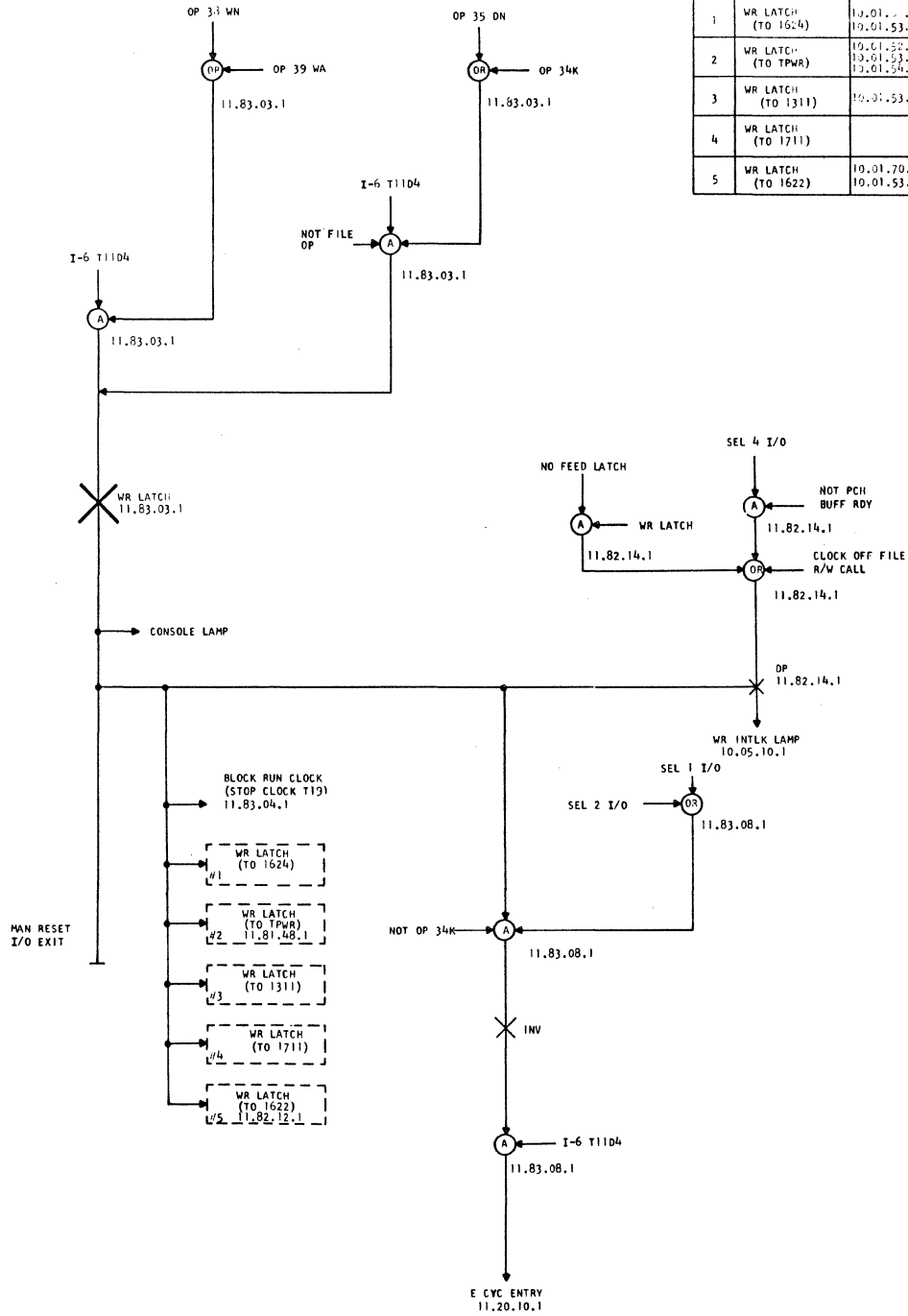


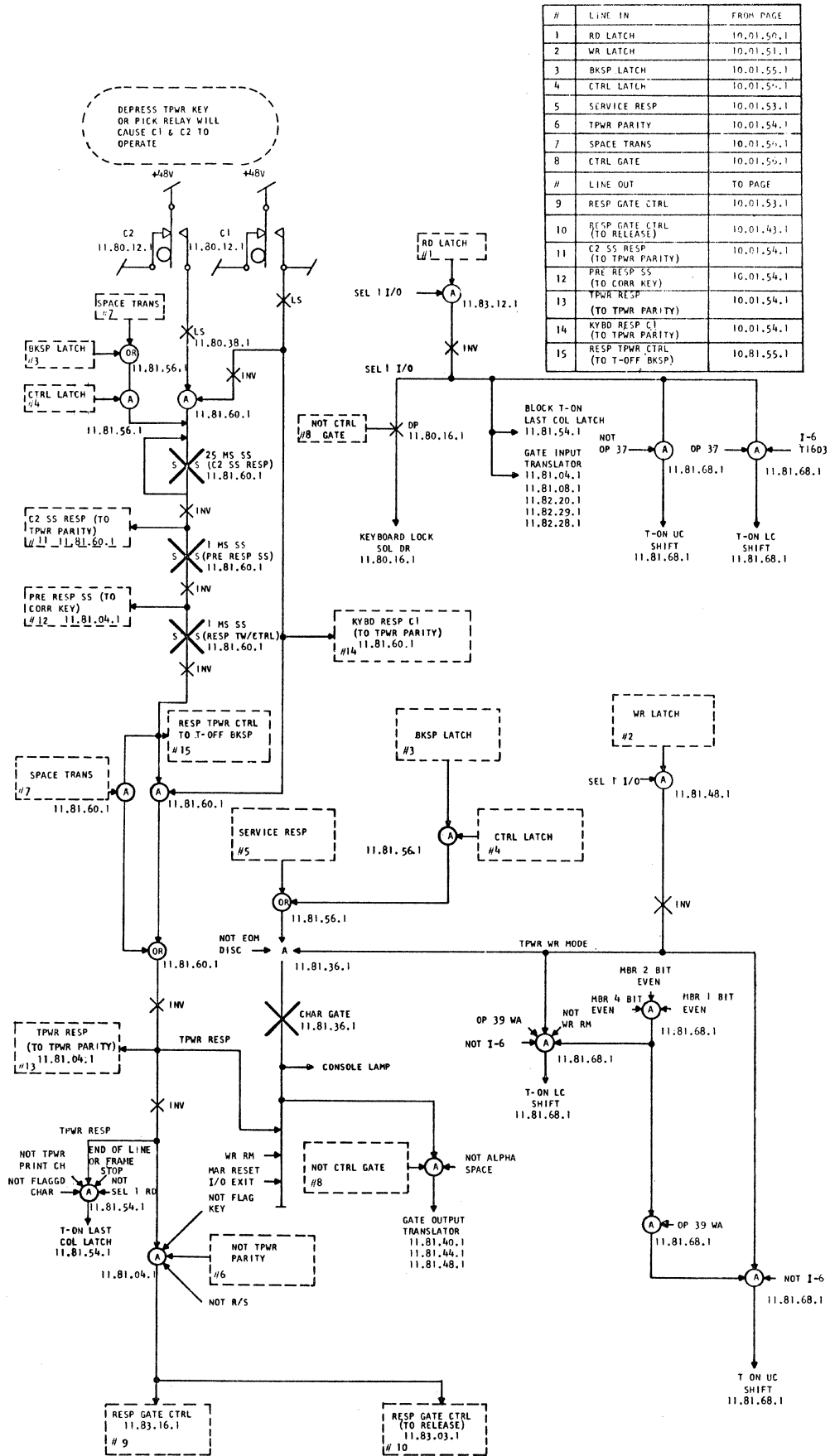


#	LINE IN	FROM PAGE
6	1ST CYC INSERT (T6D3)	10.01.48.1
#	LINE OUT	TO PAGE
1	RD LATCH (TO 1621)	10.01.66.1 10.01.53.1
2	RD LATCH (TO TPWR)	10.01.52.1 10.01.53.1 10.01.54.1
3	RD LATCH (TO 1311)	10.01.53.1
4	RD LATCH (TO 1711)	
5	RD LATCH (TO 1622)	10.01.70.1 10.01.53.1

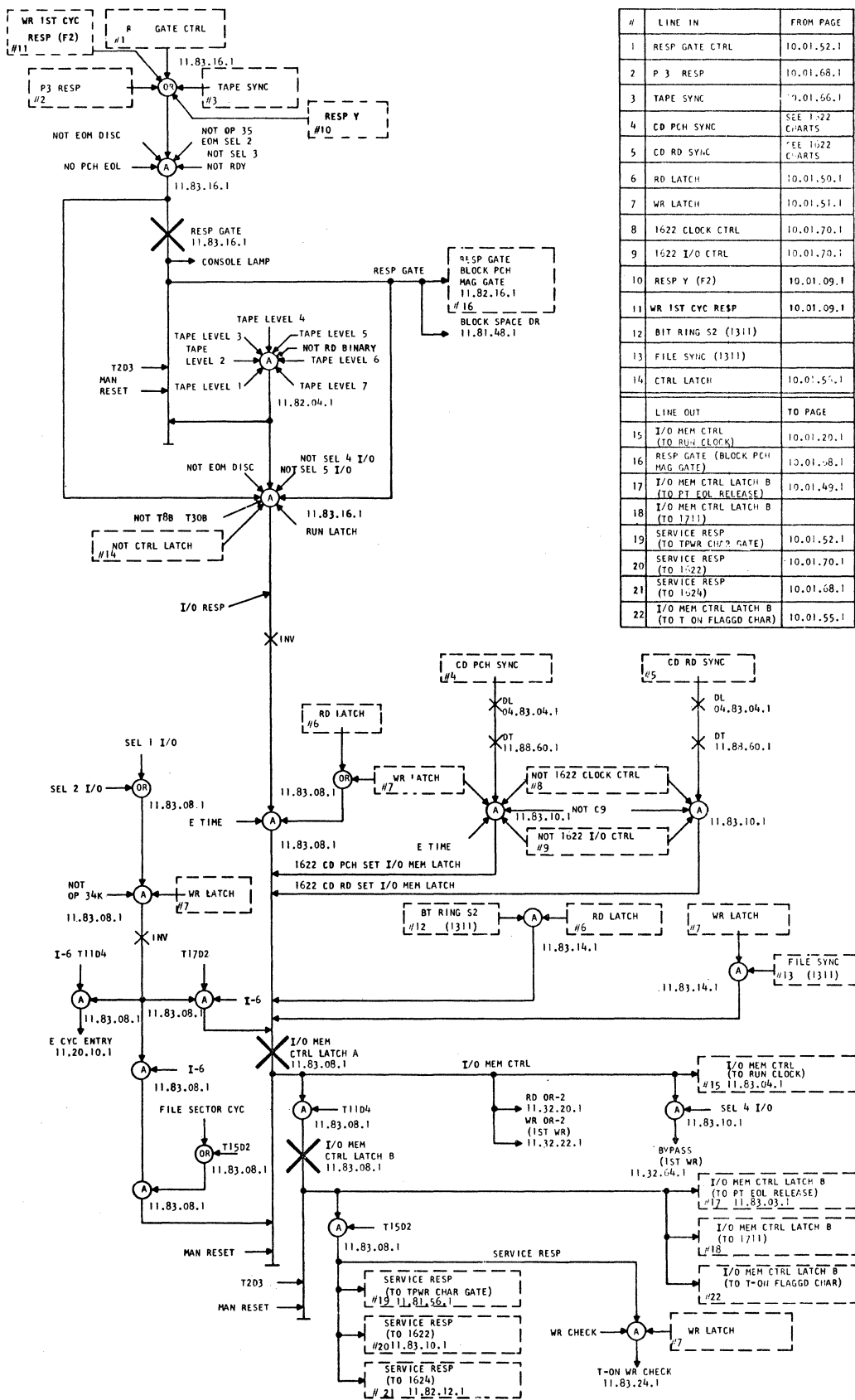


#	LINE OUT	TO PAGE
1	WR LATCH (TO 1624)	10.01.52.1 10.01.53.1
2	WR LATCH (TO TPWR)	10.01.52.1 10.01.53.1 10.01.54.1
3	WR LATCH (TO 1311)	10.01.53.1
4	WR LATCH (TO 1711)	
5	WR LATCH (TO 1622)	10.01.70.1 10.01.53.1

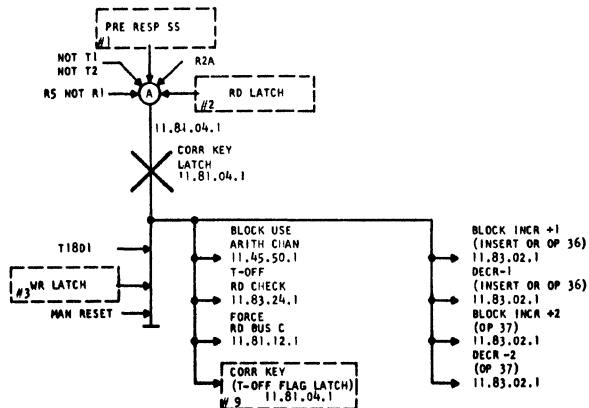




#	LINE IN	FROM PAGE
1	RD LATCH	10.01.50.1
2	WR LATCH	10.01.51.1
3	BKSP LATCH	10.01.55.1
4	CTRL LATCH	10.01.55.1
5	SERVICE RESP	10.01.53.1
6	TPWR PARITY	10.01.54.1
7	SPACE TRANS	10.01.55.1
8	CTRL GATE	10.01.55.1
#	LINE OUT	TO PAGE
9	RESP GATE CTRL	10.01.53.1
10	RESP GATE CTRL (TO RELEASE)	10.01.43.1
11	C2 SS RESP (TO TPWR PARITY)	10.01.54.1
12	PRE RESP SS (TO CORR KEY)	10.01.54.1
13	TPWR RESP (TO TPWR PARITY)	10.01.54.1
14	KYBD RESP C1 (TO TPWR PARITY)	10.01.54.1
15	RESP TPWR CTRL (TO T-OFF BKSP)	10.81.55.1

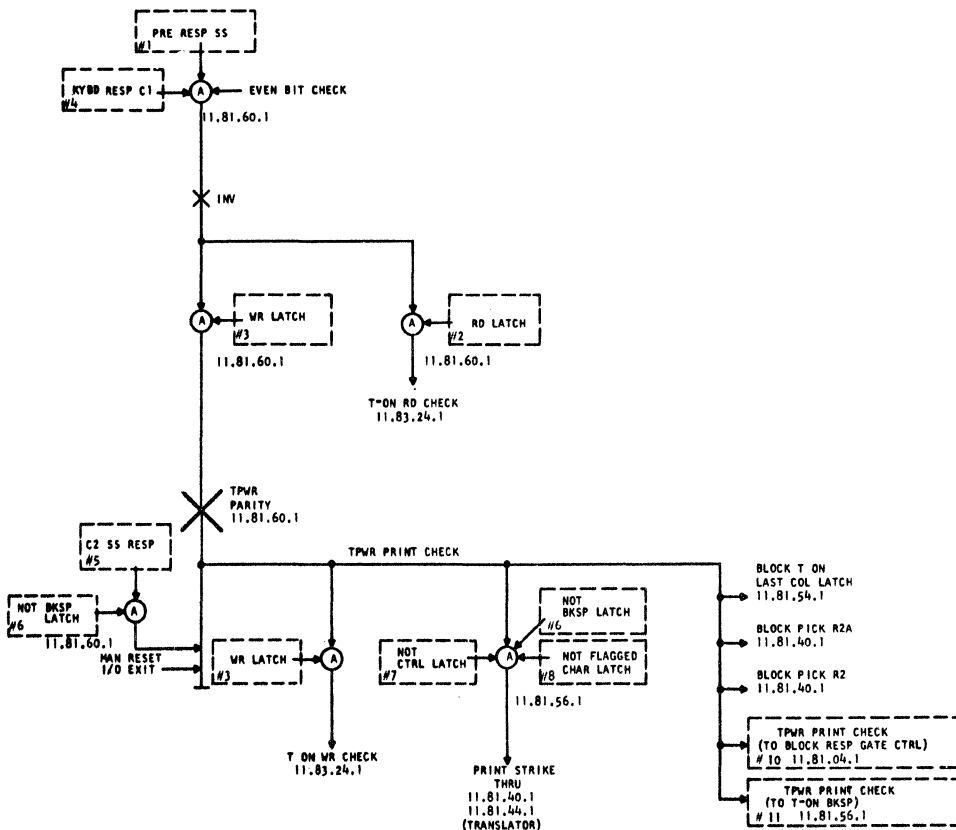


#	LINE IN	FROM PAGE
1	RESP GATE CTRL	10.01.52.1
2	P 3 RESP	10.01.68.1
3	TAPE SYNC	10.01.66.1
4	CD PCH SYNC	SEE 1-22 CHARTS
5	CD RD SYNC	SEE 1-22 CHARTS
6	RD LATCH	10.01.50.1
7	WR LATCH	10.01.51.1
8	1622 CLOCK CTRL	10.01.70.1
9	1622 I/O CTRL	10.01.70.1
10	RESP Y (F2)	10.01.09.1
11	WR 1ST CYC RESP	10.01.09.1
12	BIT RING S2 (1311)	
13	FILE SYNC (1311)	
14	CTRL LATCH	10.01.55.1
LINE OUT	TO PAGE	
15	I/O MEM CTRL (TO RUN CLOCK)	10.01.20.1
16	RESP GATE (BLOCK PCH MAG GATE)	10.01.08.1
17	I/O MEM CTRL LATCH B (TO PT EOL RELEASE)	10.01.49.1
18	I/O MEM CTRL LATCH B (TO 1711)	
19	SERVICE RESP (TO TPWR CH? GATE)	10.01.52.1
20	SERVICE RESP (TO 1-22)	10.01.70.1
21	SERVICE RESP (TO 1624)	10.01.68.1
22	I/O MEM CTRL LATCH B (TO T-ON FLAGGD CHAR)	10.01.55.1

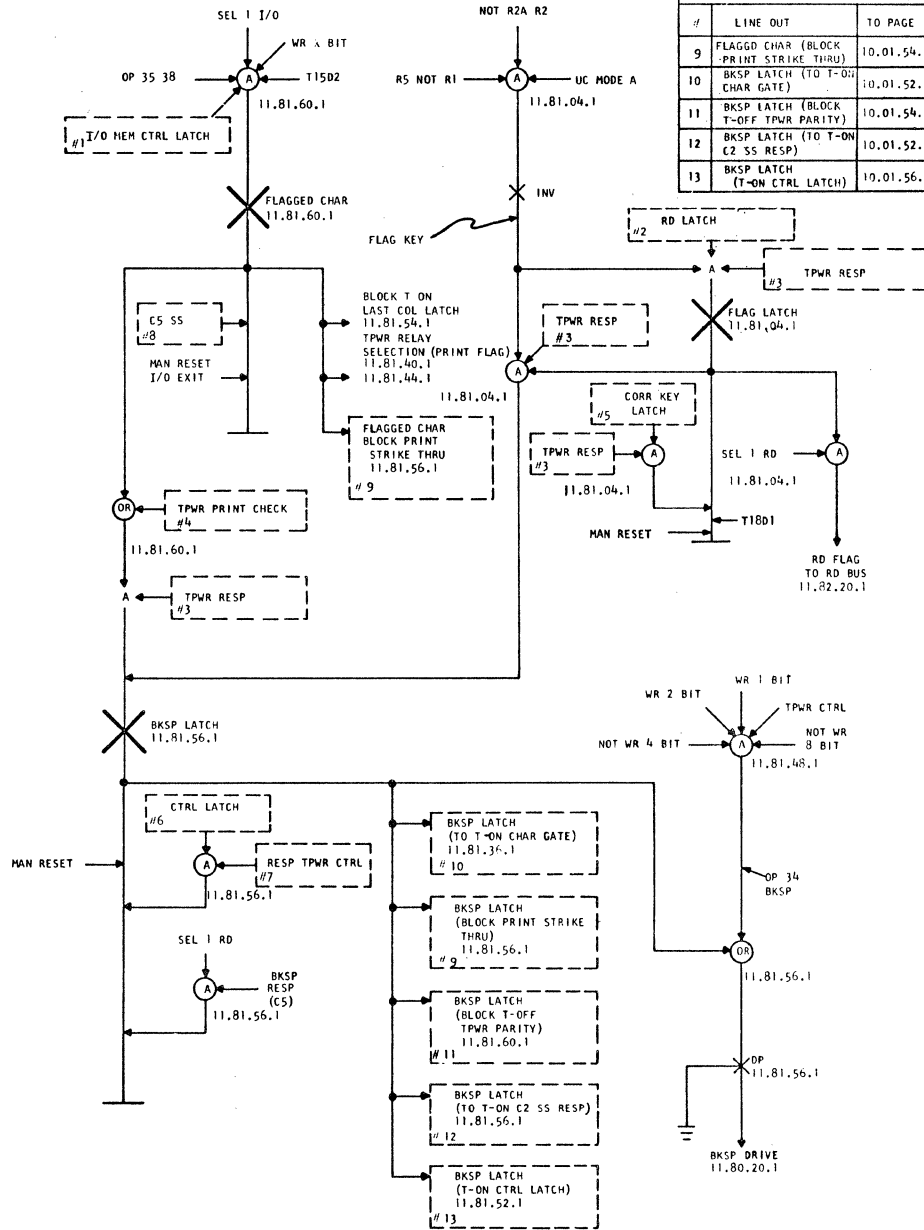


#	LINE IN	FROM PAGE
1	PRE RESP SS	10.01.52.1
2	RD LATCH	10.01.50.1
3	WR LATCH	10.01.51.1
4	KYBD RESP C1	10.01.52.1
5	C2 SS RESP	10.01.52.1
6	BKSP LATCH	10.01.55.1
7	CTRL LATCH	10.01.56.1
8	FLAGGED CHAR LATCH	10.01.55.1
LINE OUT		TO PAGE
9	CORR KEY (T-OFF FLAG LATCH)	10.01.55.1
10	TPWR PRINT CHECK (TO BLOCK RESP GATE CTRL)	10.01.52.1
11	TPWR PRINT CHECK (TO T-ON BKSP)	10.01.05.1

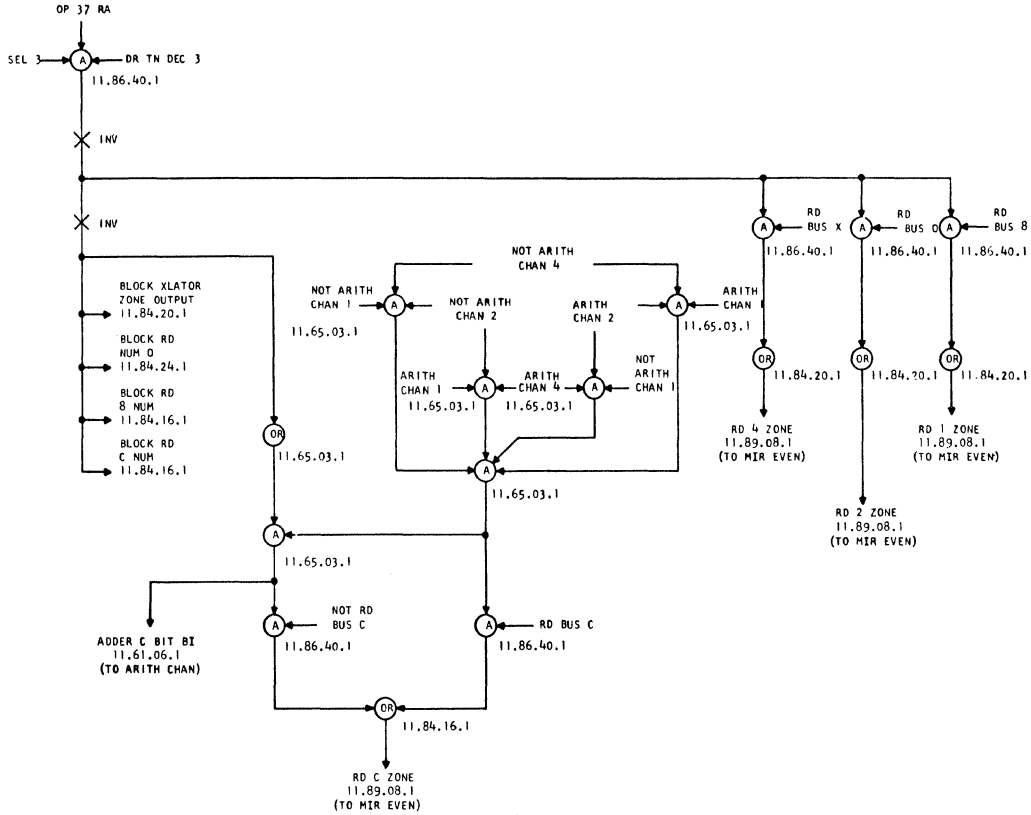
TPWR PARITY



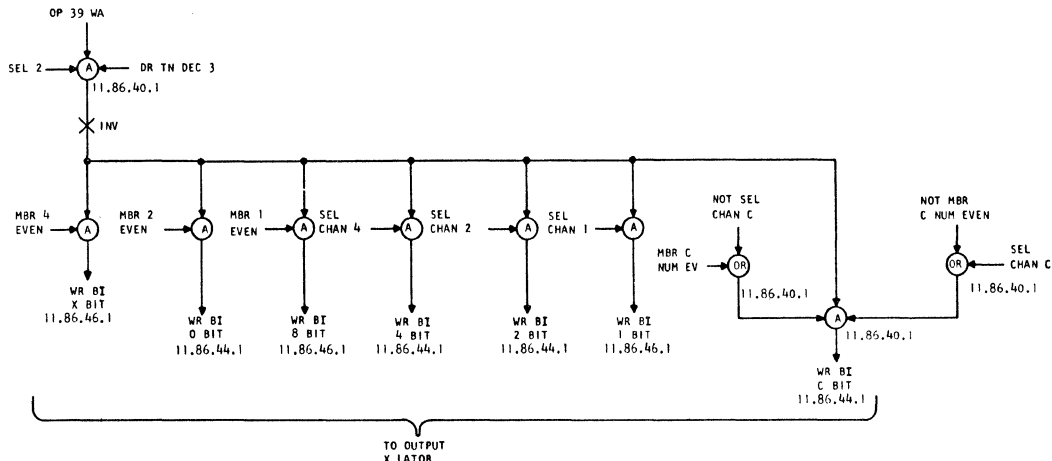
#	LINE IN	FROM PAGE
1	I/O MEH CTRL LATCH B	10.01.53.1
2	RD LATCH	10.01.50.1
3	TPWR RESP	10.01.52.1
4	TPWR PRINT CHECK	10.01.54.1
5	CORR KEY LATCH	10.01.54.1
6	CTRL LATCH	10.01.50.1
7	RESP TPWR CTRL	10.01.52.1
8	C5 SS	10.01.56.1
#	LINE OUT	TO PAGE
9	FLAGGD CHAR (BLOCK PRINT STRIKE THRU)	10.01.54.1
10	BKSP LATCH (TO T-ON CHAR GATE)	10.01.52.1
11	BKSP LATCH (BLOCK T-OFF TPWR PARITY)	10.01.54.1
12	BKSP LATCH (TO T-ON C2 SS RESP)	10.01.52.1
13	BKSP LATCH (T-ON CTRL LATCH)	10.01.56.1

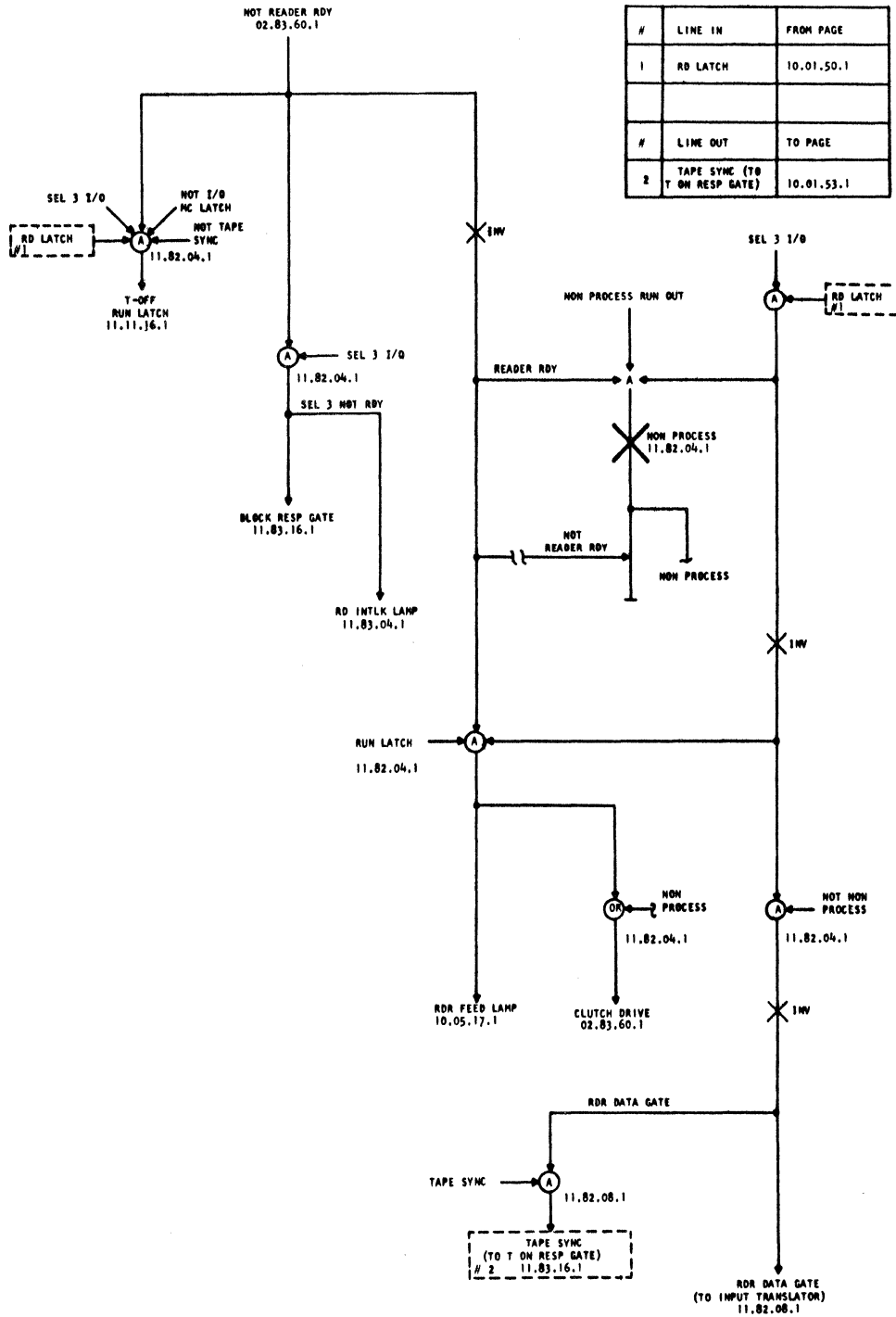


RD BINARY
37 XXXXX 03300



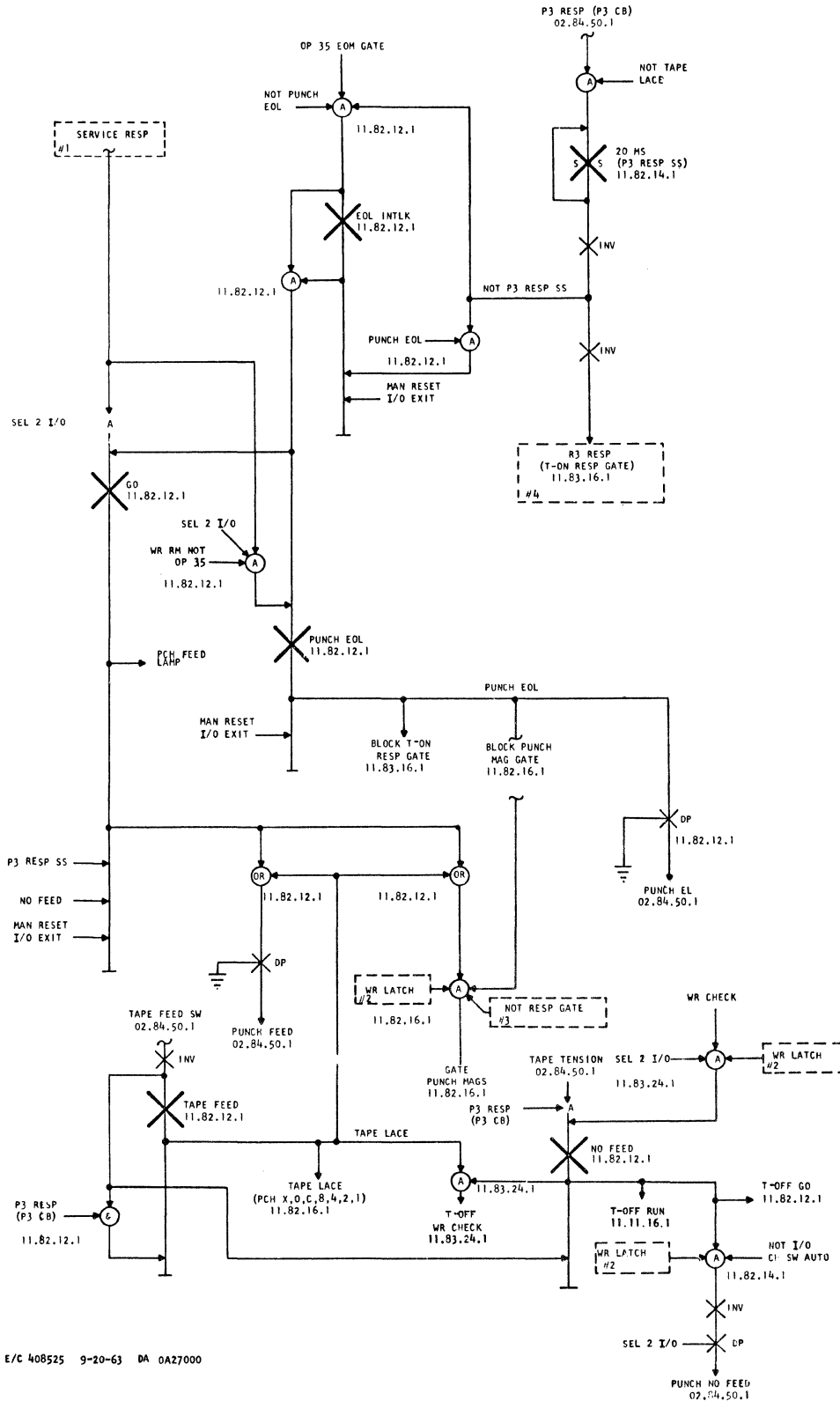
WR BINARY
39 XXXXX 03200

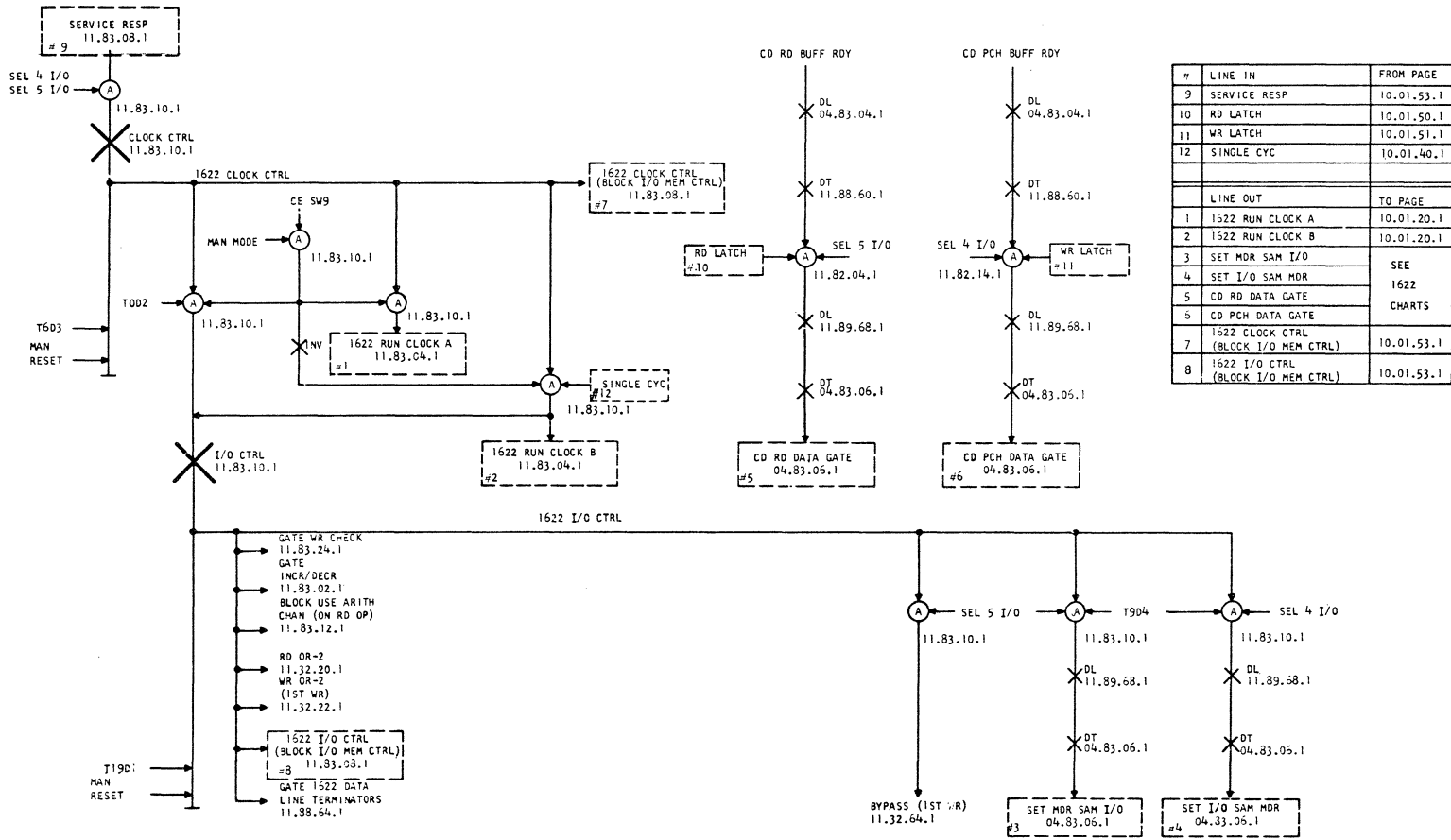


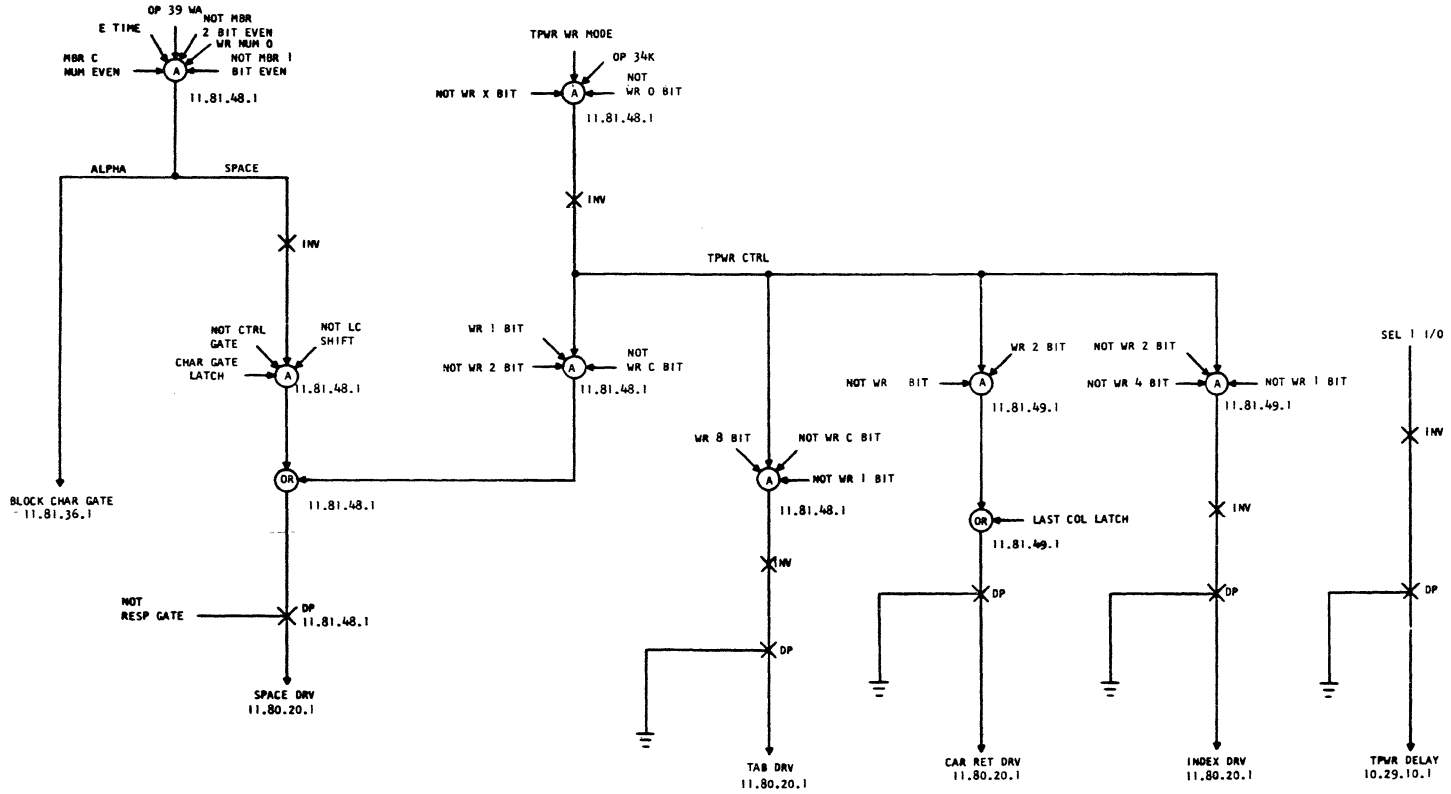


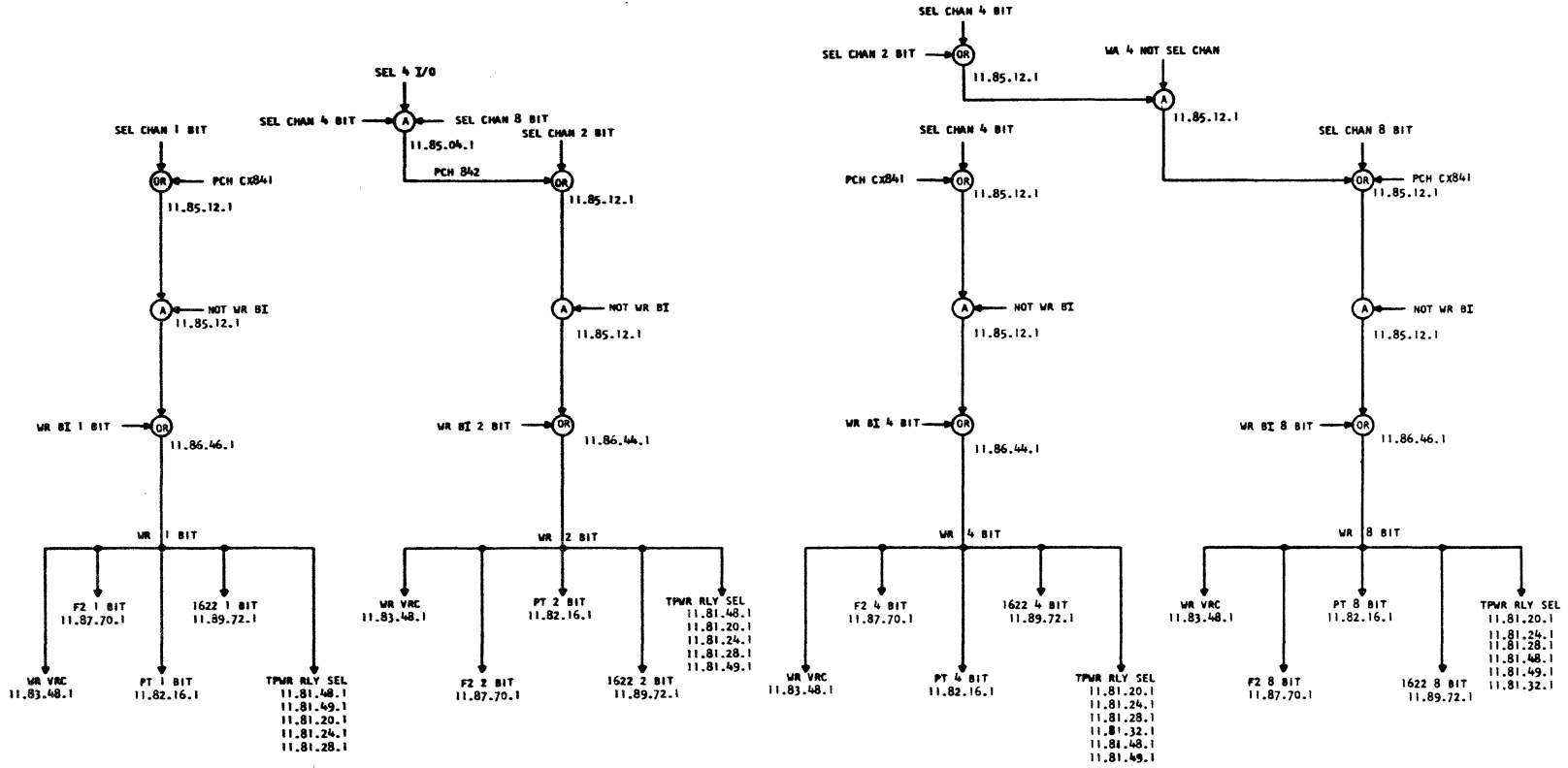
#	LINE IN	FROM PAGE
1	RD LATCH	10.01.50.1
#	LINE OUT	TO PAGE
2	TAPE SYNC (TO T ON RESP GATE)	10.01.53.1

#	LINE IN	FROM PAGE
1	SERVICE RESP	10.01.53.1
2	WR LATCH	10.01.51.1
3	RESP GATE	10.01.53.1
#	LINE OUT	TO PAGE
4	P3 RESP (T-ON RESP GATE)	10.01.53.1

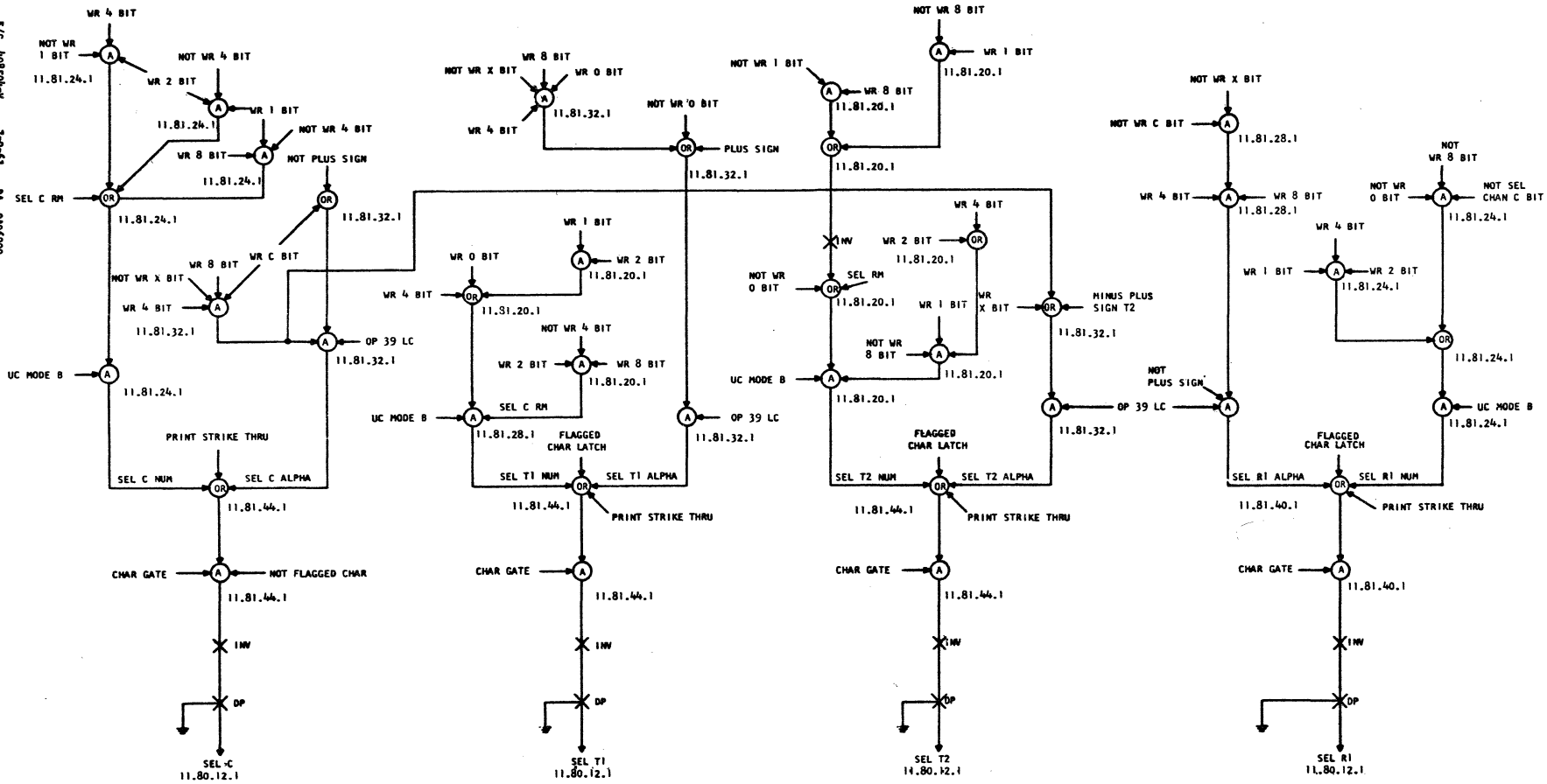








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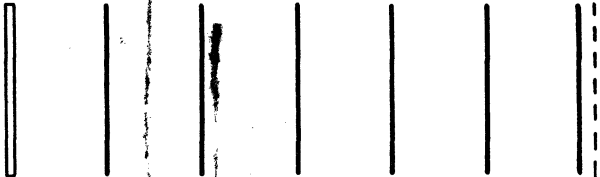
TPWR SEL RELAYS
C, T1, T2, R1

1620 II

10.01.97.1

1620

**Model 2
System**



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