

47000

DRAWING NUMBER		TITLE	USED IN	ORIGI-NATOR	DRAWN BY	ENG'G APPROVAL	GRADED			CLASS	REMARKS
							1	2	3		
A 47000	//	FIG. 6 TYPICAL RESOLVER CONVERSION SYSTEM		J DINTENASS	M. CLARK						~ OBSOLETE ~
A 47001	//	FIG. 9 PHASE SHIFTER CONVERSION SYSTEM		J DINTENASS	M. CLARK						~ OBSOLETE ~
A 47002	//	BLOCK DIAGRAM OF SINGLE CHANNEL ALL-ELECTRONIC ANALOG SYSTEM		J DINTENASS	R BARBARON						~ OBSOLETE ~
A 47003	//	LOGARITHMIC MULTIPLIER		J DINTENASS	R BARBARON						~ OBSOLETE ~
A 47004	//	DIGITAL CONVERTER WITH STORED TRIG. TABLE FIG. 22		J DINTENASS	M. CLARK						~ OBSOLETE ~
R 47005	// -2	BLOCK DIAGRAM 1077 BUFFER		Mc VICKAR	M. MATAS						ENG. RESEARCH ASSOCIATES
D 47006	//	BLOCK DIAGRAM AUXILIARY SYSTEM		Mc VICKAR	M. MATAS						ENG. RESEARCH ASSOCIATES
SD 47007	✓	INCOMPLETE FIRST DRAFT COMPREHENSIVE LOGICAL DIAGRAM FOR ONE COMPLETE CENTER		R MAVER	R MAVER						~ CONFIDENTIAL ~
SB 47008	// -1	TEST CHARACTER GENERATOR		K OLSEN	K OLSEN						
A 47009	// -1	SINE WAVE SAMPLING		DINTENASS	A. DUSCH						MN
SD 47010	// -1	SUMMARY OF BLOCK SCHEMATICS: INTERNAL MACHINE		R MAVER	R MAVER						2 SHEETS RESTRICTED
SD 47011	// -4	PROPOSED BLOCK DIAGRAM MTC		P BAGLEY	P BAGLEY						~ OBSOLETE ~
SD 47012	// -1	BLOCK DIAGRAM, DRUM STORAGE SYSTEM, MTC		P BAGLEY	P BAGLEY						~ OBSOLETE ~ SUPERSEDED BY SD-56291
J 47013	// -1	THE 4225 B2 MULTIPLIER OUTLINE BR		A KLEIN	M. OKEN						MN
SA 47014	✓	GENERAL LOGICAL OUTLINE FOR XD-1		R MAVER	R MAVER						~ CONFIDENTIAL ~ ~ OBSOLETE ~ SUPERSEDED BY SA-56987
SC 47015	//	BLOCK DIAGRAM - MAGNETIC CORE MEMORY II MTC		W. CANTY	F. BRIDGMAN						
SB 47016	// -2	WRITE CONTROL AUXILIARY		R PADDOCK	R PADDOCK						
B 47017	//	BLOCK DIAGRAM FOR DESIGN PROCEDURE H (S)		LINVILLE	STRATIS						F-2060 SN-627
A 47018	//	EQUIVALENT BLOCK DIAGRAM FOR DESIGN PROCEDURE AT H(S)		B LINVILLE	M. OKEN						F-2061 SN-628
A 47019	//	ARITHMETIC ELEMENT		S THOMPSON	M. CLARK						MN
SB 47020		DIGITAL DISPLAY		M. EPSTEIN	M. EPSTEIN						~ CONFIDENTIAL ~ ~ OBSOLETE ~

DRAWING NUMBER	TITLE	USED IN	ORIGINATOR	DRAWN BY	ENG'G APPROVAL	GRADED			CLASS	REMARKS
						1	2	3		
SA 47021	INTENSITY CONTROL FOR CATHARTRON		H ZIEMAN	H ZIEMAN						
SC 47022	CATEGORY AND FEATURE SELECTION AT DISPLAY CONSOLE (LD-1)		R GERHARDT	R GERHARDT						~ CONFIDENTIAL ~
SC 47023	MEMORY TEST SETUP I		D DOLFO	E GUDITZ						
B 47024	DIGITAL CONVERTER USING TRIGONOMETRIC METAL COUNTERS		J DINTENFELD	J GRINE						MN
B 47025	BLOCK DIAGRAM, AUTOMATIC CORE TESTER, TE		R SENELEY	P BISHKOP	J McEUGHER	D BROWN				
SB 47026	MAGNETIC DRUM TEST SETUP #1		H ANDERSON	H ANDERSON						
SB 47027	BLOCK DIAGRAM 1230 SPECIAL NOTE #3		B MORRIS	B MORRIS						~ OBSOLETE ~ SUPERSEDED BY SB-47040
SA 47028	BLOCK DIAGRAM 1231, TIMING SYNCHRONIZER SPECIAL NOTE #3		B MORRIS	B MORRIS						~ OBSOLETE ~ SUPERSEDED BY SB-47040
SA 47029	BLOCK DIAGRAM 1232, "B" DETECTOR - SPECIAL NOTE #3		B MORRIS	B MORRIS						~ OBSOLETE ~ SUPERSEDED BY SB-47040
SA 47030	BLOCK DIAGRAM 1233, STATUS INDICATOR SPECIAL NOTE #3		B MORRIS	B MORRIS						~ OBSOLETE ~ SUPERSEDED BY SB-47040
SA 47031	BLOCK DIAGRAM 1234, START-STOP SYNCHRONIZER, SPECIAL NOTE #3		B MORRIS	B MORRIS						~ OBSOLETE ~ SUPERSEDED BY SB-47040
SA 47032	BLOCK DIAGRAM 1235, "A" REGISTER - SPECIAL NOTE #3		B MORRIS	B MORRIS						~ OBSOLETE ~ SUPERSEDED BY SB-47040
SA 47033	BLOCK DIAGRAM 1236, "C" REGISTER - SPECIAL NOTE #3		B MORRIS	B MORRIS						~ OBSOLETE ~ SUPERSEDED BY SB-47040
SA 47034	BLOCK DIAGRAM 1237, MODE SELECTOR - SPECIAL NOTE #3		B MORRIS	B MORRIS						~ OBSOLETE ~ SUPERSEDED BY SB-47040
SB 47035	ADDRESS SELECTION MEMORY TEST SETUP II		R DOLFO	R DOLFO						
SC 47036	PROPOSED BLOCK DIAGRAM FOR FERRANTI PETR CONTROL		F IRISH	F IRISH						
47037										
SB 47038	BLOCK DIAGRAM GROUP AND FIELD CONTROL MTC		P BAGLEY	P BAGLEY						
D 47039	SIMPLIFIED BLOCK DIAGRAM, MTC		P BAGLEY	M OAHEN	W OGDEN					MN
C 47040	1230 SPECIAL NOTE #3 WMT		B MORRIS	M OAHEN	ESRICH					
SB 47041	OUTLINE OF DIGITAL DISPLAY SYSTEM WITH FLEXIBLE FORMAT XD-1		M EPSTEIN	M EPSTEIN						~ CONFIDENTIAL ~

47021

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47042

DRAWING NUMBER		TITLE	USED IN	ORIGI-NATOR	DRAWN BY	ENG'G APPROVAL	GRADED			CLASS	REMARKS
							1	2	3		
B 47042	✓	SIMPLIFIED BLOCK DIAGRAM OF TRACK-WHILE-SCAN		JW FORRESTER	M OKEN						~CONFIDENTIAL~ F-2138 SN-649 MN
B 47043	✓	HEIGHT FINDING		JW FORRESTER	S STRATIS						~CONFIDENTIAL~ F-2167 SN-678 MN
SA 47044	✓	ABC CRYSTAL TESTER		F YATES	F YATES						
B 47045	✓	IDENTIFICATION		JW FORRESTER	M OKEN						~CONFIDENTIAL~ F-2171 SN-682 MN
SC 47046	✓	DIGITAL EXPANSION BLOCK DIAGRAM AND CONTROL (FSQ-7)		R GERHARDT	R GERHARDT						
D 47047	✓	BLOCK SCHEMATIC, MITE UNIT FOR BUFFER DRUM, RM-156, WWI		E WIERLIN	C SMALL	K McVICAR			ES RICH		~OBSOLETE~
B 47048	✓	BLOCK DIAGRAM OF CIRCUIT FOR SWITCHING COEFFICIENT MEASUREMENT		SILVER	A STRATIS						MN
A 47049	✓	BLOCK DIAGRAM FOR FORWARD PROGRAM CONTROL		R MAGLIO	A GRIFFIN						MN
SB 47050	✓	SECOND ROUGH DRAFT DIGITAL DISPLAY BLOCK DIAGRAM		M EPSTEIN	M EPSTEIN						~CONFIDENTIAL~
SB 47051	✓	PRELIMINARY BLOCK DIAGRAM CRT FILTER SYSTEM		ES RICH	ES RICH						
SC 47052	✓	CATEGORY T FEATURE SELECTION AT GENERAL DISPLAY FRAME (XD-1)		R GERHARDT	R GERHARDT						~CONFIDENTIAL~
A 47053	✓	SYNOPSIS FOR COMPUTING ELEMENTS BLOCK DIAG. FOR T-8 SOLUTION OF $y'' = x^2y$		H NEUMANN	S STRATIS						MN
A 47054	✓	BLOCK DIAG. FOR SOLUTION $y'' = -2fw_0^2 y$ $wd^2 + c$, BLOCK DIAG. FOR SOLUTION OF $x'' = xy$ etc.		H NEUMANN	A STRATIS						MN
A 47055	✓	BLOCK DIAGRAM STORAGE SELECTION	THESIS	G YOUNG	BARBARAN						MN
B 47056	✓	BLOCK DIAGRAM DATA PROCESSOR INTERCHANGE METHOD	THESIS	G YOUNG	CARBONARO						MN
B 47057	✓	BLOCK DIAGRAM, DATA PROCESSOR	THESIS	G YOUNG	C SMALL						MN
A 47058	✓	BLOCK DIAGRAM, DATA PROCESSOR, GRID METHOD	THESIS	G YOUNG	C SMALL						MN
B 47059	✓	BLOCK DIAGRAM DATA PROCESSOR DOUBLE GRID METHOD	THESIS	G YOUNG	ACARBONARO						MN
D 47060	✓	SYSTEM BLOCK DIAGRAM		S H DODD	CARBONARO					Declassified 2-25-57	~CONFIDENTIAL~ SD-708 F-2196
A 47061	✓	THE CONTROLLED DISPLAY UNIT	REPORT	B KLEIN	R BARBARAN						
B 47062	✓	A SELECTION SWITCH FOR THE CLASS II MULTIPLIER	REPORT	B KLEIN	R BARBARAN						

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47063

DRAWING NUMBER	TITLE	USED IN	ORIGINATOR	DRAWN BY	ENG'G APPROVAL	GRADED			CLASS	REMARKS
						1	2	3		
C 47063	THE ACS BA, 2x2 MULTIPLY-ADDER WITH ACR	REPORT	B. KLEIN	B. BARBAROW						
A 47064	ARITHMETIC SECTION OF CLASS II MULTIPLIERS	REPORT	B. KLEIN	M. OKEN						
D 47065	FURTHER DETAIL OF CLASS III MULTIPLICATION		W. KLEIN	B. BARBAROW						
D 47066	ARITHMETIC SECTION FOR ACS B2, g=2 AND m=16 (OMITTING BR)		W. KLEIN	M. OKEN						
A 47067	A CONTROL FOR CLASS III MULTIPLIERS		W. KLEIN	M. OKEN						
B 47068	THE ACS BA, 1x2 MULTIPLY-ADDER WITH ACR		W. KLEIN	M. OKEN						
SB 47069	BLOCK DIAGRAM MAGNETIC DRUM TEST SETUP #2		H. ANDERSON	H. ANDERSON						
D 47070	THE ACS BA, 3x2 MULTIPLY-ADDER WITH ACR AND AR (m=16)		W. KLEIN	C. SMALL						
SB 47071	CHARACTER POSITIONING CIRCUIT I, FSG-7		R. GERHARDT	R. GERHARDT						
SB 47072	CHARACTER POSITIONING CIRCUITS II, FSG-7		R. GERHARDT	R. GERHARDT						
SB 47073	CHARACTER POSITIONING CIRCUITS III, FSG-7		R. GERHARDT	R. GERHARDT						
SB 47074	DID TIMING AND SHIFTING		M. EPSTEIN	M. EPSTEIN						CONFIDENTIAL
SB 47075	BLOCK DIAGRAM, PETA CONTROL, MTC		J. ACKLEY	J. ACKLEY						
SB 47076	POSSIBLE AUTOMATIC MEMORY DISPLAY		J. CRANE	J. CRANE						
B 47077	FLIP FLOP STORAGE		K. RATHBONE	C. SMALL						MN
B 47078	FLIP FLOP STORAGE CIRCUIT		K. RATHBONE	C. SMALL						MN
B 47079	TEST STORAGE		K. RATHBONE	A. BARBAROW						MN
B 47080	CLOCK PULSE CONTROL		K. RATHBONE	A. BARBAROW						MN
SD 47081	BLOCK DIAGRAM EQUIPMENT FOR XD-1 MAPPER TEST		B. MORRIS	B. MORRIS						
C 47082	MAGNETIC-CORE MEMORY CONTROL (TWO-BANK OPERATION)		A. ROBERTS	J. GRINE						MN
SB 47083	DID SYSTEM BLOCK DIAGRAM		M. EPSTEIN	M. EPSTEIN						CONFIDENTIAL

APPROVED FOR PUBLIC RELEASE. CASE 06-1104.

DRAWING NUMBER	TITLE	USED IN	ORIGINATOR	DRAWN BY	ENG'G APPROVAL	GRADED			CLASS	REMARKS
						1	2	3		
SB 47084	DID SLOT COUNTER AND SPECIAL COMMANDS		M. EPSTEIN	M. EPSTEIN 2-18-54						CONFIDENTIAL
SA 47085	FIG 1 THE ASYNCHRONOUS SYSTEM		R. HOPKINS	R. HOPKINS 3-2-54						
SA 47086	FIG 2 THE SYNCHRONOUS SYSTEM (#3)		R. HOPKINS	R. HOPKINS 3-2-54						
SA 47087	FIG 3 THE MIXED SYSTEM (#1)		R. HOPKINS	R. HOPKINS 3-2-54						
SA 47088	SKETCH DRUM SWITCHING SCHEME TD-1		H. BOYD	H. BOYD 3-5-54						
SC 47089	BLOCK DIAGRAM CHARACTER POSITIONING CIRCUITS SITUATION DISPLAY GENERATOR X D-1		R. GERHARDT	R. GERHARDT 3-13-54						
SA 47090	A PULSED SEQUENTIAL SWITCHING CIRCUIT		W. KLEIN	W. KLEIN 3-15-54						
D 47091	MTS POWER SUPPLY CONTROL		D. FISHER	D. FISHER 2-16-54				HUSLER 7-27-55		(E-47091 ORSO)
C 47092	LINE DIAGRAM, 532, W/WI PHOTOELECTRIC TAPE READER		G. YOUNG	M. CROCKEN 3-26-54	R. GOULD 4-9-54			ESRICH 4-9-54	MN	
B 47093	BLOCK DIAGRAM APC TRANSISTORIZED CHECKING SYSTEM		E. COHLER	B. PETRIUS 3-22-54						
C 47094	MAGNETIC CORE SHIFT REGISTER EVALUATOR		J. SCHULTZ	J. GRINE 3-22-54						
A 47095	ADVANCE CURRENT PULSE GENERATOR		J. SCHULTZ	J. GRINE 3-22-54						MN
A 47096	BLOCK DIAGRAM OF COMPUTATIONAL SYSTEM	THESIS	D. HOGAN	B. PETRIUS 3-24-54						MN
A 47097	BLOCK DIAGRAM OF A PROCESS CONTROL SYSTEM WITH NUMEROUS TERMINAL DEVICES	THESIS	D. HOGAN	B. PETRIUS 3-24-54						MN
SB 47098	SIMPLIFIED BLOCK DIAGRAM DRUM PARITY CHECK SYSTEM			L. HOLMES 3-24-54						
SA 47099	BATTLED CHECKING LOGIC FOR MEMORY TEST SETUP VI		E. GUDITZ	E. GUDITZ 3-25-54						
SA 47100	MTS II PATTERN MONITOR		E. GUDITZ	E. GUDITZ 3-25-54						
SA 47101	MARKER GENERATOR LOGIC MTS VI		E. GUDITZ	E. GUDITZ 3-25-54						
D 47102	DATA SCREENING - TRACK - WHILE - SCAN		ATTRIDGE	M. OKEN 3-26-54						CONFIDENTIAL
SC 47103	TD-1 DESIGN NOTES, BLOCK OUTLINE		R. MAYER	R. MAYER 3-31-54						SECRET 4 SHEETS SUPERSEDED 12/14 ORSOLETIC-47103-13-14
SB 47104	A POSSIBLE LOGICAL DESIGN FOR G-A SECTION OUTPUT STORAGE ELEMENT		R. HOPKINS	R. HOPKINS 4-1-54						

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							1	2	3		
SB 47105	//	A POSSIBLE LOGICAL DESIGN FOR G-G SECTION (1300 PPS) OUTPUT STORAGE ELEMENT		R HOPKINS	R HOPKINS						
SA 47106	✓	BLOCK DIAGRAM DID TIMING AND INTENSIFY		M. EPSTEIN	M. EPSTEIN						~ CONFIDENTIAL ~ ~ OBSOLETE ~
SB 47107	✓	BLOCK DIAGRAM DID SLOT NUMBER POSITION AND ERASE		M. EPSTEIN	M. EPSTEIN						~ CONFIDENTIAL ~ ~ OBSOLETE ~
SB 47108	✓	BLOCK DIAGRAM DID CHARACTER STORAGE SPECIAL COMMANDS X POSITION		M. EPSTEIN	M. EPSTEIN						~ CONFIDENTIAL ~ ~ OBSOLETE ~
SB 47109	//	DELTA-MEASUREMENT SET-UP		H. MOGENSEN	H. MOGENSEN						
B 47110	//	BLOCK DIAGRAM TEST SYSTEM OF MAGNETIC MEMORY AND 16X16 SWITCH SHOWING ETC	THESIS	J RAFFEL	B PETRELIS						MN
SC 47111	✓	BLOCK OUTLINE, XD-1 ADMINISTRATIVE (XD-1) DESIGN NOTES, BLOCK OUTLINE		R. MAYER	R. MAYER						~ SECRET ~ SHEET 1 OF 4 SUPERSEDES SC-47103
SC 47112	✓	BLOCK OUTLINE, XD-1 INSTRUCTIONS ETC.		R. MAYER	R. MAYER						~ SECRET ~ SHEET 2 OF 4 SUPERSEDES SC-47103
SC 47113	✓	BLOCK OUTLINE, XD-1 EXTERNAL ETC.		R. MAYER	R. MAYER						~ SECRET ~ SHEET 3 OF 4 SUPERSEDES SC-47103
SC 47114	✓	BLOCK OUTLINE, XD-1 INTERNAL ETC.		R. MAYER	R. MAYER						~ SECRET ~ SHEET 4 OF 4 SUPERSEDES SC-47103
B 47115	//	BLOCK DIAGRAM OF SW MEASUREMENT EQUIPMENT		V. MERTON	U. SMALL						MN
C 47116	✓	BLOCK DIAGRAM OF CONTROL SECTION OF THE F-104 INTERSECTION SIMULATOR		D. NEWMAN	B. PETRELIS						~ SECRET ~ MN
SB 47117	✓	FRAME DRAWING, MI DRUM ENTRY		R. MAYER	R. MAYER						~ CONFIDENTIAL ~
C 47118	//	SYSTEM DIAGRAM DISPLAY 4.0		R. FALLOWS	R. FALLOWS						MN
D 47119	//	BLOCK DIAGRAM OF DISCRETE-TIME SORTING UNIT		H. LEWIS	S. LEINE						
SD 47120	✓	FRAME DRAWING, SELECTION + IO CONTROL (XD-1 DESIGN NOTES, FRAME DRAWING SERIES)		R. MAYER	R. MAYER						
SD 47121	✓	FRAME DRAWING, PROGRAM ELEMENT TEST MEM. (XD-1 DESIGN NOTES, FRAME DRAWING SERIES)		R. MAYER	P. BAGLEY						DECLASSIFIED 7-6-54
SB 47122	✓	ELEMENT DIAGRAM SITUATION DISPLAY GENERATOR		R. FALLOWS	R. FALLOWS						
SB 47123	✓	4.3 DIGITAL DISPLAY GENERATOR ELEMENT DIAGRAM		M. EPSTEIN	M. EPSTEIN						
SD 47124	✓	FRAME DRAWING, ARITHMETIC ELEMENT (XD-1 DESIGN NOTES, FRAME DRAWING SERIES)		R. MAYER	R. MAYER						
SB 47125	✓	BLOCK DIAGRAM SITUATION DISPLAY GENERATOR		R. FALLOWS	R. FALLOWS						~ CONFIDENTIAL ~

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						1	2	3		
SB 47126	TEST LOGIC FOR DRUM WRITE CRT		H HENEGAR	H HENEGAR						
SA 47127	A POSSIBLE STATUS TRANSMITTER		R HOPKINS	R HOPKINS						
SB 47128	FRAME DRAWING, MI, I/O BUE ENTRY (XD-1 DESIGN NOTES FRAME DRAW SERIES)		R MAYER	R MAYER						
SA 47129	-1 CORE TEST LOGIC		J FREEMAN	H MOSELEY						
SA 47130	SW MEASUREMENT SET-UP		H MOSELEY	H MOSELEY						
SA 47131	RELAY LOGIC FOR CORE TEST SET UP		H MOSELEY	H MOSELEY						
SA 47132	SYMBOLS USED ON XD-1 DESIGN NOTES (PRELIMINARY SHEET)		R MAYER	R MAYER						
SA 47133	COMPUTER-DRUM PHASE DIAGRAM		R MAYER	R MAYER						
SD 47134	FRAME DRAWING, INSTRUCTIONAL ^{FRAME} SCHEMATIC PART (XD-1 DESIGN NOTES FRAME DRAW SERIES)		R MAYER	R MAYER						
SA 47135	XD-1 MEMORY LAYOUT		R MAYER	R MAYER						~ CONFIDENTIAL ~
SA 47136	XD-1 OPERATION CODE		R MAYER	R MAYER						~ CONFIDENTIAL ~
A 47137	CURRENT CALIBRATOR BLOCK DIAGRAM AND WAVEFORMS		CHILDRESS	BARBARON						MN
B 47138	SIMPLIFIED BLOCK DIAGRAM OF WWI VOLTAGE INTERLOCK SYSTEM		D MORRISON	S PETERS						MN
SD 47139	FRAME DRAWING, SITUATION DISPLAY ELEMENT (XD-1 DESIGN NOTES, FRAME DRAWING SERIES)		R MAYER	R MAYER						~ CONFIDENTIAL ~
SB 47140	ROUGH SKETCH OF DIGITAL DISPLAY		R MAYER	R MAYER						
SA 47141	-3 LOGIC, DELTA MEASUREMENT - M.D. I		J FREEMAN	H MOSELEY						
SD 47142	-1/ BLOCK DIAGRAM - DISPLAY TESTER		R FALLOWS	R FALLOWS						3 SHEETS; OBSOLETE ~
SA 47143	LIFE TEST EQUIPMENT		J CHILDRESS	H MOSELEY						
D 47144	BLOCK DIAGRAM OF CORE TESTER		R DANFORD	R BARBARON						MN
C 47145	BLOCK DIAGRAM OF MEMORY PLANE TESTS		R DANFORD	R BARBARON						
SI 47146	EXTERNAL CONNECTIONS, CORE TESTER SET 17		J FREEMAN	H MOSELEY						

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47147

DRAWING NUMBER		TITLE	USED IN	ORIGI-NATOR	DRAWN BY	ENG'G APPROVAL	GRADED			CLASS	REMARKS
							1	2	3		
B 47147	//	XRAY SELECTION PLANE SYSTEM 256X256 MEMORY		W.CANTY	W.CANTY						MN
		XRAY SELECTION PLANE SYSTEM 128X128 MEMORY			F-10-54						
SD 47148	//	MEMORY TEST SET UP VI		E.GUDITZ	E.GUDITZ						MN
					F-13-54						
A 47149	✓	BASIC BLOCK DIAGRAM OF TWS IN THE 1953 CAPS COD SYSTEM		J. LEYENSON	A. GRIFFIN						CONFIDENTIAL
					8-16-54						
D 47150	✓	BLOCK DIAGRAM RADAR DATA INPUT PROGRAM 1954 CAPS COD SYSTEM		E. WOLF	B. PETRELLI						CONFIDENTIAL MH
					F-19-54						
D 47151	✓	BLOCK DIAGRAM DATA ANALYSIS RADAR DATA INPUT PROGRAM 1954 CAPS COD SYSTEM		E. WOLF	A. CARBONNO						CONFIDENTIAL MH
					8-19-54						
SA 47152	//	PROPOSED MESSAGE GENERATOR AND CHECKER LOGIC VI		HANDERSON	HANDERSON						
					F-11-54						
A 47153	✓	BLOCK DIAGRAM TIME INTERVAL MEASURING LOGIC		C. HILBERT	M. HAYWOOD						
					8-20-54						
SB 47154	//	-1 LOGIC, DELTA MEASUREMENT		H. MOSKOWITZ	H. MOSKOWITZ						
					8-20-54						
SB 47155	//	-1 POWER DISTRIBUTION BLOCK DIAGRAM		F. WATSON	F. WATSON						
					2-26-54						
SB 47156	//	-1 POWER DISTRIBUTION BLOCK DIAGRAM		F. WATSON	F. WATSON						
					2-26-54						
SB 47157	//	POWER DISTRIBUTION BLOCK DIAGRAM		F. WATSON	F. WATSON						
					2-26-54						
SB 47158	//	-1 TIME INTERVAL DIVIDE OPERATION		C. HILBERT	M. HAYWOOD						
					8-20-54						
SB 47159	//	-1 BLOCK DIAGRAM OF THE LOGIC SCALE FACTOR		C. NORMAN	C. NORMAN						
					8-20-54						
SB 47160	//	-1 BLOCK DIAGRAM SHIFT LEFT AND CYCLE LEFT OPERATION		C. NORMAN	C. NORMAN						
					8-20-54						
SD 47161	✓	REWORK MRI MANNER BLOCK DIAGRAM FOR F-8-7		RISING	HANDERSON						
					9-7-54						
SB 47162	//	-1 BLOCK DIAGRAM SHIFT RIGHT OPERATION		C. NORMAN	C. NORMAN						
					9-10-54						
SB 47163	//	-2 BLOCK DIAGRAM MULTIPLY AND ROUND OFF AND MULTIPLY AND HOLD		C. NORMAN	C. NORMAN						
					9-10-54						
B 47164	✓	BLOCK DIAGRAM #2 TRACKS WITH OVERLAPPING SEARCH AREAS SUB-PROGRAM		H. PETERSON	B. PETRELLI						CONFIDENTIAL
					9-22-54						
B 47165	✓	BLOCK DIAGRAM #3 MONITOR SELECTION AND TRACK ASSIGNMENT PROGRAM		H. PETERSON	B. PETRELLI						CONFIDENTIAL
					7-22-54						
C 47166	✓	BLOCK DIAGRAM #1 DATA HISTORY ANALYSIS PROGRAM		H. PETERSON	B. PETRELLI						CONFIDENTIAL
					7-22-54						
SB 47167	//	BLOCK DIAGRAM TEST CONTROL SWITCHES		C. NORMAN	C. NORMAN						
					9-24-54						

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							1	2	3		
SB 47168	W	BLOCK DIAGRAM PUSH BUTTON CONTROLS		C. NORMAN	C. NORMAN						
B 47169	V	BLOCK DIAGRAM OF FINE GRAIN DATA SYSTEM		A. HUGHES	J. RANDALL						CONFIDENTIAL
SA 47170	W	SELECTION PLANE DRIVER BREND BOARD		SHANSKY	SHANSKY						
SB 47171	W	CORE MEMORY POWER DISTRIBUTION -450 V TX CRTS 606 + 607, WWT		L. HOLMES	R. BARBAROW	L. HOLMES					
SB 47172	W	BLOCK DIAGRAM, OUTPUT CODER, WWT		R. PADDOCK	R. PADDOCK						OBsolete
SB 47173	W	BLOCK DIAGRAM INITIATE RECORD 2 P. 3 YC		C. NORMAN	E. M. ROAD						OBsolete SUP BY SC-47252
SA 47174	W	DECODER STAGE AND DECODER LADDER		H. ZIEMAN	H. ZIEMAN						
A 47175	W	BLOCK DIAGRAM OF HIGH SPEED CORE DRIVER		S. BRADSPER	M. FENTRELL						MN
SB 47176	W	BLOCK SCHEMATIC, SHIFT REG PARITY MEV - OR E I OUTPUT CODER, WWT		L. NACOTT	C. BARRY						MN - OBsolete
SB 47177	W	SPECIAL TIME TAG ADDER (30000 DIPPED)		J. P. MAY	J. P. MAY						MN
SA 47178	W	SDV AND DIF. MONITORING TEST UNIT		P. MESSEMEIER	P. MESSEMEIER						MV
D 47179	W	BLOCK DIAGRAM 256 X 256 JOPE MEMORY, CM		E. GUDITZ	J. PEREWS	J. MITCHELL	P. MIHNI				
SB 47180	W	BLOCK DIAGRAM CHARACTER DISPLAY CONSOLE		H. ZIEMAN	H. ZIEMAN						
SA 47181	W	DISPLAY LINE DRIVER PS DIFFERENTIAL OPERATIONS		H. ZIEMAN	H. ZIEMAN						
SA 47182	W	LINE DRIVER PS. MEMORY LEVEL REG. UNITS		H. ZIEMAN	H. ZIEMAN						
W 47183	W	LOGIC PROGRAM MEMO'S		G. WICKLEY	P. MOTT						
SD 47184	W	COMPUTER CLEAR AND STARTOVER CLEAR		C. NORMAN	C. NORMAN						
SD 47185	W	COMPUTER COMPLIMENT SYSTEM		C. NORMAN	C. NORMAN						
SA 47186	W	BLOCK DIAGRAM OF 117 1000 2, 010101	TFE 012	P. DONALDSON	P. DONALDSON						
SD 47187	W	MAGNETIC DRUM INTERMEDIATE INPUT SYSTEM (C. 117)		V. P. DONALDSON	P. DONALDSON						
SD 47188	W	BLOCK DIAGRAM MTC CONNECTION TO (XD-1) DISPLAY FRAMES		R. H. GOULD	R. H. GOULD						

47189

DRAWING NUMBER	TITLE	USED IN	ORIGI-NATOR	DRAWN BY	ENG'G APPROVAL	GRADED			CLASS	REMARKS
						1	2	3		
SC-47189 ✓	18 DIGIT TEST MULTIPLIER	DIAGRAM	K. OLSEN	KAWYER						
D-47190 ✓	FLOW DIAG. - RM 756 CONSOLIDATED TEST PROGRAM, T-3600, CONTROL PROGRAM		MORRISON	MORRISON						MN
C-47191 ✓	FLOW DIAGRAM II SUCCESSIVE APPROXIMATIONS TO REACTIVE POWER FLOW		A. FALCION	A. FALCION						
SB-47192 ✓	BLOCK DIAGRAM - EXPERIMENTAL MULTIPLIER		J. FORGIE	J. FORGIE						
SB-47193 ✓	CONTROL FOR EXPERIMENTAL MULTIPLIER		FORGIE	FORGIE						
SB-47194 ✓	STEP COUNTER & SEQUENCE CONTROL FOR EXPER. MULTIPLIER		FORGIE	FORGIE						
SB-47195 ✓	ACCUMULATOR DIGIT FOR EXPER. MULTIPLIER		FORGIE	FORGIE						
SB-47196 ✓	B REGISTER DIGIT FOR EXPER. MULTIPLIER		FORGIE	FORGIE						
A-47197 ✓	BLOCK DIAGRAM RADIST TIMING SYSTEM IN WWII		R. STAHL	R. BARBOV						CONFIDENTIAL ✓
SB-47198 ✓	PROPOSED 21 DIGIT TX-1 BLOCK DIAGRAM		K. OLSEN	PARENFUL						
SC-47199 ✓	8 DIGIT TEST MULTIPLIER		K. OLSEN	SANDER						
47200	LA FOUNTAIN CO. A SIZE BOOK									
47201	LA FOUNTAIN CO. A SIZE BOOK									
47202										
47203										
47204										
47205										
47206										
47207										
47208										
47209										

DL-449

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