## Assembly Instructions - CT-CA Computer Cursor Control Board

The CT-CA Computer Cursor Control Board is a plug-on option used with the CT-1024 terminal system:that provides complete manual and/or computer control of cursor positioning. It is attached to the terminal system by simply plugging it onto jacks J 3 and J4 of the main terminal system. Note that either the manual or computer controlled cursor boards may be plugged onto the cursor control jacks, J3 and J4, but only one board may be plugged on at a time. The function of the manual cursor board is to allow incremental positioning of the cursor up, down, left or right. It also provides home-up, erase to end of line (EOL) and erase to end of frame (EOF). The functions are enabled by depressing the appropriate pushbutton switch ( 7 each which are not being supplied with the kit). The Computer Cursor Control Board has the same functions, but may also be controlled by the computer with ASCII control characters.

The circuitry provides the switch debouncing necessary to prevent multiple cursor counting thus insuring the cursor jumps only one position each time a directional button is depressed. The control switches themselves are SPST normally open pushbutton switches. The debouncing delay provided is $100 \mathrm{milli}-$ seconds, but longer delays can be achieved by making electrolytic capacitor C1. larger. The entire circuit is built up on a $31 / 16^{\prime \prime} \times 41 / 2^{\prime \prime}$ fibreglass circuit board which is plugged onto the main board on connector strips J3 and J4 just behind the memory board. Switch connections to the cursor board are provided on the nine pin connector attached to its circuit board.

## PC Board Assembly

NOTE: Since all of the holes on the PC board have been plated thru, it is only necessary to solder the components from the bottom side of the board. The plating provides the electrical connection from the "BOTTOM" to the "TOP" foil of each hole. It is important that none of the connections be soldered until all of the components of each group have been installed on the board. This makes it much easier to interchange components if a mistake is made during assembly. Be sure to use a low wattage iron (not a gun) with a small tip. Do not use acid core solder or any type paste flux. We will not guarantee or repair any kit on which either product has been used. Use only the solder supplied with the kit or a $60 / 40$ alloy resin core equivalent. Remember all of the connections are soldered on the bottom side of the board only.

Attach all of the resistors to the board. As with all other components unless noted, use the parts list and component layout drawing to locate each part and install from the "TOP" side of the board bending the leads along the "ВОТТОМ" side of the board and trimming so that $1 / 16$ " to $1 / 8$ " of wire remains. Solder.

Install all of the capacitors on the board. Be sure to orient the electrolytic capacitors correctly. The polarity is indicated on the component layout drawing. Solder.
) Install the transistor and diode on the board. The diode must be turned so the banded end corresponds with that shown on the component layout drawing, and the transistor must be turned so its lead configuration matches with that of the board. Leave a $1 / 4^{\prime \prime}$ space between the transistor case and the circuit board. Solder.

Install all of the intergrated circuits on the board being very careful to install each in its correct position. Do not bend the leads on the back side of the board. Doing so makes it very difficult to remove the integrated circuits should replacement ever be necessary. The semicircle notch on the end of the package is used for reference and should match with that shown on the component layout drawing for each of the IC's. Make sure the integrated circuits are down firmly against the board and solder.

Now attach the two ten pin female connectors to the board. These must be installed from the "TOP" side of the board and pressed down so the connectors seat firmly against the board. Solder.

Attach the 9 pin connector JMC-1 to the circuit board from the "TOP" side making sure to orient it exactly as shown in the component layout drawing. Note that the connector already has the pins installed. Make sure all of the pins are firmly against the nylon support. They can work loose when pressing the connector onto the board. Solder.

Now that most of the components have been installed on the board, double check to make sure that all have been installed correctly in their proper location.

Now check very carefully to make sure that all components have been soldered. It is very easy to miss some connections when soldering which can really cause some hard to find problems later during the check out phase. Also check for solder "bridges" and "cold" solder joints which are also a common problem.
Looking at the board from the "TOP" side with the connectors at the bottom, press the nylon indexing plug into the second pin from the right on the left connector (J3 pin 9).

Do not install jumpers on the board at this time.
This completes the circuit board assembly phase of the instructions. This board should not be installed onto the main terminal board until the main board itself is working and has been completely checked out according to the checkout phase of the terminal assembly instructions.

## Manual Cursor Control Switch Wiring

The following steps should be followed if you wish to wire in pushbutton switch to allow manual control of the cursor．If you do not wish to install the switches permanently you can ground the proper pins at JMC－1 one at a time for the checkout procedure described in the＂Final Assembly＂section．
（ ）All of the cursor control switches are connected to the cursor board th－ rough connector JMC－1．Mount your normally open SPST pushbutton control switches wherever you wish，preferably near the operator．Wire them so that when the switch is depressed，the appropiate control function pin is grounded．The $⿰ ⿰ 三 丨 ⿰ 丨 三 一 22$ or heavier gauge wires terminating at the JMC－1 con－ nector must have the appropiate connector pins attached and soldered as per the table below．All of the pins should be female except those con－ necting to pins 3,4 and 9 which are male．

| PIN NUMBER | PIN TYPE | FUNCTION |
| :---: | :--- | :--- |
|  | FEMALE | CURSOR RIGHT |
| 1 | FEMALE | CURSOR UP |
| 3 | MALE | CURSOR DOWN |
| 3 | MALE | ERASE TO EOF |
| 5 | FEMALE | ERASE TO EOL |
| 6 | FEMALE | CURSOR LEFT |
| 7 | FEMALE | NO CONNECTION |
| 8 | FEMALE | GROUND |
| 9 | MALE | HOME UP |

（ ）Snap each of the above mentioned pins into the 9 pin nylon housing from the numbered side using the table above．Make sure to snap the correct pin into its correct location．Note that the nylon connector block is marked with the assigned pin numbers．The intermixed pins provide the indexing nec－ essary for proper orientation．

Final Assembly
（ ）First of all remove power from your main terminal board．
（ ）Orient your cursor board so the＂TOP＂side of the board faces toward the main terminal＇s memory board，and press the board down onto connectors J3 and J4．If your indexing plug was installed correctly and the board is oriented correctly，the cursor board should go on easily．
（ ）Now plug on connector $J M C-1$ and apply power to the system．As each switch is depressed（or each respective pin of $J M C-1$ is grounded）the appropiate function should be executed．If not，disconnect power immediately，remove the board and check the cursor board over carefully for solder＂bridges＂ and＂cold＂solder joints．If you do not find the problem here revert back
to the main terminal board instructions where the cursor positioning functions are tested and check to make sure the cursor circuitry on the main board is functioning properly. Install jumpers A through K after the manual cursor portion of the circuit is checked out and functioning properly. Read how it works before deciding on which jumpers to install.

## Cursor Control Functional Descriptions

| CURSOR UP - | Moves the cursor up one line each time the switch is depressed, unless the cursor is already on the top line in which case the cursor jumps to the bottom line. |
| :---: | :---: |
| CURSOR DOWN - | Moves the cursor down one line each time the switch is depressed, unless the cursor is already on the bottom line in which case the cursor jumps to the top line. |
| CURSOR LEFT - | Moves the cursor left one position each time the switch is depressed, unless the cursor is already in the left most character position in which case the cursor jumps to the right most character position. |
| CURSOR RIGHT - | Moves the cursor right one position each time the switch is depressed, unless the cursor is already in the right most character position in which case the cursor jumps to the leftmost character position. |
| HOME UP - | Positions the cursor to the upper leftmost position on the screen. |
| ERASE EOF - | Erases from the cursor position to the end of the screen on the page of data being displayed. |
| ERASE EOL - | Erases from the cursor position to the end of the line on the page of data being displayed. |

To test your unit install the following jumpers:

| A to 5 | $F$ to 2 |
| :--- | :--- | :--- |
| $B$ to | G to 5 |
| C to 3 | H to 4 |
| $D$ to 0 | $J$ to 6 |
| E to 1 | K to 7 |

Install the cursor board in your terminal and apply power. The following functions should be executed:

CTRL $S$ gives HOME UP

| CTRL $P$ | gives CURSOR RIGHT |  |
| :--- | :--- | :--- |
| CTRL $Q$ | gives CURSOR DOWN |  |
| CTRL R gives CURSOR LEFT |  |  |
| CTRL | gives CURSOR UP |  |
| CTRL T gives | ERASE END OF LINE |  |
| CTRL $F$ | gives | ERASE END OF FRAME |
| CTRI $W$ | gives START READ (if terminal is equipped with screen read) |  |

When aking the above tests with a serial interface installed be sure to have the terminal set up for an ECHO mode. (Either the serial interface jumpered for ECHO or an ECHO through a compater).

If the above functions are not implemented correctly remove power and discontinee use antil the problem is found.

## Programeing the Computer Controlled Cursor

The computer coatrol portion of this board has a BCD Decoder that activates the same control functions as the manual switches. It has an extra decoded output that is used for START READ, or for any other function that requires a negative pulse such as STOP READ. The START READ is implementel on the board. All other extra functions must be implemented by the user with jupers to spare pins of 13 and J4.

All selected codes must fall within one of the four groups of control codes and selected by jumpers as follows:


A brief table of which jumpers to install for your particular needs is given on the mext page. For example to generate the CURSOR UP command from a NAK command jumper A to $5, B$ to $\frac{1}{4}$ and $G$ to 5 . A control $U$ would then generate a CURSOR UP.

Remember that you can work only in ore group of eight control commands at a time; you camot $1 x$ them. Also note that there are two 4 and two 5 points on

| JUAPERS |  | OCTAL | HEX | BINARY OUTPUT CODE 1B1-B7 | CHARACTER |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Ato $\overline{5}$ and to $\overline{4}^{4}$ | FN to 0 FN to 1 FN to. 2 FN to 3 FN to 4 FN to 5 FN to 6 FN to | $\begin{aligned} & 000 \\ & 001 \\ & 002 \\ & 003 \\ & 004 \\ & 005 \\ & 006 \\ & 007 \end{aligned}$ | $\begin{aligned} & 00 \\ & 01 \\ & 02 \\ & 03 \\ & 04 \\ & 05 \\ & 06 \\ & 07 \end{aligned}$ | 0000000 0000001 0000010 0000011 0000100 0000101 0000110 0000111 | $\begin{aligned} & \text { NUL } \\ & \text { SOH C/A } \\ & \text { STX } C / B \\ & \text { ETX } \\ & \text { EOT } \\ & \text { ENQ } \\ & \text { ACK } \\ & \text { BEL } \end{aligned}$ |
| to 5 and to 4 | FN to 0 FN to 1 FN to 2 FN to 3 FN to 4 FN to 5 FN to 6 FN to 7 | $\begin{aligned} & 010 \\ & 011 \\ & 012 \\ & 013 \\ & 014 \\ & 015 \\ & 016 \\ & 017 \end{aligned}$ | $\begin{aligned} & 08 \\ & 09 \\ & O A \\ & O B \\ & O C \\ & O D \\ & O E \\ & O F \end{aligned}$ | 0001000 0001001 0001010 0001011 0001100 0001101 0001110 0001111 | $\begin{aligned} & \text { BS } \\ & \text { HT } \\ & \text { LF } \\ & \text { VT } \\ & \text { FF } \\ & \text { CR } \\ & \text { SO } \\ & \text { S1 } \end{aligned}$ |
| $\begin{aligned} & \text { to } 5 \\ & \text { to } \frac{5}{4} \end{aligned}$ | F to 0 <br> FN to 1 <br> FN to 2 <br> FN to 3 <br> FN to 4 <br> FN to 5 <br> FN to 6 <br> FN to 7 |  | $\begin{array}{ll} C / P & 10 \\ C / Q & 11 \\ C / R & 12 \\ C / S & 13 \\ L G / & 14 \\ C / 4 & 15 \\ C & 16 \\ R T R & 17 \end{array}$ | 0010000 0010001 0010010 0010011 0010100 0010101 0010110 0010111 | $\begin{aligned} & \text { DLE } \\ & \text { DC1 } \\ & \text { DC2 } \\ & \text { DC3 } \leftarrow \\ & \text { DC4 } \\ & \text { NAK } \\ & \text { SYN } \leftarrow E E 耳 \\ & \text { ETB } \end{aligned}$ |
| A to 5 and $B$ to 4 | FN to 0 FN to FN to 2 FN to FN to F FN to FN to FN | $\begin{aligned} & 030 \\ & 031 \\ & 032 \\ & 033 \\ & 034 \\ & 035 \\ & 036 \\ & 037 \end{aligned}$ | $\begin{aligned} & 18 \\ & 19 \\ & 1 \mathrm{~A} \\ & 1 \mathrm{~B} \\ & 1 \mathrm{C} \\ & 1 \mathrm{D} \\ & 1 \mathrm{E} \\ & 1 \mathrm{~F} \end{aligned}$ | 0011000 <br> 0011001 <br> 0011010 <br> 0011011 <br> 0011100 <br> 0011101 <br> 0011110 <br> 0011111 | $\begin{gathered} \text { CAN } \\ \text { EM } \\ \text { SUB } \\ \text { ESC } \\ \\ \text { RS } \end{gathered}$ |

M is the respective point corresponding to the following:

## POINT

| C | HOME UP |
| :---: | :---: |
| D | CURSOR RIGHT C/S - 3 |
| E | CURSOR DOWN C/X - LAN |
| $F$ | CURSOR LEFT C/A |
| G | CURSOR UP C/W-\$ |
| H | ERASE TO END OF LINE |
| J | ERASE TO END OF FRAME (PAGE) |
| K | START SCREEN READ |

the board. The $A$ and $B$ jumpers go to the nearest 4 and 5 when $a$ jumer to these points is required.

## Notice to Serial and Parallel Interface Users

For those of you who have your CT-1024 connected to a computer through a serial of parallel interface please note the following comments:

Sertal Interface: The ERASE functions take time to perform and could cause probleme at 600 and 1200 baud. One RUB OUT following an erase command for 600 baud and two RUB OUTS for 1200 baud will help kill time for you. The RUB OUTS may cause a question mark to be displayed occasionally. If chis is objectionable use two SPACES followed by two BACR CURSOR commands.

Parallel Interface: Program a 30 millisecond wait loop after each erase function. Do not send out either NULL or RUB OUTS to kill time.

How it Works (Manual Cursor Section)

Since all of the pushbutton control switches are normally open, the switch Inpets are all tied ligh with resistors R1-R7. The inputs are directed to the output NAND gates (IC1 and IC6A, IC6B, IC6C) through inverters IC5 and IC6D. Note that none of the control switches affect the output gates unless the gating from IC2 fs high. IC4 monitors the control switches and goes high when any one of the seven switches is depressed. This immediately sets the $\bar{Q}$ output of IC2A low where it will remain for approximately 100 milliseconds at which time $\bar{Q}$ will go bigh main. This then fires IC2B forcing its $Q$ output high for 1 microsecond gating the appropiate control comand onto the TV typewriter circuitry. Transistor Q1 advances the cursor position horizontally when it is activated by the screen read option if it is used.

## How It Works (Computer Control Section)

The ASCII code is a seven bit code with an eight bit that is normally used for party check. This code is often defined in octal form as a three digit number. The null code is all zero's and defined in octal as " 000 " 8 . The rub out is all one's, and is defined as " 177 " 8 . The codes defined as control codes have both bits 6 and 7, as zero's, i.e. all codes from " 000 " 8 to "037" 8 are control codes. Some of these codes are; " $012^{4 "} 8$ line feed and " $015^{\prime \prime} 8$ carriage return.

The "A" and "B" jumpers select bit " 4 " and bit " 5 " of the ASCII code respectively, $\mathrm{IC}-7$ and IC-8 generate a 200 nanosecond pulse if both bits 6 and 7 equal " 0 " and bits ${ }^{\prime \prime} 4^{\prime \prime}$ and " $5^{\prime \prime}$ are as selected by jumpers " $A$ " and " $B^{\prime \prime}$. This pulse is used as an activate signal for IC-3. IC-3 monitors the bit 1 , bit 2,
and bit 3 input line. It outputs a pulse on one of the eight decoded outputs for the duration of the activate signal on pin 12 . The pulse may be connected to any control function pad by the use of jumpers. A suitable TTL data book should be consulted for a truth table for IC3. The numbered pads marked 0 through 7 correspond to the octal notation of the least significant digit of the control code.

## In Case of Problems

If you have problems on some phase of operation of the manual cursor portion of the board, the best recommendation is to remove power and recheck your assembly over very carefully with the instructions. If you still cannot find the problen and feel secure in your understanding of digital circuits, you can troubleshoot the unit with an oscillioscope. This does, however, require a thorough understanding of how the unit works as is described in the "how it works" section. If you are still not able to locate the problem or prefer not to service the unit yourself, please consult us before sending the unit in for repairs.

Repairs will be done for a basic rate charge plus parts. The basic rate for the computer controlled cursor board CT-CA is $\$ 5.00$. If you must return your board send $\$ 5.00$ in the form of a money order or cashiers check along With the board. When repairs are finished the board will be returned to you COD for parts charges, if any. Do not send personal checks for repair work.

Pack all parts to be returned carefully and insure. We will not accept delivery on any parcels that arrive in damaged condition. Make check or

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Parts List - Computer Cursor Board
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## Resistors

R1-R7, R9, R12-R14
R10, R11.
R8

1 K ohm. $1 / 4$ watt resistor 5.6 K ohm $1 / 4$ watt resistor 2.2K ohm " " "

## Capacitors

## Cl

C2, C3, C4, C5 C6

33 mfd @6 VDC electrolytic capacitor
100 pfd polvstyrene capacitor
0.1 mfd disc capacitor

Semiconductors

D1
ICl, IC6
IC2
IC4
IC5, IC8
Q1
IC3
IC7

1N4148/1N914 silicon diode or equiv. 7403 quad NAND gate (open collector) 74123 dual one shot
74308 input NAND gate
7404 hex inverter 2N5129 silicon transistor 7445 BCD decoder 7400 quad NAND gate


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Schematic - Computer Controlled Cursor Board


[^0]:    - money order payable to Southwest Technical Products Corporation.

