2920 FAULT CODE DICTIONARY

STORAGE TECHNOLOGY STANDARD INTERFACE

This dictionary corresponds to given release levels of subsystem microcode. To ascertain the microcode release level, use the following front panel sequence:

<ENTER ADDR>,1FFA,<ENTER>: displays msb of the release level <ENTER>: displays lsb of the release level

It is important that the user of this dictionary ensures the matching of dictionary text to microcode level. The following table associates microcode EC and release level to dictionary page changes. The latest EC covered by this dictionary is indicated by the last entry of this table or any subsequent EC's which do not indicate an impact to this publication.

EC	Microcode Level	Page(s) Changed
49576	A115	Initial Release
49582	A116	A-66 to A-69
49649	A117	All pages
49709	A118	All pages
49755	A119	A-10,36-39,46-47
49790	A119/B103	All pages
49908	A119/B104	All pages

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EC 49908

Code	Detected by	Fault DescriptionFRU'S		•
001	TESTOO	Drive not londed before fewered motion request		
001	IESTOU	Drive not loaded before forward motion request.		
002	TESTOO	EOT status detected before forward motion set.		
		Card: WR F Slot: A4 A3		
003	TESTOO	EOT detected via sensors during forward motion.		
		Card: WR IF Slot: A4 A3		
011	TEST01	Drive not loaded before backward motion request.		
012	TEST01	BOT status detected before backward motion set.		
		Card: WR IF Slot: A4 A3		
013	TEST01	BOT detected via sensors during backward motion.		
		Card: WR IF Slot: A4 A3		
021	TEST02	Drive not loaded before motion request.		
022	TEST02	EOT status detected before motion set.		
		Card: WR IF Slot: A4 A3		
023	TEST02	EOT detected via sensors during forward motion.		
		Card: WR IF Slot: A4 A3		
031	TEST03	Drive not loaded before motion request.		
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Code	Detected by	Fault DescriptionFRU'S
032	TEST03	EOT status detected before motion set.
		Card: WR IF Slot: A4 A3
033	TEST03	BOT or EOT detected during motion via sensors.
		Card: WR F Slot: A4 A3
041	TEST01	Reject or Machine check during mode switch. See description of reject codes preceeding code E01.
091	TEST09	Can not execute maintenance reel/capstan driver while drive is loaded.
092	TEST09	Tape presence was detected in thread path via sensors. Can not execute maintenance reel/capstan driver.
		Card: WR F Slot: A4 A3
0E 1	TESTOE	Maintenance write requires drive loaded.
0E 2	TESTOE	Maintenance write will not run on file protected tape.
0E 3	TESTOE	EOT status detected before motion.
OE4	TESTOE	EOT detected via sensors during forward motion.
OF 1	TESTOF	Maintenance write requires drive loaded.
OF 2	TESTOF	Maintenance write will not run on file protected tape.
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Code	Detected by	Fault DescriptionFRU'S
OF 3	TESTOF	EOT status detected before motion.
OF4	TESTOF	EOT detected via sensors during forward motion.
121	TEST12	Initial write of memory compared incorrectly while testing functional RAM (A000-A7FF).
		Card: IF Slot: A3
122	TEST12	Read,write complement,read sequence failed while testing functional RAM (A000-A7FF).
		Card: IF Slot: A3
123	TEST12	Initial write of memory compared incorrectly while testing diagnostic RAM (8000-87FF).
		Card: IF Slot: A3
124	TEST12	Read,write complement,read sequence failed while testing diagnostic RAM (8000-87FF).
		Card: IF Slot: A3
125	TEST12	Checksum incorrect in PROM 1 (0000-1FFF).
		Card: IF Šlot: A3
126	TEST12	Checksum incorrect in PROM 2 (2000-3FFF).
		Lard: IF Slot: A3

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Code	Detected by	Fault DescriptionFRU'S
127	TEST12	Checksum incorrect in PROM 3 (4000-5FFF). Card: IF Slot: A3
128	TEST12	Checksum incorrect in PROM 4 (COOO-DFFF). Card: 1F Slot: A3
129	TEST12	Checksum incorrect in PROM 5 (E000-FFFF). Card: IF Slot: A3
12A	TEST12	Release level mis-match between proms. Locations: PROM1 = (1FFA,B) PROM2 = (3FFA,B) PROM3 = (5FFA,B) PROM4 = (DFFA,B) PROM5 = (FFFA,B) Card: IF Slot: A3
131	TEST13	Counters were loaded with large timeout values and interrupt controller was initialized. When read before complete count-down of counters the IRR register of the controller was expected to be zero. Status A-0 = active counter inputs to 8259 (should be 0)
		Card: IF KK DP WR SV Slot: A3 A5 A4 A1

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Code	Detected	Fault DescriptionFRU'S	
	by		

132 TEST13 Counters were loaded with counts such that timeouts would be expected in the order 0,1,2. The counter 0 output was the one and only expected at the IRR register of the interrupt controller (8259) at this time.

> Status A-O = active counter inputs to 8259 (should be O1 hex)

Card: IF KK DP WR SV Slot: A3 A5 A4 A1

133 TEST13 Counters were loaded with counts such that timeouts would be expected in the order 0,1,2. Outputs from counters 0 and 1 were the only expected at the IRR register of the interrupt controller (8259) at this time.

> Status A-O = active counter inputs to 8259 (should be 21 hex) Card: IF Slot: A3

134 TEST13 Counters were loaded with counts such that timeouts would be expected in the order 0,1,2. Outputs from all 3 counters were expected at the IRR register of the interrupt controller (8259) at this time.

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Status A-O = active counter inputs to 8259 (should be 61 hex) Card: IF Slot: A3

142 TEST14 Spurious interrupts received by controller.

Status A-O = interrupts recv'd (should be O) Card: IF Slot: A3

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Code	Detected	Fault DescriptionFRU'S	
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144	TEST14	Interrupts from counters not received correctly.
		Status A-O = interrupts received (should be 01H; counter 0 only) Card: IF Slot: A3
146	TEST14	Interrupts from counters not received correctly.
		Status A-O = interrupts received (should be 21H; counters O & 1) Card: IF Slot: A3
148	TEST14	Interrupts from counters not received correctly.
	·	Status A-O = interrupts received (should be 61H; counters 0,1 & 2) Card: IF Slot: A3
151	TEST15	Multiple keyboard columns active in sense register (6010) for a given row selection (601A). Check for proper panel cable connection. Card: IF KK
		Slot: A3
153	TEST15	More than one front panel key detected (6010) for different row selections (601A). Check for proper panel cable connection.

Card: IF KK Slot: A3

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Code	Detected by	Fault DescriptionFRU'S

181 TEST18 Position counter (SV card, XCS chip) could not be looped correctly (wrt: 6023; rd: 6025).

> Status A-0: expected pattern Status A-1: actual pattern

Card: IF SV DP WR Slot: A3 A1 A5 A4

183 TEST18 Velocity register (SV card, XCS chip) could not be looped correctly (wrt: 6026,28,29; rd: 6027).

> Status A-0: pattern Status A-2: should be pattern shifted left once

Card: IF SV DP WR Slot: A3 A1 A5 A4

185 TEST18 Machine swing arm position (SV card, XRS chip) could not be looped correctly (wrt: 6030,32; rd: 6034).

> Status A-0: pattern Status A-3: should be pattern shifted left twice

Card: IF SV DP WR Slot: A3 A1 A5 A4

187 TEST18 File swing arm position (SV card, XRS chip) could not be looped correctly (wrt: 6031,33; rd: 6035).

> Status A-0: pattern Status A-4: should be pattern shifted left 3 bits

Card: IF SV DP WR Slot: A3 A1 A5 A4

1B1 TEST1B Data path status B (6042) not 0 with resets active. Card: DP IF WR SV Slot: A5 A3 A4 A1

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Code	Detected by	Fault DescriptionFRU'S
1B3	TESTIB	Data path status B (6042) not indicating CRC + CRCA (Oóh) after resets cleared and GCR mode set.
		Card: DP IF WR SV Slot: A5 A3 A4 A1
184	TEST1B	Dead track register (6040) not inactive (FFh) following data path reset.
		Card: DP IF Slot: A5 A3
1 B5	TEST1B	Data path status A (6041) not 0 following data path reset.
		Card: DP IF Slot: A5 A3
186	TESTIB	Data path status C (6043) not inactive (04h) following data path reset.
		Card: DP IF Slot: A5 A3
187	TEST1B	Phase pointer register (6044) not 0 following data path reset.
		Card: DP IF Slot: A5 A3
188	TEST1B	Amp sensor register (6045) not 0 following data path reset.
		Card: DP IF Slot: A5 A3

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Code	Detected by	Fault DescriptionFRU'S

1D1 TEST1D Following the disabling of all sensors (6068), only file protect status (6060) should have been active.

Status A-0: sensor bit(s) in error

Card: WR IF Slot: A4 A3

Sensor: EOT/BOT, File Protect, Leader

1D2 TEST1D After enabling EOT sensor only (6068), sensor status (6060) was incorrect.

Status A-0: sensor bit(s) in error

Card: WR IF Slot: A4 A3

Sensor: EOT/BOT, File Protect, Leader

1D3 TESTID After enabling BOT sensor only (6068), sensor status (6060) was incorrect.

Status A-0: sensor bit(s) in error

Card: WR IF Slot: A4 A3

Sensor: EOT/BOT, File Protect, Leader

1D4 TEST1D After enabling tape present sensor only (6068), status (6060) was incorrect.

Status A-0: sensor bit(s) in error

Card: WR IF Slot: A4 A3

Sensor: EOT/BOT, File Protect, Leader

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Code	Detected	Fault DescriptionFRU'S
	by	

105	TEST1D	After enabling leader sensor only (6068), status (6060) was incorrect.
		Status A-0: sensor bit(s) in error
		Card: WR IF Slot: A4 A3
		Sensor: EOT/BOT, File Protect, Leader
106	TESTID	After enabling file protect sensor only (6068), status (6060) was incorrect.
		Status A-0: sensor bit(s) in error
		Card: WR IF Slot: A4 A3
		Sensor: EOT/BOT, File Protect, Leader
1D7	TESTID	After disabling the write/erase currents (6068), current status (6061) should indicate off and stable.
		Status A-0: sensor bit(s) in error
		Card: WR IF Rd/Wrt head,cable Slot: A4 A3
1F1	TEST1F	Functional code detected machine check condition during swing arm extend/retract cycle.
		Status A-6: Machine check code See listing for code Fxx, where xx= contents of this status location

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Code	Detected	
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1F2 TEST1F INDEX's from both arm position sensors (6061) were detected when retracting from 'EXTENDED'. However, the FILE arm's upper EPO area ('INDEX' off) was not detected before the MACHINE arm reached 'Retracted'.

> Check FILE arm sensor for 'INDEX' off capability. Check MACH arm sensor for false 'INDEX'.

Card: SV WR IF Slot: A1 A4 A3

1F3 TEST1F INDEX's from both arm position sensors (6061) were detected when retracting from 'EXTENDED'. However, the MACHINE arm's upper EPO area ('INDEX' off) was not detected before the FILE arm reached 'Retracted'.

> Check MACH arm sensor for 'INDEX' off capability. Check FILE arm sensor for false 'INDEX'.

Card: SV WR IF Slot: A1 A4 A3

1F8 TEST1F Software count of MACHINE arm tachs (6036) was extremely low through the 'INDEX' area. This may indicate inoperable phase A and/or B tach lines.

> Status B-2,B-3: MACH index dist (low, high byte) Allowed range: 00F0h -> 0108h

Check MACH arm tach assembly for tach signals.

Card: SV WR IF Slot: A1 A4 A3

Code	Detected	Fault DescriptionFRU'S
	by	

1F9 TEST1F Software count of FILE arm tachs (6036) was extremely low through the 'INDEX' area. This may indicate inoperable phase A and/or B tach lines. Status B-4,B-5: FILE index dist (low, high byte) Allowed range: 00F0h -> 0108h Check FILE arm tach assembly for tach signals. Card: SV WR IF Slot: A1 A4 A3

1FA TEST1F Software count of MACHINE arm tachs (6036) thru 'INDEX' area was not as expected.

> Status B-2,B-3: MACH index dist (low, high byte) Allowed range: 00F0h -> 0108h

Card: SV WR IF Slot: A1 A4 A3

1FB TEST1F Software count of FILE arm tachs (6036) through 'INDEX' area was not as expected.

> Status B-4,B-5: FILE index dist (low, high byte) Allowed range: OOFOh -> O108h

Card: SV WR IF Slot: A1 A4 A3

1FC TEST1F Insufficient EPO margin ('EXTENDED' to 'INDEX') was indicated by the MACH arm tach (6036).

> Status B-0: MACH epo distance (quarter tachs) minimum: 03 hex

Check MACH arm tach assembly.

Card: SV WR IF Slot: A1 A4 A3

Code	Detected by	Fault DescriptionFRU'S	
1FD	TEST1F	Insufficient EPO margin ('EXTENDED' to 'INDEX') was indicated by the FILE arm tach (6036).	:
		Statue B-1, EllE and distance (quarter tacks)	

minimum: 03 hex

Check FILE arm tach assembly.

Card: SV WR IF Slot: A1 A4 A3

1FE TEST1F Software counts of MACHINE arm position tachs were within allowable limits. However, hardware counter (read from 6034 and placed in Status B-6) was not within 10 (decimal) counts of the software count. (Note: only the low byte of software count is used in the compare).

> Status B-2,B-3: mach index dist (low, high byte) Status B-6: mach index dist (from hardware)

Card: SV WR IF Slot: A1 A4 A3

1FF TEST1F Software counts of FILE arm position tachs were within allowable limits. However, hardware counter (read from 6035 and placed in Status B-7) was not within 10 (decimal) counts of the software count. (Note: only the low byte of software count is used in the compare).

> Status B-4,B-5: file index dist (low, high byte) Status B-7: file index dist (from hardware)

Card: SV WR IF Slot: A1 A4 A3

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Code	Detected by	Fault DescriptionFRU'S
21	TEST22	Write and/or Read complete (6014) failed to initialize following a reset (601E).
		STATUS B-0: patterns in order of execution O- FF's to data path 1- 00's to data path 2- AA55 pattern 3- 55AA pattern 4- walking 0 bit 5- walking 1 bit
		STATUS B-1: byte count (range: 5 -> 8) STATUS B-2: byte pointer (walking bit patterns) STATUS B-3: bit pointer (""")
		Card: DP IF Slot: A5 A3
222	TEST22	Attempt to clear data-path-complete interrupt from interrupt controller (8259: IRR reg) failed see code 221 for status locations.
,		Card: DP IF Slot: A5 A3
223	TEST22	After setting byte count (IF card) and allowing write transfer, write complete did not occur see code 221 for status locations.
		Card: DP IF Slot: A5 A3
224	TEST22	Data path complete was returned early; before the expected termination of postamble writing see code 221 for status locations.
		Card: DP IF

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Code	Detected by	Fault DescriptionFRU'S	
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225 TEST22 After issuing read command, setting short reset to data path front end (601D), and allowing data transfer, timeout occurred waiting for read complete--see code 221 for status locations.

> Card: DP IF WR Slot: A5 A3 A4

226 TEST22 After readback of given byte count, data-pathcomplete interrupt (indicating completion of postamble write) did not occur--see code 221 for status locations.

> Card: DP RD IF WR Slot: A5 A6 A3 A4

227 TEST22 Parity was incorrect (6014) following completion of readback--see code 221 for status locations.

> Card: DP IF Slot: A5 A3

- 229 TEST22 Reject occurred during speed switch operation. See description of reject codes preceeding code E01.
- 22A TEST22 Following completion of write and read portions of the loop-write-read, DPSTATA (6041) was not as expected (00h or 01h)--see code 221 for status locations.

Card: DP RD WR IF Slot: A5 A6 A4 A3

22B TEST22 Following completion of write and read portions of the loop-write-read, DPSTATB (6042) was not as expected (00h or 02h)--see code 221 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

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Code	Detected	Fault DescriptionFRU'S
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22C TEST22 Following completion of write and read portions of the loop-write-read, DPSTATC (6043) was not as expected (00h -> 07h)--see code 221 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

22F TEST22 Following completion of write and read portions of the loop-write-read without detectable errors, write and read buffer data miscompared--see code 221 for status locations.

> Card: DP IF Slot: A5 A3

231 TEST23 Data path "Status A" (6041) not indicating velocity error only (02 hex) following a "loop-write-to-read" record written at 12.5% high velocity.

STATUS A-O: failing status bits

Card: DP IF Slot: A5 A3

232 TEST23 Data path "Status A" (6041) not zero following a "loop-write-to-read" record written at 5.6% high velocity.

> Card: DP IF Slot: A5 A3

233 TEST23 Data path "Status A" (6041) not zero following a "loop-write-to-read" record written at 8.2% low velocity.

> Card: DP IF Slot: A5 A3

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Code	Detected
	by

234 TEST23 Data path "Status A" (6041) not indicating velocity error only (02 hex) following a "loop-write-to-read" record written at 15.1% low velocity.

STATUS A-O: failing status bits

Card: DP IF Slot: A5 A3

239 TEST23 Reject occurred during speed switch operation. See description of reject codes preceeding code E01.

241 TEST24 Write and/or Read complete (6014) failed to initialize following a reset (601E).

> STATUS B-0: patterns in order of execution--0- FF's to data path 1- 00's to data path 2- AA55 pattern 3- 55AA pattern 4- walking 0 bit 5- walking 1 bit 6- pseudo random (long records) STATUS B-1: byte count (range: 1 -> 6) STATUS B-2: byte pointer (walking bit patterns) STATUS B-3: bit pointer (" " ")

Card: DP IF Slot: A5 A3

242 TEST24

Attempt to clear data-path-complete interrupt from interrupt controller (8259: IRR reg) failed-see code 241 for status locations.

Card: DP IF Slot: A5 A3

243 TEST24

24 After setting byte count (IF card) and allowing write transfer, write complete did not occur-see code 241 for status locations.

Card: DP IF Slot: A5 A3

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Code	Detected	Fault DescriptionFRU'S	
	by		

244 TEST24 Data path complete was returned early; before the expected termination of postamble writing-see code 241 for status locations.

> Card: DP IF Slot: A5 A3

245 TEST24 After issuing read command, setting short reset to data path front end (601D), and allowing data transfer, timeout occurred waiting for read complete--see code 241 for status locations.

> Card: DP IF Slot: A5 A3

246 TEST24 After readback of given byte count, data-pathcomplete interrupt (indicating completion of postamble write) did not occur-see code 241 for status locations.

> Card: DP RD IF WR Slot: A5 A6 A3 A4

247 TEST24 Parity was incorrect (6014) following completion of readback--see code 241 for status locations.

Card: DP IF Slot: A5 A3

- 249 TEST24 Reject occurred during speed switch operation. See description of reject codes preceeding code E01.
- 24A TEST24 Following completion of write and read portions of the loop-write-read, DPSTATA (6041) was not as expected (00h or 01h)--see code 241 for status locations.

Card: DP RD WR IF Slot: A5 A6 A4 A3

Code	Detected
	by

24B TEST24 Following completion of write and read portions of the loop-write-read, DPSTATB (6042) was not as expected (00h or 08h)--see code 241 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

24C TEST24 Following completion of write and read portions of the loop-write-read, DPSTATC (6043) was not as expected (00h -> 07h)--see code 241 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

24F TEST24 Following completion of write and read portions of the loop-write-read without detectable errors, write and read buffer data miscompared--see code 241 for status locations.

> Card: DP IF Slot: A5 A3

251 TEST25 Data path "Status A" (6041) not indicating velocity error only (02 hex) following a "loop-write-to-read" record written at 12.5% high velocity.

STATUS A-O: failing status bits

Card: DP IF Slot: A5 A3

252 TEST25 Data path "Status A" (6041) not zero following a "loop-write-to-read" record written at 5.6% high velocity.

> Card: DP IF Slot: A5 A3

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Code	Detected	Fault DescriptionFRU'S
	by	

253 TEST25 Data path "Status A" (6041) not zero following a "loop-write-to-read" record written at 8.2% low velocity.

Card: DP IF Slot: A5 A3

254 TEST25 Data path "Status A" (6041) not indicating velocity error only (02 hex) following a "loop-write-to-read" record written at 15.1% low velocity.

STATUS A-O: failing status bits

Card: DP IF Slot: A5 A3

259 TEST25 Reject occurred during speed switch operation. See description of reject codes preceeding code E01.

261 TEST26 Write and/or Read complete (6014) failed to initialize following a reset (601E).

> STATUS C-O: dead tracks (tracks O-7, initially O1) STATUS C-1: dead tracks (track P, initially O0)

STATUS B-0: patterns in order of execution--4- walking 0 bit 6- pseudo random (long records) STATUS B-1: byte count (8 bytes for pattern 4) STATUS B-2: byte pointer (walking bit patterns) STATUS B-3: bit pointer (""")

Card: DP IF Slot: A5 A3

262 TEST26 Attempt to clear data-path-complete interrupt from interrupt controller (8259: IRR reg) failed-see code 261 for status locations.

> Card: DP IF Slot: A5 A3

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Code	Detected	Fault DescriptionFRU'S
	by	

263 TEST26 After setting byte count (IF card) and allowing write transfer, write complete did not occur-see code 261 for status locations.

> Card: DP IF Slot: A5 A3

264 TEST26 Data path complete was returned early; before the expected termination of postamble writing-see code 261 for status locations.

> Card: DP IF Slot: A5 A3

265 TEST26 After issuing read command, setting short reset to data path front end (601D), and allowing data transfer, timeout occurred waiting for read complete--see code 261 for status locations.

> Card: DP IF Slot: A5 A3

266 TEST26 After readback of given byte count, data-pathcomplete interrupt (indicating completion of postamble write) did not occur--see code 261 for status locations.

> Card: DP RD IF WR Slot: A5 A6 A3 A4

267 TEST26 Parity was incorrect (6014) following completion of readback--see code 261 for status locations.

Card: DP IF Slot: A5 A3

269 TEST26 Reject occurred during speed switch operation. See description of reject codes preceeding code E01.

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Code	Detected	Fault DescriptionFRU'S
	by	

26A TEST26 Following completion of write and read portions of the loop-write-read, DPSTATA (6041) was not as expected (08h or 09h)--see code 261 for status locations. Card: DP RD WR IF

Slot: A5 A6 A4 A3

26B TEST26 Following completion of write and read portions of the loop-write-read, DPSTATB (6042) was not as expected (00h or 02h)--see code 261 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

26C TEST26 Following completion of write and read portions of the loop-write-read, DPSTATC (6043) was not as expected (00h -> 07h)--see code 261 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

26F TEST26 Following completion of write and read portions of the loop-write-read without detectable errors, write and read buffer data miscompared--see code 261 for status locations.

> Card: DP IF Slot: A5 A3

Code	Detected by	Fault DescriptionFRU'S	
271	TEST27	Write and/or Read complete (6014) failed	

Card: DP IF Slot: A5 A3

272 TEST27 Attempt to clear data-path-complete interrupt from interrupt controller (8259: IRR reg) failed-see code 271 for status locations.

> Card: DP IF Slot: A5 A3

273 TEST27 After setting byte count (IF card) and allowing write transfer, write complete did not occursee code 271 for status locations.

> Card: DP IF Slot: A5 A3

274 TEST27 Data path complete was returned early; before the expected termination of postamble writing-see code 271 for status locations.

> Card: DP IF Slot: A5 A3

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Code	Detected	Fault DescriptionFRU'S
	by	

275 TEST27 After issuing read command, setting short reset to data path front end (601D), and allowing data transfer, timeout occurred waiting for read complete--see code 271 for status locations.

> Card: DP IF Slot: A5 A3

276 TEST27 After readback of given byte count, data-pathcomplete interrupt (indicating completion of postamble write) did not occur-see code 271 for status locations.

> Card: DP RD IF WR Slot: A5 A6 A3 A4

277 TEST27 Parity was incorrect (6014) following completion of readback--see code 271 for status locations.

Card: DP IF Slot: A5 A3

- 279 TEST27 Reject occurred during speed switch operation. See description of reject codes preceeding code E01.
- 27A TEST27 Following completion of write and read portions of the loop-write-read, DPSTATA (6041) was not as expected (08h or 09h)--see code 271 for status locations.

Card: DP RD WR IF Slot: A5 A6 A4 A3

27B TEST27 Following completion of write and read portions of the loop-write-read, DPSTATB (6042) was not as expected (00h or 08h)--see code 271 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

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Code	Detected	
	by	

27C TEST27 Following completion of write and read portions of the loop-write-read, DPSTATC (6043) was not as expected (00h -> 07h)--see code 271 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

27F TEST27 Following completion of write and read portions of the loop-write-read without detectable errors, write and read buffer data miscompared--see code 271 for status locations.

> Card: DP IF Slot: A5 A3

281 TEST28 Write and/or Read complete (6014) failed to initialize following a reset (601E).

> STATUS C-O: dead tracks (tracks 0-7, initially 03) STATUS C-1: dead tracks (track P, initially 00)

STATUS B-0: patterns in order of execution--4- walking 0 bit 6- pseudo random (long records) STATUS B-1: byte count (8 bytes for pattern 4) STATUS B-2: byte pointer (walking bit patterns) STATUS B-3: bit pointer (""")

Card: DP IF Slot: A5 A3

282 TEST28 Attempt to clear data-path-complete interrupt from interrupt controller (8259: IRR reg) failed-see code 281 for status locations.

> Card: DP IF Slot: A5 A3

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Code	Detected by	Fault DescriptionFRU'S

283 TEST28 After setting byte count (IF card) and allowing write transfer, write complete did not occur--see code 281 for status locations.

Card: DP IF Slot: A5 A3

284 TEST28 Data path complete was returned early; before the expected termination of postamble writing-see code 281 for status locations.

> Card: DP IF Slot: A5 A3

285 TEST28 After issuing read command, setting short reset to data path front end (601D), and allowing data transfer, timeout occurred waiting for read complete--see code 281 for status locations.

> Card: DP IF Slot: A5 A3

286 TEST28 After readback of given byte count, data-pathcomplete interrupt (indicating completion of postamble write) did not occur-see code 281 for status locations.

> Card: DP RD IF WR Slot: A5 A6 A3 A4

- 289 TEST28 Reject occurred during speed switch operation. See description of reject codes preceeding code E01.
- 28A TEST28 Following completion of write and read portions of the loop-write-read, DPSTATA (6041) was not as expected (74h; 09h bits don't care)--see code 281 for status locations.

Card: DP RD WR IF Slot: A5 A6 A4 A3

97712

Code	Detected	Fault DescriptionFRU'S
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28B TEST28 Following completion of write and read portions of the loop-write-read, DPSTATB (6042) was not as expected (00h; 0Bh bits don't care)--see code 281 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

28C TEST28 Following completion of write and read portions of the loop-write-read, DPSTATC (6043) was not as expected (00h -> 07h)--see code 281 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

28D TEST28 CRC-C and BUPER did not both set in DPSTATB (6042) at some time during multi-track error PE loopwrite to read testing.

> Card: DP IF Slot: A5 A3

291 TEST29 Write and/or Read complete (6014) failed to initialize following a reset (601E).

STATUS C-O: dead tracks (tracks O-7, initially O3) STATUS C-1: dead tracks (track P, initially O0)

STATUS B-0: patterns in order of execution--4- walking 0 bit 6- pseudo random (long records) STATUS B-1: byte count (6 bytes for pattern 4) STATUS B-2: byte pointer (walking bit patterns) STATUS B-3: bit pointer (""")

Card: DP IF Slot: A5 A3

EC 49908

Code	Detected	Fault DescriptionFRU'S
	by	

292 TEST29 Attempt to clear data-path-complete interrupt from interrupt controller (8259: IRR reg) failed-see code 291 for status locations. Card: DP IF Slot: A5 A3 293 TEST29 After setting byte count (IF card) and allowing write transfer, write complete did not occur-see code 291 for status locations. Card: DP IF Slot: A5 A3 294 TEST29 Data path complete was returned early; before the expected termination of postamble writing-see code 291 for status locations. Card: DP IF Slot: A5 A3 295 TEST29 After issuing read command, setting short reset to data path front end (601D), and allowing data transfer, timeout occurred waiting for read complete--see code 291 for status locations. Card: DP IF Slot: A5 A3 296 TEST29 After readback of given byte count, data-path-

complete interrupt (indicating completion of postamble write) did not occur-see code 291 for status locations.

> Card: DP RD IF WR Slot: A5 A6 A3 A4

297 TEST29 Parity was incorrect (6014) following completion of readback-see code 291 for status locations.

Card: DP IF Slot: A5 A3

Code	Detected	Fault DescriptionFRU'S
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299 TEST29 Reject occurred during speed switch operation. See description of reject codes preceeding code E01.

29A TEST29 Following completion of write and read portions of the loop-write-read, DPSTATA (6041) was not as expected (10h,11h,18h, or 19h)--see code 291 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

29B TEST29 Following completion of write and read portions of the loop-write-read, DPSTATB (6042) was not as expected (00h or 08h)--see code 291 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

29C TEST29 Following completion of write and read portions of the loop-write-read, DPSTATC (6043) was not as expected (00h -> 07h)--see code 291 for status locations.

Card:	DP	RD	WR	I F
Slot:	A5	A6	A4	A3

TEST29 Following completion of write and read portions of the loop-write-read without detectable errors, write and read buffer data miscompared--see code 291 for status locations.

> Card: DP IF Slot: A5 A3

> > EC 49908

29F

Code	Detected	Fault DescriptionFRU'S
	by	

2C1 TEST2C Write and/or read complete (6014) failed to initialize following a reset (601E). STATUS B-O: patterns in order of execution--0- FF's to data path 1- 00's to data path 2- AA55 pattern 3- 55AA pattern 4- walking 0 bit 5- walking 1 bit 6- pseudo random (long records) STATUS B-1: byte count (range: 5 -> 8) STATUS B-2: byte pointer (walking bit patterns) STATUS B-3: bit pointer (" ") Card: DP IF Slot: A5 A3 2C2 TEST2C Attempt to clear data-path-complete interrupt from interrupt controller (8259: IRR reg) failed--See code 2C1 for status locations. Card: DP IF Slot: A5 A3 2C3 TEST2C After setting byte count (IF card) and allowing write transfer, write complete did not occur--See code 2C1 for status locations. Card: DP IF Slot: A5 A3 2C4 TEST2C Data path complete was returned early; before the expected termination of postamble writing--See code 2C1 for status locations.

Card: DP IF Slot: A5 A3

	-			
	Code	Detected	Fault DescriptionFRU'S	
	205	TEST2C	After issuing read command, setting short reset	
			to data path front end (601D), and allowing data transfer, timeout occurred waiting for read com-	
	• •		pletesee code 2C1 for status locations.	•
			Card: DP IF WR	
			Slot: A5 A3 A4	
	206	TEST2C	After readback of given byte count, data-path-	
		:	complete interrupt (indicating completion of postamble write) did not occurrence code 201 for	
*			status locations.	
			Card: DP RD IF WR	
			Slot: A5 A6 A3 A4	
	207	TEST2C	Parity was incorrect (6014) following completion	
			of readbacksee code 201 for status locations.	
			Card: DP IF	
			SIOT: A5 A3	
	200	TECTOR	The last second during second suitable second is	
	ZUY	IESIZC	See description of reject codes preceeding code E01.	
	2CA	TEST2C	Following completion of write and read portions	
			of the loop-write-read, DPSTATA (6041) was not as expected (00h or 01h)see code 201 for status	
			locations.	
			Card: DP RD WR IF	
			Slot: A5 A6 A4 A3	
	:			
	2CB	TEST2C	Following completion of write and read portions	
			expected (00h or 02h)see code 2C1 for status	
			locations.	
1 -	:		Card: DP RD WR IF	
			Slot: A5 A6 A4 A3	
	32		EC 49908 97712	
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Code	Detected	Fault DescriptionFRU'S
	by	

2CC TEST2C Following completion of write and read portions of the loop-write-read, DPSTATC (6043) was not as expected (00h -> 07h)--see code 2C1 for status locations. Card: DP RD WR IF Slot: A5 A6 A4 A3

2CF TEST2C Following completion of write and read portions of the loop-write-read without detectable errors, write and read buffer data miscompared--see code 2C1 for status locations.

> Card: DP IF Slot: A5 A3

2E1 TEST2E Write and/or Read complete (6014) failed to initialize following a reset (601E).

> STATUS B-0: patterns in order of execution--O- FF's to data path 1- 00's to data path 2- AA55 pattern 3- 55AA pattern 4- walking 0 bit 5- walking 1 bit 6- pseudo random (long records) STATUS B-1: byte count (range: 1 -> 6) STATUS B-2: byte pointer (walking bit patterns) STATUS B-3: bit pointer ("""")

- Card: DP IF Slot: A5 A3
- 2E2 TEST2E Attempt to clear data-path-complete interrupt from interrupt controller (8259: IRR reg) failed-see code 2E1 for status locations.

Card: DP IF Slot: A5 A3

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	Code	Detected by	Fault DescriptionFRU'S	
14		_,]
	2E3	TEST2E	After setting byte count (IF card) and allowing write transfer, write complete did not occur see code 2E1 for status locations.	
			Card: DP IF Slot: A5 A3	
	2E4	TEST2E	Data path complete was returned early; before the expected termination of postamble writing see code 2E1 for status locations.	
			Card: DP F Slot: A5 A3	
	2E5	TEST2E	After issuing read command, setting short reset to data path front end (601D), and allowing data transfer, timeout occurred waiting for read com- pletesee code 2E1 for status locations.	
•				
			Slot: A5 A3	
	2E6	TEST2E	After readback of given byte count, data-path- complete interrupt (indicating completion of postamble write) did not occursee code 2E1 for	
			Status locations.	
		;	STOL: NO NO NO NO	
	2E7	TEST2E	Parity was incorrect (6014) following completion of readbacksee code 2E1 for status locations.	
			Card: DP IF Slot: A5 A3	
	2E9	TEST2E	Reject occurred during speed switch operation. See description of reject codes preceeding code E0	1.
	34		EC 49908	97712
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Code	Detected	Fault DescriptionFRU'S
	by	

2EA TEST2E Following completion of write and read portions of the loop-write-read, DPSTATA (6041) was not as expected (00h or 01h)--see code 2E1 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

2EB TEST2E Following completion of write and read portions of the loop-write-read, DPSTATB (6042) was not as expected (00h or 08h)--see code 2E1 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

2EC TEST2E Following completion of write and read portions of the loop-write-read, DPSTATC (6043) was not as expected (00h -> 07h)--see code 2E1 for status locations.

> Card: DP RD WR IF Slot: A5 A6 A4 A3

2EF TEST2E Following completion of write and read portions of the loop-write-read without detectable errors, write and read buffer data miscompared--see code 2E1 for status locations.

> Card: DP IF Slot: A5 A3

321 TEST32 While in EPO'd state and driving both machine and file DAC's (SV) through drive range (7Fh to 80h), current mode feedback indicated multiple null points.

> Status A-O: first negative drive feedback (mach) Status A-1: first negative drive feedback (file)

Card: SV IF EPO Relay Slot: A1 A3 MBD

97712

EC 49908

Code	Detected by	Fault DescriptionFRU'S	
322	TEST32	Machine/File reel current mode feedback (6050) indicated null points outside range: FOh <-> OFh.	-
		Status A-O: machine reel null Status A-1: file reel null	

Card: SV IF Slot: A1 A3

323 TEST32

With EPO reset (current driven thru reels) and DAC drive applied, initial current mode feedback (-pump up/down) was incorrect. This is represented by a FF byte in any of the following status...

Status	B-0:	MACH	reel	current	time,	DAC	-	7F
Status	B-1:	MACH	reel	current	time,	DAC	=	40
Status	B-2:	MACH	reel	current	time,	DÁC	=	20
Status	B-3:	MACH	reel	current	time,	DAC	=	10
Status	B-4:	MACH	reel	current	time,	DAC	-	EF
Status	B-5:	MACH	reel	current	time,	DAC	=	DF
Status	B-6:	MACH	reel	current	time,	DAC	=	BF
Status	B-7:	MACH	reel	current	time,	DAC	= .	80*
Status	B-8:	FILE	reel	current	time,	DAC	*	7F
Status	B-9:	FILE	reel	current	time,	DAC		40
Status	B-A:	FILE	reel	current	time,	DAC	.=	20
Status	B-B:	FILE	reel	current	time,	DAC	=	10
Status	B-C:	FILE	reel	current	time,	DAC	=	EF
Status	B-D:	FILE	reel	current	time,	DAC	=	DF
Status	B-E:	FILE	reel	current	time,	DAC	=	BF
Status	B-F:	FILE	reel	current	time,	DAC	=	80
			•					

Card: SV IF Slot: A1 A3

97712
Code	Detected	Fault DescriptionFRU'S
	by	

324 TEST32 With EPO reset (current driven thru MACHINE reel), current feedback was not detected. The timeout for this feedback is indicated by a FE in any of.. Status B-O: MACH reel current time, DAC = 7F Status B-1: MACH reel current time, DAC = 40 Status B-2: MACH reel current time, DAC = 20 Status B-3: MACH reel current time, DAC = 10 Status B-4: MACH reel current time, DAC = EF Status B-5: MACH reel current time, DAC = DF Status B-6: MACH reel current time, DAC = BF Status B-7: MACH reel current time, DAC = 80 Check MACHINE reel motor cabling. Card: SV IF Machine Reel EPO Relay Slot: A1 A3 Motor MBD

325 TEST32

With EPO reset (current driven thru FILE reel), current feedback was not detected. The timeout for this feedback is indicated by a FE in any of..

Status B-8: FILE reel current time, DAC = 7F Status B-9: FILE reel current time, DAC = 40 Status B-A: FILE reel current time, DAC = 20 Status B-B: FILE reel current time, DAC = 10 Status B-C: FILE reel current time, DAC = EF Status B-D: FILE reel current time, DAC = DF Status B-E: FILE reel current time, DAC = BF Status B-F: FILE reel current time, DAC = 80

Check FILE reel motor cabling.

Card: SV File Reel EPO Relay Slot: A1 Motor MBD

326 TEST32

In current mode, the two reel DAC's were each driven through 8 levels: most positive to most negative. The resulting current feedback times did not follow the relative level of drive.

Card: SV Slot: A1

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ode Detected Fault DescriptionFRU'S by

327 TEST32 The feedback time of MACHINE or FILE reel current was not reduced by a power supply switch to the higher rewind voltage. The expected ratio:

Normal/Rewind > 1.125

Status B-0: Normal-V feedback (Machine) Status C-0: Rewind-V feedback (Machine)

Status B-8: Normal-V feedback (File) Status C-8: Rewind-V feedback (File)

Card: SV AK Slot: Al Pwr Supply

328 TEST32

In threading mode (voltage feedback), each reel DAC was driven to a forward (08h) and a backward (F7h) level. The "-Pump Up (Down)" signals were not as expected. (In the following status, FF indicates both Up & Down signals were active; FE indicates Up or Down was active longer than 1.5 ms).

Status A-8: Machine reel accel time (Fwd) Status A-9: Machine reel accel time (Bkwd) Status A-A: File reel accel time (Fwd) Status A-B: File reel accel time (Bkwd)

Card: SV Slot: Al

329 TEST32

In threading mode (voltage feedback), each reel DAC was driven to a forward (08h) and a backward (F7h) level. Motion of the MACHINE reel was not detected (this is indicated by a FD in the status below).

Status A-8: Machine reel accel time (Fwd) Status A-9: Machine reel accel time (Bkwd)

Card: SV Machine reel Slot: A1 Motor

EC 49908

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Code	Detected	Fault DescriptionFRU'S
	by	

32A TEST32 In threading mode (voltage feedback), each reel DAC was driven to a forward (08h) and a backward (F7h) level. Motion of the FILE reel was not detected (this is indicated by a FD in the status below).

> Status A-A: File reel accel time (Fwd) Status A-B: File reel accel time (Bkwd)

Card: SV File reel Slot: A1 Motor

32B TEST32 No detectable capstan motion after 4 ms drive pulse applied (maximum positive drive).

Card:	sv	Capstan/Tach	EPO Relay
Slot:	A 1	Motor	MBD

32C TEST32 In pro loaded

In processor controlled mode, the capstan DAC was loaded to generate 4 ms pulses of varying magnitude. The resulting capstan positions did not indicate displacement relative to the drive magnitude.

Status B-O: Capstan distance, DAC = 7F (max pos) Status B-1: Capstan distance, DAC = 40 Status B-2: Capstan distance, DAC = 20 Status B-3: Capstan distance, DAC = 10 Status B-4: Capstan distance, DAC = EF Status B-5: Capstan distance, DAC = DF Status B-6: Capstan distance, DAC = BF Status B-7: Capstan distance, DAC = 80 (max neg) Card: SV Capstan/Tach

Slot: Al Motor

32D TEST32 Capstan position count (6024,25) not indicating 50 ips change rate within 100 milli-seconds.

Card:	S۷	Capstan/Tach
Slot:	A 1	Motor

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Code	Detected	tected Fault DescriptionFRU'S	
	by		

32E TEST32 After successfully ramping capstan to 50 ips (verified by position counter: 6025) velocity control mode was enabled. Tach-A at diagnostic sense register (6050) was inactive or period was more than 50% over nominal sometime during the check of 1000 tach lines (one revolution).

> Card: SV Capstan/Tach Slot: A1 Motor

32F TEST32 After successfully ramping capstan to 50 ips (verified by position counter: 6025) velocity control mode was enabled. Velocity error (6027) was monitored for a complete revolution of the capstan. Low and high velocities did not meet test requirements:

Allowed range

Status	C-0:	Low velocity	FAh	-> OEh
Status	C-1:	High velocity		-> 12h

Card: SV Capstan/Tach Slot: A1 Motor

331 TEST32

Machine check occurred during the initial unload operation.

Status A-6: Machine check code See description for code Fxx, where xx= contents of this status location

332 TEST32 Following successful reel and capstan servo testing, a load operation resulted in a machine check.

> Status A-6: Machine check code See description for code Fxx, where xx= contents of this status location

333 TEST32 Load task did not complete due to non-machine check interrupt (door open, undefined NMI, etc.).

Card:	S٧	IF	WR	EOT/BOT
Slot:	A 1	A3 .	A4	Sensor
			- E -	
			EC	49908

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Code	Detected by	Fault DescriptionFRU'S	
334	TEST32	Door open detected during test.	
		Card: SV IF Slot: A1 A3	
341	TEST34	STATUS A-5 = 0: Drive not loaded	
		<pre>STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)</pre>	
342	TEST34	EOT status detected.	
343	TEST34	Machine check occurred during wait for stable turn around conditions.	
		Status A-1: motion number (01h -> 20h: fwd)	
		(21h -> 3Fh: bkwd) Status A-6: Machine check code See description for code Fxx, where xx= contents of this status location	
344	TEST34	Reject resulted from switch to 50 ips motion.	
		Status A-5: Reject code See description for code Exx, where xx= contents of this status location	
345	TEST34	Machine check occurred during acceleration phase of test. See status and FRU information for code 343.	
346	TEST34	Machine check occurred during sustained velocity phase of test. See status and FRU information for code 343.	
347	TEST34	Machine check occurred during deceleration phase of test. See status and FRU information for code 343.	
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Code	Detected	Fault DescriptionFRU'S
	by	

348 TEST34 Machine check occurred during stop-lock phase of test. See status and FRU information for code 343.

34A TEST34 Acceleration characteristics were not within spec. Check for excessive tape path drag.

> Status A-1: motion number (01h -> 20h: fwd) (21h -> 3Fh: bkwd)

> > parameter: should be:

Status B-0:Cumulative ramp errorDDh -> 23hStatus B-1:Feed-forward term08h -> 28h

Card: SV Capstan/Tach Slot: Al Motor

34B TEST34 Arm position did not reach zero-error (+/- 5) within required time on long forward or bkwd motion. The thresholds for this test are valid only when using a full 10.5-inch reel. Tape slip may be indicated by this failure (examine & clean capstan).

> Status A-1: motion number (01h= 1st fwd) (21h= 1st bkwd)

parameter:should be:Status B-2:Mach arm recovery time00h -> C8hStatus B-3:File arm recovery time00h -> C8h

Card: SV Capstan/Tach Slot: A1 Motor

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EC 49908

Code	Detected by	Fault DescriptionFRU'S

34C TEST34 Sustained velocity characteristics not within spec.

Status A-1:motion number(01h -> 20h: fwd)
(21h -> 3Fh: bkwd)parameter:should be:Status B-4:Maximum VelocityStatus B-5:Minimum VelocityStatus B-5:Feed-forward termStatus B-6:Feed-forward termCard:SVCapstan/TachSlot:A1

34D TEST34 Deceleration characteristics not within spec.

Status A-1: motion number (01h -> 20h: fwd) (21h -> 3Fh: bkwd)

		parameter:	should be:
Status	B-7:	Cumulative ramp error	DDh -> 23h
Status	B-8:	Feed-forward term	D8h -> F8h
Card:	SV	Capstan/Tach	
Slot:	Al	Motor	

34E TEST34 Stop-lock positioning outside limits.

Status A-1: motion number (O1h -> 2Oh: fwd) (21h -> 3Fh: bkwd)

		parameter:		should be:
Status Status	B-9: B-A:	Low position High position		F6h -> OAh F6h -> OAh
Card:	sv c	apstan/Tach	· · ·	

Slot: Al Motor

EC 49908

ode	Detected by	Fault DescriptionFRU'S
51	TEST35	STATUS A-5 = 0: Drive not loaded
		<pre>STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)</pre>
52	TEST35	EOT status detected.
53	TEST35	Machine check occurred during wait for stable turn around conditions.
		Status A-1: motion number (01h -> 20h: fwd) (21h -> 3Fh: bkwd) Status A-6: Machine check code See description for code Fxx, where xx= contents of this status location
	. ¹	
55	TEST35	Machine check occurred during acceleration phase of test. See status and FRU information for code 353.
56	TEST35	Machine check occurred during sustained velocity phase of test.
		See status and FRU information for code 353.
57	TEST35	Machine check occurred during deceleration phase of test.
		See status and FRU information for code 353.
58	TEST35	Machine check occurred during stop-lock phase of test. See status and FRU information for code 353.

Code	Detected by	Fault DescriptionFRU'S

35A TEST35 Acceleration characteristics were not within spec. Check for excessive tape path drag. Status A-1: motion number (01h -> 20h: fwd) (21h -> 3Fh: bkwd) parameter: should be: Status B-0: Cumulative ramp error DDh -> 23h Status B-1: Feed-forward term 08h -> 28h Card: SV Capstan/Tach Slot: A1 Motor

35B TEST35 Arm position did not reach zero-error (+/- 5) within required time on long forward or bkwd motion. The thresholds for this test are valid only when using a full 10.5-inch reel. Tape slip may be indicated by this failure (examine & clean capstan).

> Status A-1: motion number (01h= 1st fwd) (21h= 1st bkwd)

> > parameter: should be:

Status B-2: Mach arm recovery time 00h -> C8h Status B-3: File arm recovery time 00h -> C8h

Card: SV Capstan/Tach Slot: A1 Motor

35C TEST35 Sustained velocity characteristics not within spec.

Status A-1: motion number (01h -> 20h: fwd) (21h -> 3Fh: bkwd)

	parameter:	should be:
Status	B-4: Maximum Velocity	EDh -> 13h
Status	B-5: Minimum Velocity	EDh -> 13h
Status	B-6: Feed-forward term	F4h -> 14h
Card:	SV Capstan/Tach	
Slot:	A1 Motor	

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Code	Detected
	by

35D TEST35 Deceleration characteristics not within spec.

> Status A-1: motion number (01h -> 20h: fwd)(21h -> 3Fh: bkwd)

should be: parameter: Status B-7: Cumulative ramp error DDh -> 23h Status B-8: Feed-forward term D8h -> F8h Card: SV Capstan/Tach Slot: A1 Motor

35E TEST35 Stop-lock positioning outside limits.

> Status A-1: motion number (01h -> 20h: fwd) $(21h \rightarrow 3Fh: bkwd)$

		parameter:	should be:
Status	B-9:	Low position	F6h -> 0Ah
Status	B-A:	High position	F6h -> 0Ah
Carde	sv c	anatan /Tach	

.ara: Capstan/Tach Slot: A1 Motor

361 TEST36 STATUS A-5 = 0: Drive not loaded

> STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)

362 TEST36 Machine check occurred during fwd motion (50 ips).

> Status A-6: Machine check code See description for code Fxx, where xx= contents of this status location

Check power supply voltages (AK card).

363 TEST36 Machine check occurred during velocity ramp up from 50 to 170 ips. See status and FRU information for code 362.

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Code	Detected by	Fault DescriptionFRU'S
364	TEST36	Machine check occurred during attempted velocity ramp up from 170 to 220 ips. See status and FRU information for code 362.
365	TEST36	Following BOT detection during rewind, a machine check occurred during the ramp down from approx 220 ips to 50 ips. See status and FRU information for code 362.
366	TEST36	Immediately after rewind ramp down at BOT, a machine check occurred during the settling time. See status and FRU information for code 362.
367	TEST36	Machine check occurred during turn around operation. See status and FRU information for code 362.
368	TEST36	Maximum velocity attained during 125 foot rewind was less than 165 ips (OC7h). Status B-O: maximum velocity (VL-VR) Check for early EOT sticker on tape or spurious
		EOT detections. Card: SV AK Slot: Al Pwr Supply
421	TEST42	<pre>STATUS A-5 = 0: Drive not loaded STATUS A-5 not 0: Speed switch reject code (see description for code Exx. where</pre>
422	TFST42	xx = contents of this status location)
423	TEST42	Reject or Machine check from initial rewind. See description of reject codes preceeding code E01.

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				. : :	
	Code	Detected by	Fault DescriptionFRU'S		· .
• •	424	TEST42	Current on or unstable (6061) afer rewind.		
	/		Card: WR IF RD Slot: A4 A3 A6		
	425	TEST42	Immediately after setting erase, status was in error (6061).		
			Card: WR IF RD Slot: A4 A3 A6		
	426	TEST42	Interrupt from erase transition was not seen. Card: WR IF	• •	
	427	TEST42	Stable erase-only status was not seen in sense		
			Card: WR IF Rd/Wrt Slot: A4 A3 Head	•	
	428	TEST42	Stable write mode status was not seen in sense register (6061).		
		•	Card: WR IF Rd/Wrt Slot: A4 A3 Head		
	429	TEST42	Interrupt from write transition was not seen.		
			Slot: A4 A3		
	42A	TEST42	Amplitude sensor active without motion (6045 or 6043). Card: WR IF DP	· .	
			Slot: A4 A3 A5		
	48		EC 49908	97712	
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Code	Detected by	Fault DescriptionFRU'S
90	TESTAS	Machine check occurred during forward write motion
420	123142	Status A-6: Machine check code See description for code Fxx, where xx= contents of this status location
42C	TEST42	Amplitude sensor not as expected during 30 foot all track write.
		Check tape quality.
		Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P
		Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head
42D	TEST42	Amplitude sensor not as expected during 1 foot write of one track only.
		Check tape quality.
		Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P
	,	Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head
42E	TEST42	Amplitude sensor not as expected during 1 foot write of all but one track.
		Check tape quality.
		Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P
		Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head

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 42F TEST42 Amplitude sensor detected during feed-through check (all tracks writing; no motion). Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head 431 TEST43 STATUS A-5 = 0: Drive not loaded STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location) 432 TEST43 Tape must be write enabled 433 TEST43 Reject or Machine check from initial rewind. See description of reject codes preceeding code E01. 434 TEST43 Current on or unstable (6061) afer rewind. Card: WR IF RD Slot: A4 A3 A6 435 TEST43 Immediately after setting erase, status was in error (6061). Card: WR IF RD Slot: A4 A3 A6 436 TEST43 Interrupt from erase transition was not seen. Card: WR IF 	Code	Detected by	Fault DescriptionFRU'S
 Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head 431 TEST43 STATUS A-5 = 0: Drive not loaded STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location) 432 TEST43 Tape must be write enabled 433 TEST43 Reject or Machine check from initial rewind. See description of reject codes preceeding code E01. 434 TEST43 Current on or unstable (6061) afer rewind. Card: WR IF RD Slot: A4 A3 A6 435 TEST43 Immediately after setting erase, status was in error (6061). Card: WR IF RD Slot: A4 A3 A6 436 TEST43 Interrupt from erase transition was not seen. Card: WR IF 	42F	TEST42	Amplitude sensor detected during feed-through check (all tracks writing; no motion).
Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head 431 TEST43 STATUS A-5 = 0: Drive not loaded STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location) 432 TEST43 Tape must be write enabled 433 TEST43 Reject or Machine check from initial rewind. See description of reject codes preceeding code E01. 434 TEST43 Current on or unstable (6061) afer rewind. Card: WR IF RD Slot: A4 A3 A6 435 TEST43 Immediately after setting erase, status was in error (6061). Card: WR IF RD Slot: A4 A3 A6 436 TEST43 Interrupt from erase transition was not seen. Card: WR IE			Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P
 431 TEST43 STATUS A-5 = 0: Drive not loaded STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location) 432 TEST43 Tape must be write enabled 433 TEST43 Reject or Machine check from initial rewind. See description of reject codes preceeding code E01. 434 TEST43 Current on or unstable (6061) afer rewind. Card: WR IF RD Slot: A4 A3 A6 435 TEST43 Immediately after setting erase, status was in error (6061). Card: WR IF RD Slot: A4 A3 A6 436 TEST43 Interrupt from erase transition was not seen. Card: WR IF 			Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head
 STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location) 432 TEST43 Tape must be write enabled 433 TEST43 Reject or Machine check from initial rewind. See description of reject codes preceeding code E01. 434 TEST43 Current on or unstable (6061) afer rewind. Card: WR IF RD Slot: A4 A3 A6 435 TEST43 Immediately after setting erase, status was in error (6061). Card: WR IF RD Slot: A4 A3 A6 436 TEST43 Interrupt from erase transition was not seen. Card: WR IF 	431	TEST43	STATUS A-5 = 0: Drive not loaded
 432 TEST43 Tape must be write enabled 433 TEST43 Reject or Machine check from initial rewind. See description of reject codes preceeding code E01. 434 TEST43 Current on or unstable (6061) afer rewind. Card: WR IF RD Slot: A4 A3 A6 435 TEST43 Immediately after setting erase, status was in error (6061). Card: WR IF RD Slot: A4 A3 A6 436 TEST43 Interrupt from erase transition was not seen. Card: WR IF 			<pre>STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)</pre>
 433 TEST43 Reject or Machine check from initial rewind. See description of reject codes preceeding code E01. 434 TEST43 Current on or unstable (6061) afer rewind. Card: WR IF RD Slot: A4 A3 A6 435 TEST43 Immediately after setting erase, status was in error (6061). Card: WR IF RD Slot: A4 A3 A6 436 TEST43 Interrupt from erase transition was not seen. Card: WR IF 	432	TEST43	Tape must be write enabled
 434 TEST43 Current on or unstable (6061) afer rewind. Card: WR IF RD Slot: A4 A3 A6 435 TEST43 Immediately after setting erase, status was in error (6061). Card: WR IF RD Slot: A4 A3 A6 436 TEST43 Interrupt from erase transition was not seen. Card: WR IE 	433	TEST43	Reject or Machine check from initial rewind. See description of reject codes preceeding code E01.
Card: WR IF RD Slot: A4 A3 A6 435 TEST43 Immediately after setting erase, status was in error (6061). Card: WR IF RD Slot: A4 A3 A6 436 TEST43 Interrupt from erase transition was not seen. Card: WR IF	434	TEST43	Current on or unstable (6061) afer rewind.
 435 TEST43 Immediately after setting erase, status was in error (6061). Card: WR IF RD \$10t: A4 A3 A6 436 TEST43 Interrupt from erase transition was not seen. Card: WR IF 			Card: WR IF RD Slot: A4 A3 A6
Card: WR IF RD Slot: A4 A3 A6 436 TEST43 Interrupt from erase transition was not seen. Card: WR IF	435	TEST43	Immediately after setting erase, status was in error (6061).
436 TEST43 Interrupt from erase transition was not seen.			Card: WR IF RD Slot: A4 A3 A6
Card: WR IF	436	TEST43	Interrupt from erase transition was not seen.
Slot: A4 A3			Card: WR IF Slot: A4 A3

Code	Detected by	Fault DescriptionFRU'S
437	TEST43	Stable erase-only status was not seen in sense register (6061).
		Card: WR IF Rd/Wrt Slot: A4 A3 Head
438	TEST43	Stable write mode status was not seen in sense register (6061).
		Card: WR IF Rd/Wrt Slot: A4 A3 Head
43A	TEST43	Amplitude sensor active without motion (6045 or 6043).
		Card: WR IF DP Slot: A4 A3 A5
43B	TEST43	Machine check occurred during forward write motion.
		Status A-6: Machine check code See description for code Fxx, where xx= contents of this status location
43C	TEST43	Amplitude sensor not as expected during 30 foot all track write.
		Check tape quality.
		Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P
		Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head

Code	Detected	Fault DescriptionFRU'S	
	by		

43D TEST43 Amplitude sensor not as expected during 1 foot write of one track only.

Check tape quality.

Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P

Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head

43E TEST43 Amplitude sensor not as expected during 1 foot write of all but one track.

Check tape quality.

Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P

Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head

43F TEST43 Amplitude sensor detected during feed-through check (all tracks writing; no motion).

Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P

Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head

481 TEST48 STATUS A-5 = 0: Drive not loaded

STATUS A-5 not 0: Speed switch reject code
 (see description for code Exx, where
 xx = contents of this status location)

482 TEST48 Tape must be write enabled

483 TEST48 Reject or Machine check from initial rewind. See description of reject codes preceeding code E01.

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Code	Detected	Fault DescriptionFRU'S
	by	

484	TEST48	Reject or Machine check from internal write command.
		Status A-0, A-1: Low, high byte of record number.
		See description of reject codes preceeding code E01.
485	TEST48	Write overrun status (6042) during internal write (data supplied to DP from IF card only).
		Status A-0,A-1: Low, high byte of record number. Range: 000-100 hex.
		Card: IF DP Slot: A3 A5
486	TEST48	Bus parity error status (6042) during internal write (data supplied to DP from IF card only).
		Status A-0,A-1: Low, high byte of record number. (Range: 000-100 hex)
		Card: IF DP Slot: A3 A5
488	TEST48	Reject or Machine check from back-space-block or erase-gap command during write error recovery. See description of reject codes preceeding code E01.
489	TEST48	During write error recovery, all 5 retries failed.
		Status A-0,A-1: Low, high byte of record number. (Range: 000-100 hex)
		Check tape quality.
		Card: DP WR RD IF Slot: A5 A4 A6 A3

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Code	Detected by	Fault DescriptionFRU'S
48A	TEST48	Reject or Machine check from write-tape-mark command.
		Status A-0,A-1: Low, high byte of record number (first WTM occurs at 10 bex)
		See description of reject codes preceeding code E01.
48B	TEST48	During the writing of 256 PE records, more than one temporary write error occurred.
		Status A-2: IOTAL failing writes
		JLALUS A-J: IEMPORARY WRITE ERRORS (1 data check in 6 attempte)
		Status A-4: Media defects
		(>1 data check in 6 attempts)
		** Read/Write Error Tallies **
	-	Status A-8: Data checks Status A-C: Multi-trks
		Status A-9: Velocity Status A-D: Part. recd's
		Status A-A: End Data Chks Status A-E: Un-corr.
		Status A-B: Corrections Status A-F: CRC errors
		Status B-O -> B-8: Dead Track counters O-7,P
		Status C-O -> C-8: Phase error counters O-7,P
		Card: DP WR RD IF
		Slot: A5 A4 A6 A3
	4	
491	TEST49	STATUS A-5 = 0: Drive not loaded
		STATUS A-5 not 0: Speed switch reject code
		(see description for code Exx, where
		<pre>xx = contents of this status location)</pre>
492	TEST49	Reject or machine check from initial rewind.
		see description of reject codes preceeding code for.
493	TEST49	Reject or Machine check from internal read forward.
		See description of reject codes preceeding code E01.

Code	Detected	Fault DescriptionFRU'S
	by	

Unexpected tape mark (did not follow a 16 record

group). Tape must have been written by TEST48. Status A-0, A-1: Low, high byte of record number (Range: 000-100 hex) Card: DP Slot: A5 497 TEST49 Unexpected tape mark (more than 256 records read correctly, but tape mark did not follow 16 record group). Tape must have been written by TEST48. Status A-0, A-1: Low, high byte of record number (Range: 000-100 hex) Card: DP Slot: A5 499 TEST49 Data miscompare following read without data check (tape must have been written by TEST48). Comparison involved 32 bytes of write buffer (8000-801F) and 32 bytes of read buffer (8020-803F). Data should match record number below: Status A-0, A-1: Low, high byte of record number (Range: 000-100 hex) IF Card: DP RD Slot: A5 A6 A 3 49B TEST49 Reject or Machine check from internal read forward over expected tape mark. Status A-0, A-1: Low, high byte of record number

(Range: 000-100 hex)

See description of reject codes preceeding code E01.

496 TEST49

Code	Detected by	Fault DescriptionFRU'S
49C	TEST49	Tape mark status not detected when expected (but 16 record group read OK).
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex, first TMK @ 10
		Card: DP
		Slot: A5
49E	TEST49	Failed internal record read with 5 retries.

** Read/Write Error Tallies **

Status A-8: Data checksStatus A-C: Multi-trksStatus A-9: VelocityStatus A-D: Part. recd'sStatus A-A: End Data ChksStatus A-E: Un-corr.Status A-B: CorrectionsStatus A-F: CRC errors

Status B-O -> B-8: Dead track counters O-7, P Status C-O -> C-8: Phase error counters O-7, P

Card: DP Slot: A5

49F TEST49 Reject or Machine check from internal Back-Space-Block command during read error recovery. See description of reject codes preceeding code E01.

4A1 TEST4A STATUS A-5 = 0: Drive not loaded

STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)

4A2 TEST4A Reject or Machine check from internal read backward operation (searching for EOF tape marks). See description of reject codes preceeding code EO1.

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Code	Detected	Fault DescriptionFRU'S
	by	
1	Code	Code Detected by

4A3 TEST4A Reject or Machine check from internal read forward operations (searching for EOF tape marks) or read backward operations (if positioning around TMK's). See description of reject codes preceeding code EO1.

4A4 TEST4A After finding 2 tape marks reading forward, read backward operation did not produce tape mark status.

> Card: DP Slot: A5

4A5 TEST4A Reject or Machine check from internal read backward. See description of reject codes preceeding code E01.

4A6 TEST4A ID-burst status (6043) detected before reading 256 records. Tape must have been written by TEST48.

Status A-0: record number (range: FF-00)

Card: DP

Slot: A5

4A7 TEST4A Data miscompare following read without data check (tape must have been written by TEST48). Comparison involved 32 bytes of write buffer (8000-801F) and 32 bytes of read buffer (8020-803F). Data should match record number below:

Status A-0: record number expected (range: FF-00)

Card: DP RD IF Slot: A5 A6 A3

4A8 TEST4A Unexpected tape mark (did not follow a 16 record group). Tape must have been written by TEST48.

Status A-0: record number (range: FF-00)

Card: DP Slot: A5

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Code	Detected by	Fault DescriptionFRU'S
· ·		
+AE	TEST4A	Failed internal record read with 5 retries. $\overset{\circ}{\sim}$
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex)
		** Read/Write Error Tallies **
		Status A-8: Data checksStatus A-C: Multi-trksStatus A-9: VelocityStatus A-D: Part. recd'sStatus A-A: End Data ChksStatus A-E: Un-corr.Status A-B: CorrectionsStatus A-F: CRC errors
		Status B-O -> B-8: Dead track counters O-7, P Status C-O -> C-8: Phase error counters O-7, P
		Card: DP IF Slot: A5 A3
AF	TEST4A	Reject or Machine check from internal Forward-Space- Block command during read error recovery. See description of reject codes preceeding code E01.
+B1	TEST4B	STATUS A-5 = 0: Drive not loaded
		<pre>STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)</pre>
+B2	TEST4B	Reject or Machine check from initial rewind. See description of reject codes preceeding code E01.
4B3	TEST4B	Reject or Machine check from read backward.

Code	Detected by	Fault DescriptionFRU'S
4B4	TEST4B	Data checks occurred on 5 retries of read backward operation. Check tape quality.
		** Read/Write Error Tallies **
		Status A-8: Data checksStatus A-C: Multi-trksStatus A-9: VelocityStatus A-D: Part. recd'sStatus A-A: End Data ChksStatus A-E: Un-corr.Status A-B: CorrectionsStatus A-F: CRC errors
		Status B-O -> B-8: Dead track counters O-7, P Status C-O -> C-8: Phase error counters O-7, P
		Card: DP RD Slot: A5 A6
485	TEST4B	Reject or Machine check from forward-space-block command during read backward error recovery. See description of reject codes preceeding code E01.
486	TEST4B	Reject or Machine check from read forward. See description of reject codes preceeding code E01.
4B7	TEST4B	Data checks occurred on 5 retries of read forward operation. Check tape quality.
		** Read/Write Error Tallies **
		Status A-8: Data checksStatus A-C: Multi-trksStatus A-9: VelocityStatus A-D: Part. recd'sStatus A-A: End Data ChksStatus A-E: Un-corr.Status A-B: CorrectionsStatus A-F: CRC errors
		Status B-O -> B-8: Dead track counters O-7, P Status C-O -> C-8: Phase error counters O-7, P
· · · · · · · · · · · · · · · · · · ·		Card: DP RD Slot: A5 A6
4B8	TEST4B	Reject or Machine check from backward-space-block command during read forward error recovery. See description of reject codes preceeding code E01.

Code	Detected by	Fault DescriptionFRU'S	
4B9	TEST4B	Reject or Machine check from forward-space-file. See description of reject codes preceeding code EC)1.
4BA	TEST4B	Reject or Machine check from backward-space-file. See description of reject codes preceeding code EC	01.
4 B B	TEST4B	Reject or Machine check from forward-space-block. See description of reject codes preceeding code EC	01.
4BC	TEST4B	Reject or Machine check from backward-space-block See description of reject codes preceeding code EC	D1.
4BD	TEST4B	Tape Mark status set when not expected indicating possible positioning problem. (Tape must have been written by TEST48) Card: DP	
).BE	, 	Slot: A5	
402	123140	problem. (Tape must have been written by TEST48). Card: DP Slot: A5	
4BF	TEST4B	Tape Mark status not set when not expected indicating possible positioning problem. (Tape must have been written by TEST48)	
		Card: DP Slot: A5	
4C1	TEST4C	<pre>STATUS A-5 = 0: Drive not loaded STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)</pre>	
4C2	TEST4C	Tape must be write enabled	
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Code	Detected by	Fault DescriptionFRU'S
4C3	TEST4C	Reject or Machine check from initial rewind. See description of reject codes preceeding code E01.
4C4	TEST4C	Reject or Machine check from internal write command.
		Status A-0,A-1: Low, high byte of record number. (Range: 000-100 hex)
		See description of reject codes preceeding code E01.
4C5	TEST4C	Write overrun status (6042) during internal write (data supplied to DP from IF card only).
		Status A-0,A-1: Low, high byte of record number. Range: 000-100 hex.
		Card: IF DP Slot: A3 A5
4c6	TEST4C	Bus parity error status (6042) during internal write (data supplied to DP from IF card only).
		Status A-0,A-1: Low, high byte of record number. (Range: 000-100 hex)
		Card: IF DP Slot: A3 A5
4c8	TEST4C	Reject or Machine check from back-space-block or erase-gap command during write error recovery. See description of reject codes preceeding code E01.
409	TEST4C	During write error recovery, all 5 retries failed.
		Status A-0,A-1: Low, high byte of record number. (Range: 000-100 hex)
		Check tape quality.
	`.	Card: DP WR RD IF
		Slot: A5 A4 A6 A3

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Code De	tected by	Fault DescriptionFRU'S	
4CA TE	ST4C R	eject or Machine check from write-tape-mark	
	c	ommand.	
	S	tatus A-0,A-1: Low, high byte of record number	
	S	(first WTM occurs at 10 hex) ee description of reject codes preceeding code E01.	
4СВ ТЕ	ST4C C	uring the writing of 256 GCR records, more than ne temporary write error occurred.	
	S	tatus A-2: Total failing writes	
	5	tatus A-3: Temporary write errors (1 data check in 6 attempts)	
-	9	tatus A-4: Media defects (>1 data check in 6 attempts)	
	ł	* Read/Write Error Tallies **	
	. 5	tatus A-8: Data checks Status A-C: Multi-trks	
		tatus A-9: Velocity Status A-D: Part. recd's	
	9	tatus A-B: Corrections Status A-F: CRC errors	
		tatus B-O -> B-8: Dead Track counters O-7,P tatus C-O -> C-8: Phase error counters O-7,P	
	(ard: DP WR RD IF lot: A5 A4 A6 A3	
4D1 TE	ST4D S	TATUS A-5 = 0: Drive not loaded	
	9	TATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)	
4D2 TE	ST4D P	eject or Machine check from initial rewind. See description of reject codes preceeding code E01.	
4D3 TE	ST4D I	Reject or Machine check from internal read forward.	
	:	Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex)	
·	:	See description of reject codes preceeding code E01.	
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Code	Detected	Fault DescriptionFRU'S
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4D6	TEST4D	Unexpected tape mark (did not follow a 16 record group). Tape must have been written by TEST4C.
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex)
		Card: DP
		Slot: A5
4D7	TEST4D	Unexpected tape mark (more than 256 records read correctly, but tape mark did not follow 16 record group). Tape must have been written by TEST4C.
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex)
		Card: DP
		Slot: A5
4D9	TEST4D	Data miscompare following read without data check (tape must have been written by TEST4C). Comparison involved 32 bytes of write buffer (8000-801F) and 32 bytes of read buffer (8020-803F). Data should match record number below:
		Status 4-0 4-1. Low high byte of record number
		(Range: 000-100 hex)
		Card: DP RD IF
		Slot: A5 A6 A3
4DB	TEST4D	Reject or Machine check from internal read forward over expected tape mark.
		Status A-0,A-1: Low, high byte of record number
		(Range: 000-100 hex)
		See description of reject codes preceeding code E01.
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4DC	TEST4D	Tape mark status not detected when expected (but 16 record group read OK).
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex, first TMK @ 10)
		Card: [©] DP Slot: [©] A5
4DE	TEST4D	Failed internal record read with 5 retries.
		Status A-0, A-1: Low, high byte of record number (Range: 000-100 hex)
		** Read/Write Error Tallies **
		Status A-8: Data checksStatus A-C: Multi-trksStatus A-9: VelocityStatus A-D: Part. recd'sStatus A-A: End Data ChksStatus A-E: Un-corr.Status A-B: CorrectionsStatus A-F: CRC errors
		Status B-O -> B-8: Dead track counters O-7, P Status C-O -> C-8: Phase error counters O-7, P
		Card: DP Slot: A5
+DF	TEST4D	Reject or Machine check from internal Back-Space Block command during read error recovery. See description of reject codes preceeding code E01.
4E 1	TEST4E	STATUS A-5 = 0: Drive not loaded
		<pre>STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)</pre>
4E2	TEST4E	Reject or Machine check from internal read backward operation (searching for EOF tape marks). See description of reject codes preceeding code EO1.

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Code	Detected by	Fault Descri	ptionFRU'S	
			ngaranga tanan ta	
4E3	TEST4E	Reject or Machine check f operations (searching for backward operations (if p See description of reject	rom internal read forward EOF tape marks) or read positioning around TMK's). codes preceeding code EO1	•
4E4 ·	TEST4E	After finding 2 tape mark backward operation did no	s reading forward, read ot produče tape mark status	•
		Card: DP Slot: A5		
			joon (g	
4E5 '	TEST4E	Reject or Machine check f See description of reject	rom internal read backward codes preceeding code E01	•
1.56	TECTLE		tested before modime 200	
460	IESI4E	records. Tape must have b	peen written by TEST4C.	
		Status A-0: record numbe	er (range: FF-00)	
		Card: DP		
		Slot: A5		
4E7	TEST4E	Data miscompare following (tape must have been writ involved 32 bytes of writ 32 bytes of read buffer f match record number below	g read without data check ten by TEST4C). Comparison te buffer (8000-801F) and (8020-803F). Data should v:	•
		Status A-0: record numbe	er expected (range: FF-00)	
		Card: DP RD IF		
		STOL: 45 40 45		
4E8	TEST4E	Unexpected tape mark (dic group). Tape must have b	not follow a 16 record been written by TEST4C.	
		Status A-0: record numbe	er (range: FF-00)	
		Card: DP		
		Slot: A5		
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Code	Detected by	Fault DescriptionFRU'S
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4EE	TEST4E	Failed internal record read with 5 retries.
		Status A-0, A-1: Low, high byte of record number (Range: 000-100 hex)
		** Read/Write Error Tallies **
		StatusA-8:Data checksStatusA-C:Multi-trksStatusA-9:VelocityStatusA-D:Part.recd'sStatusA-A:End Data ChksStatusA-E:Un-corr.StatusA-B:CorrectionsStatusA-F:CRC errors
		Status B-O -> B-8: Dead track counters O-7, P Status C-O -> C-8: Phase error counters O-7, P
		Card: DP IF Slot: A5 A3
4EF	TEST4E	Reject or Machine check from internal Forward-Space Block command during read error recovery. See description of reject codes preceeding code E01.
4F 1	TEST4F	STATUS A-5 = 0: Drive not loaded
		<pre>STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)</pre>
4F2	TEST4F	Reject or Machine check from initial rewind. See description of reject codes preceeding code E01.
4F3	TEST4F	Reject or Machine check from read backward. See description of reject codes preceeding code E01.

Code	Detected	Fault DescriptionFRU'S
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		Constants of the West -
4F4	TEST4F	Data checks occurred on 5 retries of read backward
		operation. Check tape quality.
		** Read/Write Error Tallies **
		Statue A-8. Data chocke - Statue A-C. Multi-trke
		Status A-9: Velocity Status A-D: Part. recd's
		Status A-A: End Data Chks Status A-E: Un-corr.
		Status A-B: Corrections Status A-F: CRC errors
		Status B-O -> B-8: Dead track counters 0-7, P
		Status C-O -> C-8: Phase error counters O-7, P
		Card: DP PD
		Slot: A5 A6
4F5	TEST4F	Reject or Machine check from forward-space-block
		command during read backward error recovery.
		See description of reject codes preceeding code E01.
4F6	TEST4F	Reject or Machine check from read forward.
		See description of reject codes preceeding code E01.
4F7	TEST4F	Data checks occurred on 5 retries of read forward
	•	operation. Check tape quality.
		** Read/Write Frror Tallies **
		Status A-8: Data checks Status A-C: Multi-trks
		Status A-9: Velocity Status A-D: Part. recd's
		Status A-B: Corrections Status A-F: CRC errors
		Status B-O -> B-8: Dead track counters O-7, P
		Status L-0 -> L-0: Phase error counters 0-7, P
		Card: DP RD
		Slot: A5 A6
4F8	TEST4F	Reject or Machine check from backward-space-block
		command during read forward error recovery.
		see description of reject codes preceeding code cor.
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Code Detected by	Fault DescriptionFRU'S	
4F9 TEST4F	Reject or Machine check from forward-space-file. See description of reject codes preceeding code	E01.
4FA TEST4F ·	Reject or Machine check from backward-space-file See description of reject codes preceeding code	e. E01.
4FB TEST4F	Reject or Machine check from forward-space-block See description of reject codes preceeding code	E01.
4FC TEST4F	Reject or Machine check from backward-space-bloc See description of reject codes preceeding code	E01.
4FD TEST4F	Tape Mark status set when not expected indication possible positioning problem. (Tape must have been written by TEST4C)	ng
	Card: DP Slot: A5	
FE TEST4F	Data miscompare indicating possible positioning problem. (Tape must have been written by TEST4C)	•
	Card: DP Slot: A5	
4FF TEST4F	Tape Mark status not set when not expected indicating possible positioning problem. (Tape must have been written by TEST4C)	
	Card: DP Slot: A5	
521 TEST52	STATUS A-5 = 0: Drive not loaded	
	STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)	
522 TEST52	Tape must be write enabled	· .
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Code	Detected by	Fault DescriptionFRU'S
523	TEST52	Reject or Machine check from initial rewind. See description of reject codes preceding code E01.
524	TEST52	Current on or unstable (6061) afer rewind.
		Card: WR IF RD Slot: A4 A3 A6
525	TEST52	Immediately after setting erase, status was in error (6061).
		Card: WR IF RD TER Slot: A4 A3 A6
526	TEST52	Interrupt from erase transition was not seen.
	: ·	Card: WR IF Slot: A4 A3
527	TEST52	Stable erase-only status was not seen in sense register (6061).
		Card: WR IF Write/Erase head Slot: A4 A3
528	TEST52	Stable write mode status was not seen in sense register (6061).
		Card: WR IF Write/Erase head Slot: A4 A3
529	TEST52	Interrupt from write transition was not seen.
		Card: WR IF Slot: A4 A3

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Code	Detected by	Fault DescriptionFRU'S
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52A	TEST52	Amplitude sensor active without motion (6045 or 6043).
		Card: WR IF DP Slot: A4 A3 A5
52B	TEST52	Machine check occurred during forward write motion.
		Status A-6: Machine check code See description for code Fxx, where xx= contents of this status location
52C	TEST52	Amplitude sensor not as expected during 30 foot all track write.
		Check tape quality.
		Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P
		Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head
52D	TEST52	Amplitude sensor not as expected during 1 foot write of one track only.
		Check tape quality.
		Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P
		Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head

Code Detected Fault DescriptionFRU'S by	
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52E TEST52 Amplitude sensor not as expected during 1 foot write of all but one track. 31.3.5.1 . () Check tape quality. Status B-0.7: error count for tracks 0.7 Status B-8: error count for tracks P Card: WR RD DP Rd/Wrt o andr Slot: A4 A6 A5 Head -A au 52F TEST52 Amplitude sensor detected during feed-through check (all tracks writing; no motion). Status B-0,7: error count for tracks 0,7 error count for tracks P Status B-8: Rd/Wrt Card: WR RD DP Slot: A4 A6 A5 Head 531 TEST53 STATUS A-5 = 0: Drive not loaded STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location) Tape must be write enabled 532 TEST53 533 TEST53 Reject or Machine check from initial rewind. See description of reject codes preceeding code E01. Current on or unstable (6061) afer rewind. 534 TEST53 Card: WR 1 F RD Slot: Α4 Α3 A6 535 TEST53 Immediately after setting erase, status was in error (6061).

> Card: WR IF RD Slot: A4 A3 A6

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Code	Detected by	Fault DescriptionFRU'S
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536	TEST53	Interrupt from erase transition was not seen.
		Card: WR IF Slot: NDE12
537	TEST53	Stable erase-only status was not seen in sense register (6061).
		Card: WR IF Rd/Wrt Slot: A4 A3 Head
538	TEST53	Stable write mode status was not seen in sense register (6061).
		Card: WR IF Rd/Wrt Slot: A4 A3 Head
539	TEST53	Interrupt from write transition was not seen.
		Card: WR IF Slot: A4 A3
53A	TEST53	Amplitude sensor active without motion (6045 or 6053).
		Card: WR IF DP Slot: A4 A3 A5
53B	TEST53	Machine check occurred during forward write motion.
		Status A-6: Machine check code See description for code Fxx, where xx= contents of this status location

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Code	Detected	Fault DescriptionFRU'S
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53C TEST53 Amplitude sensor not as expected during 30 foot all track write. Check tape quality. Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head

53D TEST53 Amplitude sensor not as expected during 1 foot write of one track only.

Check tape quality.

Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P

Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head

53E TEST53 Amplitude sensor not as expected during 1 foot write of all but one track.

Check tape quality.

Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P

Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head

53F TEST53 Amplitude sensor detected during feed-through check (all tracks writing; no motion).

Status B-0,7: error count for tracks 0,7 Status B-8: error count for tracks P

Card: WR RD DP Rd/Wrt Slot: A4 A6 A5 Head

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Code	Detected by	Fault DescriptionFRU'S
581	TEST58	STATUS A-5 = 0: Drive not loaded STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)
582	TEST58	Tape must be write enabled
583	TEST58	Reject for Machine check from initial rewind. See description of reject codes preceeding code E01.
584	TEST58	Reject or Machine check from internal write command. Status A-0,A-1: Low, high byte of record number. Range: 000-100 hex. See description of reject codes preceeding code E01.
585	TEST58	<pre>Write overrun status (6052) during internal write (data supplied to DP from IF card only). Status A-0,A-1: Low, high byte of record number. Range: 000-100 hex. Card: IF DP Slot: A3 A5</pre>
586	TEST58	Bus parity error status (6052) during internal write (data supplied to DP from IF card only). Status A-0,A-1: Low, high byte of record number. (Range: 000-100 hex) Card: IF DP Slot: A3 A5
588	TEST58	Reject or Machine check from back-space-block or erase-gap command during write error recovery. See description of reject codes preceeding code E01.

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Code	Detected	Fault DescriptionFRU'S
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589	TEST58	During write error recovery, all 5 retries failed.
		Status A-0,A-1: Low, high byte of record number. (Range: 000-100 hex)
		の注 Check tape quality.
		Slot: A5 A4 A6 A3
58A	TEST58	Reject or Machine check from write-tape-mark command.
		Status A-0,A-1: Low, high byte of record number
		(first WTM occurs at 10 hex)
		See description of reject codes preceeding code E01.
500	TECTEO	
208	153150	one temporary write error occurred.
		Status A-2: Total failing writes
		Status A-3: Temporary write errors (1 data check in 6 attempts)
		Status A-4: Media defects
		(>1 data check in 6 attempts)
		<pre>** Read/Write Error Tallies **</pre>
		Status A-8: Data checks Status A-C: Multi-trks
		Status A-9: Velocity Status A-D: Part. recd's Status A-A: End Data Chks Status A-E: Un-corr.
		Status A-B: Corrections Status A-F: CRC errors
		Status B-O -> B-8: Dead Track counters O-7 P
		Status C-O -> C-8: Phase error counters 0-7,P
		Card: DP WR RD IF
		Slot: A5 A4 A6 A3
591	TEST59	STATUS A-5 = 0: Drive not loaded
		STATUS A-5 not 0: Speed switch reject code
		(see description for code Exx, where

xx = contents of this status location)

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Code	Detected bv	Fault DescriptionFRU'S
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592	TEST59	Rej or machine check from initial rewind. See scription of reject codes preceeding code E01.
593	TEST59	Rej or Machine check from internal read forward. See Acription of reject codes preceeding code E01.
596	TEST59	Unex defined tape mark (did not follow a 16 record group Tape must have been written by TEST58.
		Statu ¹ ♥ ≻0,A-1: Low, high byte of record number ₩ \$ \$ (Range: 000-100 hex)
		Card: DP Slot: A5
597	TEST59	Unexpected tape mark (more than 256 records read correctly, but tape mark did not follow 16 record group). Tape must have been written by TEST58.
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex)
		Card: DP Slot: A5
599	TEST59	Data miscompare following read without data check (tape must have been written by TEST58). Comparison involved 32 bytes of write buffer (8000-801F) and 32 bytes of read buffer (8020-803F). Data should match record number below:
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex)
		Card: DP RD IF Slot: A5 A6 A3

Code	Detected by	Fault DescriptionFRU'S
59В	TEST59	Reject or Machine check from internal read forward over expected tape mark.
		Status A-O,A-1: Low, high byte of record number (Range: 000-100 hex)
		See description of reject codes preceeding code E01.
59C	TEST59	Tape mark status not detected within expected (but 16 record group read OK).
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex, first TMK @ 10)
		Card: DP Slot: A5
59E	TEST59	Failed internal record read with 5 retries.
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex)
		** Read/Write Error Tallies **
		Status A-8: Data checksStatus A-C: Multi-trksStatus A-9: VelocityStatus A-D: Part. recd'sStatus A-A: End Data ChksStatus A-E: Un-corr.Status A-B: CorrectionsStatus A-F: CRC errors
		Status B-O -> B-8: Dead track counters O-7, P Status C-O -> C-8: Phase error counters O-7, P
		Card: DP Slot: A5
59F	TEST59	Reject or Machine check from internal Back-Space- Block command during read error recovery. See description of reject codes preceeding code E01.

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Code	Detected by	Fault DescriptionFRU'S
5A 1	TEST5A	STATUS A-5 = 0: Drive not loaded STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)
5A2	TEST5A	Reject or Machine check from internal read backward operation (searching for EOF tape marks). See description of reject codes preceeding code EO1.
5A3	TEST5A	Reject or Machine check from internal read forward operations (searching for EOF tape marks) or read backward operations (if positioning around TMK's). See description of reject codes preceeding code EO1.
584	TEST5A	After finding 2 tape marks reading forward, read backward operation did not produce tape mark status. Card: DP Slot: A5
545	TEST5A	Reject or Machine check from internal read backward. See description of reject codes preceeding code E01.
586	TEST5A	ID-burst status (6053) detected before reading 256 records. Tape must have been written by TEST58.
		Status A-0: record number (range: FF-00)
		Card: DP Slot: A5

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Code	Detected	Fault DescriptionEPULS
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547	TEST5A	Data miscompare following read without data check (tape must have been written by TEST58). Comparison involved 32 bytes of write buffer (8000-801F) and 32 bytes of read buffer (8020-803F). Data should match record number below:
		Status A-0: record number $expected$ (range: FF-00)
		Card: DP RD IF no 150 Slot: A5 A6 A3 no 150 Dese
588	TEST5A	Unexpected tape mark (did not follow a 16 record group). Tape must have been written by TEST58.
		Status A-0: record number (range: FF-00)
	,	Card: DP Slot: A5
5AE	TEST5A	Failed internal record read with 5 retries.
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex)
		** Read/Write Error Tallies **
		Status A-8: Data checks Status A-C: Multi-trks

Status A-8: Data checks Status A-C: Multi-trks Status A-9: Velocity Status A-D: Part. recd's Status A-A: End Data Chks Status A-E: Un-corr. Status A-B: Corrections Status A-F: CRC errors Status B-0 -> B-8: Dead track counters 0-7, P Status C-0 -> C-8: Phase error counters 0-7, P Card: DP IF Slot: A5 A3

5AF TEST5A Reject or Machine check from internal Forward-Space-Block command during read error recovery. See description of reject codes preceeding code E01.

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Code	Detected by	Fault DescriptionFRU'S
5B1	TEST5B	STATUS A-5 = 0: Drive not loaded
		STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where D-A Rx = contents of this status location) D-A at E-A at
5B2	TEST5B	Reject ør Machine check from initial rewind. See description of reject codes preceeding code E01.
5B3	TEST5B	Reject or Machine check from read backward. See description of reject codes preceeding code E01.
584	TEST5B	Data checks occurred on 5 retries of read backward operation. Check tape quality.
		** Read/Write Error Tallies **
		Status A-8: Data checksStatus A-C: Multi-trksStatus A-9: VelocityStatus A-D: Part. recd'sStatus A-A: End Data ChksStatus A-E: Un-corr.Status A-B: CorrectionsStatus A-F: CRC errors
		Status B-O -> B-8: Dead track counters O-7, P Status C-O -> C-8: Phase error counters O-7, P
		Card: DP RD Slot: A5 A6.
585	TEST5B	Reject or Machine check from forward-space-block command during read backward error recovery. See description of reject codes preceeding code E01.
586	TEST5B	Reject or Machine check from read forward. See description of reject codes preceeding code E01.

Code	Detected	Fault DescriptionFRU'S
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•	** Read/Write Error Tallies **
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	Status A-8: Data checksStatus A-C: Multi-trksStatus A-9: VelocityStatus A-D: Part. recd'sStatus A-A: End Data ChksStatus A-E: Un-corr.Status A-B: CorrectionsStatus A-F: CRC errorsStatus A-B: CorrectionsStatus A-F: CRC errors
	Status B-0 -> B-8: Dead track counters 0-7, P
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	Card: DP RD শকরেজ Slot: A5 A6
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588 TEST5B I	Reject or Machine check from backward-space-block command during read forward error recovery. See description of reject codes preceeding code E01.
5B9 TEST5B	Reject or Machine check from forward-space-file. See description of reject codes preceeding code E01.
5BA TEST5B	Reject or Machine check from backward-space-file. See description of reject codes preceeding code E01.
5BB TEST5B	Reject or Machine check from forward-space-block. See description of reject codes preceeding code E01.
5BC TEST5B	Reject or Machine check from backward-space-block. See description of reject codes preceeding code E01.
5BD TEST5B	Tape Mark status set when not expected indicating possible positioning problem. (Tape must have been written by TEST58) Card: DP

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Code	Detected by	Fault DescriptionFRU'S
5BE	TEST5B	Data miscompare indicating possible positioning problem. (Tape must have been written by TEST58). Card:#31DP Slot: A5
5BF	TEST5B	Tape Mark status not set when not expected indicating possible positioning problem. (Tape: must have been written by TEST58) Terd:endP Slot: A5
501	TEST5C	STATUS A-5 = 0: Drive not loaded
		STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)
5C2	TEST5C	Tape must be write enabled
5C3	TEST5C	Reject or Machine check from initial rewind. See description of reject codes preceeding code E01.
5C4	TEST5C	Reject or Machine check from internal write command.
		Status A-0,A-1: Low, high byte of record number. (Range: 000-100 hex) See description of reject codes preceeding code E01.
505	TEST5C	Write overrun status (6052) during internal write (data supplied to DP from IF card only).
		Status A-0,A-1: Low, high byte of record number. Range: 000-100 hex. Card: IF DP Slot: A3 A5

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Code	Detected	Fault DescriptionFRULS
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506	TEST5C	Bus parity error status (6052) during internal write (data supplied to DP from IF_{er} card only).
		Status A-0,A-1: Low, high byte of record number. (Range: 000-100 hex)
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		Card: IF DP
		SIDT: A3 A5
		1 5/6 7 11/30 -
5C8	TEST5C	Reject or Machine check from backmespace-block or erase-gap command during write error recovery. See description of reject codes in preceeding code E01.
509	TEST5C	During write error recovery, all 5 retries failed.
		Status A-0,A-1: Low, high byte of record number. (Range: 000-100 hex)
		Check tape quality.
		Card: DP WR RD IF Slot: A5 A4 A6 A3
5CA	TEST5C	Reject or Machine check from write-tape-mark command.
		Status A-0,A-1: Low, high byte of record number (first WTM occurs at 10 hex)

See description of reject codes preceeding code E01.

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Code	Detected by	Fault DescriptionFRU'S
5CB	TEST5C	During the writing of 256 GCR records, more than one temporary write error occurred.
		Status A-2:Total failing writesStatus A-3:Temporary write errors (1 data check in 6 attempts)Status A-4:Media defects (>1 data check in 6 attempts)
		** Read/Write Error Tallies **
		Status:A=8: Data checksStatus:A=C: Multi-trksStatus:A=9: VelocityStatus:A=D: Part. recd'sStatus:A=A: End Data ChksStatus:A=E: Un-corr.Status:A=B: CorrectionsStatus:A=F: CRC errors
		Status B-O -> B-8: Dead Track counters O-7,P Status C-O -> C-8: Phase error counters O-7,P
		Card: DP WR RD IF Slot: A5 A4 A6 A3
5D 1	TEST5D	STATUS A-5 = 0: Drive not loaded
		STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)
5D2	TEST5D	Reject or Machine check from initial rewind. See description of reject codes preceeding code E01.
503	TEST5D	Reject or Machine check from internal read forward. Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex) See description of reject codes preceeding code E01.

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5D6 TEST5D Unexpected tape mark (did not follow a 16 record group). Tape must have been written by TEST5C. Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex) Card: DP Slot: A5 A EW.

5D7 TEST5D Unexpected tape mark (more than 25% records read correctly, but tape mark did not follow 16 record group). Tape must have been written by TEST5C.

- Card: DP
- Slot: A5
- 5D9 TEST5D Data miscompare following read without data check (tape must have been written by TEST5C). Comparison involved 32 bytes of write buffer (8000-801F) and 32 bytes of read buffer (8020-803F). Data should match record number below:

Status A-0, A-1: Low, high byte of record number (Range: 000-100 hex)

Card: DP RD IF Slot: A5 A6 A3

5DB TEST5D Reject or Machine check from internal read forward over expected tape mark.

Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex) See description of reject codes preceeding code E01.

Code	Detected by	Fault DescriptionFRU'S
5DC	TEST5D	Tape mark status not detected when expected (but 16 record group read OK).
		Status A-0, A-1: Low, high byte of record number (Range: 000-100 hex, first TMK @ 10
	•	Card: [©] DP Slot: [®] A5
5DE	TEST5D	Failed internal record read with 5 retries.
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex)
		<pre>** Read/Write Error Tallies **</pre>
		Status A-8: Data checksStatus A-C: Multi-trksStatus A-9: VelocityStatus A-D: Part. recd'sStatus A-A: End Data ChksStatus A-E: Un-corr.Status A-B: CorrectionsStatus A-F: CRC errors
		Status B-O -> B-8: Dead track counters O-7, P Status C-O -> C-8: Phase error counters O-7, P
		Card: DP Slot: A5
5DF	TEST5D	Reject or Machine check from internal Back-Space Block command during read error recovery. See description of reject codes preceeding code E01.
5E 1	TEST5E	STATUS A-5 = 0: Drive not loaded
		STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)
5E2	TEST5E	Reject or Machine check from internal read backward operation (searching for EOF tape marks). See description of reject codes preceeding code EO1.
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Code	Detected	Fault DescriptionFRU'S
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5E3 TEST5E Reject or Machine check from internal read forward operations (searching for EOF tape marks) or read backward operations (if positioning around TMK's). See description of reject codes preceeding code E01. 5E4 TEST5E After finding 2 tape marks reading forward, read backward operation did not produce tape mark status. Card: DP Slot: A5 11 11 1 1 2 5E5 TEST5E Reject or Machine check from internal read backward. See description of reject codes preceeding code E01. 5E6 TEST5E ID-burst status (6053) detected before reading 256 records. Tape must have been written by TEST5C. Status A-0: record number (range: FF-00) Card: DP Slot: A5 5E7 TEST5E Data miscompare following read without data check (tape must have been written by TEST5C). Comparison involved 32 bytes of write buffer (8000-801F) and 32 bytes of read buffer (8020-803F). Data should match record number below: Status A-0: record number expected (range: FF-00) Card: DP RD 1 F Slot: A5 A6 A3 5E8 TEST5E Unexpected tape mark (did not follow a 16 record group). Tape must have been written by TEST5C. Status A-0: record number (range: FF-00) Card: DP Slot: A5

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Code	Detected by	Fault DescriptionFRU'S
5EE	TEST5E	Failed internal record read with 5 retries.
		Status A-0,A-1: Low, high byte of record number (Range: 000-100 hex)
		** Read/Write Error Tallies **
		StatusA-8: Data checksStatusA-C: Multi-trksStatusA-9: VelocityStatusA-D: Part. recd'sStatusA-A: End Data ChksStatusA-E: Un-corr.StatusA-B: CorrectionsStatusA-E: CPC errors
		Status A-B: Corrections Status A-F: CRC errors
		Status C-0 -> C-8: Phase error counters 0-7, P
		Card: DP IF Slot: A5 A3
5EF	TEST5E	Reject or Machine check from internal Forward-Space Block command during read error recovery. See description of reject codes preceeding code E01.
5F 1	TEST5F	STATUS A-5 = 0: Drive not loaded
		<pre>STATUS A-5 not 0: Speed switch reject code (see description for code Exx, where xx = contents of this status location)</pre>
5F2	TEST5F	Reject or Machine check from initial rewind. See description of reject codes preceeding code E01.
5F3	TEST5F	Reject or Machine check from read backward. See description of reject codes preceeding code E01.

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Code	Detected by	Fault DescriptionFRU'S
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5F4	TEST5F	Data checks occurred on 5 retries of read backward operation. Check tape quality.
		** Read/Write Error Tallies **
		Status A-8: Data checksStatus A-C: Multi-trksStatus A-9: VelocityStatus A-D: Part. recd'sStatus A-A: End Data ChksStatus A-E: Un-corr.Status A-B: CorrectionsStatus A-F: CRC errors
		Status B-O -> B-8: Dead track counters 0-7, P Status C-O -> C-8: Phase error counters 0-7, P
		Card: DP RD Slot: A5 A6
5F5	TEST5F	Reject or Machine check from forward-space-block command during read backward error recovery. See description of reject codes preceeding code E01.
5F6	TEST5F	Reject or Machine check from read forward. See description of reject codes preceeding code E01.
5F7	TEST5F	Data checks occurred on 5 retries of read forward operation. Check tape quality.
		** Read/Write Error Tallies **
		Status A-8: Data checksStatus A-C: Multi-trksStatus A-9: VelocityStatus A-D: Part. recd'sStatus A-A: End Data ChksStatus A-E: Un-corr.Status A-B: CorrectionsStatus A-F: CRC errors
		Status B-O -> B-8: Dead track counters O-7, P Status C-O -> C-8: Phase error counters O-7, P
		Card: DP RD Slot: A5 A6
5F8	TEST5F	Reject or Machine check from backward-space-block command during read forward error recovery. See description of reject codes preceeding code E01.

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Code	Detected by	Fault DescriptionFRU'S	
5F9	TEST5F	Reject or Machine check from forward-space-file. See description of reject codes preceeding code E01.	
5FA	TEST5F	Reject or Machine check from backward-space-file. See description of reject codes preceeding code E01.	
5FB	TEST5F	Reject or Machine check from forward-space-block. See description of reject codes preceeding code E01.	
5FC	TEST5F	Reject or Machine check from backward-space-block. See description of reject codes preceeding code E01.	
5FD	TEST5F	Tape Mark status set when not expected indicating possible positioning problem. (Tape must have been written by TEST5C) Card: DP Slot: A5	
5FE	TEST5F	Data miscompare indicating possible positioning problem. (Tape must have been written by TEST5C). Card: DP Slot: A5	
5F F	TEST5F	Tape Mark status not set when not expected indicating possible positioning problem. (Tape must have been written by TEST5C) Card: DP Slot: A5	
CO 1	FEIDLE	Test requested for execution does not exist in internal routine library.	

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Code	Detected	Fault DescriptionFRU'S
	by	

The following codes are not displayed on the panel as such, but referred to by other code descriptions in this dictionary. The Exx codes indicate rejects, where xx is the hex equivalent of reject codes normally returned to the host system. In this case they were actually returned to the internal diagnostics which has left them in a fixed status location:

Status A-5 = xx, see code Exx

E01 The subsystem is not in ready status.

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E03 During a write operation, TRAK was not returned within 75 milliseconds of TREQ.

Card: IF DP Slot: A3 A5

E05 File protect status was detected on a write request.

EO6 Erase status was not detected.

Card: WR IF Slot: A4 A3

EO8 Read status was not detected.

Card: WR IF Slot: A4 A3

E09 If read operation: No density status (DP card) within 5 inches. If write operation: DP interrupt during writing of ID track, or DP reject status during writing of ID track. Check tape quality.

> Card: DP WR Slot: A5 A4

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Cod	le Detected by	Fault DescriptionFRU'S	
EOC	:	Write status was not detected.	
		Card: WR IF Slot: A4 A3	
EOF		Noise detected during an erase gap or during a read/write command sequence.	
		Check tape quality.	
		Card: DP RD IF Slot: A5 A6 A3	
, E1 ⁻	1	Machine Check condition detected.	
		Status A-6 = Code of machine check: xx See description of code Fxx.	
E1;	3	Backward operation requested at BOT.	
El	ł	During the writing of ARA burst portion of tape ID, data path (DP) returned reject status.	
		Check tape quality.	
		Card: DP RD IF Slot: A5 A6 A3	
El	5	Blank tape: PE25 foot limit; GCR15 foot limit.	
EI	3	Following write of ID track at BOT, proper density status was not returned from data path (DP).	
		Card: WR DP SV IF Slot: A4 A5 A1 A3	
E 1 9)	LWR attemped with tape loaded and away from bot.	
. E 1	A	Subsystem failed to initiate tape motion.	
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Code	Detected by	Fault DescriptionFRU'S
E1B		During a read back check of a write operation, data was detected in the ibg area either before or after the record written.
		Check tape quality.
		Card: RD Read/Write head (feedthru) Slot: A6
F 1 D		Descend and found during a background and second
ETU		an incorrectly written record.
		Check tape quality.
		Card: DP RD IF Slot: A5 A6 A3
E1E		During a write from BOT, data path (DP) rejected ARA ID after successfully writing ID track and ARA burst.
		Check tape quality.
		Card: DP RD IF Slot: A5 A6 A3
E1F		No data detected during the read back check of a write or write tape mark command.
		Check tape quality.
		Card: DP RD IF Slot: A5 A6 A3

Code	Detected Fault DescriptionFR	U'S
F01	'TAPE PRESENT' not seen during thr	ead operation.
	lf blower inactive during load pro 1) Power supply high voltage (A 2) J18/P18 connector 3) Solid state relay (motherboa 4) Blower Motor	cedure, check: K card). rd).
	If not detecting tape in path (FIL accelerated): Card: WR EOT/BOT IF Slot: A4 Sensor A3	E reel never
FO2	Failed to load tape in three retri Check that leader is free.	es.
	If blower inactive during load pro 1) Power supply high voltage (A 2) J18/P18 connector 3) Solid state relay (motherboa 4) Blower Motor	cedure, check: K card). rd).
·	lf no FILE reel rotation: Card: SV AK Slot: Al Pwr Supply	
	If leader not detected (forward FI Card: WR Leader IF Slot: A4 Sensor A3	LE reel motion):
F03	Failed to sense leader during load Check that leader is free.	•
	If leader not detected (forward FI Card: WR Leader IF Slot: A4 Sensor A3	LE reel motion):
FO4	Leader status always asserted duri	ng load attempt.
	Card: WR Leader IF Slot: A4 Sensor A3	
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Code	Detected by	Fault DescriptionFRU'S
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F05		BOT not found during forward search.
		Check for reflective sticker on tape.
		Check EOT/BOT SENSOR connections.
		Card: WR IF
		Slot: A4 A3
F06		BOT failed to drop in expected time.
		Check EOT/BOT SENSOR connections.
		Card: WR IF
		Slot: A4 A3
F08		File protect status check was inconclusive.
		Check FILE PROTECT SENSOR connections.
		Card: WR IF
		Slot: A4 A3
F09		SENSOR ERROR (6060) status indicated.
		Check all sensor connections.
		Card: WR IF
		Slot: A4 A3
F 10		Swing arms not both retracted on load initiation.
		Check swing arm retraction tolerances.
F11		Swing arms still retracted after extend command.
		Check swing arm motor and mechanism for jams. Check motor drive cable connections.
		Card: SV WR IF
		Slot: A1 A4 A3
F 1 2		'INDEX' not detected during servoing of swing arms.
		Lard: WK IF SV

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Code	Detected by	Fault DescriptionFRU'S	
F13	LOAD	Swing arms failed to reach 'EXTENDED' status in 2 seconds.	
		<pre>If arms not in 'EXTENDED' position and no motion: Check retractor motor and mechanism for jams. Check retractor motor fuse (inline). Check power supply (+/- 24/36 volts). Card: SV IF Slot: A1 A3</pre>	
		If arms at 'EXTENDED' position: Check 'EXTENDED' switch and connections. Card: WR IF Slot: A4 A3	
F14	LOAD	From 'EXTENDED', failed to sense both swing arms in 'INDEX' area in 900 ms.	
		Check 'EXTENDED' switch function (probe 6061). If display (04 bit) does not change when EXTENDED switch is manually toggled:	
		Card: Extended WR IF Slot: Switch A4 A3	
		Check 'INDEX' from both sensors (probe 6061) by manually moving arms from 'EXTENDED' position. O1 bit represents MACH arm (upper) 'INDEX'. O2 bit represents FILE arm (lower) 'INDEX'.	
		lf neither bit toggles: Card: WR IF Slot: A4 A3	
		lf only 01 bit toggles: Card: WR FILE ARM/TACH Slot: A4 SENSOR	
		lf only 02 bit toggles: Card: WR MACH ARM/TACH Slot: A4 SENSOR	
		<pre>If NO swing arm motion in retract direction: Check power supply (+/- 24/36 volts). Card: SV IF Slot: A1 A3</pre>	
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Code	Detected by	Fault DescriptionFRU'S

F16 LOAD After sensing 'EXTENDED' (switch) and 'INDEX' active (rotary sensors) for both arms, failed to detect arms retracted (INDEX's reset then set again). Check 'EXTENDED' switch function (probe 6061). If display (04 bit) does not change when EXTENDED switch is manually toggled: Card: Extended WR IF Slot: Switch A4 A3 Otherwise (display does change): Card: SV WR IF File/Mach Slot: A1 A4 A3 Tach Asmbly F19 LOAD initiating a After manually controlled load (horizontally configured machine or double depression of "LOAD" push-button), 30 seconds expired without an operator indication to continue (moving tape leader into thread channel or an additional "LOAD" depression). Door must also be closed to continue. If blower inactive during load procedure, check: 1) Power supply high voltage (AK card). 2) J18/P18 connector 3) Solid state relay (motherboard). 4) Blower Motor If not detecting tape in path: Card: WR EOT/BOT IF Slot: A4 Sensor A 3 F20 Data path issued reject for unknown reasons. Card: DP 1 F Slot: A5 A3 F 30 Following the servoing of the swing arms, an unstable capstan was detected. Check for proper tape threading. Card: SV IF AK Slot: A1 A3 (Power Sply)

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Code	Detected by	Fault DescriptionFRU'S
F.40		Drive unable to return to BOT; EPO forced. Operator RESET or turn-around failure.
	-	Card: SV IF Slot: Al A3
F 50		Door interlock switch is indicating open door.
		Card: SV IF Slot: A1 A3
F 7 0		Data path interrupt failed to occur in expected time.
	÷	Card: DP IF Slot: A5 A3
F71		Position count interrupt failed to occur in expected time.
	•	Card: SV IF Slot: A1 A3
F80		Drive did not reach velocity in 10 ms.
		Card: SV IF Slot: A1 A3
F81		Turn around conditions not met within 1 second.
•		Card: SV IF Slot: A1 A3
F82		Failed to reach 'stop' condition in 10 ms.
		Card: SV IF Slot: A1 A3

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Code	Detected by	Fault DescriptionFRU'S
F 85		Acceleration or deceleration ramp out of spec.
		Card: SV LE
		Slot: A1 A3
F 90		Write or erase current on after read mode request.
		Card: WR IF
		Slot: A4 A3
F91		Write or erase current failed during write.
		Card: WR IF
		Slot: A4 A3
F92		Write current on while in erase-only mode.
		Card: WR IF
		Slot: A4 A3
F93		Erase current failure while in erase-only mode
		Card: WR IF
		SIOT: A4 A3
F96		Nonmaskable interrupt (NMI): Unknown or multiple.
		Card: SV IF
		SIOL: AI AS
F97		Nonmaskable interrupt (NMI): Power failure.
		Card: SV IF
		SIOL: AI AJ
F 98		Nonmaskable interrupt (NMI): Swing arms looped out.
		Card: SV IF
		SIOT: AI AJ
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Code Detected by Fault Description--FRU'S : · . F 99 Nonmaskable interrupt (NMI): Watch-dog timer expired SV IF A1 A3 Card: Slot: 100 EC 49908 97712