

MULTIMEMEORY

.Mutlibus<sup>tm</sup> Compatible for 8 Bit Processors

Sixteen 24 Pin Memeory Sockets

Memory Type Independently Configured for each socket, by header or by dip switch, for any of the following memeories: 2708, 2516/2716, 2532, or 2732 Eproms 2316, 2332 or 2332A Masked Roms 4118 or 4028 Static Rams

Memory Addresses Independently Assigned for each socket, by wire wrap jumpers to any multiple of 1K bytes within 64K address space

Up to 64K Bytes of Eprom or Rom Up to 32K Bytes of Static Ram

Inhibit Logic for each of Two Banks of 8 Memories Memory Access Time Wire Wrap Selectable Power Consumption: 2 Watts Plus Memories