

ARTEC ELECTRONICS, INC.

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MULTICIRCUIT PROTOTYPING BOARD

MULTIBUStm COMPATIBLE

105 x 48 0.1" grid pad array space for 130 16 pin wire wrap sockets compatible with any 0.1" grid package, such as hybrids, modules, etc.

3 User I/O edge connectors: 50 pin, 40 pin, 26 pin Mechanically compatible with 0.1" edge board connectors or header connectors

Ground plane on entire component side

Simple power distribution compatible with Lear Siegler pin bars

Solder pads for bypass capacitors

MULTIPOWER PROTOTYPING BOARD

Solder pads for standard 2" square DC-DC converters and associated bypass capacitors

Space for 120 16 pin wire wrap sockets

MULTIPURPOSE INTERFACE PROTOTYPING BOARD

- Complete buffering and control logic for multibus slave interface prototyping compatible with bus amster applications (such as 8257 DMA controllers)
- Fully buffered 16 bits address, 8 bits data simple extensions to 20 bits address, 16 bits data
- Switch selectable 12 bit address decoding (true, false, don't care), plus 1:8 on-board decoder
- Switch selectable I/O mapping or memeory mapping

Fully buffered interrupts

Space for 95 16 pin wire wrap sockets

MULTIPURPOSE INTERFACE/MULTIPOWER PROTOTYPING BOARD

Space for 90 16 pin wire wrap sockets