

# Matrix Computations in Basic on a Microcomputer

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This EPrint is a reissue of the 1985 technical report [1]. That report was published as [2] but without the appendices, which are

Appendix A.	Basic and Comal	29
Appendix B.	Summary of Machine and Language Specifications	32
Appendix C.	Commodore 64 Assembly Language BLAS Listing	39
Appendix D.	BBC Microcomputer Assembly Language BLAS Listing	46
Appendix E.	BBC Microcomputer SGEFA/SGESL Test Program	53
Appendix F.	CBM Comal-80 SGEFA/SGESL Test Program	56
Appendix G.	Amstrad CPC 464 Benchmark Program	59

Since the appendices contain material of historical interest that is not readily available elsewhere, it seems appropriate to re-issue it in the MIMS EPrint series. The following pages are scanned from the surviving original Epson dot matrix printout.

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# References

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- [2] Nicholas J. Higham. Matrix computations in Basic on a microcomputer. IMA Bulletin, 22(1/2):13–20, 1986.

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# MATRIX COMPUTATIONS IN BASIC ON A

# MICROCOMPUTER

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The views and opinions expressed herein are those of the author and not necessarily those of the Department of Mathematics.

#### ABSTRACT

We consider the efficient implementation of matrix computations in interpreted Basic on a microcomputer. Linear equations routines SGEFA and SGESL from the LINPACK library of Fortran programs are translated into Basic and run on four microcomputers: the Commodore 64, the Amstrad CPC 464, the BBC Microcomputer, and the BBC with a Z-80 second processor. The computational cost of the routines is found to be dominated by subscripting calculations rather than by floating point arithmetic. For the BBC Microcomputer and the Commodore 64, the BLAS routines which constitute the inner loops of SGEFA and SGESL are coded in assembly language; speed increases of factors 2.8 (BBC) and 5.3 (Commodore 64) accrue, and the improved execution times are comparable to ones which have been quoted for the more powerful and expensive IBM PC running under a Fortran compiler. The computational cost of the routines using coded BLAS is found to be dominated by floating point arithmetic, subscripting calculations and other overheads having been reduced to a negligible level, and it is concluded that these hybrid Basic/assembly language routines extract near optimum performance from their host machines. Our findings are shown to be applicable to any matrix routine whose computational cost can be measured in "flops".

Keywords: matrix computations, Basic, microcomputer, interpreter, assembly language, LINPACK, BLAS.

# CONTENTS

1 .	Intro	oduct	ion 1				
2.	Trans	ilati	ng Two LINPACK Subroutines into Basic 4				
2	Assen	nbly	Language BLAS 7				
	3.1	Theo	pretical Gains in Efficiency 7				
	3.2	Prac	tical Implementation 10				
<u>A</u> "	Test	Resu	ults 13				
5.	Bench	ımarl	ks for Matrix Computations 21				
6.	Concluding Remarks 27						
Appe	endix	Α.	Basic and Comal 29				
Appe	endix	Β.	Summary of Machine and Language Specifications 32				
App	endix	C.	Commodore 64 Assembly Language BLAS Listing 39				
App	⊇ndi×	D.	BBC Microcomputer Assembly Language BLAS Listing 46				
Арр	endix	E.	BBC Microcomputer SGEFA/SGESL Test Program 53				
Арри	endix	F.	CBM Comal-80 SGEFA/SGESL Test Program 56				
App	endix	G.	Amstrad CPC 464 Benchmark Program 59				
Ref	erence	25 (	60				

## 1. Introduction

Stewart (1981) describes his experiences in implementing a linear equations solver on three hand-held calculators. His routine for the Hewlett Packard HP-41C, coded in the machine's low level programming language, solved a system of linear equations of order 10 in 250 seconds. Dongarra (1984) gives a list of the times taken by various micro-, mini- and mainframe computers to solve a linear system of order 100 using standard linear equations software written in Fortran. The timings include one for the IBM PC microcomputer: this machine solved the 100x100 problem in 20 minutes.

For several years the present author has used in his research the Commmodore Pet and Commodore 64 microcomputers (Higham, 1984a, 1984b, 1984c), which in terms of cost and computing power lie between the hand-held calculators and the more powerful microcomputers such as the IBM PC. Unlike the calculators used by Stewart in Stewart (1981) the author's microcomputers run a high level programming language, Basic, but they are not equipped to run Fortran, the language of choice for scientific computation on large computers.

Consideration of the papers of Stewart and Dongarra led us to ask the following questions.

- (1.1) How should algorithms for matrix computations be implemented on a microcomputer in order to make the best possible use of the machine's processing power, if Basic is the only available high-level language?
- (1.2) What will be the dominant computational costs in implementations that answer question (1.1)?

-1-

(1.3) How can one make use of the rich supply of high quality

Fortran software when coding algorithms in Basic? We investigate these questions in this report.

In this work we experimented with four microcomputers: the Commodore 64, the Amstrad CPC 464, the standard BBC Microcomputer, and the BBC with a Z-80 second processor (we will regard the last two configurations as different machines). All the machines were used in their standard interpreted Basic programming environment; in addition the Commodore 64 was used with the Basic-related Comal programming language. For details of Basic and Comal, and an explanation of the differences between an interpreter and a compiler, see Appendix A and the references cited therein. The technical specifications of the four machines and of their particular language implementations are described in Appendix B.

At this point we pause to define two terms that we will use frequently in the following sections. Machine code (or machine language) is the collection of instructions that a microprocessor recognises and can execute as fundamental operations. To the microprocessor, a machine code instruction is simply a binary bit pattern that specifies an action to be performed. Assembly language is a low level language bearing a one to one relationship to machine code; it allows the use of mnemonics to refer to machine code instructions, and symbolic names (or labels) to refer to numeric values and addresses. The translation from assembly language to machine code is carried out by an **assembler**. Programming in assembly language is easier, less prone to error, and much less tedious than programming in machine code.

-2-

In sections 2 and 3 we describe the development of efficient hybrid Basic/assembly language translations of two standard Fortran subroutines for solving systems of linear equations. Section 4 presents and analyses the results of timing experiments carried out on the four test machines using the hybrid routines and, for comparison, the equivalent purely Basic versions.

In section 5 we introduce a set of benchmarks for interpreted Basics and apply them to the four test machines. The results obtained are used to gain insight into the results of section 4. Finally, in section 6 we summarise our findings in relation to questions (1.1), (1.2) and (1.3).

The view taken in this work is that one wishes to use the fastest and most accurate special-purpose algorithms available for solving on a microcomputer the problem at hand (cf. K. Stewart (1980)). This is the view that is naturally taken by a numerical analysis researcher who uses a microcomputer as a more convenient, easy-to-use substitute for a mainframe computer. An alternative approach, taken by Nash (1979, 1985), is to develop compact, versatile routines for small computers that are easy to implement and to maintain, and that can be used to solve a variety of computational problems; some loss of efficiency is accepted in return for the economies achieved. We believe that our findings concerning the efficiency of interpreted Basic programs could usefully be employed in enhancing the efficiency of the compact routines, such as those in Nash (1985), albeit with loss of machine independence.

-3-

#### 2. Translating Two LINPACK Subroutines into Basic

To investigate questions (1.1), (1.2) and (1.3), and to enable us to compare our experiments with those of Stewart and Dongarra, we decided to focus on the problem of solving a system of linear equations - probably the most fundamental and widely occurring problem in numerical linear algebra. We took as our starting point the routines SGEFA and SGESL in the LINPACK library of Fortran programs for analysing and solving linear systems (Dongarra, Bunch, Moler and Stewart, 1979). SGEFA performs LU factorisation of a matrix A, using a column oriented version of Gaussian elimination with partial pivoting, and SGESL uses the factorisation to solve a linear system Ax=b (Dongarra *et al.*, 1979, Ch. 1).

Consider the following outline of the factorisation algorithm used by SGEFA. Here A=(a1) is an nxn real matrix. Algorithm 2.1.

For k=1, ..., n-1

(2.1) Find the smallest  $r \ge k$  such that

la\_k = max { laik ! : i=k, ..., n}

Swap akk and ark

(2.2) For i=k+1, ..., n

```
Mik = -aik/akk
```

Endfor i

For j=k+1, ..., n

Swap aky and ary

(2.3) For i=k+1, ..., n

 $a_{ij} = a_{ij} + m_{ik} * a_{kj}$ 

```
Endfor i
```

Endfor j

Endfor k.

-4-

In the Fortran code SGEFA the loops (2.2) and (2.3), and the search (2.1), are executed by the Basic Linear Algebra Subprograms (BLAS) (Lawson, Hanson, Kincaid and Krogh, 1979). The BLAS are a collection of Fortran subprograms for carrying out various basic computations with vectors, including scaling a vector by a constant (SSCAL), searching for a component of largest absolute value (ISAMAX), and adding a constant times one vector to another vector (SAXPY). Note that it is because of Fortran's flexibility regarding the passing of array parameters to subprograms that the computations on the two-dimensional array A in (2.1), (2.2) and (2.3) can be accomplished by calls to the vector oriented BLAS.

In developing a Basic equivalent of SGEFA it is desirable to translate directly from the Fortran code, rather than to code from Algorithm 2.1. As well as reducing the programming effort this approach should ensure that nuances and subtleties in the Fortran coding that are not explicit in the algorithmic notation are carried over to the Basic version. In any case, for many Fortran codes, including some of the LINPACK routines, a fully detailed algorithmic description at the ar, element level is not readily available.

However, of the versions of Basic considered here only one supports procedures and this, BBC Basic, does not allow arrays to be passed as parameters. Therefore the BLAS and the calls to the BLAS cannot be translated directly into Basic. One way to overcome this difficulty is to replace the BLAS calls by the equivalent in-line code - as is done in some Fortran implementations of LINPACK (Stewart, 1977; Dongarra *et al.*, 1979, p. 1.23).

-5-

An alternative approach is to write the BLAS in assembly language; the BLAS calls can then be replaced by machinespecific Basic statements that pass control to the specially written machine code routines. This approach promises to achieve the dual aim of increased efficiency, since machine code generally runs much faster than interpreted Basic code and the bulk of the computation in SGEFA is done inside the BLAS. In fact it is true for most of the LINPACK routines that if the total number of assignments, array element references and floating point additions and multiplications is  $O(n^q)$  (q=2, 3), then only  $O(n^{q-1})$  of these operations are performed outside the BLAS.

We have tried both approaches towards translating the BLAS. In section 4 we compare the performances of programs based on the two approaches. But first, in the next section, we examine in detail the theoretical and the practical aspects of coding the BLAS in assembly language for use with a Basic interpreter on a microcomputer.

-6-

## 3. Assembly Language BLAS

## 3.1 Theoretical Gains in Efficiency.

Before describing the details of coding the BLAS in assembly language we first consider what we can hope to achieve by using these special BLAS with an interpreted Basic.

One of the characteristics of the 6502 and Z-80 central processing units (CPUs) of our test machines is that their instruction sets do not contain a multiply operation; therefore all four machines must carry out floating point arithmetic in software. The four Basic interpreters contain individual collections of floating point arithmetic subroutines and, under the reasonable assumption that these routines are efficiently coded, it is sensible to attempt to make use of these routines in the assembly language BLAS. In addition to simplifying the programming effort this approach should ensure that the coded BLAS perform, bitwise, precisely the same arithmetic (and hence sustain precisely the same rounding errors) as would their inline Basic equivalents. However, since in this way the very same floating point calculations are performed in the coded BLAS as in the equivalent Basic, it is not immediately clear what gains in efficiency the coded BLAS will engender. To investigate this question consider the inner loop (2.3) in Algorithm 2.1. When translated to Basic from its Fortran implementation in SGEFA this loop takes the form

(3.1) FOR I=K+1 TO N

A(I,J)=A(I,J)+T\*A(I,K)

NEXT I.

When this loop is executed in an interpreted Basic the main computational costs, over and above the inherent floating point

-7-

arithmetic, are incurred when the following tasks are performed.

- Parse the source code, to determine the operations to be performed.
- (2) Set up the I loop (this involves initialising the loop variable, and evaluating the upper and lower loop limits and the STEP, which defaults to 1), then repeatedly increment the loop variable, test against the upper limit and jump to the start of the loop as necessary.
- (3) Search for the simple variables I, J, K, N, T and the array A in the (dynamically allocated) storage area.
- (4) Evaluate the address in storage of the array elements

A(I,J) and A(I,K), that is, perform subscripting. Note that the Basic interpreter will carry out operations (3) and (4) during every execution of the second statement in the loop.

With the use of assembly language BLAS these overheads to the floating point arithmetic can effectively be removed. To see why, consider, for example, CBM Basic. In this Basic a SYS command can be used to pass control to a machine code routine. Thus the command SYS SAXPY calls the machine code routine at the address held in the variable SAXPY. Unlike the other three Basics, CBM Basic ostensibly does not provide for the passing of multiple parameters to a machine code routine. However it is possible to emulate such a facility by using a nonstandard SYS command of the form

SYS SAXPY, N-K, T, A(K+1,K), A(K+1,J).

This syntax is accepted by the interpreter and control is passed to the SAXPY routine. The routine can pick up the value N-K, the address of the variable T, and the addresses of the elements A(K+1,K) and A(K+1,J), by calling expression evaluation and variable address search routines in the Basic interpreter. Using this parameter information the machine code routine can itself effect the computations implied in (3.1), making direct calls to the interpreter's floating point arithmetic routines.

Clearly, overhead (1) is removed, since the interpretation is done by the programmer when writing the assembly language. Overhead (3) becomes negligible for large N-K, because the searching for variables is done only once, at the start of the machine code routine, rather than every time a variable is encountered on executing the loop interpretively. Overhead (2) is now insignificant because the integer addition and comparison operations involved in the looping are fundamental operations for the microprocessor, and these operations are no longer being performed interpretively.

Finally, and most importantly, overhead (4) is greatly reduced, for only two full subscripting calculations are required: those which evaluate the addresses of the array elements in the SYS statement. Thereafter, the assembly language routine can take advantage of the known, constant increment between the addresses in storage of the array elements which must be accessed successively. In CBM Basic arrays are stored by column, and floating point numbers occupy five bytes of storage, so the constant increment between the addresses of A(K+1,J), A(K+2,J), ..., A(N,J) in (3.1) is five bytes.

The above considerations suggest that assembly language BLAS will be appreciably more efficient than the equivalent Basic code, through the reduction to a negligible level of the overheads associated with the floating point arithmetic.

-9-

We wish to emphasise that the above discussion is applicable only to *interpreted* Basics. In a compiled Basic (or Fortran) environment, where the compiler itself may generate assembler code or machine code, assembly language BLAS may be no more efficient than the compiled equivalent source code - this behaviour was observed using Fortran in Lawson *et al.* (1979), for example.

#### 3.2 Practical Implementation.

In order to write assembly language BLAS for a particular microcomputer one needs two main tools. The first is an assembler. Good assemblers are available for each of the four microcomputers; see Appendix B.

The second tool is documentation for the floating point arithmetic routines in the Basic interpreter. One needs to know details of the routines for

- loading and storing the floating point accumulator (the work area in which floating point arithmetic is performed by the Basic interpreter),
- performing floating point addition and multiplication,
- calculating the absolute value and the square root,
- comparing two floating point numbers.

It is also necessary to determine whether arrays are stored by column or by row, how many bytes each floating point number occupies, and which memory locations can safely be used for temporary storage (of pointers and intermediate sums, for example) without affecting the subsequent operation of the Basic interpreter. We have been able to find this "inside information" for two of the four machines: the Commodore 64 (West, 1982; Bathurst, 1983) and the BBC Microcomputer (Pharo, 1984). In both cases the information was obtained from sources independent of the manufacturer. Given the competitive nature of the microcomputer industry it is not surprising if the manufacturers are unwilling to publish technical details concerning the inner working of their Basic interpreters.

We have written a subset of the BLAS in 6502 assembly language for the Commodore 64 and for the BBC Microcomputer; we hope to repeat the exercise for the Z-80 machines if and when the necessary documentation becomes available. We based the routines on the Fortran BLAS listings in (Dongarra *et al.*, 1979), but we did not "unroll" the loops. Since all calls to the BLAS in LINPACK have "INCX=INCY=1" (Dongarra *et al.*, 1979, p. A1) we assumed these values for INCX, INCY instead of treating them as parameters.

The coding for the Commodore 64 presented no major difficulties, since the author was already familiar with the intricate CBM Basic interpreter. A partial listing of the assembler code (for routines SASUM, SAXPY, ISAMAX and SSCAL only) is given in Appendix C. Complete understanding of the code requires a good knowledge of 6502 assembly language, but the informed reader should be able to follow the broad outline using the information given in comment lines.

We were able to use very similar coding for the BBC version of the BLAS. However, a problem was encountered, for BBC Basic stores arrays by rows. Thus the increment between the addresses of A(I,J) and A(I+1,J) depends on the array dimension; in fact, assuming that A is dimensioned DIM A(N,N), the increment is 5\*(N+1), since each element occupies 5 bytes and BBC Basic subscripts start at zero. This difficulty could be

-11-

overcome by coding the BLAS in exactly the same way as for the Commodore 64, so that the BLAS access in succession contiguously stored array elements, and by re-writing SGEFA and SGESL so as to generate sequential access across the rows of A, instead of down the columns. Instead however, to avoid changing SGEFA and SGESL, we decided to treat the address increment as a "global" parameter. The BBC BLAS assume that the increment between the addresses of the array elements to be accessed successively is given by the value of the static integer variable M% (static variables, whose address is fixed, are peculiar to BBC Basic). Thus a BLAS call with one-dimensional array parameters should be preceded by the assignment M%=5, while for two-dimensional arrays the required assignment is M%=5\*(N+1). This simple approach does not permit a BLAS call with both one- and twodimensional array parameters; to avoid this limitation we stored the right-hand side vector b (which is manipulated by the solve routine SGESL) in the otherwise unused, zero'th column of A. The BBC Basic program which we used to generate and test the BBC BLAS is listed in Appendix D.

-12-

# 4. Test Results

In this section we give the results of tests carried out on the four microcomputers using Basic translations of LINPACK's SGEFA and SGESL, using both in-line BLAS and assembly language BLAS (for the machines for which these were written).

Because of the nature of interpreted Basic, many factors influence program performance (that is, execution times), and the degree of influence varies from one Basic to another. Some example factors are the following.

- (1) The order (with respect to program flow at run time) of first use of variables, and of declaration of arrays. In CBM Basic the access times are fastest for the earliest defined variables or arrays, but in Locomotive Basic (on the Amstrad CPC 464) the access time is independent of the order of definition.
- (2) The use of multi-statement lines. A given program will usually run faster if the number of distinct lines in the source code is reduced - by combining lines wherever possible.
- (3) The presence of spaces and REM (remark) statements. The interpreter has to scan over spaces and REMs, so their presence in frequently executed sections of the code can have an adverse affect on run times.
- (4) In some Basics (for example, BBC Basic and Locomotive Basic), expressions involving variables of only integer type are evaluated more rapidly than the corresponding expressions containing floating point variables. In other Basics (including CBM Basic and CBM Comal) the converse is true, because integer arithmetic is not supported and so

-13-

integer values must be converted to floating point before a numeric expression can be evaluated.

Clearly, then, it is difficult to compare the performance of one interpreted Basic with another, even if the same program can be run unaltered under both Basics: aspects of the code which are beneficial to the performance of one Basic may be detrimental to the performance of the other.

In our tests we have endeavoured to ensure that each Basic is treated "fairly". The translation of SGEFA and SGESL was carried out first into CBM Basic and thence into the other three Basics and Comal, with care taken to ensure that the five different codings were as similar as possible, particularly with respect to factors (1), (2) and (3) above. The only major difference between the five implementations concerns factor (4): in all except the CBM Basic and Comal versions integer variables were used where appropriate. Since our purpose is not essentially to compare the performance of different Basics, we believe that our limited efforts at optimising the code for each Basic are justified.

The two BBC Microcomputer versions of SGEFA and SGESL, the first with in-line BLAS and the second with calls to the assembly language BLAS, are listed in Appendix E together with the test program in which they were used. For each machine our approach was to time the execution of SGEFA and SGESL for n=5, 10, 20, ..., using random A and b. The elements of A and x were generated as pseudo-random numbers in the interval [-1,1], using whatever random number generator the Basic provided, and the right-hand side b was formed as b=Ax. The error in the computed solution was monitored to ensure that the

-14-

routines were working correctly. The machines' built-in clocks were used to time the routines; the units in which the clocks count vary from 1/60 th of a second (Commodore 64) to 1/300 th of a second (Amstrad CPC 464), so we quote the times to one decimal place at most.

Only one linear system was solved for each n. A separate experiment, on the Commodore 64, in which for fixed n several seeds were used for the random number generator produced timings varying by only a few percent, so we believe our approach of using only one random matrix for each n produces reliable results.

The results are reported in Tables 4.1 and 4.2. "Coded BLAS" denotes the use of assembly language BLAS. The blank entries in the tables correspond to values of n which were too large for the available memory space.

We offer the following comments and observations on Tables 4.1 and 4.2.

(1) The SGESL timings are insignificant, for large n, compared to those of SGEFA. This is to be expected since the total counts of floating point operations, array element references and assignments for the two routines are of orders  $n^2$  and  $n^3$  respectively.

(2) In every case the 10x10 system was solved in less than 11 seconds. This compares to the 250 or more seconds required by the hand-held calculators in Stewart (1981) to solve a problem of the same size, and gives some indication of the difference in processing power between these two classes of machine.

-15-

N			CBM 64 Coded BLAS					
5	5	1.33	0.75	1.23	0.39	0.26	0.54	0.83
10		8.90	3.43	7.92	2.47	1.26	3.25	4.39
20		62.6	17.2	53.6	18.0	7.63	23.7	29.5
30	5 5	202	47.3	170	58.9	22.8	76.1	94.9
4Ø	8	466	99.9	392	137	51.3	177	219
50		896	181	-	266	96.3	341	422
60		1535	298	-	458	162	584	722
7Ø		2416	455	-	-	-	922	1140
80		-		-	-	_	1371	1694
90		_	_	-	-	-	1946	-

Table 4.1. SGEFA timings in seconds.

Table 4.2. SGESL timings in seconds.

	2		CBM 64 Coded BLAS	Comal		Coded BLAS		
5		0.57	0.38				0.22	0.34
10		1.97	0.93	1.75	0.56	0.39	0.76	1.03
20	8	7.18	2.53	6.30	2.11	1.20	2.86	3.59
30	1	15.6	4.75	13.7	4.66	2.39	6.22	7.76
40	-	27.2	7.58	23.8	8.16	4.02	10.9	13.5
50	8	42.1	11.0	-	12.6	6.00	16.9	20.9
60	1	60.1	15.0	-	18.1	8.39	24.2	29.7
7Ø	1	81.2	19.8	-	_	-	32.7	40.4
80	1	-	_	-	-	_	42.6	52.4
90				-	-	-	53.8	-

(3) Consider the tabulated times for the pure Basic, inline BLAS versions of SGEFA and SGESL. According to the results shown, the BBC Microcomputer is fastest by a significant margin. The following ratios of execution times hold, approximately.

(a) Commodore 64 / BBC = 3.4,
(b) Amstrad CPC 464 / BBC = 1.6,
(c) BBC Z-80 / BBC = 1.3.

The first ratio might be considered surprisingly large, given that the Commodore 64 and the BBC Microcomputer use essentially the same microprocessor. The ratio can partly be explained by the fact that the BBC's 6502 microprocessor runs at twice the clock rate of the Commodore's 6510 (though it is not clear to us whether doubling the clock speed on a given machine should, in theory, halve the run times). Furthermore, it appears that BBC Basic for the 6502 was written with speed of program execution as a prime consideration. Ratios (b) and (c) provide an interesting comparison between the performance of the 6502 and the Z-80 CPUs, especially as BBC Basic for the Z-80 has a nearly identical specification to standard BBC Basic for the 6502.

(4) The speed up ratios resulting from the use of assembly language BLAS in SGEFA are given in Table 4.3. The "asymptotic" speed up ratios of 5.3 and 2.8, for the Commodore 64 and the BBC Microcomputer respectively, are very pleasing and provide excellent justification for the effort expended in coding the BLAS. The reason for these differing improvements in execution speed, and the efficiency relative to the theoretical optimum of

-17-

the routines using the coded BLAS, are examined in the next section.

Table 4.3. Speed up ratios for SGEFA.

N		3	5	10	20	30	4Ø	50	60	7Ø
CBM	64	1	1.8	2.6	3.6	4.3	4.7	5.0	5.2	5.3
BBC			1.5	2.0	2.4	2.6	2.7	2.8	2.8	-

(5) The quoted timings for CBM Comal are roughly 16% faster than those for CBM Basic. However, in the Comal versions of SGEFA and SGESL we used a special (and very convenient) feature of Comal which allows an assignment statement of the form S:=S+T to be replaced by the shorthand form S:+T. For example, we coded A(I,J):=A(I,J)+T\*A(I,K) as A(I,J):+T\*A(I,K) (see the listings in Appendix F). When we changed the shorthand assignments back into the longer form the Comal timings increased by approximately 30% and they then exceeded the Commodore 64 Basic timings by 11%. This 30% increase in execution time can be explained by the fact that the short form involves one less subscripting operation than the long form; see the timing results in the next section. Clearly, when applied to array element expressions, the shorthand form S:+T is a very effective tool for increasing the efficiency of programs for matrix computations in CBM Comal.

(6) In the Commodore 64 and BBC Microcomputer tests the computed solutions returned by the routines using the coded BLAS were in every case identical to those returned by the purely Basic routines. This confirms our expectation that the assembly language BLAS would perform precisely the same arithmetic as the in-line, Basic BLAS.

-18-

We have used the test results to estimate the times that would be required to solve a linear system of order 100 were the test machines able to accommodate systems of this order. The n=100 times were obtained by extrapolating on the times for the largest value of n available:

 $t_{100} = (100/n)^3 t_n (SGEFA) + (100/n)^2 t_n (SGESL).$ 

In Table 4.4 we compare these estimates with five actual timings given in Dongarra (1984); Dongarra's timings were obtained using standard Fortran versions of SGEFA and SGESL. Three mainframe computer timings are included to help to put the performance of the microcomputers into perspective.

Table 4.4.	Estimates	of	tn (SGEFA)	+	tn(SGESL)	for	n=100.

Machine	l Seconds
CBM 64	, 7209 (120 mins)
Amstrad CPC 464	3390 (56 mins)
Apple III Pascal compiler	1   2813 (47 mins) 
BBC Z-80	(46 mins)
BBC	2171 (36 mins)
CBM 64 Coded BLAS	:   1367 (23 mins) 
IBM PC Microsoft Fortran 3.1 compiler	:   1225 (20 mins)   
BBC Coded BLAS	:   773 (13 mins) 
VAX 11/780 Fortran VMS compiler	4.13
CDC 7600 Fortran FTN compiler	0.21
CDC Cyber 205 Fortran FTN compiler	: 0.082 :

We note from Table 4.4 that the BBC Microcomputer, using coded BLAS, is, in these experiments, 37% faster than the IBM PC running under a Fortran compiler, and that the Commodore 64 with coded BLAS is only 12% slower than the IBM PC. These comparisons surprised us, because the IBM PC uses an Intel 8088 CPU, which, in contrast to the 8-bit 6502 and Z-80 CPUs, is a 16bit processor, and the 8088 contains multiply and divide instructions: in other words, the 8088 is a substantially more powerful processor than the 6502 or the Z-80.

## 5. Benchmarks for Matrix Computations

To help to explain the results of section 4 and to gain further insight into them, we have developed a set of benchmarks for interpreted Basics which measure the computational costs of floating point arithmetic and subscripting calculations. Our method is to time a small, carefully chosen, set of Basic statements and to extract the desired information by differencing the timings. Timings have been obtained for each of the four Basics, and Comal, using the test program listed in Appendix G.

The test program times the execution of a loop (lines 170-210) whose core is a line consisting solely of a colon (the statement separator in Basic). Then a similar loop (lines 250-290), in which the colon is followed by a single statement, is timed. The difference between the two times is the time required to execute the statement, multiplied by the total loop count. This technique for timing the execution of a statement in an interpreted Basic is described in West (1982, p.16). The colon is necessary because we need to account for the time required to process the line number of the line on which the statement stands, and this timing cannot be obtained directly because in Basic a line number may not be followed by an empty line.

The tests are based on statements involving variables that have earlier in the program been assigned random values. We have found that in the Basics tested, the execution times for floating point operations depend on the arguments; however we believe the timings obtained with random arguments to be representative.

-21-

The statements used in line 270 of the test program, and the times for execution of the statements within the loop, are tabulated for the four machines in Table 5.1. Note that these times should be divided by the loop count,  $n^2=25^2$ , to obtain the time for a single execution of the statement. Also tabulated are differences which can be expected to provide good general estimates of the time required to perform one- and twodimensional subscripting and the three arithmetic operations. For example, the difference between the times for the statements T=R+S and T=R approximates the time which is required for a floating point addition, once the operands have been evaluated.

Statement	CBM 64	CBM 64 Comal			
(a) T=R	0.90	0.98	0.34	0.45	0.42
(b) T=R+S	1.78	1.91	0.68	0.91	1.00
(c) T=R*S	2.93	3.07	1.51	1.45	1.63
(d) T=R/S	3.18	3.32	1.63	1.67	1.88
(e) T=B(I)	2.23	2.38	0.55	0.89	1.02
(f) T=A(I,J)	3.52	4.08	0.90	1.30	1.52
(g) A(I,J)=A(I,J)+R*A(K,J)	11.87	13.28	3.59	4.47	5.54
t <sub>+</sub> = (b)-(a) ~ '+'	0.88	0.93	0.34	0.46	0.58
t <sub>*</sub> = (c)-(a) ~ '*'	2.03	2.09	1.17	1.00	1.21
t, = (d)-(a) ~ '/'	2.28	2.34	1.29	1.22	1.46
$t_{1} = (e) - (a) \sim (I)$	1.33	1.40	0.21	Ø.44	0.60
$t_{1J} = (f) - (a) \sim (I,J)$	2.62	3.10	0.56	0.85	1.10

Table 5.1. Times in seconds for 625 executions of a Basic statement.

Much useful information can be gleaned from Table 5.1. First, consider statement (g). The time required to execute a statement of this form on a particular computer system, and in a particular programming language, is termed a **flop** (Golub and Van Loan, 1983, p.32). Single statements of the form of statement (g) form the nucleus of the innermost loops of SGEFA and SGESL (see the listings in Appendix E), and are executed  $n^3/3 +$   $O(n^2)$  and  $n^2 + O(n)$  times respectively; thus we might expect the execution times of the pure Basic versions of SGEFA and SGESL to be well approximated, for large n, by  $n^3/3$   $t_{flop}$  and  $n^2 t_{flop}$  respectively, where  $t_{flop}$  is the time for a single execution of statement (g). This is indeed the case, as is shown by Table 5.2.

#### Table 5.2.

	tn(SGEF	A)/(n <sup>3</sup> /3 t <sub>flop</sub> )	F E	t <sub>n</sub> (SGE	SL)/(n² t <sub>flop</sub> )
N	. 30	60	1	30	60
CBM 64	1.18	1.12	1	0.91	0.88
BBC	1.14	1.11	8 8 1	0.90	Ø.88
BBC Z-80	1.18	1.13	1	Ø.97	0.94
Amstrad CPC 464	i 1.19	1.18	1	0.97	0.93

(The SGEFA estimates are overestimates bacause they ignore the  $O(n^2)$  parts of the computations. The SGESL estimates are underestimates because  $t_{flop}$  is based on two-dimensional subscripting, whereas the SGESL flop invloves less expensive, one-dimensional subscripting.)

-23-

Thus in the microcomputer Basics tested here, the time required for solution of a linear system by Gaussian elimination is proportional to the flop time. We now look more closely at the component computational costs in a flop.

Consider statement (g) in Table 5.1. The main tasks to be performed when evaluating this statement in an interpreted Basic are the following:

- parse the statement and evaluate the addresses in storage
  - of A and R, then carry out
- three two-dimensional subscripting operations,
- one floating point multiplication,
- one floating point addition.

We can use the timings t(g),  $t_{1J}$ ,  $t_*$  and  $t_*$  in Table 5.1 to express the cost of these tasks as a percentage of one flop.

#### Table 5.3. Components of a flop.

	Subscripting	Multiplication	Addition	Parse/Addr.
CBM 64	66%	17%	7%	10%
CBM 64 Comal	70%	16%	7%	7%
BBC	47%	33%	9%	11%
BBC Z-80	57%	22%	10%	11%
Amstrad CPC 464	60%	22%	10%	8%

Table 5.3 shows that in all five Basics the largest single contribution to a flop comes from subscripting calculations, this contribution varying from 47% in BBC Basic to 70% in CBM Comal. In every case the floating point arithmetic accounts for less than half a flop, with variation between 23% in CBM Comal and 42% in BBC Basic. We conclude that in solving a linear system on our test machines, using Basic translations of SGEFA and SGESL with inline BLAS, the dominant computational cost is *subscripting*: it accounts for between one half and two thirds of the execution time.

To see why subscripting calculations can be so expensive we examined a dissassembly of the CBM Basic interpreter (Bathurst, 1983). In outline, the interpreter performs the following actions to evaluate A(I,J), assuming A has been dimensioned DIM A(N,N). First, the base address of the array A is calculated, by searching through the array table. Next the two subscripts are evaluated, using a general purpose "evaluate floating point expression" routine, and these floating point values are converted to 4-byte integers, with checks for out-ofbounds subscripts. The offset of the element A(I,J), in terms of the number of array elements, is evaluated as I+(J-1)\*(N+1), and the offset in bytes is calculated by multiplying the result by 5 (the length of each array element). These two multiplications are carried out by a general purpose 16-bit integer multiplication routine, so special advantage is not taken of the operand 5. It appears, then, that CBM Basic's relative inefficiency at subscripting is due, at least in part, to its failure to take advantage both of integer subscripts (when these are present) and of the simple form of the operand 5 in the second 16-bit multiplication.

We now use Table 5.3 to explain the speed up ratios in Table 4.3. As explained in section 3, the use of assembly language BLAS effectively removes the overheads to the floating point arithmetic in evaluating statement (g) in Table 5.1. Thus,

-25-

assuming that for large n the execution times for the routines using coded BLAS are proportional to the time for an "assembly language flop", we can predict the speed up ratios, using Table 5.3, as follows.

> CBM 64 : 100/24 = 4.17 BBC : 100/42 = 2.38

Comparing with Table 4.3 we see that the predictions are reasonably good, though, perhaps surprisingly, they are somewhat pessimistic for large n.

Our findings about computational cost, and about speed increase with the use of coded BLAS, are applicable not only to the Gaussian elimination algorithm, but to any other algorithm for matrix computations whose cost can reliably be measured in flops (most of the algorithms in LINPACK, for example). We conclude that for flop dominated matrix algorithms the use of assembly language BLAS will induce near optimum machine performance on the two microcomputers for which they have been written, for the dominant computational cost in such implementations will be that associated with the floating point arithmetic, and this arithmetic is performed using machine code routines from within the Basic interpreter which we assume are efficiently coded.

-26-

# 6. Concluding Remarks

We have shown that it is feasible to translate Fortran subroutines from the LINPACK library (Dongarra *et al.*, 1979) into Basic, so that they can be used on those microcomputers for which Basic is the standard programming language. Two approaches to translating the BLAS were considered. The first was simply to replace the BLAS calls by the equivalent in-line Basic code. We found that in the resulting pure Basic programs the dominant computational cost is subscripting, rather than floating point arithmetic.

The second approach was to code the BLAS in assembly language and to make use of machine dependent features in the Basic which allow a machine code subroutine to be called and multiple parameters to be passed. This was done for the Commodore 64 and the BBC Microcomputer. On the Commodore 64, for n=70, the Basic version of SGEFA which uses assembly language BLAS runs 5.3 times faster than the version using inline BLAS. On the BBC Microcomputer, for n=60, the corresponding speed increase is 2.8. While speedy program execution is not necessarily a prime requirement when solving problems numerically on microcomputers (Nash, 1985), these substantial increases in efficiency are well worth having if computations with long run times are to be performed.

Importantly, we have seen that the versions of SGEFA using assembly language BLAS and running under interpreted Basic produce near optimum machine performance, in the sense that their computational cost is dominated by the cost of the inherent floating point arithmetic. In other words, even if the whole SGEFA subroutine were to be coded in assembly language (a

-27-

formidable task!) the resulting gains in efficiency over the Basic program using coded BLAS would be relatively small.

We conclude that for programming matrix computations in interpreted Basic on a microcomputer, a carefuly coded set of assembly language BLAS is a very useful tool. Its use facilitates the translation into Basic of Fortran programs which use the BLAS (such as those in LINPACK), and at the same time enables the translated programs to make efficient use of the available processing power - something that cannot usually be achieved when working with a Basic interpreter. Furthermore, the assembly language BLAS enable the programmer coding in Basic directly to enjoy the benefits of using simple, one-line BLAS calls to perform basic vector operations: careful use of the BLAS can produce greater modularity and improved readability of programs (cf. Appendix D).

#### Acknowledgements

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As an experiment this report was produced on a Commodore 64, using the wordprocessor Vizawrite 64 and an Epson FX-80 printer.

-28-

#### Appendix A: Basic and Comal.

#### Basic.

The Basic programming language was invented by J.G. Kemeny and T.E. Kurtz at Dartmouth College, New Hampshire in 1964. The language was designed for use by novice programmers in an interactive, time-sharing environment, but the range of usage of Basic has expanded beyond this originally intended audience. Basic is available on many mainframe computers and is the principal language on most low cost microcomputers, often being permanently stored in read only memory.

Disappointingly, Basic suffers from a lack of standardisation. Although there exists an ANSI standard (ANSI, 1978), few Basics adhere to it, and in general, a program written in one version of Basic will require modification to enable it to run in another.

Loosely, Basic can be described as a simplified subset of Fortran. Some of the major differences between Basic and Fortran are as follows. (These comments are not applicable to all Basics; for example BBC Basic supports procedures with local variables - see Appendix B.)

- There are no statement numbers in Basic, so GOTO is directed to a line number.
- (2) Named, program independent subroutines with parameter passing are not supported in Basic. Subroutines are called by line number, as in GOSUB 100, and an exit point is marked with RETURN, as in Fortran.
- (3) All variables are global to the whole program in Basic. A numeric variable is by default of type real unless its identifier is terminated by the % character, which denotes

integer type (though not all Basics support integer variables). Identifiers are often restricted to two characters in length.

- (4) Multi-statement lines are allowed in most Basics, the statement separator being a colon (usually).
- (5) If the condition in an 'IF condition THEN...' statement is false, then the rest of the (generally multi-statement) line is ignored.

Excellent references for Basic are the books by Kemeny and Kurtz (1980) and Alcock (1977). Other useful references include Lientz (1976) and Genz and Hopkins (1980), both of which contain comparisons between different dialects of Basic, and Brown (1979).

The four microcomputer Basics that we have used in this work are interpreted rather than compiled. The major way in which a compiler differs from an interpreter is that a compiler translates the source code into machine language (perhaps via assembly language) before the program is executed; it is this machine language translation that is executed by the CPU. In contrast, an interpreter translates the source code during execution of the program: each statement is translated as and when it is encountered. If a statement is executed n times, then an interpreter will translate it n times, whereas a compiler will translate it only once, in the initial compilation phase. See Brown (1979, p. 38) for further details on the differences between compilers and interpreters. Generally, a given program on a fixed computer can be expected to run faster under a compiled Basic than under an interpreted Basic. The principal reasons for most microcomputer Basics being

-30-

interpreted are that a Basic interpreter lends itself more readily to interactive programming, is more convenient to use, and is usually more economical in its use of memory space, than a Basic compiler.

#### Comal.

Comal was developed by B.R. Christensen and B. Loefstedt in Denmark in 1973. Comal can be thought of as a hybrid between Basic and Pascal: it combines the interactive nature and simple syntax of Basic with the structured programming features (but not the data structures) of Pascal. Specifically, most Basic commands and intrinsic functions are supported, but to these are added the following features (among others):

long variable names, procedures and multi-line functions with full parameter passing, WHILE-ENDWHILE and REPEAT-UNTIL loops, global IF-THEN-ELSE-ENDIF and a CASE statement.

Comal appears to be relatively little known, compared to Basic, outside Denmark. Public domain versions of Comal for Commodore computers are distributed by the Independent Commodore Products User Group, England, and the Comal User Group, U.S.A. Implementations which run under the CP/M operating system are available commercially.

Good references for Comal are Lindsay (1983), which documents CBM Comal-80, and Atherton (1982).

### APPENDIX B: Summary of Machine and Language Specifications.

The purpose of this appendix is to summarise the technical details of the test machines and their Basic or Comal language implementations.

All four machine configurations use one or both of the MOS Technology (now Commodore Semiconductor Group) 6502/6510 and the Zilog Z-80 microprocessors. Both microprocessors have an 8-bit data bus and a 16-bit address bus; consequently, the basic unit of data on which the processors act is one byte (8 bits) and the maximum amount of addressable memory is 64K bytes, where 1K byte =  $2^{10}$  = 1024 bytes. Neither processor contains a hardware multiplier.

The memory map of each machine contains a combination of random access memory (RAM), which can be written to and read from, and read only memory (ROM), in which is stored the machine's operating system and the Basic interpreter.

For each of the Basics we summarise under the following headings the features that are relevant to matrix computations. User RAM This is the amount of memory space available to the Basic programmer for storage of the Basic program and its variables.

Arithmetic We describe the floating point and integer number systems of a particular Basic by quoting five numbers: b, t, L, U, m. For floating point numbers, b is the base, t is the number of base b digits in the mantissa, and L, U are exponents representing the underflow level and the overflow level respectively (Golub and Van Loan, 1983, p. 32). The last number, m, is the number of base b digits in which integers are stored. In fact, all the Basics considered here use b=2,

-32-

rounded floating point arithmetic with t=32, and each stores integers in two's complement form. Thus in each Basic the unit roundoff (Golub and Van Loan, 1983, p. 33)

 $u = 1/2 b^{1-t} = 2^{-32} \sim 2.33 \times 10^{-10}$ 

and integers m must lie in the range

 $-2^{t-1} \le m \le 2^{t-1} - 1.$ 

Integer Arithmetic Some Basics perform true integer arithmetic (addition, subtraction and multiplication) between integer operands; others automatically convert integer values to floating point before evaluating an expression, even if all the components are of integer type.

**Structure** This refers to the provision of structured programming constructs such as procedures, If-Then-Else, and Repeat-Until and While-Wend loops.

Identifiers Most microcomputer Basics do not restrict the length of variable names. However, in some Basics only the first two characters are significant, so that, for example, the identifiers TEST and TEMP are synonymous. Furthermore, some Basics prohibit embedded keywords in an identifier (usually the ones that do not require spaces to be placed around keywords): for example, TOTAL may be an illegal identifier because TO is a Basic keyword. Clearly, these restrictions pose difficulties in the translation of Fortran programs to Basic. Array Storage Multi-dimensional arrays can be stored in essentially two ways: with the k'th subscript varying more rapidly than the (k+1)st, for all k, or vice versa (Brown, 1979, p. 186). For the two-dimensional arrays of interest in matrix computations the respective storage schemes are "by column" and "by row". For example, after DIM A(2,2), the

-33-

elements of A may be stored in the order

(0,0), (1,0), (2,0), (0,1), (1,1), (2,1), (0,2), (1,2), (2,2)(by column), or

(0,0), (0,1), (0,2), (1,0), (1,1), (1,2), (2,0), (2,1), (2,2) (by row). Which storage scheme is used becomes of interest when one wishes to access array elements from assembly language. In all the Basics considered here, accessing array elements by column is no faster and no slower than accessing array elements by row (cf. Dongarra *et al.* (1979, p.I.5)).

Machine Language Routines This entry describes the mechanism provided in Basic for calling machine language routines and for passing parameters to such routines.

Assembler This entry describes the availability of assemblers for the machines.

Interpreter Documentation The final entry describes the availability of documentation for the internal interpreter routines. This documentation should describe the location and the purpose of the main subroutines in the interpreter and it should explain how to use the subroutines from an assembly language program.

#### Commodore 64

(Commodore Business Machines, 1982; West, 1982; Bathurst, 1983). Microprocessor

6510 microprocessor running at 0.985 MHz (U.K. version) or 1.022 MHz (U.S.A. version). The 6510 has the same instruction set as the 6502.

#### Language: Basic

Commodore Basic 2 interpreter occupying 8K of ROM; this is developed from a 1977 Basic written by Microsoft Software. User RAM 38K. A further 4K is available for use by machine code routines.

Arithmetic (b, t, L, U, m) = (2, 32, -128, 127, 16).

Integer Arithmetic Not supported.

Structure No structured constructs.

Identifiers The first two characters only are significant. Embedded keywords are not allowed.

Array Storage By column.

Machine Language Routines Called by the SYS command. Ostensibly, SYS does not take parameters, but they can be included provided that the machine language routine takes the responsibility for evaluating the parameter values and/or addresses (by calling general purpose evaluation routines in the Basic interpreter).

Assembler Many assemblers are commercially available. Interpreter Documentation Readily available from sources independent of the manufacturer. Excellent references are West (1982) and Bathurst (1983).

Language: Comal (Atherton, 1982; Lindsay, 1983).

Version 0.64S of CBM Comal-80 interpreter (soft loaded from

disk). Occupies approximately 24K of RAM.

User RAM 12K.

Arithmetic (b, t, L, U, m) = (2, 32, -128, 127, 16).

Integer Arithmetic Not supported.

Structure Well structured; see Appendix A.

Identifiers Long. All characters are significant and embedded keywords are allowed.

Array Storage See Note (1).

Machine Language Routines Called by the SYS command.

Parameters are not supported.

Assembler See Basic entry.

Interpreter Documentation See Note (2).

## BBC Microcomputer - Model B

(Coll and Allen, 1982; Pharo, 1984).

# Microprocessor

6502 microprocessor running at 2 MHz.

## Language: Basic

BBC Basic interpreter occupying 16K of ROM.

User RAM 25K (in screen mode 7 - less in other modes). Arithmetic (b, t, L, U, m) = (2, 32, -128, 127, 32). For further details see Wichmann (1983).

Integer Arithmetic Supported.

Structure Procedures with local variables and parameters (simple variables only) which are called by value; REPEAT-UNTIL loop; single line IF-THEN-ELSE.

Identifiers Long. All characters are significant and embedded keywords are allowed.

Array Storage By row.

Machine Language Routines Called by the CALL command, which takes parameters. The parameters must be variables or array elements (not expressions); their addresses and types are evaluated by the interpreter and stored in a parameter block. Assembler BBC Basic contains a built-in 6502 assembler. Assembly language may be freely mixed with the Basic source code.

Interpreter Documentation The integer and floating point arithmetic routines are thoroughly documented in Pharo (1984).

BBC Microcomputer (Model B) with Torch Z-80 Second Processor (Torch Computers, 1982).

### Microprocessor

Z-80A microprocessor running at 4 MHz, in addition to the 6502 in the standard BBC machine. The 6502 is dedicated to input/output and the Z-80 performs the data processing.

Z80 version of the BBC Basic interpreter, which is soft loaded from disk and occupies approximately 16K of RAM. User RAM 48K.

Arithmetic, Integer Arithmetic, Structure and Identifiers as for BBC Basic (6502).

Array Storage See Note (1).

Machine Language Routines Similar to BBC Basic.

Assembler Z80 version of the 6502 assembler in BBC Basic. Interpreter Documentation See Note (2).

## Amstrad CPC 64

(Amsoft, 1984; Locomotive Software, 1984).

## Microprocessor

Z-80A microprocessor running at 4 MHz.

#### Language: Basic

Locomotive Software Basic interpreter occupying 16K of ROM. User RAM 42.5K.

Arithmetic (b, t, L, U, m) = (2, 32, -128, 127, 16).

Integer Arithmetic Supported.

Structure WHILE-WEND loop and single line IF-THEN-ELSE.

Identifiers Long. All characters are significant and embedded keywords are allowed.

Array Storage By column.

Machine Language Routines Called by the CALL command. This is very similar to the CALL statement in BBC Basic but it allows parameters to be passed by address or by value. A useful additional feature of this Basic is that it allows the user to define new commands, which are accessed by name instead of via a CALL statement.

Assembler Several assemblers are commercially available. Interpreter Documentation See Note (2).

Note (1) In these cases I was unable to determine the method of array storage.

Note (2) In these cases I was unable to obtain documentation.

#### -38-

100 ! 1.00 P.M. 12-5-85 110 ! SAVE"BLASHOW.4".8:VERIFY"\*".8 120 ! 490 ! \*\*\*\*\* 500 ! ASSEMBLY LANGUAGE BLAS ROUTINES FOR THE COMMODORE 64. TO BE CALLED FROM A C64 BASIC PROGRAM. 501 ! LISTING OF SASUM, SAXPY, ISAMAX, SSCAL ONLY. 502 ! 510 1 512 ! ### 6502 ASSEMBLY LANGUAGE ### BRIEFLY, MAIN INSTRUCTIONS ARE 514 ! JSR: CALL SUBROUTINE, WHICH IS TERMINATED BY RTS (= 'RETURN'). 516 ! JMP: UNCONDITIONAL JUMP ('GOTO'). 518 ! LDA/LDX/LDY: LOAD ACCUMULATOR/X-REGISTER/Y-REGISTER. 519 ! STA: STORE THE ACCUMULATOR. 520 ! INC/DEC: INCREMENT/DECREMENT MEMORY BY ONE. 521 ! BEQ/BNE: BRANCH IF RESULT OF PREVIOUS OPERATION WAS ZERO/NONZERO. 522 ! 523 ! ### ASSEMBLER NOTES ### 525 ! THIS LISTING IS IN MIKRO ASSEMBLER (SUPERSOFT, HARROW, ENGLAND) FORMAT. 526 ! '!' DENOTES A COMMENT LINE OR REMAINDER OF LINE. 527 ! '\$' SPECIFIES A HEXADECIMAL (BASE 16) NUMBER. 528 ! 'STORE = \$5C' DEFINES THE LABEL STORE TO REPRESENT THE VALUE 92. 529 1 530 ! ### IMPLEMENTATION NOTES ### 531 ! EACH BLAS ROUTINE IS CALLED BY AN EXTENDED SYS STATEMENT WHOSE 532 ! FORM IS DEFINED IN A COMMENT LINE AT THE START OF THE ROUTINE. 533 ! 'SX()' DENOTES AN ELEMENT OF THE ARRAY SX, WHICH MAY HAVE ANY 534 ! DIMENSIONS. THE BLAS ROUTINES ACCESS ARRAY ELEMENTS IN THE ORDER THAT 535 ! THEY ARE STORED IN MEMORY, I.E., BY COLUMN FOR 2-DIM'L ARRAYS A(N,N). 536 ! PARAMETER 'N' MAY BE AN EXPRESSION (E.G. 'N-K+1' OR 'M\*3') BUT THE 537 ! OTHER PARAMETERS MUST BE SIMPLE VARIABLES OR ARRAY ELEMENTS, OF 538 ! TYPE FLOATING POINT (NOT INTEGER). 540 ! 'N' MUST EVALUATE TO 0 <= N <= 32767. IN THIS 545 ! IMPLEMENTATION, FOR N=0, SASUM, SDOT, SNRM2 CORRECTLY RETURN 0, BUT 550 ! ISAMAX RETURNS 1 (ISAMAX IS UNLIKELY TO BE CALLED WITH N=0). 600 1 900 \*=\$C000 ! ASSEMBLE CODE IN SPARE 4K BLOCK STARTING AT \$C000 910 ! 920 ! NOTATION: 922 ! 940 ! FP1, FP2 = FLOATING POINT ACCUMULATORS 1, 2 970 ! MEM.AY := '(A,Y)' = FL.PT. NUMBER AT ADDRESS A+256\*Y 980 ! MEM.XY := '(X,Y)' = FL.PT. NUMBER AT ADDRESS X+256\*Y 995 ! 1000 ! ADDRESSES OF (ROM) ROUTINES IN THE BASIC INTERPRETER: 1010 1 1020 EVAL = \$AD8A 1025 ! GETS & EVALUATES NUMERIC EXPRESSION FROM TEXT, RESULT PLACED IN FP1. 1028 ! = \$AEFD ! CHECK FOR COMMA 1030 COMMA 1040 INTEGER 1030 COMMA = \$B7F7 ! FP1 -> INTEGER AT (Y,A) = \$B391 1045 INTFLP ! FP1 := FLOAT((Y,A)) 1048 !

Appendix C: Commodore 64 Assembly Language BLAS Listing.

-39-

1050 PTRGET = \$8088 1055 ! GETS NAME AND POINTER TO A VARIABLE. RETURNS WITH (A,Y) POINTING TO 1056 ! EXPONENT (OF FIRST ELEMENT IF ARRAY), FOR NUMERIC VARIABLE. 1060 ! 1070 LOADFP1 = \$BBA2 1080 SAVEFP1 = \$BBD4 ! FP1 := MEM.AY ! MEM.XY := FP1 1130 ! = \$B86A = \$BA2B ! FP1 := FP1+FP2 1140 ADD 1150 MULT ! FP1 := FP1\*FP2 1160 ABS = \$BC58 ! FP1 := ABS(FP1) = \$BF71 1170 SQRT ! FP1 := SQRT(FP1) 1178 ! 1180 COMPARE = \$BC5B ! COMPARE FP1 WITH MEM.AY 1185 ! A=0 IF EQUAL, A=1 IF FP1 > MEM.AY, A=\$FF IF FP1 < MEM.AY 1200 ! 1210 ADDMEM = \$B867 ! FP1 := FP1+MEM.AY 1220 MULTMEM = \$BA28 ! FP1 := FP1\*MEM.AY 1230 ! 2000 ! TEMPORARY STORAGE: 2002 ! 2005 STORE ! 'FP3' : \$5C-\$60 = \$50 ! 'FP3' := FP1 2007 FP1TOSTORE = \$BBC7 2010 ! 2020 NLOW = \$F7 2030 NHIGH = \$F8 2040 ! = \$F9 2050 PLOW1 2060 PHIGH1 = \$FA ! POINTER (PTR1) 2070 PLOW2 = \$FB ! 2080 PHIGH2 = \$FC (PTR2) 2090 PLOW3 = \$FD 2100 PHIGH3 = \$FE Į. (PTR3) 2110 ! 3000 ! FLOATING POINT ACCUMULATORS: 3010 ! 3020 FP1 = \$61 \$61-\$66 3030 FP2 = \$69 ! \$69-\$6E 3050 ! 9998 !------9999 ! 10000 ! "REAL FUNCTION SASUM (N.SX)" 10010 ! 10015 ! SUM OF ABSOLUTE VALUES OF A VECTOR 10017 ! 10020 ! SYS ASUM, N, SX(), S 10030 ! 10100 SASUM JSR GETN ! EVALUATE 1ST PARAMETER 10110 !#### JSR GET1 10120 ! (PTR1) -> SX() 10140 ! 10150 JSR ZEROSTORE ! SUM:=0 10160 ! 10170 LOOPSA LDA NLOW ! N=0? 10180
10190 ORA NHIGH BEQ FINSA ! IF SO, FINISHED 10200 !

10210		LDA	PLOW1 PHIGH1 LOADFP1		SET UP ACCUM.
10220		LDY	PHIGH1	!	AND Y-REG.
10230		JSR	LOADFP1	1.	THEN CALL ROM ROUTINE
10232	!				
10235		JSR	ABS	i	FP1 := ABS(FP1)
10238					
10240		LDA	# <store< td=""><td></td><td></td></store<>		
10242		LDY	#>STORE		
10245		JSR	ADDMEM	1	FP1 := FP1 + SUM
10250	1				
10260		JSR	FP1TOSTORE	1	SUM := FP1
10280					
10300		ISR	BUMP1	ł	CALL SUBROUTINES AT
10360		ISR	NEONMI	i	END DE LISTING.
10420		TMP	LOOPSA	ì	CALL SUBROUTINES AT END OF LISTING. LOOP BACK
10430		UNF	LOUPSA		LOOF DRCK
10430	ETNEA	TCD	сомма	T	CTOPE DECINIT (ED1) IN
10440	FINDR	JOR	DIDOLT	-	STORE RESULT (FP1) IN VARIABLE. THIS CODE CALLED BY ISAMAX ALSO.
10400		JSK	PIRGEI	:	VARIABLE. THIS LODE CALLED BY
10460		IAX		:	ISAMAX ALSU.
10470		JSR	SAVEFP1		
10480		RTS			
10490					
		and per any part like			ar an an an
10999					
11010	! "SUBROUTI	NE SA	XPY (N,SA,SX,SY)"		
11020	!				
11025	! VECTOR=VE	CTOR+	CONST*VECTOR: SY() := !	SY	()+SA*SX()
11027	1				
A A Set day /					
		N,SA,	SX(),SY()		
	! SYS AXPY,	N,SA,	SX(),SY()		
11030 11040	! SYS AXPY, !				
11030 11040 11050	! SYS AXPY, ! Saxpy		GETN		
11030 11040 11050 11060	! SYS AXPY, ! SAXPY !####	JSR	GETN	I	(PTR3) -> SA
11030 11040 11050 11060 11070	! SYS AXPY, ! SAXPY !####	JSR JSR	GETN	!	(PTR3) -> SA (PTR2) -> SX()
11030 11040 11050 11060 11070 11080	! SYS AXPY, ! SAXPY !####	JSR JSR JSR	GETN	! !	(PTR3) -> SA (PTR2) -> SX() (PTR1) -> SY()
11030 11040 11050 11050 11070 11080 11080	! SYS AXPY, ! SAXPY !####	JSR JSR JSR	GETN GET3 GET2		(PTR3) -> SA (PTR2) -> SX() (PTR1) -> SY()
11030 11040 11050 11060 11070 11080 11090 11100	! SYS AXPY, ! SAXPY !####	JSR JSR JSR JSR	GETN GET3 GET2 GET1		
11030 11040 11050 11060 11070 11070 11080 11090 11100 11110	! SYS AXPY, ! SAXPY !####	JSR JSR JSR JSR LDA	GETN GET3 GET2 GET1 NLOW		(PTR3) -> SA (PTR2) -> SX() (PTR1) -> SY() N=0?
11030 11040 11050 11060 11070 11080 11090 11100 11110 11120	! SYS AXPY, ! SAXPY !####	JSR JSR JSR JSR LDA ORA	GETN GET3 GET2 GET1 NLOW NHIGH		
11030 11040 11050 11060 11070 11080 11090 11100 11110 11120 11130	! SYS AXPY, ! SAXPY !#### ! LOOPSAX	JSR JSR JSR JSR LDA ORA	GETN GET3 GET2 GET1 NLOW		
11030 11040 11050 11060 11070 11080 11090 11100 11110 11120 11130 11140	! SYS AXPY, ! SAXPY !#### ! LOOPSAX	JSR JSR JSR JSR LDA ORA BEQ	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX	!	N=Ø?
11030 11040 11050 11060 11070 11080 11070 11100 11110 11120 11130 11140 11150	! SYS AXPY, ! SAXPY !#### ! LOOPSAX	JSR JSR JSR JSR LDA ORA BEQ LDA	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3	!	
11030 11040 11050 11060 11070 11080 11090 11100 11110 11120 11130 11140 11150 11160	! SYS AXPY, ! SAXPY !#### ! LOOPSAX	JSR JSR JSR LDA ORA BEQ LDA LDY	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3	!	N=Ø?
11030 11040 11050 11060 11070 11080 11090 11100 11110 11120 11130 11140 11150 11150 11170	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR LDA ORA BEQ LDA LDY	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3	!	N=Ø?
11030 11040 11050 11060 11070 11080 11090 11100 11110 11120 11130 11150 11150 11150 11180	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR LDA ORA BEQ LDA LDY JSR	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1		N=0? FP1:=SA
11030 11040 11050 11060 11070 11080 11090 11100 11110 11120 11130 11140 11150 11150 11170	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2		N=Ø?
11030 11040 11050 11060 11070 11080 11090 11100 11110 11120 11130 11140 11150 11140 11150 11160 11170 11180 11190	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA LDY	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2 PHIGH2		N=0? FP1:=SA
11030 11040 11050 11060 11070 11080 11090 11100 11110 11120 11130 11140 11150 11150 11150 11180 11190	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA LDY	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2		N=0? FP1:=SA
11030 11040 11050 11060 11070 11080 11090 11100 11110 11120 11130 11140 11150 11140 11150 11160 11170 11180 11190	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA LDY	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2 PHIGH2		N=0? FP1:=SA
11030 11040 11050 11070 11080 11090 11090 11100 11120 11130 11140 11150 11150 11150 11180 11180 11190 11200	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA LDY JSR	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2 PHIGH2	!	N=0? FP1:=SA
11030 11040 11050 11070 11080 11070 11080 11100 11110 11120 11130 11140 11150 11150 11150 11180 11180 11190 11220	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA LDY JSR LDA	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2 PHIGH2 MULTMEM	!	N=0? FP1:=SA FP1:=FP1*SX()
11030 11040 11050 11070 11080 11070 11090 11100 11120 11130 11140 11150 11150 11150 11180 11170 11200 11210 11220 11220 11240	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA LDY JSR LDA LDY	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2 PHIGH2 MULTMEM PLOW1 PHIGH1	!	N=0? FP1:=SA FP1:=FP1*SX()
11030 11040 11050 11070 11080 11070 11080 11070 11100 11110 11120 11130 11140 11150 11150 11160 11170 11180 11120 11220 11220 11220 11220	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA LDY JSR LDA LDY	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2 PHIGH2 MULTMEM PLOW1	!	N=0? FP1:=SA FP1:=FP1*SX()
11030 11040 11050 11060 11070 11080 11090 11100 11100 11120 11130 11140 11150 11150 11160 11170 11180 11220 11220 11220 11240 11250 11260	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA LDY JSR LDA LDY JSR	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2 PHIGH2 MULTMEM PLOW1 PHIGH1 ADDMEM		N=0? FP1:=SA FP1:=FP1*SX()
11030 11040 11050 11060 11070 11080 11090 11100 11120 11130 11140 11150 11140 11150 11140 11150 11120 11200 11200 11210 11220 11220 11240 11250 11270	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA LDY JSR LDA LDY JSR LDX	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2 PHIGH2 MULTMEM PLOW1 PHIGH1 ADDMEM PLOW1		N=0? FP1:=SA FP1:=FP1*SX() FP1:=FP1+SY()
11030 11040 11050 11070 11080 11070 11090 11100 11120 11130 11140 11150 11150 11150 11170 11180 11200 11200 11210 11220 11240 11250 11280 11280	! SYS AXPY, ! SAXPY !#### ! LOOPSAX !	JSR JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA LDY JSR LDA LDY JSR LDX LDY	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2 PHIGH2 MULTMEM PLOW1 PHIGH1 ADDMEM PLOW1 PHIGH1		N=0? FP1:=SA FP1:=FP1*SX() FP1:=FP1+SY()
11030 11040 11050 11060 11070 11080 11090 11100 11120 11130 11140 11150 11140 11150 11140 11150 11120 11200 11200 11210 11220 11220 11240 11250 11270	! SYS AXPY, ! SAXPY !#### ! LOOPSAX ! !	JSR JSR JSR JSR LDA ORA BEQ LDA LDY JSR LDA LDY JSR LDA LDY JSR LDX LDY	GETN GET3 GET2 GET1 NLOW NHIGH FINSAX PLOW3 PHIGH3 LOADFP1 PLOW2 PHIGH2 MULTMEM PLOW1 PHIGH1 ADDMEM PLOW1		N=0? FP1:=SA FP1:=FP1*SX() FP1:=FP1+SY()

JSR BUMP2 JSR BUMP1 ! MOVE PTRS TO NEXT 11320 11310 ! ELTS OF SX & SY. 11330 ! JSR NEQNM1 11340 JMP LOOPSAX 11400 11410 ! 11420 FINSAX RTS 11430 ! 14999 ! 15000 ! "INTEGER FUNCTION ISAMAX (N,SX)" 15010 ! 15020 ! FIND INDEX OF ELT WITH LARGEST ABSOLUTE VALUE IN VECTOR X 15030 ! 15040 ! SYS ISAMAX, N, SX(), K 15050 ! 15060 ISAMAX JSR GETN 15070 !### JSR GET1 ! (PTR1) -> SX() 15080 15090 ! ! 'CURRENT MAX' := Ø 15100 JSR ZEROSTORE 15110 ! 15120 LDA NLOW 15130 STA PLOW2 STA PLOW3 15135 ! PTR2 = N+1-INDEX OF 15140 LDA NHIGH STA PHIGH2 ! CURRENT MAX ELT. 15150 15155 STA PHIGH3 ! PTR3 = SAVED VALUE OF N 15156 INC PLOW3 PLUS 1. 1 BNE LOOPMAX 15157 INC PHIGH3 15158 15159 ! 15160 LOOPMAX LDA NLOW ! N=0? 15170 ORA NHIGH BEQ FINMAX 15180 15190 ! LDA PLOW1 15200 LDY PHIGH1 15210 ! FP1 := SX()JSR LOADFP1 15220 15225 ! 15230 JSR ABS ! FP1 := ABS(FP1) 15240 ! 15250 LDA #<STORE LDY #>STORE 15260 ! FP1 & BIGGEST SO FAR 15270 JSR COMPARE BMI LTE ! IF FP1 < ... 15280 ! IF FP1 = ... 15285 BEQ LTE 15290 ! ! FP1 IS BIGGER LDA NLOW 15300 STA PLOW2 15310 LDA NHIGH 15320 ! UPDATE 'INDEX' OF MAX ELT 15330 STA PHIGH2 15340 ! JSR FPITOSTORE ! SAVE CURRENT MAX ELT 15350 15360 ! 15370 LTE JSR BUMP1

15390 JSR NEQNM1 15400 JMP LOOPMA JMP LOOPMAX 15410 ! ! INDEX := N+1-PTR2 15420 FINMAX SEC LDA PLOW3 15422 15424 SBC PLOW2 TAY 15426 15428 LDA PHIGH3 SBC PHIGH2 15430 15435 ! JSR INTFLP ! CONVERT RESULT TO 15440 15450 JMP FINSA ! FL.PT. & GOTO SASUM 15500 ! 15998 !-----15999 ! 16000 ! "SUBROUTINE SSCAL (N,SA,SX)" 16010 ! 16020 ! SCALE VECTOR BY A CONSTANT: SX():= SA\*SX() 16030 ! 16040 ! SYS SCAL, N, SA, SX() 16050 ! 16060 SSCAL JSR GETN 16070 !#### JSR GET2 ! (PTR2) -> SA 16075 16080 JSR GET1 ! (PTR1) -> SX() 16090 ! 16100 LOOPSC LDA NLOW ! N=0? 16110 ORA NHIGH 16120 BEQ FINSC 16130 ! LDA PLOW1 16140 LDY PHIGH1 16150 16160 JSR LOADFP1 ! FP1 := SX()16170 ! 16180 LDA PLOW2 16190 LDY PHIGH2 ! FP1 := FP1\*SA 16200 JSR MULTMEM 16210 ! 16220 LDX PLOW1 LDY PHIGH1 16230 ! SX() := FP1 16240 JSR SAVEFP1 16250 ! JSR BUMP1 16260 16270 **JSR NEQNM1** 16280 JMP LOOPSC 16290 ! 16300 FINSC RTS 16310 ! 16998 !-----20060 ! ROUTINE TO EVALUATE THE PARAMETER 'N' AND STORE THE RESULT 20065 ! AS A 16-BIT INTEGER IN (NLOW, NHIGH). 20067 ! 20100 GETN JSR COMMA 20110 !### JSR EVAL 20120 JSR INTEGER 20130

-43-

20140 STY NLOW 20150 STA NHIGH 20170 RTS 20180 ! 20200 !-----20490 ! SET TO ZERO 'FP3' AND FP1, THE LATTER SO THAT SASUM, SDOT AND 20495 ! SNRM2 RETURN Ø WHEN N=0. 20497 ! 20500 ZEROSTORE LDX #4 ! 5 ELTS TO ZERO 20510 !######## 20520 LDA #Ø 20530 ! 20540 LOOPZ1 STA STORE,X 20545 STA FP1,X 20550 DEX 20560 BPL LOOPZ1 ! BRANCH IF X>=Ø 20565 STA FP1+5 20570 RTS 20580 ! 20590 ! THE FOLLOWING ROUTINES MOVE A POINTER ONTO THE NEXT ARRAY ELEMENT 20595 ! 25000 BUMP1 CLC ! BUMP PTR1 BY 5 25005 !#### 25010 LDA PLOW1 25020 ADC #5 25030 STA PLOW1 25040 BCC FIN1 25050 INC PHIGH1 25060 FIN1 RTS 25070 ! 25200 BUMP2 CLC ! BUMP PTR2 BY 5 25210 !#### 25220 LDA PLOW2 25230 ADC #5 25240 STA PLOW2 BCC FIN2 25250 INC PHIGH2 25260 25270 FIN2 RTS 25280 ! 25300 BUMP3 CLC ! BUMP PTR3 BY 5 25310 !#### 25320 LDA PLOW3 25330 ADC #5 25340 STA PLOW3 25350 BCC FIN3 25360 INC PHIGH3 25370 FIN3 RTS 25380 ! 25900 ! THE FOLLOWING ROUTINES SEARCH FOR A 25910 ! NUMERIC VARIABLE (SIMPLE VAR, OR ARRAY ELEMENT) AND 25920 ! STORE A POINTER TO THE FIRST BYTE OF THE 25930 ! FLOATING POINT NUMBER IN (PTR1), (PTR2) OR (PTR3). 25940 !

26000	GET1 !###	JSR	COMMA		
26020		ISP	PTRGET		
26030			PLOW1		
26040			PHIGH1		
26050		RTS	1 HILDHI		
26060	I	NIU			
	· · · · · · · · · · · · · · · · · · ·	ISP	COMMA		
26110	! # # #	UUN	COTTIN		
26120		ISP	PTRGET		
26130			PLOW2		
26140			PHIGH2		
26150		RTS	1 11 012		
26160	1	NIU			
26200		ISP	COMMA		
26210	! # # #	0 WIN	CONTRA		
26220		ISR	PTRGET		
26230			PLOW3		
26240			PHIGH3		
26250		RTS	1 11 2 0110		
26260	1	1110			
26285				 	
26999	1				
27000	NEQNM1	IDA	NLOW		! N := N-1
27010	! # # # # # #	Las ber 11	TT has bed TT		
27020		BNE	NM1		
27030			NHIGH		
27040	NM1		NLOW		
27050	1	dor how bur	I T has ber TT		
27060		RTS			
29995	!			 	
29996	1				
29997		END			

Appendix D: BBC Microcomputer Assembly Language BLAS Listing.

```
10 REM 2.45 P.M. 25-3-85
 20 REM SAVE "BLAS. 16"
 30 :
 40 PROCBLAS
 50 :
 60 REM TEST THE M/C BLAS
 70 :
 80 N%=22: M%=5*(N%+1)
 90 DEF FNF(X) = -1+2 * RND(1)
100 INPUT "SEED>0"; SEED: T=RND(-SEED)
110 DIM X(N%,N%), Y(N%,N%), Z(N%,N%)
120 T=FNF(1): J=INT(N%/2)
130 FOR I=1 TO N%
140
      X(I,J) = FNF(1) : Y(I,J) = FNF(1)
150
      NEXT
160 :
170 REM TEST SSCAL
180 FOR I=1 TO N%:Z(I,J)=T*X(I,J):NEXT
190 CALL SSCAL, N%, T, X(1, J)
200 FOR I=1 TO N%: PRINTABS(X(I,J)-Z(I,J)); :NEXT: PRINT
210 :
220 REM TEST SAXPY
230 FOR I=1 TO N%:Z(I,J)=Y(I,J)+T*X(I,J):NEXT
240 CALL SAXPY, N%, T, X(1, J), Y(1, J)
250 FOR I=1 TO N%:PRINTABS(Y(I,J)-Z(I,J))" : ";:NEXT:PRINT
260 :
270 REM TEST ISAMAX
280 S=0
290 FOR I=1 TO N%:T=ABS(X(I,J)): IF T>S THEN S=T:K=I
300
     NEXT
310 L%=0
320 CALL ISAMAX, N%, X(1, J), L%
330 PRINTK,L%
340 :
350 END
352 REM ---
353 REM PROCEDURE TO ASSEMBLE THE MACHINE CODE BLAS
360 :
370 DEF PROCBLAS
375 REM ########
380 :
390 REM MACHINE CODE BLAS ROUTINES SAXPY, SSCAL & ISAMAX FOR THE
400 REM BBC MODEL B (6502) MACHINE WITH BASIC2.
410 REM SIMILAR TO COMMODORE 64 VERSION BUT
420 REM (1) BBC BASIC STORES ARRAYS BY ROW, THUS THE INCREMENT BETWEEN
430 REM
            ELEMENTS (I,J) AND (I+1,J) IN STORAGE DEPENDS ON
             THE COLUMN DHMENSION. THIS IMPLEMENTATION ASSUMES
440 REM
450 REM
             THAT M% HOLDS THE INCREMENT.
                                              INTENDED USE IS
            FOR FLOATING POINT ARRAYS OF THE FORM
460 REM
470 REM
             DIM A(N%,N%) ONLY, FOR WHICH M% = 5*(N%+1)
480 REM
             IS REQUIRED.
        (2) IN SAXPY, SSCAL & ISAMAX PARAMETERS N% AND K% MUST
490 REM
500 REM
            BE INTEGER VARIABLES, NOT EXPRESSIONS.
512 :
515 REM ### ASSEMBLER NOTES ###
516 REM '\'
            DENOTES A COMMENT LINE OR REMAINDER OF LINE
```

517 REM '&' SPECIFIES A HEXADECIMAL (BASE 16) NUMBER 518 REM '.LABEL' DEFINES 'LABEL' TO TAKE THE VALUE OF THE CURRENT ADDRESS 519 : 520 : 530 REM -535 REM ### LABEL DEFINITIONS ### 536 : 540 REM PARAMETER BLOCK, OF FORM 550 REM (ND. PARAMETERS), <2-BYTE PARAMETER ADDRESS, 1-BYTE PARAMETER TYPE> 560 BLOCK=&600 570 : 580 REM ZERO PAGE POINTERS FOR ARRAY ELEMENTS ETC. 590 PLOW1=&70 600 PHIGH1=&71 610 PLOW2=&72 620 PHIGH2=&73 630 PLOW3=&74 640 PHIGH3=&75 650 : 660 REM COUNTER FOR NUMBER OF ELEMENTS 670 NLOW=&76 680 NHIGH=&77 690 : 700 REM TEMPORARY ZERO PAGE POINTER 710 TEMPLOW=&78 720 TEMPHIGH=&79 730 : 740 REM POINTER TO FL.PT. VARIABLE FOR ROM ROUTINES 750 FPLOW=&4B 760 FPHIGH=&4C 770 : 780 REM LOW 2 BYTES OF STATIC VARIABLE M%. 790 INCLOW=&434 800 INCHIGH=&435 810 : 820 REM TEMPORARY STORAGE FOR A FL.PT. VALUE: &46C-&470 830 FPSTORE=&46C 840 : 850 REM ROM ROUTINES 860 REM FWA, FWB DENOTE FLOATING POINT WORK AREAS A AND B 870 : 880 AUNP=&A3B5: REM FWA := FP.VAR 890 APACK=&A38D: REM FP.VAR := FWA 900 AMULT=&A656: REM FWA := FWA\*FP.VAR 910 APLUS=&A500: REM FWA := FWA+FP.VAR 920 APACK1=&A385: REM FPSTORE1 := FWA 930 AUNP1=&A3B2: REM FWA := FPSTORE1 940 ACLEAR=&A686: REM FWA := 0 950 ASIGN=&A1DA: REM A := SIGN (FWA) 960 ACOMP=&AD7E: REM FWA := -FWA 970 ATEST=&9A5F: REM TEST FP.VAR <-> FWA 980 : 990 REM -----1000 REM ### ASSEMBLER CODE ### 1005 : 1010 DIM MC% 500 1020 REM INPUT"LISTING (Y/N)"; Z\$ 1030 PS=2:REM IF Z\$="Y" THEN PS=3 ELSE PS =2 1040 FOR PASS%=0 TO PS STEP PS 1050 :

P%=MC% 1060 1070 E 1080 OPT PASS% 1090 1 1100 .PGETN \get no. of elements 1110 \#### 1120 LDA BLOCK+1 1130 STA TEMPLOW 1140 LDA BLOCK+2 1150 STA TEMPHIGH 1160 1170 LDY #1 \ N = 16 BIT INTEGER 1180 . NLOOP 1190 LDA (TEMPLOW),Y 1200 STA NLOW, Y 1210 DEY 1220 BPL NLOOP 1230 RTS 1240 1 1250 1 1260 .PGET1 \ get pointer to parameter #1 1270 \#### 1280 LDA BLOCK+4 1290 STA PLOW1 LDA BLOCK+5 1300 1310 STA PHIGH1 1320 RTS 1330 1 1340 1 1350 .PGET2 \ get pointer to parameter #2 1360 \#### 1370 LDA BLOCK+7 1380 STA PLOW2 1390 LDA BLOCK+8 1400 STA PHIGH2 1410 RTS 1420 1 1430 1 .PGET3 \ get pointer to parameter #3 1440 1450 \#### 1460 LDA BLOCK+10 1470 STA PLOW3 1480 LDA BLOCK+11 1490 STA PHIGH3 1500 RTS 1510 1 1520 1 1530 .FPTR1 \ fplow = ptr1 1540 \##### 1550 LDA PLOW1 1560 STA FPLOW 1570 LDA PHIGH1 1580 STA FPHIGH 1590 RTS 1600 ----1610 -1620 .FPTR2 \ fplow = ptr2 1630 \##### 1640 LDA PLOW2 1650 STA FPLOW

1660 LDA PHIGH2 1670 STA FPHIGH 1680 RTS 1690 1 1700 1 1710 .FPTR3  $\ fplow = ptr3$ 1720 \##### 1730 LDA PLOW3 1740 STA FPLOW 1750 LDA PHIGH3 1760 STA FPHIGH 1770 RTS 1780 1 1790 1 1800 .BUMP1 \ move pointer 1 to next array element 1810 \ #### 1820 CLC 1830 LDA PLOW1 1840 ADC INCLOW 1850 STA PLOW1 1860 LDA PHIGH1 1870 ADC INCHIGH 1880 STA PHIGH1 1890 RTS 1900 1 1910 2 1920 .BUMP2 \ move pointer 2 to next array element 1930 \ #### 1940 CLC 1950 LDA PLOW2 1960 ADC INCLOW 1970 STA PLOW2 1980 LDA PHIGH2 1990 ADC INCHIGH 2000 STA PHIGH2 2010 RTS 2020 1 2030 and a second 2040 .BUMP3 \ move pointer 3 to next array element 2050 \ #### 2060 CLC 2070 LDA PLOW3 2080 ADC INCLOW 2090 STA PLOW3 2100 LDA PHIGH3 2110 ADC INCHIGH 2120 STA PHIGH3 2130 RTS 2140 1 2150 and a 2160 .NEQNM1 \decrement count 2170 \ ###### 2180 LDA NLOW 2190 BNE NM1 DEC NHIGH 2200 2210 .NM1 2220 DEC NLOW 2230 RTS 2240 1 2250 1

.ZEROSTORE \ zero fl.pt. temporary store 2270 \ ######### 2280 JSR ACLEAR 2290 JSR APACK1 RTS .FABS  $\ FWA = ABS$  (FWA). Is there a ROM routine for this? \ #### 2350 JSR ASIGN AND #&FF BPL FINABS 2380 JSR ACOMP \ negate FWA .FINABS 2400 RTS . SSCAL \##### 2470 \ SCALE VECTOR BY A CONSTANT, SX = SA\*SX \ CALL (),N%,SA,SX() JSR PGETN JSR PGET1 \ (PTR1) -> SA JSR PGET2 \ (PTR2) -> SX() .LOOPSC LDA NLOW ORA NHIGH BEQ FINSC \ FINISHED IF N=0 JSR FPTR1 JSR AUNP \ FWA = SA **JSR FPTR2**  $JSR AMULT \setminus FWA = FWA*SX()$ JSR FPTR2 2680 JSR APACK \ SX() = FWA 2700 JSR BUMP2 **JSR NEQNM1** JMP LOOPSC .FINSC 2750 RTS 1 . . SAXPY \##### \ VECTOR =VECTOR + CONST\*VECTOR, SY() = SY()+SA\*SX() 2830 \ \ CALL (),N%,SA,SX(),SY() 2850 \

2860	JSR PGETN					
2870 2880	$\$ JSR PGET1 $\ (PTR1) -> SA$					
2890	JSR PGET2 \ (PTR2) $\rightarrow$ SX()					
2900	JSR PGET3 \ (PTR3) -> SY()					
2910						
2920						
2930						
2940						
2950 2960	BEQ FINSAX					
	JSR FPTR1					
2980	JSR AUNP $\setminus$ FWA = SA					
2990						
3000	JSR FPTR2					
3010	$JSR AMULT \setminus FWA = FWA*SX()$					
3020						
3030						
3040 3050	JSR APLUS \ FWA = FWA+SY()					
3060						
3070	$JSR APACK \setminus SY() = FWA$					
3080						
	JSR BUMP2					
	JSR BUMP3					
	JSR NEQNM1					
3120 3130						
	FINSAX					
3150	RTS					
3160						
3170	<b>\</b>					
3180						
3190	- ISAMAX					
3200 3210	\#######					
3220	\     FIND INDEX OF FLT WITH LODGEST ADSOLUTE HALVE AN ADDRESS					
	\ FIND INDEX OF ELT WITH LARGEST ABSOLUTE VALUE IN VECTOR X					
3240	\ CALL (),N%,SX(),K%					
3250						
	JSR PGETN					
3270 3280	JSR PGET1 $(PTR1) \rightarrow SX()$					
3290	JSR ZEROSTORE \ CURRENT MAX = 0					
3300	$\Delta = 0$					
3310	LDA NLOW					
3320	STA PLOW2					
3330	STA PLOW3					
	LDA NHIGH					
3350	STA PHIGH2 \ PTR2 = N+1-INDEX OF CURRENT MAX ELT					
3360	STA PHIGH3 \ PTR3 = SAVED VAUE OF N PLUS 1					
3370 3380	INC PLOW3 BNE LOOPMAX					
3390	INC PHIGH3					
3400						
3410	. LOOPMAX					
3420	LDA NLOW					
3430	ORA NHIGH					
3440	BEQ FINMAX					
3450						

3460 JSR FPTR1 3470 JSR AUNP  $\$  FWA = SX() 3480 \ 3490 JSR FABS \ FWA = ABS (FWA) 3500 1 3510 LDA #FPSTORE MOD 256 3520 STA FPLOW 3530 LDA #FPSTORE DIV 256 3540 STA FPHIGH 3550 1 3560 JSR ATEST \ COMPARE FWA AND BIGGEST SO FAR 3570 1 3580 BCS LTE \ IF FWA < OR = FPSTORE 3590 1 3600 LDA NLOW 3610 STA PLOW2 3620 LDA NHIGH 3630 STA PHIGH2 3640 JSR APACK1 \ STORE NEW BIGGEST 3650 1 3660 .LTE 3670 JSR BUMP1 3680 JSR NEQNM1 JMP LOOPMAX 3690 1 3700 3710 .FINMAX 3720 SEC \ ADJUST INDEX ACCORDING TO K -> N+1-K 3730 LDA PLOW3 3740 SBC PLOW2 3750 STA PLOW3 3760 LDA PHIGH3 3770 SBC PHIGH2 3780 STA PHIGH3 3790 1 3800 LDY #0 3810 JSR PGET2 \ PTR TO VAR TO ACCEPT RESULT LDA PLOW3 3820 STA (PLOW2),Y 3830 INY 3840 3850 LDA PHIGH3 3860 STA (PLOW2),Y 3870 LDA #0 \ NOW ZERO THE HIGH TWO BYTES 3880 INY 3890 STA (PLOW2),Y 3900 INY 3910 STA (PLOW2),Y 3920 RTS 3930 ] 3940 NEXT PASS% 3950 : 3960 ENDPROC

Appendix E: BBC Microcomputer SGEFA/SGESL Listing.

```
10 REM 6-2-85 10.30 A.M.
 20 REM SAVE "SGEFA.4"
 30 :
 40 I%=0:J%=0:K%=0:N%=0:L%=0:KP1%=0:NM1%=0:INF0%=0:J0B%=0
 50 T=0:S=0
 60 :
 70 SEED=1
 80 INPUT"N: ";N%
 90 :
100 VDU 3: REM VDU 2: REM PRINTER/SCREEN
110 :
120 DIM A(N%,N%),B(N%),X(N%),IPVT%(N%)
130 :
140 REM SET UP PROBLEM - RANDOM MATRIX A AND R.H.S. B
150 T=RND(-SEED)
160 FOR I%=1 TO N%:FOR J%=1 TO N%:A(I%,J%)=-1+2*RND(1):NEXT J%:NEXT I%
170 FOR I%=1 TO N%:X(I%)=-1+2*RND(1):NEXT I%
180 REM B=A*X
190 FOR I%=1 TO N%:S=0
200 FOR J%=1 TO N%:S=S+A(I%,J%)*X(J%):NEXT J%
210 B(I%)=S:NEXT I%
220 PRINT"N = ":N%:"
                      SEED = ":SEED
230 :
240 REM FACTORISE AND SOLVE (AX=B)
250 T1=TIME
260 PROCSGEFA
270 T1=TIME-T1:PRINT"SGEFA: ":T1/100:" SECONDS"
280 :
290 JOB%=0: T1=TIME
300 PROCSGESL
310 T1=TIME-T1:PRINT"SGESL: ";T1/100;" SECONDS"
320 :
330 REM CHECK ANSWER
340 S=0:FOR I%=1 TO N%
350 S=S+ABS(B(I%)-X(I%)):NEXT
360 PRINT"ONE-NORM OF ERROR = ";S
370 :
380 PRINT"-----"
390 END
400 :
410 REM -----
420 DEF PROCSGEFA
430 REM WITH IN-LINE BLAS
440 :
450 INF0%=0:NM1%=N%-1
460 IF NM1%<1 THEN 670
470 :
480 FOR K%=1 TO NM1%
490 KP1%=K%+1
     T=ABS(A(K\%,K\%)):L\%=K\%
500
    FOR J%=KP1% TO N%: IF ABS(A(J%,K%))>T THEN T=ABS(A(J%,K%)):L%=J%
510
      NEXT J%
520
     IPVT% (K%) =L%
530
     IF A(L%,K%)=0 THEN INFO%=K%:GOTO 650
540
550
     IF L%<>K% THEN T=A(L%,K%):A(L%,K%)=A(K%,K%):A(K%,K%)=T
560
     T=-1/A(K%,K%)
570
```

FOR I%=KP1% TO N%:A(I%,K%)=T\*A(I%,K%):NEXT I% 580 590 : 600 FOR J%=KP1% TO N% 610 T=A(L%,J%): IF L%<>K% THEN A(L%,J%)=A(K%,J%):A(K%,J%)=T 620 FOR I%=KP1% TO N%:A(I%,J%)=A(I%,J%)+T\*A(I%,K%):NEXT I% 630 NEXT J% 640 650 NEXT K% 660 : 670 IPVT% (N%) =N% 680 IF A(N%,N%)=0 THEN INFO%=N% 690 ENDPROC 700 : 710 REM -----720 DEF PROCSGESL 730 REM WITH IN-LINE BLAS 740 : 750 NM1%=N%-1 760 IF JOB%<>0 THEN 900 770 IF NM1%<1 THEN 850 780 : 790 FOR K%=1 TO NM1% 800 L%=IPVT%(K%):T=B(L%) IF L%<>K% THEN B(L%)=B(K%):B(K%)=T 810 820 FOR J%=K%+1 TO N%:B(J%)=B(J%)+T\*A(J%,K%):NEXT J% NEXT K% 830 840 : 850 FOR K%=N% TO 1 STEP -1 B(K%)=B(K%)/A(K%,K%):T=-B(K%) 860 IF K%>1 THEN FOR J%=1 TO K%-1:B(J%)=B(J%)+T\*A(J%,K%):NEXT J% 870 880 NEXT K% 890 : 900 REM CODE FOR TRANSPOSE SOLVE OMITTED 910 : 920 ENDPROC Versions of SGEFA/SGESL Using Assembly Language BLAS. Note: Here the vector b sits in the zero'th column of A. 420 DEF PROCSGEFA 430 REM WITH CALLS TO CODED BLAS 440 : 450 INF0%=0:NM1%=N%-1 460 IF NM1%<1 THEN 670 470 : 480 FOR K%=1 TO NM1% KP1%=K%+1 490 Q%=N%-K%+1: CALL ISAMAX,Q%,A(K%,K%),L%: L%=L%+K%-1 500 510 520 530 IPVT% (K%) =L% IF A(L%,K%)=0 THEN INFO%=K%:GOTO 650 540 IF L%<>K% THEN T=A(L%,K%):A(L%,K%)=A(K%,K%):A(K%,K%)=T 550 560 -T=-1/A(K%,K%) 570 Q%=N%-K%: CALL SSCAL,Q%,T,A(KP1%,K%) 580 590

```
600
       FOR J%=KP1% TO N%
            \mathsf{T}=\mathsf{A}\left(\mathsf{L}^{\times},\mathsf{J}^{\times}\right):\mathsf{IF}\;\;\mathsf{L}^{\times}<\mathsf{>}\mathsf{K}^{\times}\;\;\mathsf{THEN}\;\;\mathsf{A}\left(\mathsf{L}^{\times},\mathsf{J}^{\times}\right)=\mathsf{A}\left(\mathsf{K}^{\times},\mathsf{J}^{\times}\right):\mathsf{A}\left(\mathsf{K}^{\times},\mathsf{J}^{\times}\right)=\mathsf{T}
 610
             Q%=N%-K%:CALL SAXPY,Q%,T,A(KP1%,K%),A(KP1%,J%)
 620
 630
            NEXT J%
 640
       650
       NEXT K%
 660 :
670 IPVT%(N%)=N%
680 IF A(N%,N%)=0 THEN INFO%=N%
690 ENDPROC
700 :
710 REM ----
720 DEF PROCSGESL
730 REM WITH CALLS TO CODED BLAS
740 :
750 NM1%=N%-1
760 IF JOB% >0 THEN 900
770 IF NM1%<1 THEN 850
780 :
790 FOR K%=1 TO NM1%
800 L%=IPVT%(K%):T=A(L%,0)
        IF L%<>K% THEN A(L%,0)=A(K%,0):A(K%,0)=T
810
        Q%=N%-K%:CALL SAXPY,Q%,T,A(K%+1,K%),A(K%+1,0)
820
830
      NEXT K%
840 :
850 FOR K%=N% TO 1 STEP -1
        A(K_{2}, 0) = A(K_{2}, 0) / A(K_{2}, K_{2}) : T = -A(K_{2}, 0)
860
        Q%=K%-1: CALL SAXPY,Q%,T,A(1,K%),A(1,0)
870
880
        NEXT K%
890 :
900 REM CODE FOR TRANSPOSE SOLVE OMITTED
910 :
920 ENDPROC
```

Appendix F: CBM Comal-80 SGEFA/SGESL Test program.

```
0100 // 4.30 P.M. 13-1-85
0110 // SAVE"0:SGEFA.10"
0120 //
0130 // CBM COMAL-80 VER. 0.645
0140 //
0150 //
        '//' DENOTES A REMARK STATEMENT
0160 // 'S:+T' IS SHORTHAND FOR 'S:=S+T'
0170 // SUFFIX '#' DENOTES AN INTEGER VARIABLE
0180 // REF PARAMETERS IN PROCS ARE CALLED BY REFERENCE - OTHERS BY VALUE
0190 //
0200 SEED:=1
0210 ZONE 2
0220 INPUT "N =": N
0230 DIM DV$ OF 2
0240 DV$:="DS"
0250 SELECT OUTPUT DV$ // PRINTER OR SCREEN
0260 //
0270 DIM A(N,N), B(N), X(N), IPVT#(N)
0280 //
0290 // SET UP PROBLEM - RANDOM MATRIX A AND R.H.S. B (AX=B)
0300 I:=RND(-SEED)
0310 FOR I:=1 TO N DO
0320 FOR J:=1 TO N DO A(I,J):=-1+2*RND(1)
0330 ENDFOR I
0340 FOR I:=1 TO N DO X(I):=-1+2*RND(1)
0350 // B=A*X
0360 FOR I:=1 TO N DO
Ø37Ø S:=Ø
0380 FOR J:=1 TO N DO S:+A(I.J)*X(J)
0390 B(I):=S
0400 ENDFOR I
0410 PRINT "N = ";N, "SEED = ";SEED
0420 //
0430 // FACTORISE AND SOLVE
0440 T1:=JIFFIES
0450 SGEFA(A,N, IPVT#, INFO)
Ø460 T1:=JIFFIES-T1
0470 PRINT "SGEFA: ";T1;"JIFFIES,",T1/60;"SECONDS"
0480 //
0490 JOB:=0; T1:=JIFFIES
0500 SGESL(A,N, IPVT#, B, JOB)
0510 T1:=JIFFIES-T1
0520 PRINT "SGESL: ";T1; "JIFFIES, ",T1/60: "SECONDS"
0530 //
0540 // CHECK ANSWER
Ø550 S:=0
0560 FOR I:=1 TO N DO S:+ABS(B(I)-X(I))
0570 PRINT "ONE NORM OF ERROR = ";S
0580 //
0590 PRINT -----
0600 SELECT OUTPUT "DS"
0610 END
0620 //
0630 // -
```

-56-

0640 PROC SGEFA(REF A(,),N,REF IPVT#(),REF INFO) CLOSED 0650 11 0660 INFO:=0; NM1:=N-1 0670 IF NM1<1 THEN GOTO DONE 0680 11 0690 FOR K:=1 TO NM1 DO 0700 KP1:=K+1 0710 T:=ABS(A(K,K)); L:=K0720 FOR J:=KP1 TO N DO 0730 IF ABS(A(J,K))>T THEN T:=ABS(A(J,K)); L:=J 0740 ENDFOR J 0750 IPVT#(K):=L 0760 IF A(L,K)=0 THEN 0770 INFO:=K 0780 GOTO LOOPK 0790 ENDIF 0800 IF L<>K THEN T:=A(L,K); A(L,K):=A(K,K); A(K,K):=T 0810 11 0820 T:=-1/A(K,K)0830 FOR I:=KP1 TO N DO A(I,K):=T\*A(I,K) 0840 11 0850 FOR J:=KP1 TO N DO 0860 T:=A(L,J)0870 IF L<>K THEN A(L,J):=A(K,J); A(K,J):=T0880 FOR I:=KP1 TO N DO A(I,J):+T\*A(I,K) 0890 ENDFOR J 0900 11 0910 LOOPK: 0920 ENDFOR K 0930 11 0940 DONE: 0950 IPVT#(N):=N 0960 IF A(N,N)=0 THEN INFO:=N 0970 ENDPROC SGEFA 0980 // 0990 // -----1000 PROC SGESL(REF A(,),N,REF IPVT#(),REF B(),JOB) CLOSED 1010 11 1020 NM1:=N-1 IF JOB<>0 THEN 1030 1040 GOTO TRANSPOSE 1050 ENDIF IF NM1<1 THEN 1060 1070 GOTO BACKSUB 1080 ENDIF 1090 11 FOR K:=1 TO NM1 DO 1100 1110 L:=IPVT#(K); T:=B(L)1120 IF L<>K THEN B(L):=B(K); B(K):=T 1130 FOR J:=K+1 TO N DO B(J):+T\*A(J,K) 1140 ENDFOR K 1150 11 1160 BACKSUB: 1170 FOR K:=N TO 1 STEP -1 DO 1180 B(K) := B(K) / A(K,K); T := -B(K)1190 FOR J:=1 TO K-1 DO B(J):+T\*A(J,K) 1200 ENDFOR K

## Appendix G: Amstrad CPC 64 Benchmark Program.

```
The versions for the other machines are similar. Note that DEFINT
defines variables in the specified range to be of type integer.
10 REM 10.20 A.M.
                  2-1-85
20 REM a$="bench.3":speed write 1:save a$:speed write 0:save a$
30 :
40 DEFINT i-n
50 i=0:j=0:n=0:r=0:s=0:t=0:k=0:t1=0:t2=0:seed=0
60 n=25
70 DIM a(n,n), b(n)
80 seed=1: RANDOMIZE seed
90 DEF FNr(x) = -1+2*RND(1)
100 :
110 FOR i=1 TO n:FOR j=1 TO n:a(i,j)=FNr(1):NEXT j:NEXT i
120 FOR i=1 TO n:b(i)=FNr(1):NEXT i
130 k=1
140 r=FNr(1);s=FNr(1)
150 :
160 t1=TIME
170 FOR i=1 TO n
180 FOR j=1 TO n
190 :
200 NEXT j
210 NEXT i
220 t2=TIME-t1
230 :
240 t1=TIME
250 FOR i=1 TO n
260 FOR j=1 TO n
270 :a(i,j)=a(i,j)+r*a(k,j)
280 NEXT j
290 NEXT i
300 t1=TIME-t1
310 :
320 dv=0: ' dv=8 for printer
330 PRINT #dv,"-----"
340 PRINT #dv, "time: ";ROUND( (t1-t2)/300,2 ); "seconds"
350 LIST 270,#dv
```

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